CS150 - Computer Organization and Architecture
Homework #4 - Spring 2016

Due on or before Friday, April 1 at 14:20 PST

Name ━━━━━━━━━━━━━━━━━━━━━━

1. (25 pts) Please produce the RTL for the AND instruction of the atmega328. Your RTL should only partition the instruction into the FETCH and EXECUTE phases. You may provide a separate sheet of paper if desired.

2. (25 pts) Please produce the RTL for the COM instruction of the atmega328. Your RTL should only partition the instruction into the FETCH and EXECUTE phases. You may provide a separate sheet of paper if desired.