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ECE 5780

PostLab 05

1. The AUTOEND bit in the CR2 automatically sends a stop condition after transferring. This stops all communication and doesn’t allow the restart condition to be reached.
2. If 400 kHz fast-mode was used then the bits in TIMINGR would be set as follows: PRESC = 0, SCLL = 0x9, SCLH = 0x3, SDADEL = 0x1, and SCLDEL = 0x3.
3. The interrupt enable bits that match the flags are found in the control register 1 and they are as follows: TC = bit 6, NACKF = bit 4, TXIS = bit 1, ARLO = bit 7.
4. The three ranges the gyroscope operates in are x, y, and z.
5. When the SDO is set low the LSb is 0, and when it is set high, the LSb is 1. The address we used in the lab was 0x69, so if the SDO was low it would be 0x68. If the high address was 0x6B then the low address would be 0x6A.