

Digital Logic Design Laboratory Schedule

Fall 2020

August 24, 2020

Week	Due Date	Content
1	9/11	No Lab
2	9/18	Lab 0
3	9/25	Lab 1
4	10/2	Lab 2
5	10/9	Lab 3
6	10/16	Lab 4 & 5 (1st week)
7	10/23	Lab 4 & 5 (2nd week)
8	10/30	Lab 4 & 5 (3rd week)
9	11/6	Lab 6 & 7 (1st week)
10	11/13	Lab 6 & 7 (2nd week)
11	11/20	Lab 6 & 7 (3rd week)
12	11/29	Thanksgiving - No Lab
13	12/4	Lab 8 and Optional Project
14	TBD	Make-up for all students