Local modules

PLC_1 [CPU 1214C DC/DC/DC]

PLC_1			
General\Project information			
Name	PLC_1	**Author**	Raski
Comment		**Slot**	1
Rack	0		
General\Catalog information			
Short designation	CPU 1214C DC/DC/DC	**Description**	Work memory 75 KB; 24VDC power supply with DI14 x 24VDC SINK/ SOURCE, DQ10 x 24VDC and AI2 on board; 6 high-speed counters and 4 pulse outputs on board; signal board expands on-board I/O; up to 3 communication modules for serial communication; up to 8 signal modules for I/O expansion; 0.04 ms/1000 instructions; PROFINET interface for programming, HMI and PLC-to-PLC communication
Article number	6ES7 214-1AG40-0XB0	**Firmware version**	V4.0
General\Identification & Maintenance			
Plant designation		**Location identifier**	
Installation date	2022-09-08 16:02:50.849	**Additional information**	
Connection resources			
PG communication:	1	**OP communication:**	1
S7 basic communication:	0	**S7 communication:**	0
Maximum number of S7 connection resources:	38		
PROFINET interface [X1]\General			
Name	PROFINET interface_1	**Author**	Raski
Comment			
PROFINET interface [X1]\General\Project information			
Name	DI 14/DQ 10_1	**Comment**	
Name	AI 2_1	**Comment**	
Name	AQ 1x12BIT_1	**Comment**	
PROFINET interface [X1]\General\Catalog information			
Short designation	AQ1 Signal board	**Description**	Signal board AQ1 x 12 bits; plug-in terminal blocks; output: +/-10V and 0 to 20 mA; configurable diagnostics; configurable substitute output value
Article number	6ES7 232-4HA30-0XB0	**Firmware version**	V1.0
PROFINET interface [X1]\Ethernet addresses\Interface networked with			
Subnet:	PN/IE_1		
PROFINET interface [X1]\Ethernet addresses\IP protocol			
IP configuration	Set IP address in the project	**IP address:**	10.30.5.2
Subnet mask:	255.255.255.0	**Use router**	False
PROFINET interface [X1]\Ethernet addresses\PROFINET			
PROFINET device name is set directly at the device	False	**Generate PROFINET device name automatically**	True
PROFINET device name:	plc_1	**Converted name:**	plcxb1d0ed
Device number:	0		

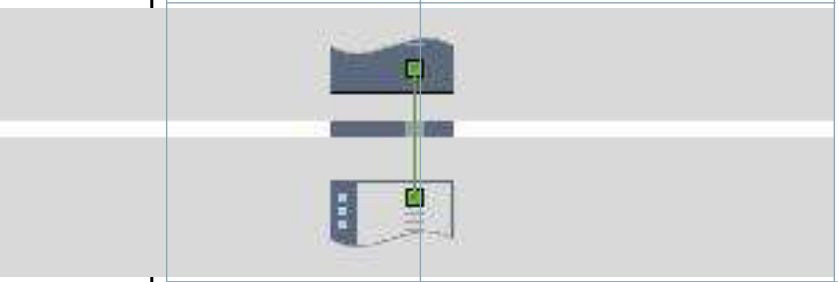
PROFINET interface [X1]\Time synchronization			
Enable time synchronization via NTP server	Enable time synchronization via NTP server		IP addresses
Server 1	0.0.0.0	**Server 2**	0.0.0.0
Server 3	0.0.0.0	**Server 4**	0.0.0.0
Update interval	10sec		
PROFINET interface [X1]\Digital inputs\Channel0			
Channel address	I0.0	**Input filters**	6.4 millisec
Enable pulse catch	0		
PROFINET interface [X1]\Digital inputs\Channel0			
Enable rising edge detection	0	**RidPrefixRisingEdgeEvent**	49152
Event name:	0	**Hardware interrupt:**	0
Rising edge0	Rising edge0		
PROFINET interface [X1]\Digital inputs\Channel0			
Enable falling edge detection	0	**RidPrefixFallingEdgeEvent**	49280
Event name:	0	**Hardware interrupt:**	0
Falling edge0	Falling edge0		
PROFINET interface [X1]\Digital inputs\Channel1			
Channel address	I0.1	**Input filters**	6.4 millisec
Enable pulse catch	0		
PROFINET interface [X1]\Digital inputs\Channel1			
Enable rising edge detection	0	**RidPrefixRisingEdgeEvent**	49153
Event name:	0	**Hardware interrupt:**	0
Rising edge1	Rising edge1		
PROFINET interface [X1]\Digital inputs\Channel1			
Enable falling edge detection	0	**RidPrefixFallingEdgeEvent**	49281
Event name:	0	**Hardware interrupt:**	0
Falling edge1	Falling edge1		
PROFINET interface [X1]\Digital inputs\Channel2			
Channel address	I0.2	**Input filters**	6.4 millisec
Enable pulse catch	0		
PROFINET interface [X1]\Digital inputs\Channel2			
Enable rising edge detection	0	**RidPrefixRisingEdgeEvent**	49154
Event name:	0	**Hardware interrupt:**	0
Rising edge2	Rising edge2		
PROFINET interface [X1]\Digital inputs\Channel2			
Enable falling edge detection	0	**RidPrefixFallingEdgeEvent**	49282
Event name:	0	**Hardware interrupt:**	0
Falling edge2	Falling edge2		
PROFINET interface [X1]\Digital inputs\Channel3			
Channel address	I0.3	**Input filters**	6.4 millisec
Enable pulse catch	0		
PROFINET interface [X1]\Digital inputs\Channel3			
Enable rising edge detection	0	**RidPrefixRisingEdgeEvent**	49155
Event name:	0	**Hardware interrupt:**	0
Rising edge3	Rising edge3		
PROFINET interface [X1]\Digital inputs\Channel3			
Enable falling edge detection	0	**RidPrefixFallingEdgeEvent**	49283
Event name:	0	**Hardware interrupt:**	0
Falling edge3	Falling edge3		

PROFINET interface [X1]\Digital inputs\Channel4			
Channel address	I0.4	**Input filters**	6.4 millisec
Enable pulse catch	0		
PROFINET interface [X1]\Digital inputs\Channel4			
Enable rising edge detection	0	**RidPrefixRisingEdgeEvent**	49156
Event name:	0	**Hardware interrupt:**	0
Rising edge4	Rising edge4		
PROFINET interface [X1]\Digital inputs\Channel4			
Enable falling edge detection	0	**RidPrefixFallingEdgeEvent**	49284
Event name:	0	**Hardware interrupt:**	0
Falling edge4	Falling edge4		
PROFINET interface [X1]\Digital inputs\Channel5			
Channel address	I0.5	**Input filters**	6.4 millisec
Enable pulse catch	0		
PROFINET interface [X1]\Digital inputs\Channel5			
Enable rising edge detection	0	**RidPrefixRisingEdgeEvent**	49157
Event name:	0	**Hardware interrupt:**	0
Rising edge5	Rising edge5		
PROFINET interface [X1]\Digital inputs\Channel5			
Enable falling edge detection	0	**RidPrefixFallingEdgeEvent**	49285
Event name:	0	**Hardware interrupt:**	0
Falling edge5	Falling edge5		
PROFINET interface [X1]\Digital inputs\Channel6			
Channel address	I0.6	**Input filters**	6.4 millisec
Enable pulse catch	0		
PROFINET interface [X1]\Digital inputs\Channel6			
Enable rising edge detection	0	**RidPrefixRisingEdgeEvent**	49158
Event name:	0	**Hardware interrupt:**	0
Rising edge6	Rising edge6		
PROFINET interface [X1]\Digital inputs\Channel6			
Enable falling edge detection	0	**RidPrefixFallingEdgeEvent**	49286
Event name:	0	**Hardware interrupt:**	0
Falling edge6	Falling edge6		
PROFINET interface [X1]\Digital inputs\Channel7			
Channel address	I0.7	**Input filters**	6.4 millisec
Enable pulse catch	0		
PROFINET interface [X1]\Digital inputs\Channel7			
Enable rising edge detection	0	**RidPrefixRisingEdgeEvent**	49159
Event name:	0	**Hardware interrupt:**	0
Rising edge7	Rising edge7		
PROFINET interface [X1]\Digital inputs\Channel7			
Enable falling edge detection	0	**RidPrefixFallingEdgeEvent**	49287
Event name:	0	**Hardware interrupt:**	0
Falling edge7	Falling edge7		
PROFINET interface [X1]\Digital inputs\Channel8			
Channel address	I1.0	**Input filters**	6.4 millisec
Enable pulse catch	0		
PROFINET interface [X1]\Digital inputs\Channel8			
Enable rising edge detection	0	**RidPrefixRisingEdgeEvent**	49160
Event name:	0	**Hardware interrupt:**	0

Rising edge8	Rising edge8		
PROFINET interface [X1]\Digital inputs\Channel8			
Enable falling edge detection	0	**RidPrefixFallingEdgeEvent**	49288
Event name:	0	**Hardware interrupt:**	0
Falling edge8	Falling edge8		
PROFINET interface [X1]\Digital inputs\Channel9			
Channel address	I1.1	**Input filters**	6.4 millisec
Enable pulse catch	0		
PROFINET interface [X1]\Digital inputs\Channel9			
Enable rising edge detection	0	**RidPrefixRisingEdgeEvent**	49161
Event name:	0	**Hardware interrupt:**	0
Rising edge9	Rising edge9		
PROFINET interface [X1]\Digital inputs\Channel9			
Enable falling edge detection	0	**RidPrefixFallingEdgeEvent**	49289
Event name:	0	**Hardware interrupt:**	0
Falling edge9	Falling edge9		
PROFINET interface [X1]\Digital inputs\Channel10			
Channel address	I1.2	**Input filters**	6.4 millisec
Enable pulse catch	0		
PROFINET interface [X1]\Digital inputs\Channel10			
Enable rising edge detection	0	**RidPrefixRisingEdgeEvent**	49162
Event name:	0	**Hardware interrupt:**	0
Rising edge10	Rising edge10		
PROFINET interface [X1]\Digital inputs\Channel10			
Enable falling edge detection	0	**RidPrefixFallingEdgeEvent**	49290
Event name:	0	**Hardware interrupt:**	0
Falling edge10	Falling edge10		
PROFINET interface [X1]\Digital inputs\Channel11			
Channel address	I1.3	**Input filters**	6.4 millisec
Enable pulse catch	0		
PROFINET interface [X1]\Digital inputs\Channel11			
Enable rising edge detection	0	**RidPrefixRisingEdgeEvent**	49163
Event name:	0	**Hardware interrupt:**	0
Rising edge11	Rising edge11		
PROFINET interface [X1]\Digital inputs\Channel11			
Enable falling edge detection	0	**RidPrefixFallingEdgeEvent**	49291
Event name:	0	**Hardware interrupt:**	0
Falling edge11	Falling edge11		
PROFINET interface [X1]\Digital inputs\Channel12			
Channel address	I1.4	**Input filters**	6.4 millisec
Enable pulse catch	0		
PROFINET interface [X1]\Digital inputs\Channel13			
Channel address	I1.5	**Input filters**	6.4 millisec
Enable pulse catch	0		
PROFINET interface [X1]\Analog inputs\Noise reduction			
Integration time	50 Hz (20 ms)		
PROFINET interface [X1]\Analog inputs\Channel0			
Channel address	IW64	**Measurement type**	Voltage
Voltage range	0..10 V	**Smoothing**	Weak (4 cycles)
Empty		**Enable overflow diagnostics**	1

PROFINET interface [X1]\Analog inputs\Channel1			
Channel address	IW66	**Measurement type**	Voltage
Voltage range	0..10 V	**Smoothing**	Weak (4 cycles)
Empty		**Enable overflow diagnostics**	1
PROFINET interface [X1]\Digital outputs			
Reaction to CPU STOP	Use substitute value		
PROFINET interface [X1]\Digital outputs\Channel0			
Channel address	Q0.0	**Substitute a value of 1 on a change from RUN to STOP.**	0
PROFINET interface [X1]\Digital outputs\Channel1			
Channel address	Q0.1	**Substitute a value of 1 on a change from RUN to STOP.**	0
PROFINET interface [X1]\Digital outputs\Channel2			
Channel address	Q0.2	**Substitute a value of 1 on a change from RUN to STOP.**	0
PROFINET interface [X1]\Digital outputs\Channel3			
Channel address	Q0.3	**Substitute a value of 1 on a change from RUN to STOP.**	0
PROFINET interface [X1]\Digital outputs\Channel4			
Channel address	Q0.4	**Substitute a value of 1 on a change from RUN to STOP.**	0
PROFINET interface [X1]\Digital outputs\Channel5			
Channel address	Q0.5	**Substitute a value of 1 on a change from RUN to STOP.**	0
PROFINET interface [X1]\Digital outputs\Channel6			
Channel address	Q0.6	**Substitute a value of 1 on a change from RUN to STOP.**	0
PROFINET interface [X1]\Digital outputs\Channel7			
Channel address	Q0.7	**Substitute a value of 1 on a change from RUN to STOP.**	0
PROFINET interface [X1]\Digital outputs\Channel8			
Channel address	Q1.0	**Substitute a value of 1 on a change from RUN to STOP.**	0
PROFINET interface [X1]\Digital outputs\Channel9			
Channel address	Q1.1	**Substitute a value of 1 on a change from RUN to STOP.**	0
PROFINET interface [X1]\Operating mode			
IO controller	True	**IO system**	
Device number	0	**IO device**	False
PROFINET interface [X1]\Analog outputs			
Reaction to CPU STOP	Use substitute value		
PROFINET interface [X1]\Analog outputs\Channel0			
Channel address	QW80	**Analog output type**	Voltage
Voltage range	+/- 10 V	**Substitute value for channel on a change from RUN to STOP**	0.000V
Empty		**Enable short circuit diagnostics**	1
Enable overflow diagnostics	1	**Enable underflow diagnostics**	1

PROFINET interface [X1]\I/O addresses\Input addresses			
Start address	0.0	**End address**	1.7
Organization block	0	**Process image**	0
PROFINET interface [X1]\I/O addresses\Input addresses			
Start address	64	**End address**	67
Organization block	0	**Process image**	0
PROFINET interface [X1]\I/O addresses\Output addresses			
Start address	0.0	**End address**	1.7
Organization block	0	**Process image**	0
PROFINET interface [X1]\I/O addresses\Output addresses			
Start address	80	**End address**	81
Organization block	0	**Process image**	0
PROFINET interface [X1]\Advanced options\Interface options			
Support device replacement without exchangeable medium	True	**Use IEC V2.2 LLDP mode**	True
Keep-Alive connection monitoring:	30s		
PROFINET interface [X1]\Advanced options\Real time settings\IO communication			
Send clock:	1.000ms		
PROFINET interface [X1]\Advanced options\Real time settings\Real time options			
Calculated bandwidth for cyclic IO data:	0.000ms	**Calculated bandwidth for cyclic IO data:**	0.000%
PROFINET interface [X1]\Advanced options\Port [X1 P1]\General			
Name	Port_1	**Author**	Raski
Comment			
PROFINET interface [X1]\Advanced options\Port [X1 P1]\Port interconnection\Local port:			
Local port:	PLC_1\PROFINET interface_1 [X1]\Port_1 [X1 P1]	**Medium:**	Copper
Cable name:	---		



PROFINET interface [X1]\Advanced options\Port [X1 P1]\Port interconnection\Partner port:			
	Monitoring of partner port is not possible	**Partner port:**	Any partner
PROFINET interface [X1]\Advanced options\Port [X1 P1]\Port options\Activate			
Activate this port for use	True		
PROFINET interface [X1]\Advanced options\Port [X1 P1]\Port options\Connection			
Transmission rate / duplex:	Automatic	**Monitor**	False
Enable autonegotiation	True		
PROFINET interface [X1]\Advanced options\Port [X1 P1]\Port options\Boundaries			
End of detection of accessible devices	False	**End of topology discovery**	False
End of the sync domain	False		
High speed counters (HSC)\HSC1\General\Enable			
Enable this high speed counter	0	**Enable this high speed counter**	0

Enable this high speed counter	0	**Enable this high speed counter**	0
Enable this high speed counter	0	**Enable this high speed counter**	0
High speed counters (HSC)\HSC1\General\Project information			
Name	HSC_1	**Comment**	
Name	HSC_2	**Comment**	
Name	HSC_3	**Comment**	
Name	HSC_4	**Comment**	
Name	HSC_5	**Comment**	
Name	HSC_6	**Comment**	
High speed counters (HSC)\HSC1\I/O addresses\Input addresses			
Start address	1000.0	**End address**	1003.7
Start address	1004.0	**End address**	1007.7
Organization block	0	**Start address**	1008.0
End address	1011.7	**Organization block**	0
Process image	0	**Start address**	1012.0
End address	1015.7	**Organization block**	0
Process image	0	**Start address**	1016.0
End address	1019.7	**Organization block**	0
Process image	0	**Start address**	1020.0
End address	1023.7	**Organization block**	0
Process image	0	**Organization block**	0
Process image	0	**Process image**	0
Pulse generators (PTO/PWM)\PTO1/PWM1\General\Enable			
Enable this pulse generator	0	**Enable this pulse generator**	0
Pulse generators (PTO/PWM)\PTO1/PWM1\General\Project information			
Name	Pulse_1	**Comment**	
Name	Pulse_2	**Comment**	
Pulse generators (PTO/PWM)\PTO1/PWM1\I/O addresses\Output addresses			
Start address	1000.0	**End address**	1001.7
Start address	1002.0	**End address**	1003.7
Organization block	0	**Organization block**	0
Process image	0	**Process image**	0
Startup			
Startup after POWER ON	Warm restart - mode before POWER OFF	**Comparison preset to actual configuration**	Startup CPU even if mismatch
Configuration time	60000ms	**OBs should be interruptible**	0
Cycle			
Cycle monitoring time	150ms		
Enable minimum cycle time for cyclic OBs	0	**Minimum cycle time**	1ms
Communication load			
Cycle load due to communication	20%		
System and clock memory\System memory bits			
Enable the use of system memory byte	1	**Address of system memory byte (MBx)**	1
First cycle	%M1.0 (FirstScan)	**Diagnostic status changed**	%M1.1 (DiagStatusUpdate)
Always 1 (high)	%M1.2 (AlwaysTRUE)	**Always 0 (low)**	%M1.3 (AlwaysFALSE)
System and clock memory\Clock memory bits			
Enable the use of clock memory byte	0	**Address of clock memory byte (MBx)**	0
10 Hz clock		**5 Hz clock**	
2.5 Hz clock		**2 Hz clock**	
1.25 Hz clock		**1 Hz clock**	

0.625 Hz clock		**0.5 Hz clock**	
Web server\General			
Activate web server on this module	False	**Permit access only with HTTPS**	False
Web server\Automatic update			
Enable automatic update	True	**Update interval**	0s
Web server\User interface languages			
Assign project language		**User interface languages**	
English (United States)		German	
English (United States)		English	
English (United States)		French	
English (United States)		Spanish	
English (United States)		Italian	
English (United States)		Chinese (simplified)	
Web server\User management			
User name		**User rights**	
Everybody			
Web server\User-defined web pages			
Application name	**HTML source path**	**Default HTML page**	**Files with dynamic content**	**Web DB number**	**Fragment DB number**
		index.htm	.htm;.html	333	334
User interface languages			
Assign project language		**User interface languages**	
English (United States)		German	
English (United States)		English	
English (United States)		French	
English (United States)		Spanish	
English (United States)		Italian	
English (United States)		Chinese (simplified)	
Time of day\Local time			
Time zone	(UTC +01:00) Berlin, Bern, Brussels, Rome, Stockholm, Vienna		
Time of day\Daylight saving time			
Activate daylight saving time	0	**Difference between standard and daylight saving time**	60mins
Time of day\Daylight saving time\Start of daylight saving time			
Starting week of the month:	Last		Sunday
of	March	**at**	01:00 a.m.
Time of day\Daylight saving time\Start of standard time			
	Last		Sunday
of	October	**at**	02:00 a.m.
Protection & Security			
Level of protection	No protection		
Protection & Security\Connection mechanisms			
Permit access with PUT/GET communication from remote partner	True		
Overview of addresses\Overview of addresses\Overview of addresses			
Inputs	True	**Outputs**	True
Address gaps	False	**Slot**	True

Type	Addr. from	Addr. to	Module	PIP	Device name	Device number	Size	Master / IO system	Rack	Slot
I	0	1	DI 14/DQ 10_1	Automatic update	PLC_1 [CPU 1214C DC/DC/DC]	-	2 Bytes	-	0	1 1
O	0	1	DI 14/DQ 10_1	Automatic update	PLC_1 [CPU 1214C DC/DC/DC]	-	2 Bytes	-	0	1 1
I	64	67	AI 2_1	Automatic update	PLC_1 [CPU 1214C DC/DC/DC]	-	4 Bytes	-	0	1 2
O	80	81	AQ 1x12BIT_1	Automatic update	PLC_1 [CPU 1214C DC/DC/DC]	-	2 Bytes	-	0	1 3
I	1000	1003	HSC_1	Automatic update	PLC_1 [CPU 1214C DC/DC/DC]	-	4 Bytes	-	0	1 16
I	1004	1007	HSC_2	Automatic update	PLC_1 [CPU 1214C DC/DC/DC]	-	4 Bytes	-	0	1 17
I	1008	1011	HSC_3	Automatic update	PLC_1 [CPU 1214C DC/DC/DC]	-	4 Bytes	-	0	1 18
I	1012	1015	HSC_4	Automatic update	PLC_1 [CPU 1214C DC/DC/DC]	-	4 Bytes	-	0	1 19
I	1016	1019	HSC_5	Automatic update	PLC_1 [CPU 1214C DC/DC/DC]	-	4 Bytes	-	0	1 20
I	1020	1023	HSC_6	Automatic update	PLC_1 [CPU 1214C DC/DC/DC]	-	4 Bytes	-	0	1 21
O	1000	1001	Pulse_1	Automatic update	PLC_1 [CPU 1214C DC/DC/DC]	-	2 Bytes	-	0	1 32
O	1002	1003	Pulse_2	Automatic update	PLC_1 [CPU 1214C DC/DC/DC]	-	2 Bytes	-	0	1 33
O	1004	1005	Pulse_3	Automatic update	PLC_1 [CPU 1214C DC/DC/DC]	-	2 Bytes	-	0	1 34
O	1006	1007	Pulse_4	Automatic update	PLC_1 [CPU 1214C DC/DC/DC]	-	2 Bytes	-	0	1 35
I	8	9	DI 16/DQ 16x24VDC _1	Automatic update	PLC_1 [CPU 1214C DC/DC/DC]	-	2 Bytes	-	0	2

Type	Addr. from	Addr. to	Module	PIP	Device name	Device number	Size	Master / IO system	Rack	Slot
O	8	9	DI 16/DQ 16x24VDC _1	Automatic update	PLC_1 [CPU 1214C DC/DC/DC]	-	2 Bytes	-	0	2

Local modules

DI 16/DQ 16x24VDC_1

DI 16/DQ 16x24VDC_1			
General\Project information			
Name	DI 16/DQ 16x24VDC_1	**Author**	Raski
Comment		**Slot**	2
General\Catalog information			
Short designation	SM 1223 DI16/DQ16 x 24VDC	**Description**	Digital input/output module DI16 x 24VDC SINK/SOURCE and DQ16 x 24VDC; configurable input delay; plug-in terminal blocks
Article number	6ES7 223-1BL32-0XB0	**Firmware version**	V2.0
DI 16/DQ 16\Project information			
Name	DI 16/DQ 16x24VDC_1	**Comment**	
DI 16/DQ 16\Digital inputs\Input filters			
I8.0 - I8.3	6.40ms	**I8.4 - I8.7**	6.40ms
I9.0 - I9.3	6.40ms	**I9.4 - I9.7**	6.40ms
DI 16/DQ 16\Digital inputs\Channel0			
Channel address	I8.0		
DI 16/DQ 16\Digital inputs\Channel1			
Channel address	I8.1		
DI 16/DQ 16\Digital inputs\Channel2			
Channel address	I8.2		
DI 16/DQ 16\Digital inputs\Channel3			
Channel address	I8.3		
DI 16/DQ 16\Digital inputs\Channel4			
Channel address	I8.4		
DI 16/DQ 16\Digital inputs\Channel5			
Channel address	I8.5		
DI 16/DQ 16\Digital inputs\Channel6			
Channel address	I8.6		
DI 16/DQ 16\Digital inputs\Channel7			
Channel address	I8.7		
DI 16/DQ 16\Digital inputs\Channel8			
Channel address	I9.0		
DI 16/DQ 16\Digital inputs\Channel9			
Channel address	I9.1		
DI 16/DQ 16\Digital inputs\Channel10			
Channel address	I9.2		
DI 16/DQ 16\Digital inputs\Channel11			
Channel address	I9.3		
DI 16/DQ 16\Digital inputs\Channel12			
Channel address	I9.4		
DI 16/DQ 16\Digital inputs\Channel13			
Channel address	I9.5		
DI 16/DQ 16\Digital inputs\Channel14			
Channel address	I9.6		
DI 16/DQ 16\Digital inputs\Channel15			
Channel address	I9.7		
DI 16/DQ 16\Digital outputs			
Reaction to CPU STOP	Use substitute value		
DI 16/DQ 16\Digital outputs\Channel0			
Channel address	Q8.0	**Substitute a value of 1 on a change from RUN to STOP.**	0

DI 16/DQ 16\Digital outputs\Channel1			
Channel address	Q8.1	**Substitute a value of 1 on a change from RUN to STOP.**	0
DI 16/DQ 16\Digital outputs\Channel2			
Channel address	Q8.2	**Substitute a value of 1 on a change from RUN to STOP.**	0
DI 16/DQ 16\Digital outputs\Channel3			
Channel address	Q8.3	**Substitute a value of 1 on a change from RUN to STOP.**	0
DI 16/DQ 16\Digital outputs\Channel4			
Channel address	Q8.4	**Substitute a value of 1 on a change from RUN to STOP.**	0
DI 16/DQ 16\Digital outputs\Channel5			
Channel address	Q8.5	**Substitute a value of 1 on a change from RUN to STOP.**	0
DI 16/DQ 16\Digital outputs\Channel6			
Channel address	Q8.6	**Substitute a value of 1 on a change from RUN to STOP.**	0
DI 16/DQ 16\Digital outputs\Channel7			
Channel address	Q8.7	**Substitute a value of 1 on a change from RUN to STOP.**	0
DI 16/DQ 16\Digital outputs\Channel8			
Channel address	Q9.0	**Substitute a value of 1 on a change from RUN to STOP.**	0
DI 16/DQ 16\Digital outputs\Channel9			
Channel address	Q9.1	**Substitute a value of 1 on a change from RUN to STOP.**	0
DI 16/DQ 16\Digital outputs\Channel10			
Channel address	Q9.2	**Substitute a value of 1 on a change from RUN to STOP.**	0
DI 16/DQ 16\Digital outputs\Channel11			
Channel address	Q9.3	**Substitute a value of 1 on a change from RUN to STOP.**	0
DI 16/DQ 16\Digital outputs\Channel12			
Channel address	Q9.4	**Substitute a value of 1 on a change from RUN to STOP.**	0
DI 16/DQ 16\Digital outputs\Channel13			
Channel address	Q9.5	**Substitute a value of 1 on a change from RUN to STOP.**	0
DI 16/DQ 16\Digital outputs\Channel14			
Channel address	Q9.6	**Substitute a value of 1 on a change from RUN to STOP.**	0
DI 16/DQ 16\Digital outputs\Channel15			
Channel address	Q9.7	**Substitute a value of 1 on a change from RUN to STOP.**	0

DI 16/DQ 16\I/O addresses\Input addresses			
Start address	8.0	**End address**	9.7
Organization block	0	**Process image**	0
DI 16/DQ 16\I/O addresses\Output addresses			
Start address	8.0	**End address**	9.7
Organization block	0	**Process image**	0