

(T _A = 25 _i C unless otherwise noted)								
Pten §n	Ð	A A						
TO ID								
Storage Temperature	T _{STG}	All	-40 to +150	iC				
Operating Temperature	T _{OPR}	All	-40 to +85	iC				
Lead Solder Temperature	T _{SOL}	All	260 for 10 sec	iC				
Junction Temperature Range	TJ	All	-40 to +100	iC				
Isolation Surge Voltage ⁽⁴⁾ (peak AC voltage, 60Hz, 1 sec duration)	V _{ISO}	All	7500	Vac(pk)				
Total Device Power Dissipation @ 25¡C	D	All	250	mW				
Derate above 25¡C	P _D	All	2.94	mW/¡C				
H .								
Continuous Forward Current	I _F	All	60	mA				
Reverse Voltage	V _R	All	6	V				
Total Power Dissipation 25¡C Ambient	В	All	120	mW				
Derate above 25¡C	P _D	All	1.41	mW/¡C				
Ð								
Off-State Output Terminal Voltage	V _{DRM}	All	600	V				
Peak Repetitive Surge Current (PW = 100 μs, 120 pps)	I _{TSM}	All	1	А				
Total Power Dissipation @ 25¡C Ambient	В	All	150	mW				
Derate above 25¡C	P _D	All All	1.76	mW/¡C				



TA = 25¡C Unless otherwise speciÞed)								
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B								
Input Forward Voltage	$I_F = 30 \text{ mA}$	V_{F}	All		1.3	1.5	V	
Reverse Leakage Current	Reverse Leakage Current V _R = 6 V		All		0.005	100	μΑ	
Ð								
Peak Blocking Current, Either Direction	- 600\/ L = 0 (note 1)		MOC316X-M		10	100	nA	
Feak Blocking Current, Either Direction	$V_{DRM} = 600V, I_F = 0 \text{ (note 1)}$	I _{DRM1}	MOC306X-M		10	500	1 11/4	
Critical Rate of Rise of Off-State Voltage	L = 0 (bauro 0, noto 3)	dv/dt	MOC306X-M	600	1500		\//\\\	
Citical Nate of Nise of Oil-State voltage	I _F = 0 (Þgure 9, note 3)	uv/ut	MOC316X-M	1000			V/µs	

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5 5 5 5	M F M b							
		I _{FT}	MOC3061-M			15	\Box	
LED Trigger Current (rated I _{FT})	main terminal Voltage = 3V (note 2)		MOC3062-M/ MOC3162-M			10	mA	
(rated IFT)	voltage = 3v (flote 2)		MOC3063-M/ MOC3163-M			5		
Peak On-State Voltage, Either Direction	$I_{TM} = 100 \text{ mA peak},$ $I_F = \text{rated } I_{FT}$	V _{TM}	All		1.8	3	V	
Holding Current, Either Direction		I _H	All		500		μΑ	

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Inhibit Voltage (MT1-MT2 voltage			MOC3061-M/2M/3M	12	20	
above which device will not trigger)	I _F = Rated I _{FT}	V _{INH}	MOC3162-M/3M	12	15	V
Leakage in Inhibited State	I_F = Rated I_{FT} , V_{DRM} = 600V, off state	I _{DRM2}	All	150	500	μΑ

6	E	5 n 5 0	M	P	M	Ą					
Isolation Voltage			f = 6	0 Hz, t	= 1 sec	;	V _{ISO}	All	7500		V

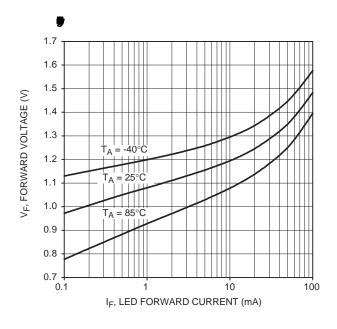
^{*}Typical values at $T_A = 25iC$

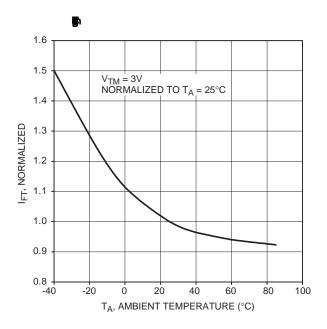
Notes

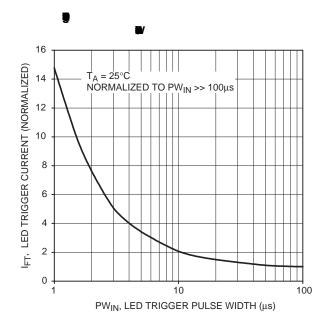
- 1. Test voltage must be applied within dv/dt rating.
- 2. All devices are guaranteed to trigger at an I_F value less than or equal to max I_{FT} . Therefore, recommended operating I_F lies between max I_{FT} (15 mA for MOC3061-M, 10 mA for MOC3062-M & MOC3162-M, 5 mA for MOC3063-M & MOC3163-M) and absolute max I_F (60 mA).
- 3. This is static dv/dt. See Figure 9 for test circuit. Commutating dv/dt is a function of the load-driving thyristor(s) only.
- 4. Isolation surge voltage, V_{ISO}, is an internal device dielectric breakdown rating. For this test, Pins 1 and 2 are common, and Pins 4, 5 and 6 are common.

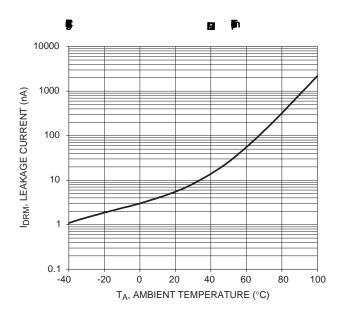






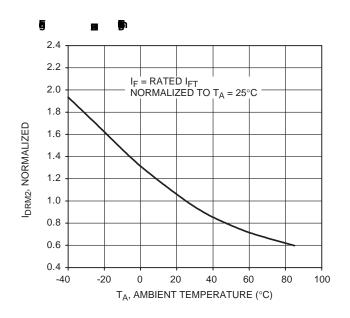


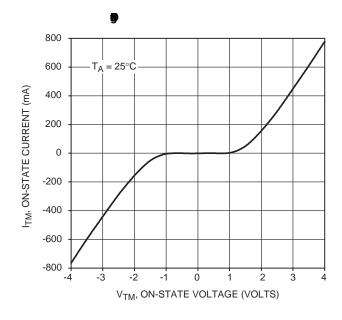


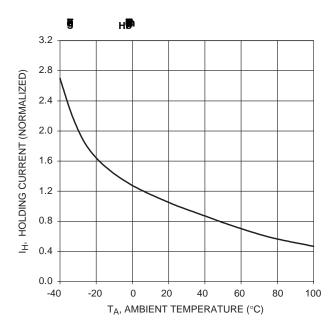


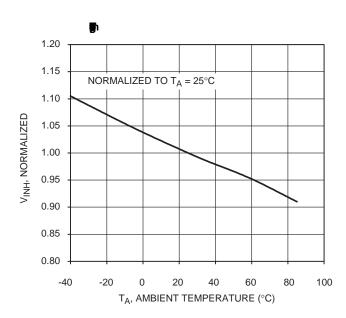






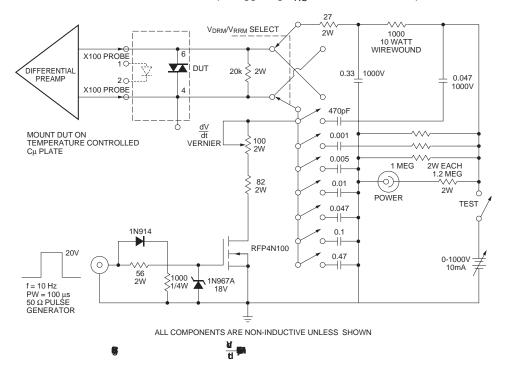








- 1. 100x scope probes are used, to allow high speeds and voltages.
- 2. The worst-case condition for static dv/dt is established by triggering the D.U.T. with a normal LED input current, then removing the current. The variable vernier resistor combined with various capacitor combinations allows the dv/dt to be gradually increased until the D.U.T. continues to trigger in response to the applied voltage pulse, even after the LED current has been removed. The dv/dt is then decreased until the D.U.T. stops triggering. \(\tau_{RC}\) is measured at this point and recorded.



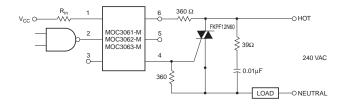
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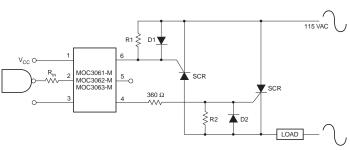
Typical circuit for use when hot line switching is required. In this circuit the "hot" side of the line is switched and the load connected to the cold or neutral side. The load may be connected to either the neutral or hot line.

 R_{in} is calculated so that I_F is equal to the rated I_{FT} of the part, 15 mA for the MOC3061-M, 10 mA for the MOC3062-M, or 5 mA for the MOC3063-M. The 39 ohm resistor and 0.01 μF capacitor are for snubbing of the triac and is often, but not always, necessary depending upon the particular triac and load used.

Suggested method of Þring two, back-to-back SCRÕs with a Fairchild triac driver. Diodes can be 1N4001; resistors, R1 and R2, are optional 330 ohm.

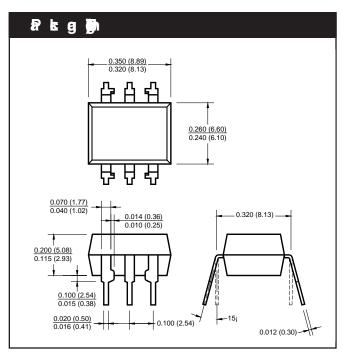
Note: This optoisolator should not be used to drive a load directly. It is intended to be a trigger device only.

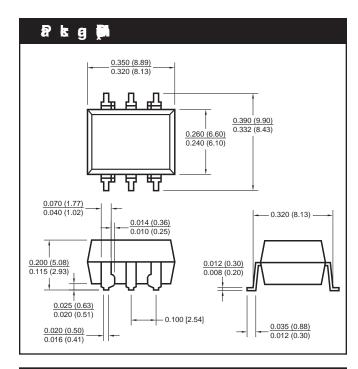


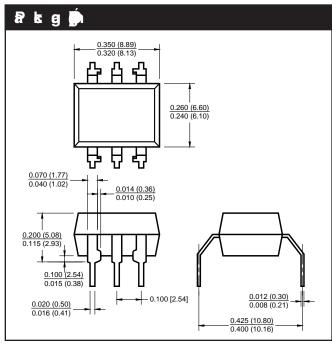


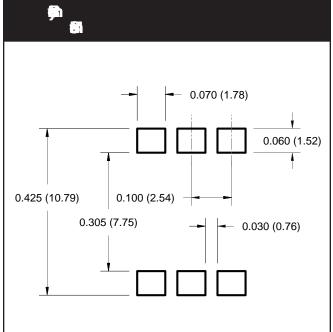










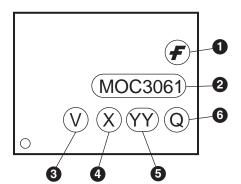


All dimensions are in inches (millimeters)



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S	S	Surface Mount Lead Bend
SR2	SR2	Surface Mount; Tape and reel
Т	Т	0.4" Lead Spacing
V	V	VDE 0884
TV	TV	VDE 0884, 0.4" Lead Spacing
SV	SV	VDE 0884, Surface Mount
SR2V	SR2V	VDE 0884, Surface Mount, Tape & Reel

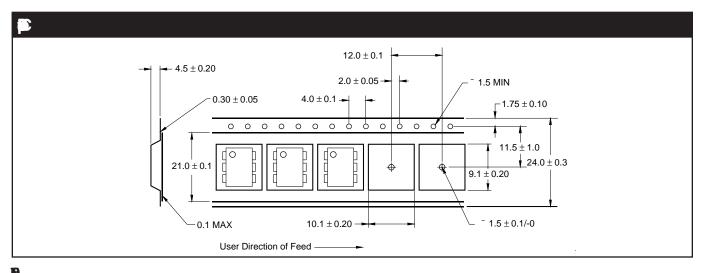


ð	
1	Fairchild logo
2	Device number
3	VDE mark (Note: Only appears on parts ordered with VDE option Đ See order entry table)
4	One digit year code, e.g., Ô3Õ
5	Two digit work week ranging from 0010 to 0530
6	Assembly package code

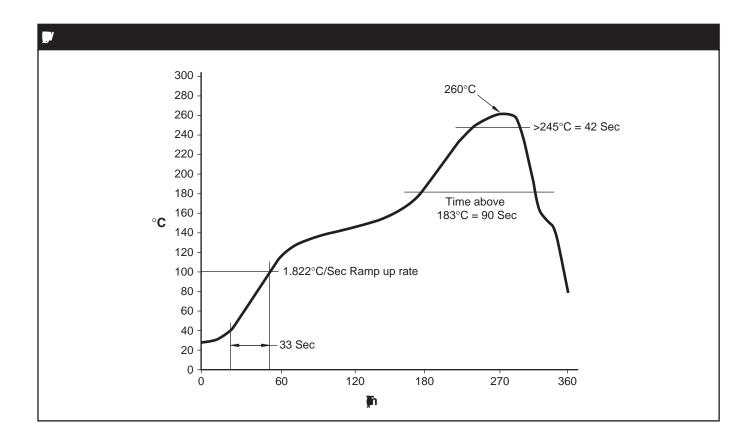
^{*}Note Đ Parts that do not have the ÔVŐ option (see deÞnition 3 above) that are marked with date code Ô325Ő or earlier are marked in portrait format.







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