EE 100,100001. LTJM U5B _UARTO_TXD/SPI1_CS1/DCAN0_RX/I2C2_SCL/ECAP1_IN_PWM1_OUT/PR1_PRU1_PRU_R30_15/PR1_PRU1_PRU_R31_15/GPI0 UARTO_RTSN/UART4_TXD/DCAN1_RX/I2C1_SCL/SPI1_D1/SPI1_CS0/PR1_EDC_SYNC1_OUT/GPI01_9 UARTI_TXD/MMC2_SDWP/DCAN1_RX/I2C1_SCL//PR1_UARTO_TXD/PR1_PRU0_PRU_R31_16/GPI00_15

D17 UART1_RTSN/TIMER5/DCANO_RX/I2C2_SCL/SPI1_CS1/PR1_UART0_RTS_N/PR1_EDC_LATCH1_IN/GPI00_13 N17 USB0_DP SubArctic AM335x N18 USBO_DM 15mm x 15mm Package USBO_DRVVBUS/GPIOO_18 | F 10 R159 0,1% USB DC A9 SPIO_SCLK/UART2_RXD/I2C2_SDA/EHRPWMOA/PR1_UARTO_CTS_N/PR1_EDIO_SOF/EMU2/GPIO0_2 SPIO_DO/UART2_TXD/I2C2_SCL/EHRPWMOB/PR1_UART0_RTS_N/PR1_EDIO_LATCH_IN/EMU3/GPIO0_3 $\frac{1}{P_{17}}$ R49 0,1% SPIO_D1/MMC1_SDWP/I2C1_SDA/EHRPWM0_TRIPZONE_INPUT/PR1_UARTO_RXD/PR1_EDIO_DATA_INO/PR1_EDIO_DATA_OUTO/GPIOO_4 SPIO_CSO/MMC2_SDWP/I2C1_SCL/EHRPWMO_SYNCI_O/PR1_UARTO_TXD/PR1_EDIO_DATA_IN1/PR1_EDIO_DATA_OUT1/GPIOO_5 SPIO_CS1/UART3_RXD/ECAP1_IN_PWM1_OUT/MMCO_POW/XDMA_EVENT_INTR2/MMCO_SDCD/EMU4/GPIOO_6 USB1_DRVVBUS/GPIO3_13 UARTO_RXD/SPI1_CSO/DCANO_TX/I2C2_SDA/ECAP2_IN_PWM2_OUT/PR1_PRU1_PRU_R30_14/PR1_PRU1_PRU1_PRU_R31_14/GPI01_10 GMII1_TXD0/RMII1_TXD0/RGMII1_TD0/MCASP1_AXR2/MCASP1_ACLKR/EQEP0B_IN/MMC1_CLK/GPI00_28 K16 UARTO_CTSN/UART4_RXD/DCAN1_TX/I2C1_SDA/SPI1_D0/TIMER7/PR1_EDC_SYNCO_OUT/GPI01_8 GMII1_TXD1/RMII1_TXD1/RGMII1_TD1/MCASP1_FSR/MCASP1_AXR1/EQEP0A_IN/MMC1_CMD/GPI00_21 K15 UART1_RXD/MMC1_SDWP/DCAN1_TX/I2C1_SDA//PR1_UART0_RXD/PR1_PRU1_PRU_R31_16/GPI00_14 GMII1_TXD2/DCANO_RX/RGMII1_TD2/UART4_TXD/MCASP1_AXR0/MMC2_DAT2/MCASP0_AHCLKX/GPI00_17 UART1_CTSN/TIMER6/DCAN0_TX/I2C2_SDA/SPI1_CS0/PR1_UART0_CTS_N/PR1_EDC_LATCH0_IN/GPI00_12 I2CO_SCL/TIMER7/UART2_RTSN/ECAP1_IN_PWM1_OUT////GPIO3_6 GMII1_TXD3/DCAN0_TX/RGMII1_TD3/UART4_RXD/MCASP1_FSX/MMC2_DAT1/MCASP0_FSR/GPI00_16 GMII1_TXEN/RMII1_TXEN/RGMII1_TCTL/TIMER4/MCASP1_AXR0/EQEP0_INDEX/MMC2_CMD/GPI03_3 H18 I2CO_SDA/TIMER4/UART2_CTSN/ECAP2_IN_PWM2_OUT////GPIO3_5 ECAPO_IN_PWMO_OUT/UART3_TXD/SPI1_CS1/PR1_ECAPO_ECAP_CAPIN_APWM_O/SPI1_SCLK/MMCO_SDWP/XDMA_EVENT_INTR2/GPIOO_7 RMII1_REFCLK/XDMA_EVENT_INTR2/SPI1_CS0/UART5_TXD/MCASP1_AXR3/MMC0_POW/MCASP1_AHCLKX/GPI00_2 MDIO_CLK/TIMER5/UART5_TXD/UART3_RTSN/MMC0_SDWP/MMC1_CLK/MMC2_CLK/GPIO0_1 GMII1_TXCLK/UART2_RXD/RGMII1_TCLK/MMC0_DAT7/MMC1_DAT0/UART1_DCDN/MCASP0_ACLKX/GPI03_9 MDIO_DATA/TIMER6/UART5_RXD/UART3_CTSN/MMC0_SDCD/MMC1_CMD/MMC2_CMD/GPIO0_0 GMII1_CRS/RMII1_CRS_DV/SPI1_D0/I2C1_SDA/MCASP1_ACLKX/UART5_CTSN/UART2_RXD/GPI03_1 LCD_DATA0/GPMC_A0//EHRPWM2A//PR1_PRU1_PRU_R30_0/PR1_PRU1_PRU_R31_0/GPI02_6 GMII1_COL/RMII2_REFCLK/SPI1_SCLK/UART5_RXD/MCASP1_AXR2/MMC2_DAT3/MCASP0_AXR2/GPI03_0 LCD_DATA1/GPMC_A1//EHRPWM2B//PR1_PRU1_PRU_R30_1/PR1_PRU1_PRU_R31_1/GPI02_7 GMI1_RXCLK/UART2_TXD/RGMI1_RCLK/MMC0_DAT6/MMC1_DAT1/UART1_DSRN/MCASP0_FSX/GPI03_10 LCD_DATA2/GPMC_A2//EHRPWM2_TRIPZONE_INPUT//PR1_PRU1_PRU_R30_2/PR1_PRU1_PRU_R31_2/GPI02_8 GMII1_RXD0/RMII1_RXD0/RGMII1_RD0/MCASP1_AHCLKX/MCASP1_AHCLKR/MCASP1_ACLKR/MCASP0_AXR3/GPI02_21 GMII1_RXD1/RMII1_RXD1/RGMII1_RD1/MCASP1_AXR3/MCASP1_FSR/EQEP0_STR0BE/MMC2_CLK/GPI02_20 _LCD_DATA3/GPMC_A3//EHRPWM2_SYNCI_O//PR1_PRU1_PRU_R30_3/PR1_PRU1_PRU_R31_3/GPI02_9 GMII1_RXD2/UART3_TXD/RGMII1_RD2/MMC0_DAT4/MMC1_DAT3/UART1_RIN/MCASP0_AXR1/GPI02_19 LCD_DATA4/GPMC_A4//EQEP2A_IN//PR1_PRU1_PRU_R30_4/PR1_PRU1_PRU_R31_4/GPI02_10 GMII1_RXD3/UART3_RXD/RGMII1_RD3/MMC0_DAT5/MMC1_DAT2/UART1_DTRN/MCASP0_AXR0/GPI02_18 LCD_DATA5/GPMC_A5//EQEP2B_IN//PR1_PRU1_PRU_R30_5/PR1_PRU1_PRU_R31_5/GPI02_ GMII1_RXERR/RMII1_RXERR/SPI1_D1/I2C1_SCL/MCASP1_FSX/UART5_RTSN/UART2_TXD/GPI03_2 LCD_DATA6/GPMC_A6/PR1_EDIO_DATA_IN6/EQEP2_INDEX/PR1_EDIO_DATA_OUT6/PR1_PRU1_PRU_R30_6/PR1_PRU1_PRU_R31_6/GPIO2_ LCD_DATA7/GPMC_A7/PR1_EDIO_DATA_IN7/EQEP2_STROBE/PR1_EDIO_DATA_OUT7/PR1_PRU1_PRU_R30_7/PR1_PRU1_PRU1_PRU_R31_7/GPIO2_1 GMII1_RXDV/LCD_MEMORY_CLK/RGMII1_RCTL/UART5_TXD/MCASP1_ACLKX/MMC2_DAT0/MCASP0_ACLKR/GPIO3_4 LCD_DATA8/GPMC_A12/EHRPWM1_TRIPZONE_INPUT/MCASPO_ACLKX/UART5_TXD/PR1_MII0_RXD3/UART2_CTSN/GPIO2_1 LCD_DATA9/GPMC_A13/EHRPWM1_SYNCI_O/MCASP0_FSX/UART5_RXD/PR1_MII0_RXD2/UART2_RTSN/GPIO2_15 LCD_DATA10/GPMC_A14/EHRPWM1A/MCASP0_AXR0//PR1_MII0_RXD1/UART3_CTSN/GPI02_16 LCD_DATA11/GPMC_A15/EHRPWM1B/MCASP0_AHCLKR/MCASP0_AXR2/PR1_MII0_RXD0/UART3_RTSN/GPI02_17 LCD_DATA12/GPMC_A16/EQEP1A_IN/MCASP0_ACLKR/MCASP0_AXR2/PR1_MII0_RXLINK/UART4_CTSN/GPI00_8 _LCD_DATA13/GPMC_A17/EQEP1B_IN/MCASP0_FSR/MCASP0_AXR3/PR1_MII0_RXER/UART4_RTSN/GPI00_9\ LCD_DATA14/GPMC_A18/EQEP1_INDEX/MCASP0_AXR1/UART5_RXD/PR1_MII_MR0_CLK/UART5_CTSN/GPI00_10 LCD_DATA15/GPMC_A19/EQEP1_STROBE/MCASPO_AHCLKX/MCASPO_AXR3/PR1_MIIO_RXDV/UART5_RTSN/GPIO0_11 LCD_PCLK/GPMC_A10//PR1_EDIO_DATA_IN4/PR1_EDIO_DATA_OUT4/PR1_PRU1_PRU_R30_10/PR1_PRU1_PRU_R31_10/GPIO2_24 LCD_VSYNC/GPMC_A8//PR1_EDIO_DATA_IN2/PR1_EDIO_DATA_OUT2/PR1_PRU1_PRU_R30_8/PR1_PRU1_PRU_R31_8/GPI02_2 LCD_HSYNC/GPMC_A9//PR1_EDIO_DATA_IN3/PR1_EDIO_DATA_OUT3/PR1_PRU1_PRU_R30_9/PR1_PRU1_PRU_R31_9/GPIO2_23 LCD_AC_BIAS_EN/GPMC_A11//PR1_EDIO_DATA_IN5/PR1_EDIO_DATA_OUT5/PR1_PRU1_PRU_R30_11/PR1_PRU1_PRU_R31_11/GPI02_2 MCASPO_AHCLKX/EQEPO_STROBE/MCASPO_AXR3/MCASP1_AXR1/EMU4/PR1_PRU0_PRU_R30_7/PR1_PRU0_PRU_R31_7/GPI03_21 A13 MCASPO_ACLKX/EHRPWMOA//SPI1_SCLK/MMCO_SDCD/PR1_PRUO_PRU_R30_0/PR1_PRUO_PRU_R31_0/GPI03_14 MCASPO_FSX/EHRPWMOB//SPI1_DO/MMC1_SDCD/PR1_PRU0_PRU_R30_1/PR1_PRU0_PRU_R31_1/GPI03_15 MCASPO_AXRO/EHRPWMO_TRIPZONE_INPUT//SPI1_D1/MMC2_SDCD/PR1_PRU0_PRU_R30_2/PR1_PRU0_PRU_R31_2/GPI03_% MLASPU_AXRU/EHRPWMU_TRIPZUNE_INPUT//SPIT_UT/MMLZ_SULU/PRT_PRUU_PRU_R3U_Z/PRT_PRUU_PRU_R3T_Z/UPIU3_I0 MCASPO_AHCLKR/EHRPWMO_SYNCI_O/MCASPO_AXR2/SPIT_CSO/ECAP2_IN_PWM2_OUT/PRT_PRUU_PRU_R30_3/PRT_PRUU_PRU_R31_3/GPI03_17 B12 MCASPO_ACLKR/EQEPOA_IN/MCASPO_AXR2/MCASP1_ACLKX/MMCO_SDWP/PR1_PRUO_PRU_R30_4/PR1_PRUO_PRU_R31_4/GPIO3_18 MCASPO_FSR/EQEPOB_IN/MCASPO_AXR3/MCASP1_FSX/EMU2/PR1_PRU0_PRU_R30_5/PR1_PRU0_PRU_R31_5/GPIO3_19 MCASPO_AXR1/EQEPO_INDEX//MCASP1_AXR0/EMU3/PR1_PRU0_PRU_R30_6/PR1_PRU0_PRU_R31_6/GPI03_20 AM3358BZCZ100 R28 33 U5_H18 R45 22 LCDPCLK 0.1 Ohm,0805 R46 0 LEDVSYNC *VDD_3V3A* 0.1uf,6.3V || C155 R52 10K,1% SYS_5V USB1_PWR USB1_DM 1500HM800mA USB1_DP 100uF,6.3V — 87520-0010BLF VDD_ADC GNDD POWERDOWN ISOLATION 0.1uf,6.3V GNDD GNDD GNDD GPIOO_7SRC <u>G</u>NDD USB HOST 0,1% GNDD 0.1uf,6.3V 0.001uf,50V инв. № Подп. ц USB_DC GNDA_ADC GNDA_ADC *VDD_3V3B* USB0_ID USB0_DP USB0_DM 4.75K,1% MFTY.100001.001 33 <u>G</u>NDD <u>G</u>NDD Лит. Масса Масштад BeagleBone Black Изм Лист № докум. Подп. Разраб. Шапран А.В. Пров. Рафиков А.Г. Т.контр. USB PC Процессор 2 из 3 CONNECTOR u USB 4. 75K, 1% GNDD Λυςποβ 1 МГТУ им. Баумана Н.контр Утв. GNDD Группа ИУ8-74

Формат А1