



EE 100100001 FLJW

V10	OSCO_IN	V10	OSCO_IN
V11	GND_OSC0	V11	VSS_OSC0
A6	OSCL_IN	A6	OSCL_IN
A5	GND_OSC1	A5	VSS_RTC
	M3		DDR_D0
	M4		DDR_D1
	N1		DDR_D2
	N2		DDR_D3
	N3		DDR_D4
	N4		DDR_D5
	P3		DDR_D6
	P4		DDR_D7
	J1		DDR_D8
	K1		DDR_D9
	K2		DDR_D10
	K3		DDR_D11
	K4		DDR_D12
	L3		DDR_D13
	L4		DDR_D14
	M1		DDR_D15
	P1		DDR_DQS0
	P2		DDR_DQSN0
	L1		DDR_DQS1
	L2		DDR_DQSN1
	J3		DDR_VTP
	J4		VREFSSTL
B15		B15	

U5A

SubArctic AM335x  
15mm x 15mm  
Package

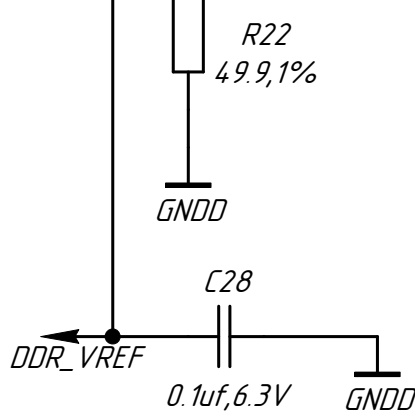
<i>OSC0_OUT</i>	<i>U11</i>	<i>OSC0_OUT</i>	<i>U11</i>
<i>OSC1_OUT</i>	<i>A4</i>	<i>OSC1_OUT</i>	<i>A4</i>
<i>DDR_A0</i>	<i>F3</i>		
<i>DDR_A1</i>	<i>H1</i>		
<i>DDR_A2</i>	<i>E4</i>		
<i>DDR_A3</i>	<i>C3</i>		
<i>DDR_A4</i>	<i>C2</i>		
<i>DDR_A5</i>	<i>B1</i>		
<i>DDR_A6</i>	<i>D5</i>		
<i>DDR_A7</i>	<i>E2</i>		
<i>DDR_A8</i>	<i>D4</i>		
<i>DDR_A9</i>	<i>C1</i>		
<i>DDR_A10</i>	<i>F4</i>		
<i>DDR_A11</i>	<i>F2</i>		
<i>DDR_A12</i>	<i>E3</i>		
<i>DDR_A13</i>	<i>H3</i>		
<i>DDR_A14</i>	<i>H4</i>		
<i>DDR_A15</i>	<i>D3</i>		
<i>DDR_BA0</i>	<i>E4</i>		
<i>DDR_BA1</i>	<i>E1</i>		
<i>DDR_BA2</i>	<i>B3</i>		
<i>DDR_CK</i>	<i>D2</i>		
<i>DDR_NCK</i>	<i>D1</i>		
<i>DDR_CKE</i>	<i>G3</i>		
<i>DDR_CSNO</i>	<i>H2</i>		
<i>DDR_CASN</i>	<i>F1</i>		
	<i>G4</i>		



A10		A10	PURZn
B5		B5	NRESET_INOUT
B18		B18	RTC_PORZn
A15	PROC_A15	A15	NNM
D14		D14	EVENT_INTR0/TIMER4/CLKOUT1/SPI1_CS1/PR1PRU1R31_16/EMU2/GPIO0_19
			EVENT_INTR1/TCLIN/CLKOUT2/TIMER7/PR1PRU0_PRUR31_16/EMU3/GPIO0_20

JTAG_TRSTn	B10	NTRST
JTAG_TMS	C11	TMS
JTAG_TDI	B11	TDI
JTAG_TCK	A12	TCK
JTAG_EMU0	C14	EMU0/GPIO3_7
JTAG_EMU1	B14	EMU1/GPIO3_8

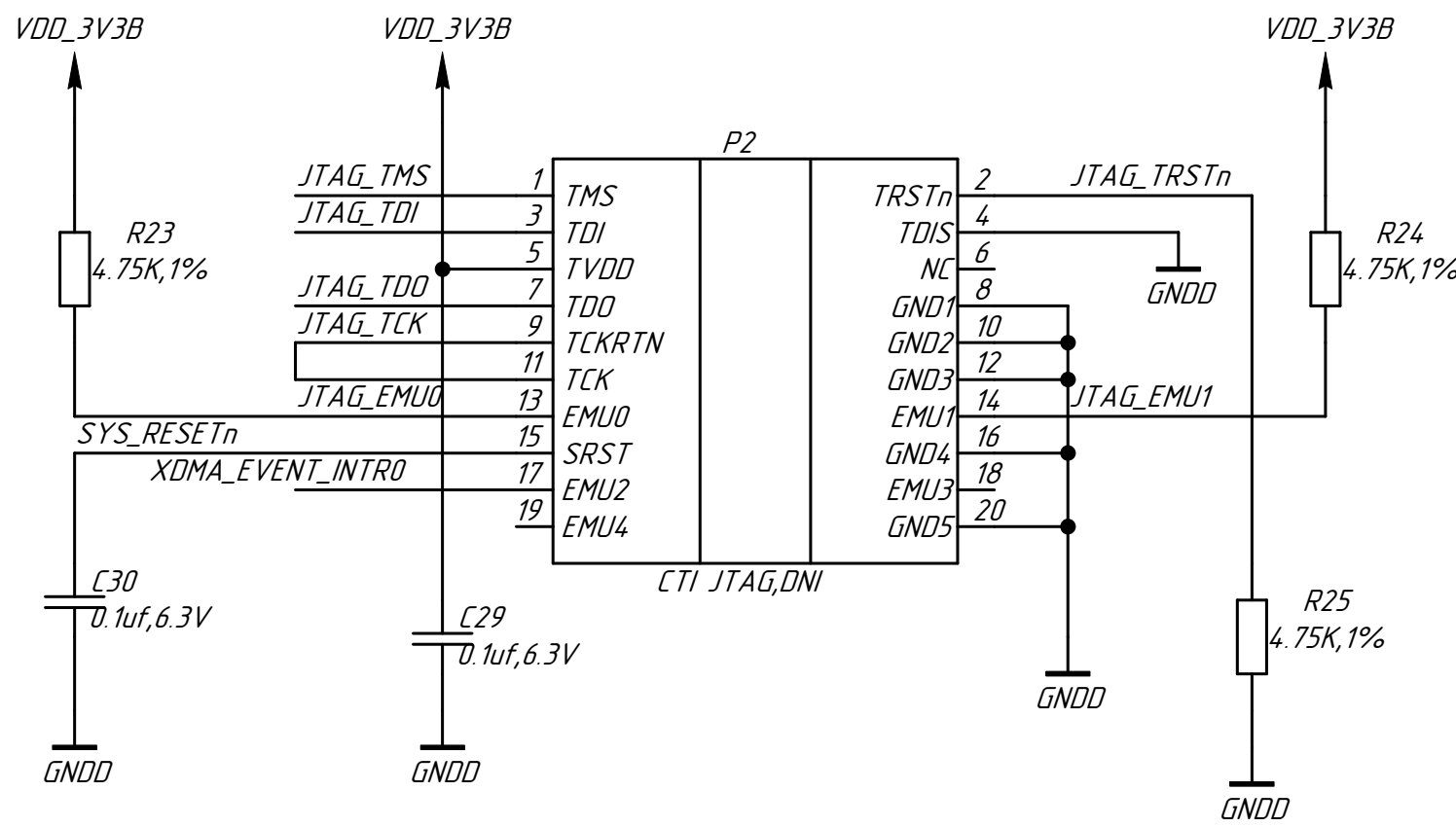
T17	GPMC_WAIT0/GM112_CRS/GPMC_CSN4/RM112_CRS_DV/MMC1_SD0D/PR1_M11_RXDV/UART4_RXD/GPIO0_30
G17	MMC0_CLK/GPMC_A24/UART3_CTSN/UART2_RXD/DCAN1_TX/PR1_PRU0_PRU_R30_12/PR1_PRU0_PRU_R31_12/GPIO2_30
G18	MMC0_CMD/GPMC_A25/UART3_RTSN/UART2_TXD/DCAN1_RX/PR1_PRU0_PRU_R30_13/PR1_PRU0_PRU_R31_13/GPIO2_31
G16	MMC0_DAT0/GPMC_A23/UART5_RTSN/UART3_TXD/UART1_RIN/PR1_PRU0_PRU_R30_11/PR1_PRU0_PRU_R31_11/GPIO2_29
G15	MMC0_DAT1/GPMC_A22/UART5_CTSN/UART3_RXD/UART1_OTRN/PR1_PRU0_PRU_R30_10/PR1_PRU0_PRU_R31_10/GPIO2_28
F18	MMC0_DAT2/GPMC_A21/UART4_RTSN/TIMER6/UART1_DSRN/PR1_PRU0_PRU_R30_9/PR1_PRU0_PRU_R31_9/GPIO2_27
F17	MMC0_DAT3/GPMC_A20/UART4_CTSN/TIMER5/UART1_DCDN/PR1_PRU0_PRU_R30_8/PR1_PRU0_PRU_R31_8/GPIO2_26
U7	GPMC_AD0/MMC1_DAT0////////GPIO1_0
R8	GPMC_AD1/MMC1_DAT1////////GPIO1_1
GPMC_AD2/MMC1_DAT2////////GPIO1_2	
T8	GPMC_AD3/MMC1_DAT3////////GPIO1_3
U8	GPMC_AD4/MMC1_DAT4////////GPIO1_4
V8	GPMC_AD5/MMC1_DAT5////////GPIO1_5
R9	GPMC_AD6/MMC1_DAT6////////GPIO1_6
T9	GPMC_AD7/MMC1_DAT7////////GPIO1_7
U10	GPMC_AD8/LCD_DATA23/MMC1_DAT0/MMC2_DAT4/EHRPWM2A/PR1_M11_MT0_CLK//GPIO0_22
T10	GPMC_AD9/LCD_DATA22/MMC1_DAT1/MMC2_DAT5/EHRPWM2B/PR1_M110_CRS//GPIO0_23
F11	GPMC_AD10/LCD_DATA21/MMC1_DAT2/MMC2_DAT6/EHRPWM2_TRIPZONE_INPUT/PR1_M110_TXEN//GPIO0_26
U12	GPMC_AD11/LCD_DATA20/MMC1_DAT3/MMC2_DAT7/EHRPWM2_SYNCLO/PR1_M110_TXD3//GPIO0_27
T12	GPMC_AD12/LCD_DATA19/MMC1_DAT4/MMC2_DAT0/EQEP2A_IN/PR1_M110_TXD2/PR1_PRU0_PRU_R30_14/GPIO1_12
R12	GPMC_AD13/LCD_DATA18/MMC1_DAT5/MMC2_DAT1/EQEP2B_IN/PR1_M110_TXD1/PR1_PRU0_PRU_R30_15/GPIO1_13
V13	GPMC_AD14/LCD_DATA17/MMC1_DAT6/MMC2_DAT2/EQEP2_INDEX/PR1_M110_TXD0/PR1_PRU0_PRU_R31_14/GPIO1_14
U13	GPMC_AD15/LCD_DATA16/MMC1_DAT7/MMC2_DAT3/EQEP2_STROBE/PR1_ECAP0_ECAP_CAPIN_APWM_0/PR1_PRU0_PRU_R31_15/GPIO1_15



AM335X

GPMC\_CLK/LCD\_MEM\_CLK/GPMC\_WAI  
GPMC\_CSN1/GPMC\_CLK/MMC1\_CLK/PRT1EDIO\_DATA\_IN6/PRT1EDIO\_DATA\_OUT6/P  
GPMC\_CSN2/GPMC\_BEIN/MMC1\_CMD/PR1EDIO\_DATA\_IN7/PR1EDIO\_DATA\_OUT7/P

GPMC\_BEIN/GM112\_COL/GPMC\_CSN6/MMC2\_DAT3/  
GPMC\_WPN/GM112\_RXERR/GPMC\_CSN5/RM112\_RXERR/  
GPMC\_A0/GM112\_TXEN/RGM112\_TCTL/RM112\_TXEN/GPMC\_A16/P  
GPMC\_A1/GM112\_RXDV/RGM112\_RCTL/MMC2\_DAT0/G  
GPMC\_A2/GM112\_TXD3/RGM112\_TD3/MMC2\_D  
GPMC\_A3/GM112\_TXD2/RGM112\_TD2/MMC2\_2  
GPMC\_A4/GM112\_TXD1/RGM112\_TD1/RM112\_1  
GPMC\_A5/GM112\_TXD0/RGM112\_TD0/RM112  
GPMC\_A6/GM112\_TXCLK/RGM112\_TCLK/MMC2\_DAT5  
GPMC\_A7/GM112\_RXCLK/RGM112\_RCLK/MMC2\_DAT5  
GPMC\_A8/GM112\_RXD3/RGM112\_RD3/MMC2\_DAT6/  
GPMC\_A9/GM112\_RXD2/RGM112\_RD2/MMC2\_DAT7/  
GPMC\_A10/GM112\_RXD1/RGM112\_RD1/RM112\_RXD  
GPMC\_A11/GM112\_RXD0/RGM112\_RD0/RM112\_RXD



Изм.	Лист	№ док.
Разраб.	Шапран	
Пров.	Рафико	
Т.контр.		
Н.контр.		
Умб.		

