

PB0	PB0	PA0
<b>XCK/TO</b>	ADC0	ADC0
PB1	PA1	PA1
<b>T1</b>	ADC1	ADC1
PB2	PA2	PA2
<b>INT2/AIN0</b>	ADC2	ADC2
PB3	PA3	PA3
<b>OC0/AIN1</b>	ADC3	ADC3
PB4	PA4	PA4
<b>SS</b>	ADC4	ADC4
PB5	PA5	PA5
<b>MOSI</b>	ADC5	ADC5
PB6	PA6	PA6
<b>MISO</b>	ADC6	ADC6
PB7	PA7	PA7
<b>SCK</b>	ADC7	ADC7
<b>ATmega DIP40</b>		
<b>RST</b>	<b>AREF</b>	<b>AREF</b>
<b>VCC</b>	<b>GND</b>	<b>GND</b>
<b>GND</b>	<b>AVCC</b>	<b>AVCC</b>
<b>XTAL2</b>	PC7	PC7
<b>XTAL1</b>	TOSC2	TOSC2
	PC6	PC6
	TOSC1	TOSC1
PB0	PC5	PC5
<b>RXD</b>	TDI	TDI
PB1	PC4	PC4
<b>TXD</b>	TDO	TDO
PB2	PC3	PC3
<b>INT0</b>	TMS	TMS
PB3	PC2	PC2
<b>INT1</b>	TCK	TCK
PB4	PC1	PC1
<b>OC1B</b>	SDA	SDA
PB5	PC0	PC0
<b>OC1A</b>	SCL	SCL
PB6	PD7	PD7
<b>ICP1</b>	OC2	OC2

PB0	PA0
<b>XCK/TO</b>	ADC0
PB1	PA1
<b>T1</b>	ADC1
PB2	PA2
<b>INT2/AIN0</b>	ADC2
PB3	PA3
<b>OC0/AIN1</b>	ADC3
PB4	PA4
<b>SS</b>	ADC4
PB5	PA5
<b>MOSI</b>	ADC5
PB6	PA6
<b>MISO</b>	ADC6
PB7	PA7
<b>SCK</b>	ADC7
<b>ATmega DIP40</b>	
<b>RST</b>	<b>AREF</b>
<b>VCC</b>	<b>GND</b>
<b>GND</b>	<b>AVCC</b>
<b>XTAL2</b>	PC7
<b>XTAL1</b>	TOSC2
	PC6
	TOSC1
PB0	PC5
<b>RXD</b>	TDI
PB1	PC4
<b>TXD</b>	TDO
PB2	PC3
<b>INT0</b>	TMS
PB3	PC2
<b>INT1</b>	TCK
PB4	PC1
<b>OC1B</b>	SDA
PB5	PC0
<b>OC1A</b>	SCL
PB6	PD7
<b>ICP1</b>	OC2

PB0	PA0	PA0
<b>XCK/TO</b>	ADC0	<b>XCK/TO</b>
PB1	PA1	PA1
<b>T1</b>	ADC1	ADC1
PB2	PA2	PA2
<b>INT2/AIN0</b>	ADC2	<b>INT2/AIN0</b>
PB3	PA3	PA3
<b>OC0/AIN1</b>	ADC3	<b>OC0/AIN1</b>
PB4	PA4	PA4
<b>SS</b>	ADC4	<b>SS</b>
PB5	PA5	PA5
<b>MOSI</b>	ADC5	ADC5
PB6	PA6	PA6
<b>MISO</b>	ADC6	ADC6
PB7	PA7	PA7
<b>SCK</b>	ADC7	ADC7
<b>ATmega DIP40</b>		
<b>RST</b>	<b>AREF</b>	<b>RST</b>
<b>VCC</b>	<b>GND</b>	<b>VCC</b>
<b>GND</b>	<b>AVCC</b>	<b>GND</b>
<b>XTAL2</b>	PC7	XTAL2
<b>XTAL1</b>	TOSC2	XTAL1
	PC6	
	TOSC1	
PB0	PC5	PD0
<b>RXD</b>	TDI	<b>RXD</b>
PB1	PC4	PD1
<b>TXD</b>	TDO	<b>TXD</b>
PB2	PC3	PD2
<b>INT0</b>	TMS	<b>INT0</b>
PB3	PC2	PD3
<b>INT1</b>	TCK	<b>INT1</b>
PB4	PC1	PD4
<b>OC1B</b>	SDA	<b>OC1B</b>
PB5	PC0	PD5
<b>OC1A</b>	SCL	<b>OC1A</b>
PB6	PD7	PD6
<b>ICP1</b>	OC2	<b>ICP1</b>

<b>rst</b>	<b>ai5</b>
<b>rx</b>	<b>ai4</b>
<b>tx</b>	<b>ai3</b>
<b>d2</b>	<b>ai2</b>
<b>d3</b>	<b>ai1</b>
<b>d4</b>	<b>ai0</b>
<b>VCC</b>	<b>gnd</b>
<b>gnd</b>	<b>aref</b>
<b>xt1</b>	<b>avcc</b>
<b>xt2</b>	<b>ai3</b>
<b>d5</b>	<b>ai2</b>
<b>d6</b>	<b>ai1</b>
<b>d7</b>	<b>ai0</b>
<b>d8</b>	<b>d9</b>

<b>rst</b>	<b>ai5</b>
<b>rx</b>	<b>ai4</b>
<b>tx</b>	<b>ai3</b>
<b>d2</b>	<b>ai2</b>
<b>d3</b>	<b>ai1</b>
<b>d4</b>	<b>ai0</b>
<b>VCC</b>	<b>gnd</b>
<b>gnd</b>	<b>aref</b>
<b>xt1</b>	<b>avcc</b>
<b>xt2</b>	<b>ai3</b>
<b>d5</b>	<b>ai2</b>
<b>d6</b>	<b>ai1</b>
<b>d7</b>	<b>ai0</b>
<b>d8</b>	<b>d9</b>

<b>rst</b>	<b>ai5</b>
<b>rx</b>	<b>ai4</b>
<b>tx</b>	<b>ai3</b>
<b>d2</b>	<b>ai2</b>
<b>d3</b>	<b>ai1</b>
<b>d4</b>	<b>ai0</b>
<b>VCC</b>	<b>gnd</b>
<b>gnd</b>	<b>aref</b>
<b>xt1</b>	<b>avcc</b>
<b>xt2</b>	<b>ai3</b>
<b>d5</b>	<b>ai2</b>
<b>d6</b>	<b>ai1</b>
<b>d7</b>	<b>ai0</b>
<b>d8</b>	<b>d9</b>

<b>RST</b>	<b>VCC</b>
<b>XTAL1</b>	<b>SCK</b>
<b>XTAL2</b>	<b>MISO</b>
<b>GND</b>	<b>MOSI</b>

<b>RST</b>	<b>VCC</b>
<b>XTAL1</b>	<b>SCK</b>
<b>XTAL2</b>	<b>MISO</b>
<b>GND</b>	<b>MOSI</b>

<b>RST</b>	<b>VCC</b>
<b>XTAL1</b>	<b>SCK</b>
<b>XTAL2</b>	<b>MISO</b>
<b>GND</b>	<b>MOSI</b>