Questions

1. What is the address of the SysTick Handler() function? Verify it (i.e. take a screenshot) in the debug environment.

- The address is at 0x08003A4

```
   42:              ++msTicks;
0x080003A4 4802       LDR              r0,[pc,#8]  ; @0x080003B0
```

2. Set up a breakpoint within the SysTick Handler() function. In the debug environment, find out the exception number in the program status register when the program runs to the breakpoint. Explain what this number means.

- The exception number is 15. This represents the systick interrupt number found in question 3.

3. Cortex-M series supports up to 256 interrupts. What is the interrupt number of SysTick that is defined in CMSIS?

- It's the 15th interrupt, labeled as -1

4. Does a higher priority value represent a higher urgency?

- A lower value reporesents higher urgency

5. Suppose a clock of 16 MHz is used to drive the system timer. What is the maximum period between two consecutive SysTick interrupts that we can possibly obtain?

- 8.4 seconds