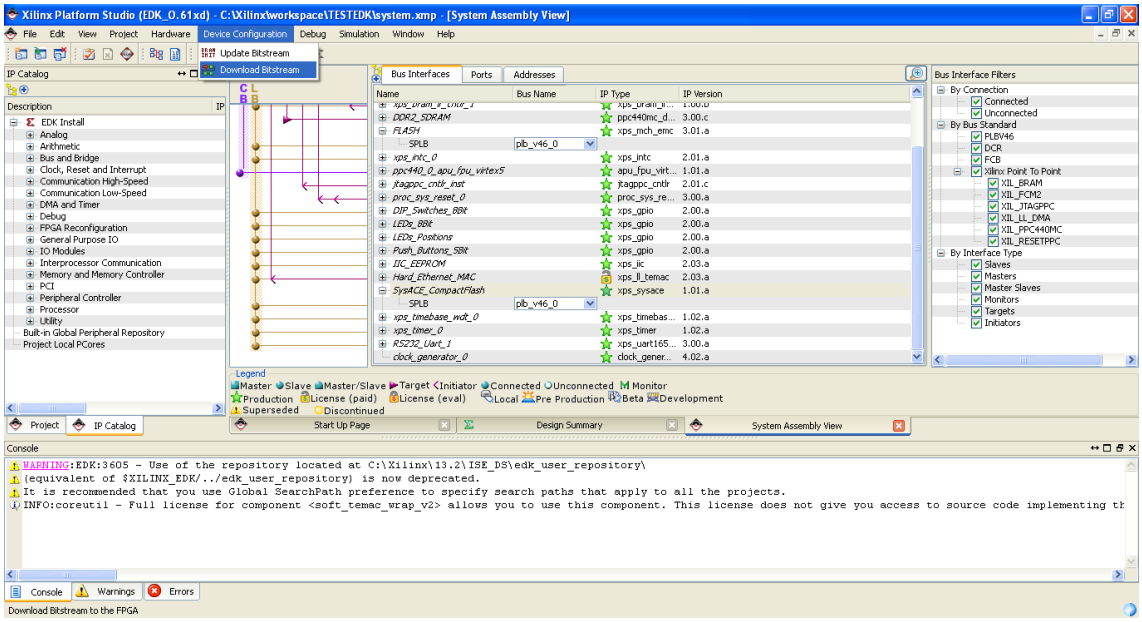
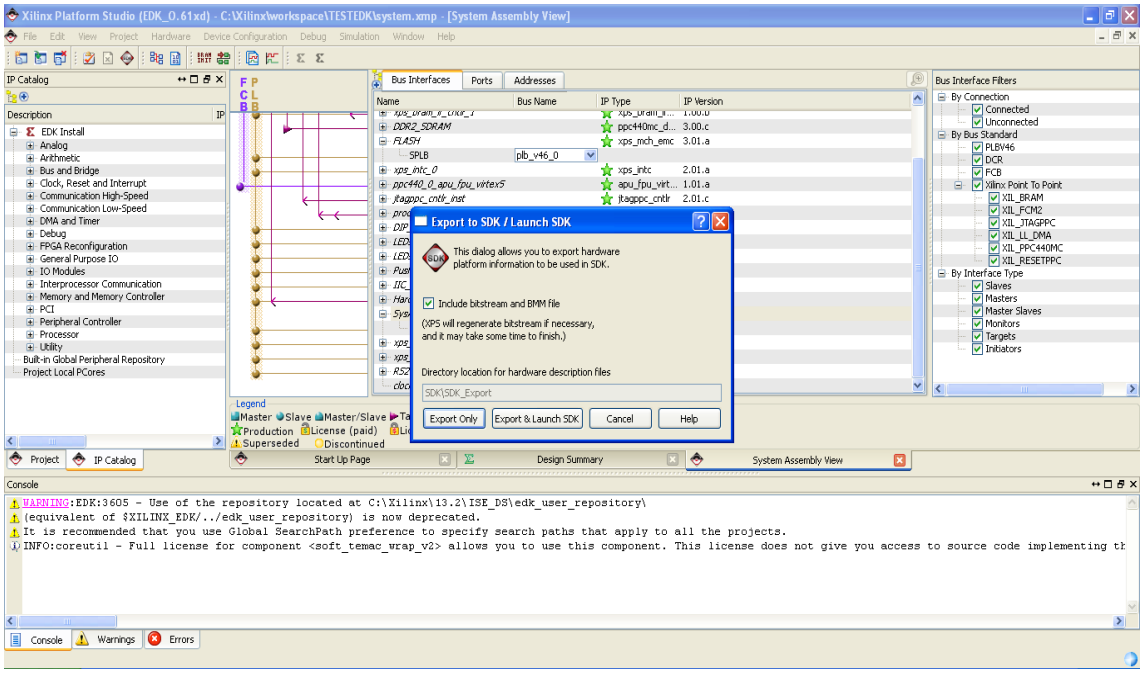
Firstly, use XPS to load into the board, the binary image of the kernel *filename.initrd.xilinx*. Open XPS, then **Device Configuration -> Download Bitstream**. Please, note you need to create a HW project before and include all peripherals you want to have.



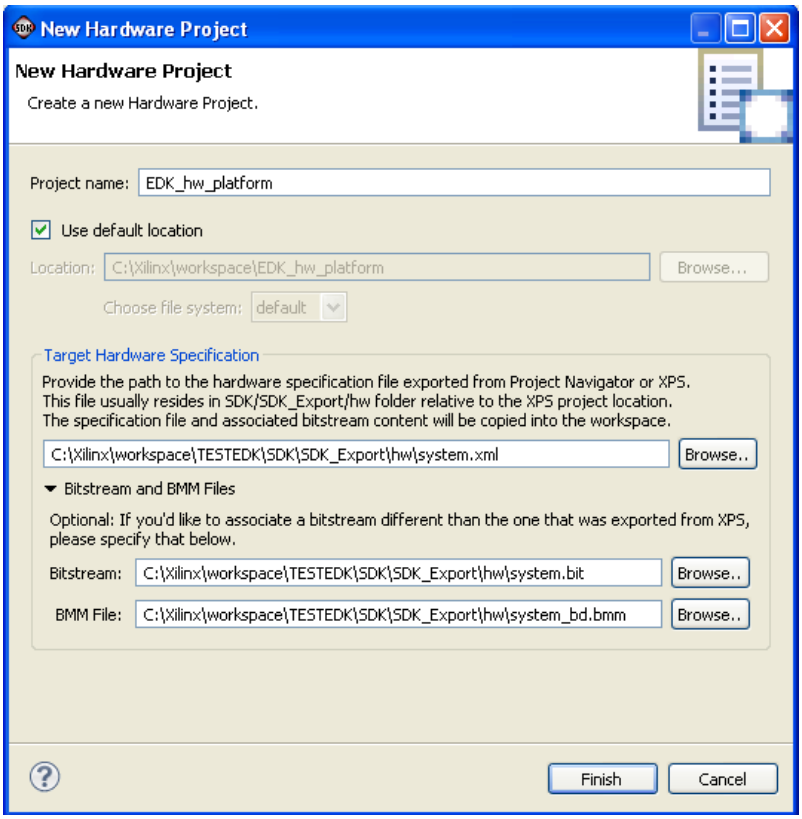
Bitstream is now created and downloaded into the FPGA through the JTAG cable. Then, open SDK and click on **Xilinx Tools -> Program Flash Memory**

Select the *initrd.xilinx* file and the RAM controller and click on **Program**.

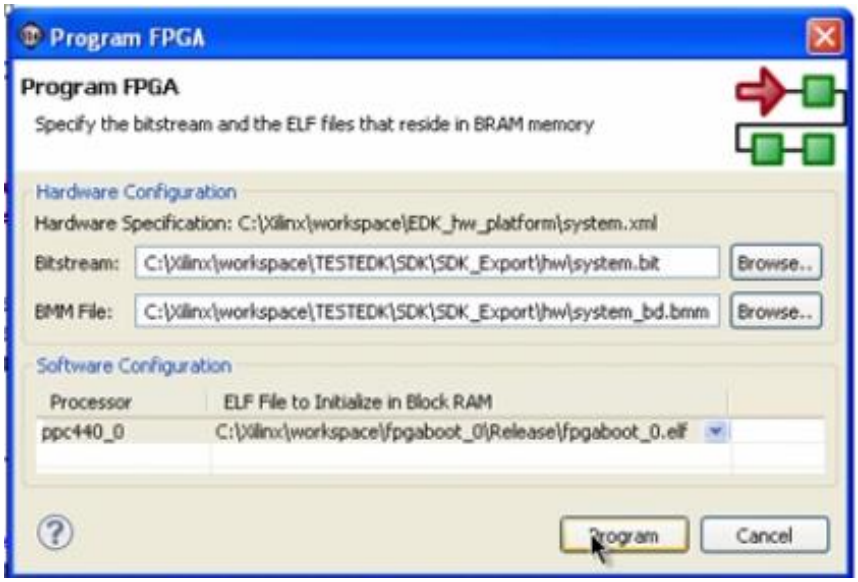
Now, in XPS click on ***Project -> Export to SDK*.**



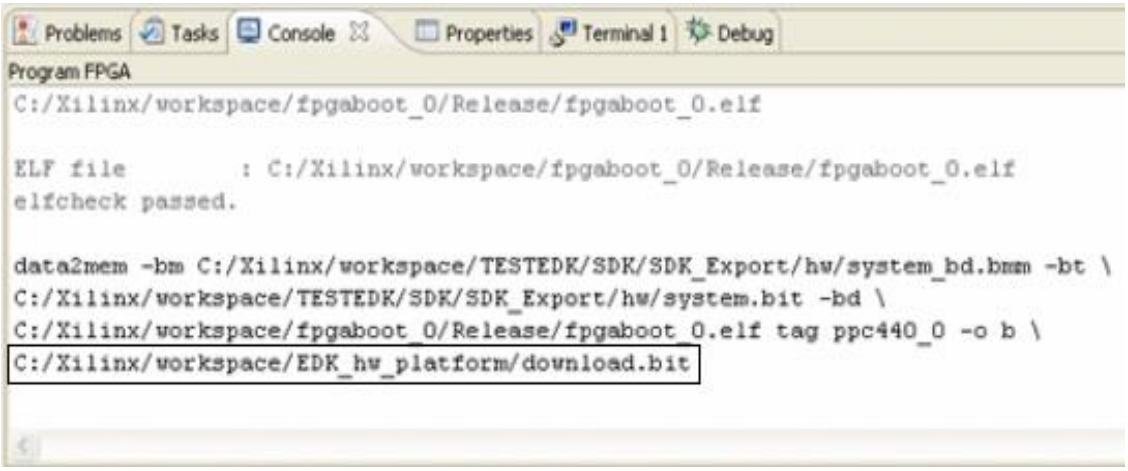
In SDK, create a new HW Platform Project and select the *system.xml* file created after the export.



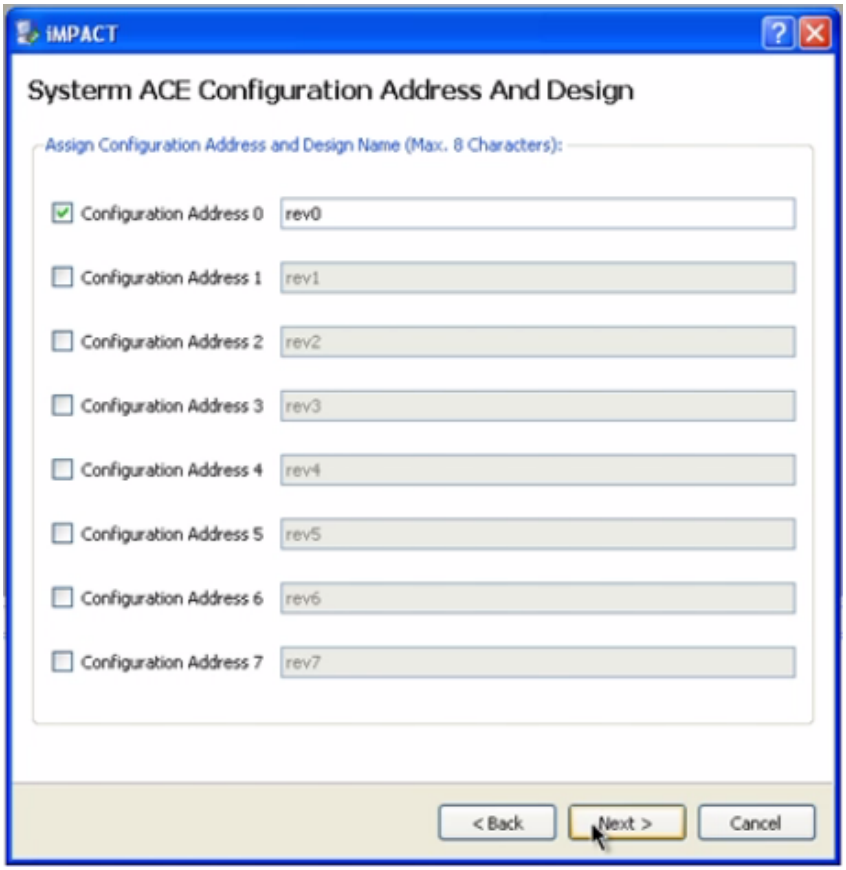
Now, click on **Xilinx Tools -> Program FPGA** to create the *.bit* file which joins HW bitstream and *elf* file of the bootloader.



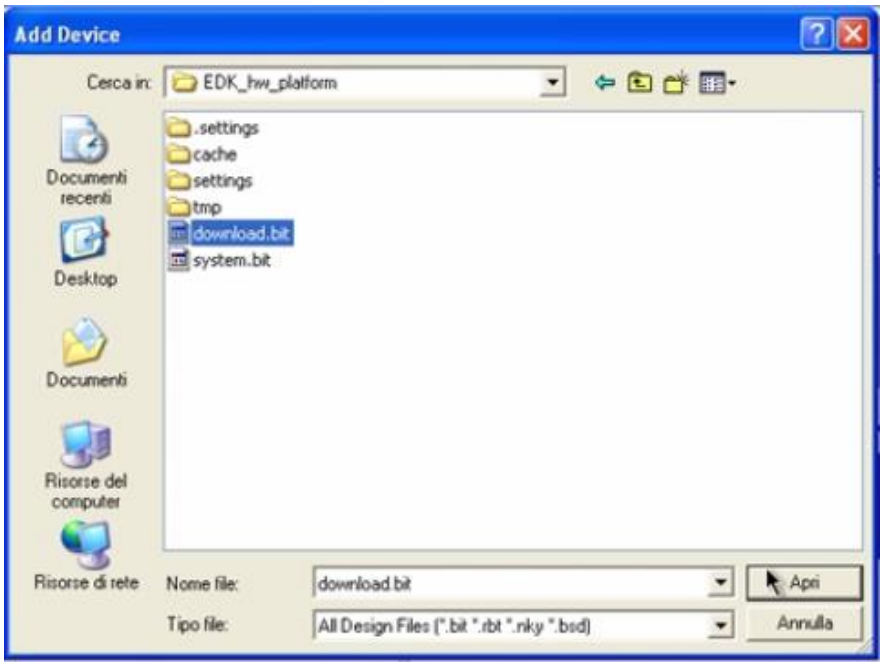
Locate the file.



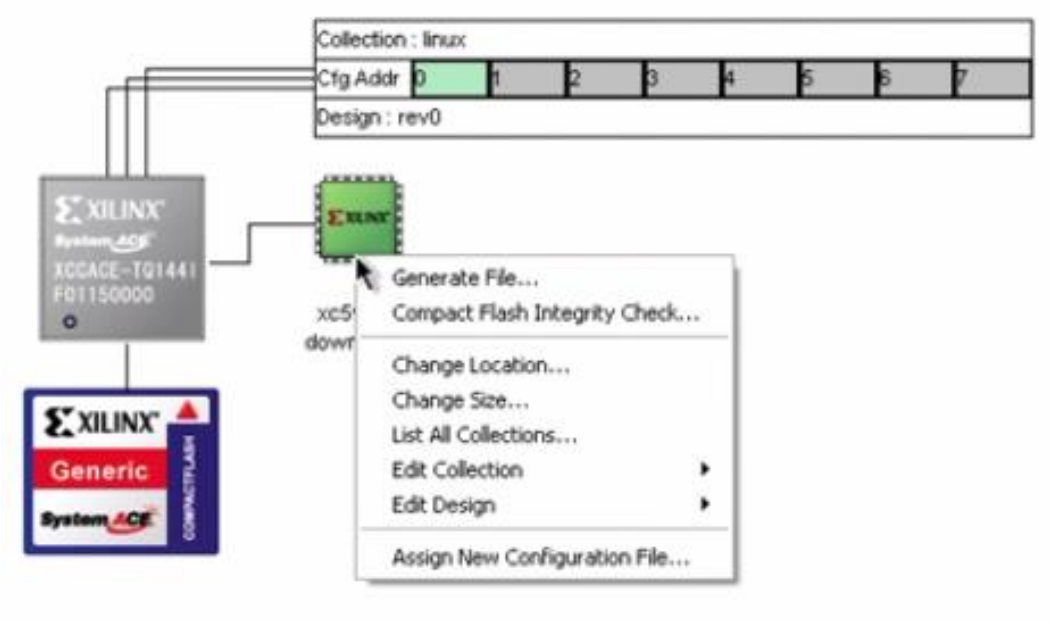
Open ISE Impact, select **iMPACT Flows -> System ACE**. Then click on **Novice**. Choose a project name (rev0 in the picture) and follow the wizard.



Select the *download.bit* file from the new window.



Click on the *xc5vfx70t* device, right-click and select **Generate File**.



Locate the new file in your workspace (../rev0) and copy it into the compact flash.