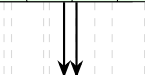
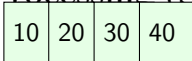
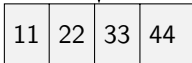
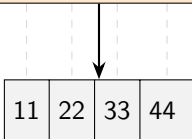


Register XY (256-bit)
Vector Processing (SIMD)



SINGLE SIMD ADD INSTRUCTION



Result Register

Total: 1 Instruction, Simultaneous Execution