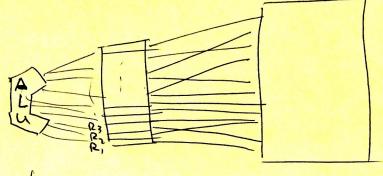


## Procestor organization



arithmeche logse unit:

fort acres register slow accer RAM

notibbe multipleaction bil shifting marking

- get data from RAM (heap, - do arithmetray with reprising starch -- put results in RAM
from registers

assembly: -move legher to foron RAM - avidentes

my comment sel code oblinous algo for assymptoher