5.3.33 dwm_int_cfg_set

5.3.33.1 Description

Enables/disables setting of the dedicated GPIO pin in case of an event. Interrupts/events are communicated to the user by setting of GPIO pin CORE_INT1. User can use the pin as source of an external interrupt. The interrupt can be processed by reading the status (dwm_status_get) and react according to the new status. The status is cleared when read. This call is available only on UART/SPI interfaces. This call do a write to internal flash in case of new value being set, hence should not be used frequently and can take in worst case hundreds of milliseconds.

Parameter			Description
Field	**Name**	**Size**	
Input	int_cfg	16-bit integer	Interrupt config flags, see Section 4.4.9
Output	err_code	8-bit integer	0, 1 or 2, see Section 4.4.1

5.3.33.2 C code

This command is not available for on-board user application. It is used only available on external interfaces (UART/SPI).

5.3.33.3 SPI/UART Generic

Declaration:

TLV request		
Type	**Length**	**Value**
0x34	0x02	**int_cfg**

Example:

TLV request		
Type	**Length**	**Value**
		int_cfg spi_data_ready (bit 1) loc_ready (bit 0)
0x34	0x02	0x03 0x00

TLV response		
Type	**Length**	**Value**
		err_code
0x40	0x01	0x00

5.3.34 dwm_int_cfg_get

5.3.34.1 Description

This API function reads the configuration flags that, if set, enables the setting of dedicated GPIO pin (CORE_INT) in case of an event internal to DWM module. This call is available only on UART/SPI interfaces.

Parameter			Description
Field	**Name**	**Size**	
Input	none		
Output	int_cfg	16-bit integer	Interrupt config flags, see Section 4.4.9

5.3.34.2 C code

This command is not available for on-board user application. It is used only available on external interfaces (UART/SPI).

5.3.34.3 SPI/UART Generic

Declaration:

TLV request	
Type	**Length**
0x35	0x00

Example:

TLV request	
Type	**Length**
0x35	0x00

TLV response					
Type	**Length**	**Value**	**Type**	**Length**	**Value**
		err_code			**int_cfg**
0x40	0x01	0x00	0x47	0x02	0x03 0x00