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Appendix B B1

Cadence Design Systems

GPDK 180 nm Mixed Signal Process Spec

REV 3.3

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Generator Information

Sample runset for 180 nm technology

Default Grid: 0.005 Valid Angle: 45 Flag Acute: true

Flag Self-intersecting: true

Revision History

DRC Revisi	ion History		
VERSION	v3.3	 	

Changes:

- 1. gpdk180 OA22 library built natively with IC6.1.3.1 release code
- 2. gpdk090 CDB library built natively with IC5.10.41_USR5.90.69 release code
- 3. Added missing 64bit context files (CCR 568109)
- 4. Renamed LEFDefaultRouteSpec to LEFDefaultRouteSpec_gpdk180 (CCR 594263)
- 5. Techfile updates made in preparation for IC6.1.3 release
- 6. Removed CDF extraneous simulation MOS parameters (CCR 595042)
- 7. Created new QRC database with 3d field solver information
- 8. Updated stream maps to include fill layer purpose
- 9. Updated DRC and Extract decks to merge drawing and fill layer purposes

------VERSION v3.2

Changes:

- 1. gpdk180 OA22 library built natively with IC6.1.1 preFCS code
- 2. Added parasitc AD/AS/PD/PS calculations to the Assura extract rules
- 3. Added ignore of "simM" to the CPH lam file to solve back annotation problem
- 4. Updated Circuit prospector liblnit settings to match new format
- 5. Corrected names in the resistor model file for ADS
- 6. Modified libInit to prevent overwrite of simulation model setup information
- 7. Modified Assura RF LVS files for VPCM inductor extraction
- 8. Modified Assura RF LVS files to save additional recognition layers
- 9. Modified Assura RF LVS files to add dummy device filtering
- 10. Modified Assura RF LVS files to avoid double counting of contact resistances
- 11. Added switch to Assura RF LVS files control amount of saved extracted layers
- 12. Modified SCparameters.cds file for RF kit
- 13. Updates made to the display.drf file for layout productivity
- 14. Updates made to the techfile layer display sections for layout productivity

VERSION v3.1		

Changes:

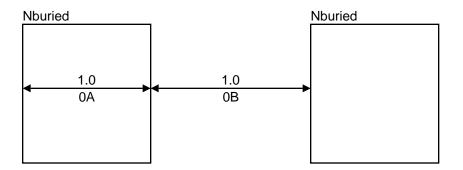
- 1. Gpdk180 OA22 library built natively with IC6.1.0 FCS code
- 2. Removed DBU settings in the library cdsenv file
- 3. Updated the CPH Lam file with VXL ignore check parameters
- 4. Added a PDK version print statement to the library libinit file
- 5. Added Circuit Prospector default settings in PDK init files

...

Sep 24, 2008	page 3
Danima Dula Crass	ifi and an a
Design Rule Spec	ifications

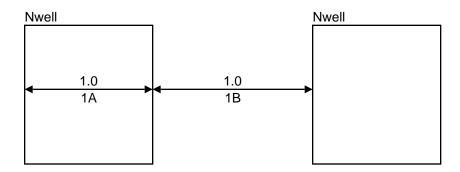
Nburied rules

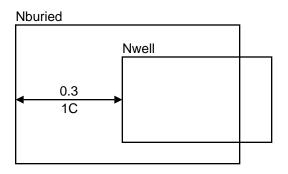
Rule No.	Description	Rule
		(um)
0A	Minimum width of an Nburied.	1.0
0B	Minimum space between two Nburied.	1.0



Nwell rules

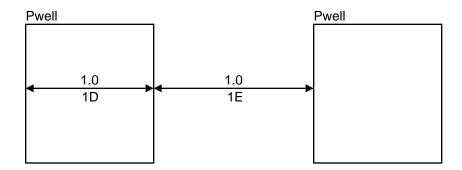
Rule No.	Description	Rule
		(um)
1A	Minimum width of an Nwell.	1.0
1B	Minimum space between two Nwell.	1.0
1C	Minimum Nburied overlap of Nwell	0.3

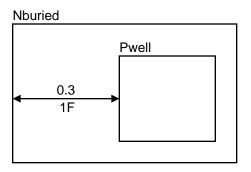




Pwell rules

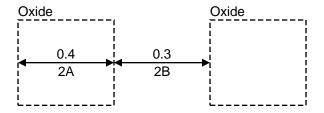
Rule No.	Description	
1D	1D Minimum width of an Pwell.	
1E Minimum space between two Pwell.		1.0
1F	Minimum Nburied overlap of Pwell	0.3

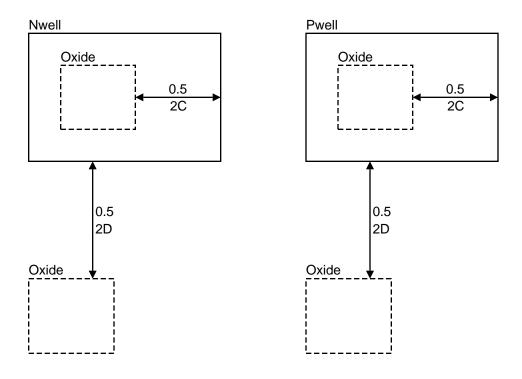




Oxide Rules

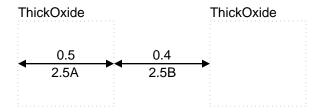
Rule No.	Description	Rule
		(um)
2A	Minimum width of an Oxide.	0.4
2B	Minimum space between two Oxides.	0.3
2C	Minimum Nwell overlap of Oxide.	0.5
2C	Minimum Pwell overlap of Oxide.	0.5
2D	Minimum spacing between Nwell and Oxide.	0.5
2D	Minimum spacing between Pwell and Oxide.	0.5

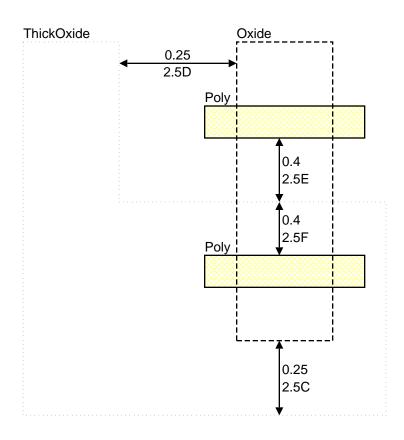




Thick Oxide Rules

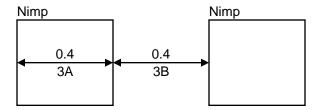
Rule No.	Description	Rule (um)
2.5A	Minimum width of an Oxide.	0.5
2.5B	Minimum space between two Oxides.	0.4
2.5C	Thick Oxide to Oxide enclosure.	0.25
2.5D	ThickOxide to Oxide spacing.	0.25
2.5E	ThickOxide to Poly spacing.	0.4
2.5F	ThickOxide to Poly enclosure.	0.4

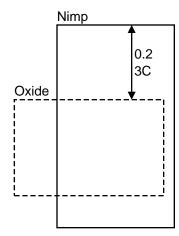


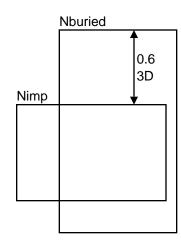


N+ Implant rules

Rule No.	Description	Rule
		(um)
ЗА	Minimum width of an Nimp.	0.4
3B	3B Minimum space between two Nimp.	
3C	3C Minimum Nimp overlap of Oxide.	
3D	Minimum Nburied overlap of Nimp.	0.6

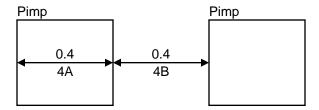


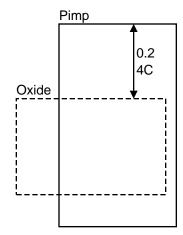


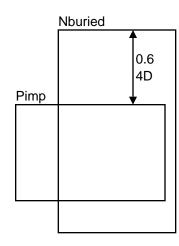


P+ Implant rules

Rule No.	Description	
4A	Minimum width of an Pimp.	0.4
4B	4B Minimum space between two Pimp.	
4C	4C Minimum Pimp overlap of Oxide.	
4D Minimum Nburied overlap of Pimp.		0.6

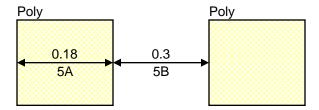


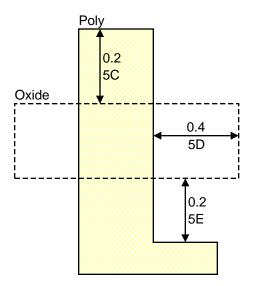




POLY Rules

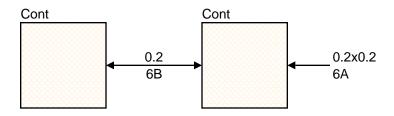
Rule No.	Description	
5A	Minimum width of an Poly.	0.18
5B	Minimum space between two Poly.	0.3
5C	Minimum Poly extension over Oxide.	0.2
5D	Minimum Oxide extension over Poly.	0.4
5E	Minimum Poly space to Oxide.	0.2

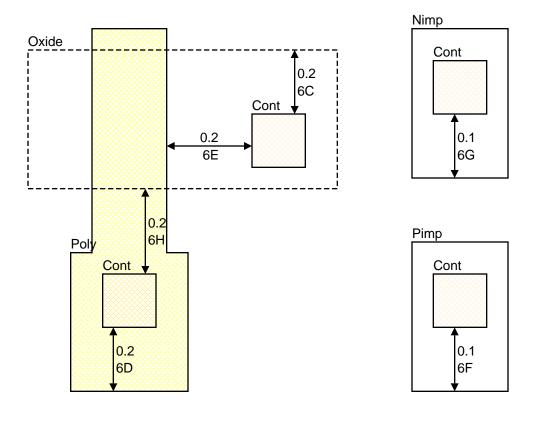




Contact rules

Rule No.	Description	Rule (um)
6A	Absolute width and length of Contact.	0.2x0.2
6B	Minimum space between two Contact.	0.2
6C	Minimum Oxide overlap of Contact.	0.2
6D	Minimum Poly overlap of Contact.	0.2
6E	Minimum Poly space to Contact.	0.2
6F	Minimum Pimp overlap of Contact.	0.1
6G	Minimum Nimp overlap of Contact.	0.1
6H	Mimimum Contact space to Oxide.	0.2





Metal rules

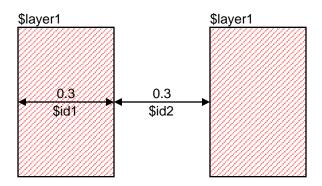
(Metalx, x = 1, 2, 3, 4, 5, 6) (Viax, x = 1, 2, 3, 4, 5)

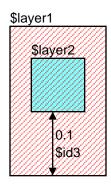
Rule No.	Description	Rule
		(um)
7A, 9A, 11A, 15A, 17A, 19A	Minimum width of an Metalx.	0.3
7B, 9B, 11B, 15B, 17B, 19B	Minimum space between two Metalx.	0.3
7C	Minimum Metal1 overlap of Contact.	0.1
9C, 11C, 15C, 17C, 19C	Minimum Metalx overlap of Viax.	0.1

macro

Macro Table

\$layer1	\$layer2	\$id1	\$id2	\$id3
Metal1	Cont	7A	7B	7C
Metal2	Via1	9A	9B	9C
Metal3	Via2NoCapInd	11A	11B	11C
Metal4	Via3	15A	15B	15C
Metal5	Via4	17A	17B	17C
Metal6	Via5	19A	19B	19C





VIA rules

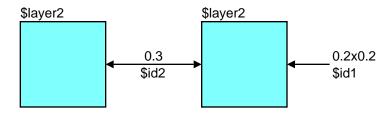
(Metalx, x = 1, 2, 3, 4, 5, 6) (Viax, x = 1, 2, 3, 4, 5)

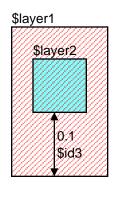
Rule No.	Description	Rule
		(um)
8,10,14,16,18A	Absolute width and Length of Viax.	0.2x0.2
8,10,14,16,18B	Minimum space between two Viax.	0.3
8,10,14,16,18C	Minimum Metalx overlap of Viax.	0.1

macro

Macro Table

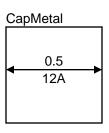
\$layer1	\$layer2	\$id1	\$id2	\$id3
Metal1	Via1	8A	8B	8C
Metal2	Via2	10A	10B	10C
Metal3	Via3	14A	14B	14C
Metal4	Via4	16A	16B	16C
Metal5	Via5	18A	18B	18C

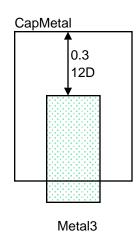


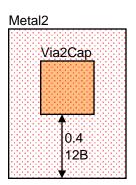


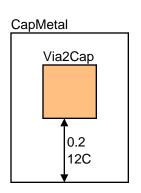
Capacitor Metal

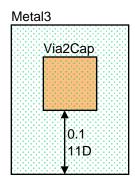
Rule No.	Description	Rule		
12A	Minimum width of CapMetal.	0.5		
12B	Minimum Metal2 overlap of Via2 on CapMetal.			
12C	Minimum CapMetal overlap of Via2.			
11D	Minimum Metal3 overlap of Via2 on CapMetal.	0.1		
12D	Minimum CapMetal overlap of Metal3.	0.3		





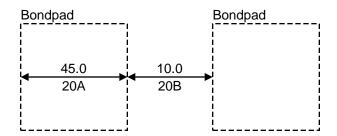






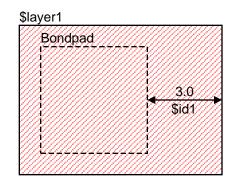
Passivation rules

Rule No.	Description	Rule (um)
20A	Minimum width of Bondpad.	45.0
20B	Minimum space of two Bondpad.	10.0
20C	Metal1 overlap of Bondpad.	3.0
20D	Metal2 overlap of Bondpad.	3.0
20E	Metal3 overlap of Bondpad.	3.0
20F	Metal4 overlap of Bondpad.	3.0
20G	Metal5 overlap of Bondpad.	3.0
20H	Metal6 overlap of Bondpad.	3.0





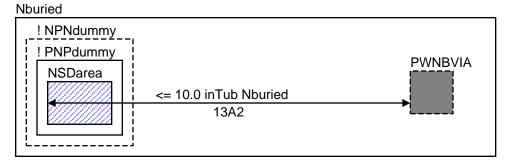


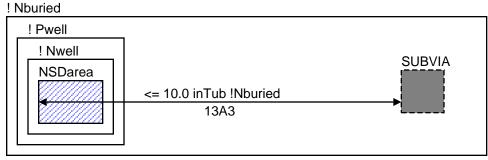


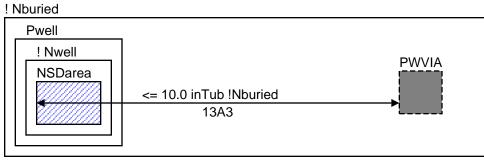
Latch-up rules

Rule No.	Description	Rule
		(um)
13A1	The maximum distance from any point inside	10.0
	a source/drain OD area to the nearest Nwell pick-up in the same NW.	
13A2	The maximum distance from any point inside	10.0
	a source/drain OD area to the nearest Nburied pick-up in the same Nburied.	
13A3	The maximum distance from any point inside	10.0
	a source/drain OD area to the nearest Substrate pick-up.	

PSDarea <= 10.0 inTub Nwell 13A1



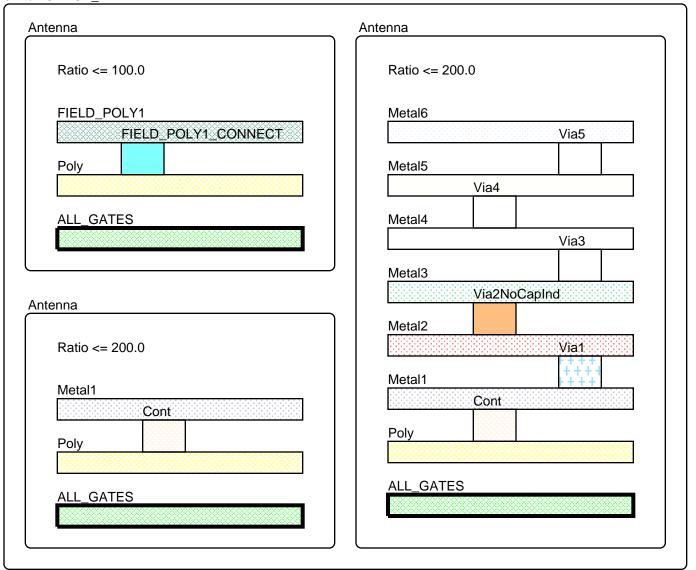




Antenna Rules

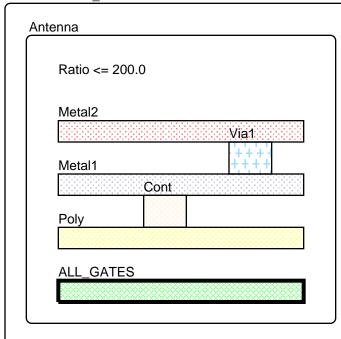
Rule No.	Description	Ratio ()
Antenna	Maximum Field Poly area to the active Poly gate area connected.	100.0
Antenna	Maximum Metal1 area to the active Poly gate area connected.	200.0
Antenna	Maximum Metal2 area to the active Poly gate area connected.	200.0
Antenna	Maximum Metal3 area to the active Poly gate area connected.	200.0
Antenna	Maximum Metal4 area to the active Poly gate area connected.	200.0
Antenna	Maximum Metal5 area to the active Poly gate area connected.	200.0
Antenna	Maximum Metal6 area to the active Poly gate area connected.	200.0

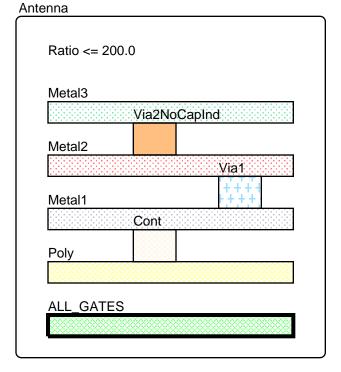
switch CHECK_ANTENNA

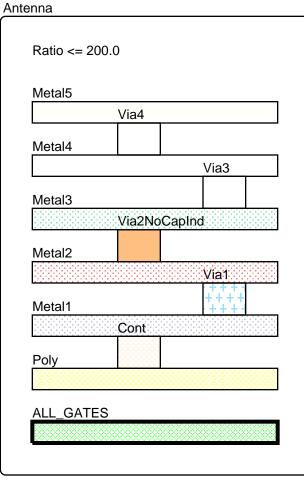


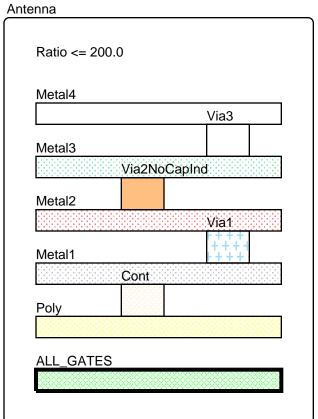
Antenna Rules (cont.)

switch CHECK_ANTENNA





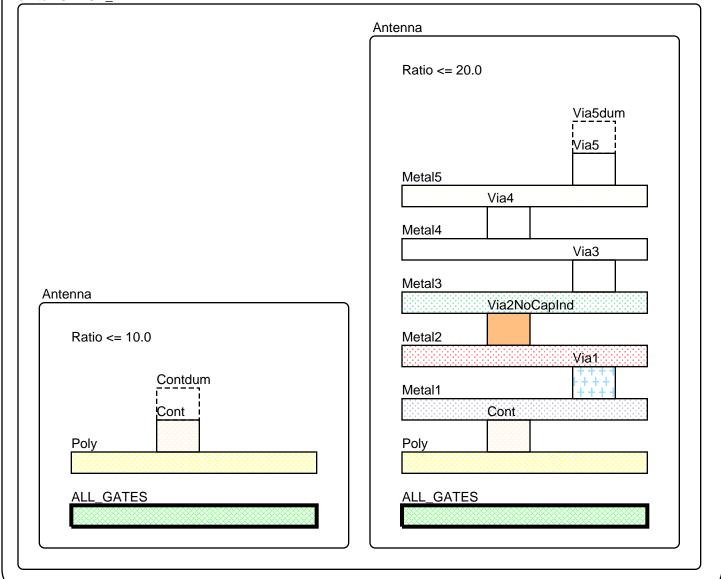




Antenna Rules (cont.)

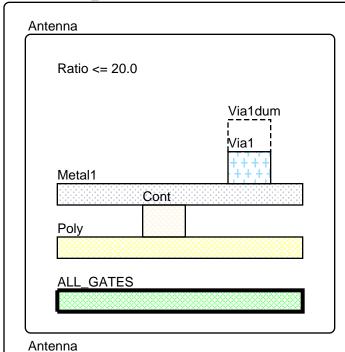
Rule No.	Description	Ratio ()
Antenna	Maximum Contact area to the active Poly gate area connected.	10.0
Antenna	Maximum Via1 area to the active Poly gate area connected.	20.0
Antenna	Maximum Via2 area to the active Poly gate area connected.	20.0
Antenna	Maximum Via3 area to the active Poly gate area connected.	20.0
Antenna	Maximum Via4 area to the active Poly gate area connected.	20.0
Antenna	Maximum Via5 area to the active Poly gate area connected.	20.0

switch CHECK_ANTENNA



Antenna Rules (cont.)

switch CHECK_ANTENNA



Ratio <= 20.0

Via4dum

Via4

Metal4

Via3

Metal3

Via2NoCapInd

Metal2

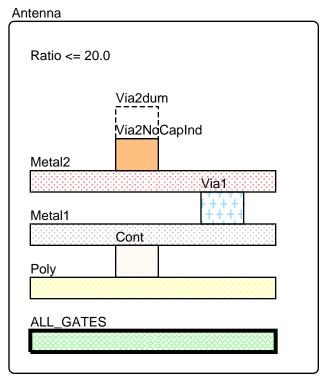
Via1

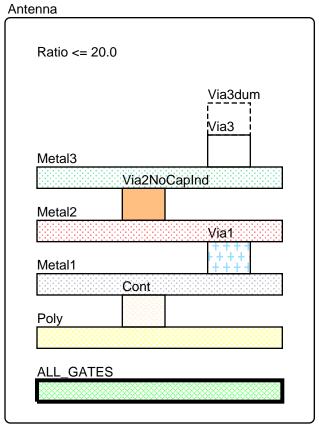
Metal1

Cont

Poly

ALL_GATES





Density rules

Rule No.	Description	Ratio ()
Density	Minimum Poly density across the full chip.	0.15
Density	Minimum Metal1 density across the full chip.	0.25
Density	Minimum Metal2 density across the full chip.	0.25
Density	Minimum Metal3 density across the full chip.	0.25
Density	Minimum Metal4 density across the full chip.	0.25
Density	Minimum Metal5 density across the full chip.	0.25
Density	Minimum Metal6 density across the full chip.	0.25

switch CHECK DENSITY

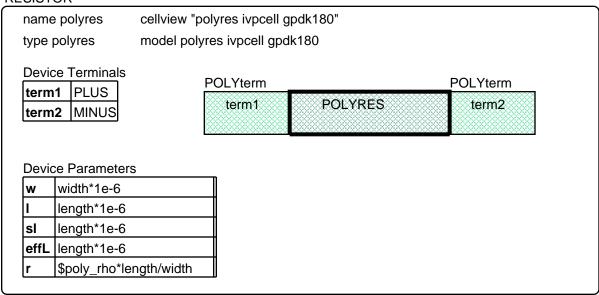
Density	Density	Density
Ratio >= 0.15	Ratio >= 0.25	Ratio >= 0.25
Poly	Metal1	Metal2
Density	Density	Density
Ratio >= 0.25	Ratio >= 0.25	Ratio >= 0.25
Metal3	Metal4	Metal5
Density		
Ratio >= 0.25		
Metal6		

Connectivity Definition Connectivity M6term Via5 M5term Via4 M4term Via3 M3term Via2NoCapInd Via2Cap CapMetal M2term Via1 INDterm1Cont INDterm2Cont INDterm1 INDterm2 M1term POLYcont PSDcont **NSDcont POLYterm NSDterm** NWVIA **PSDterm** NWELLterm JVARterm PWVIA **PWNBVIA** SUBVIA **NBVIA** JVARandde Pwell ISOPWELL | **PSUB** Nburied !

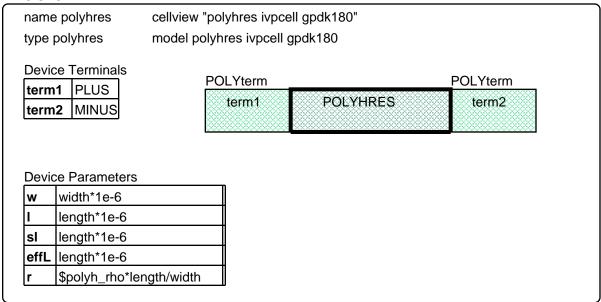
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Component LVS D)efinitions
Component Eve E	

Resistors

RESISTOR

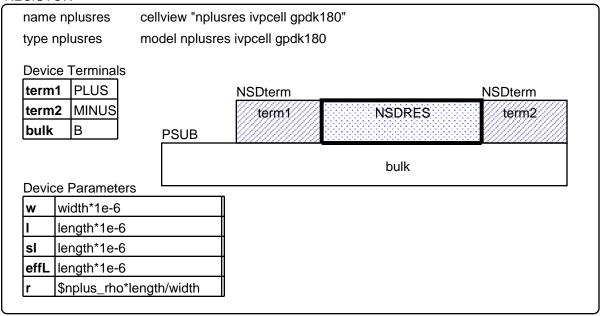


RESISTOR

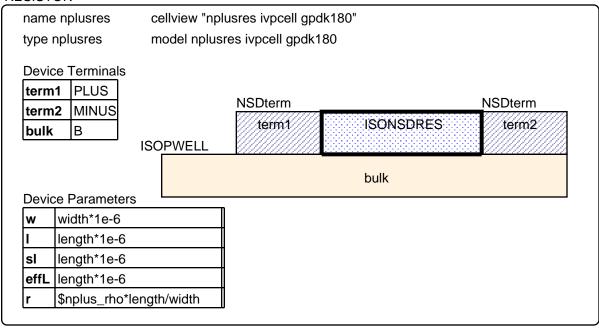


Resistors (cont.)

RESISTOR

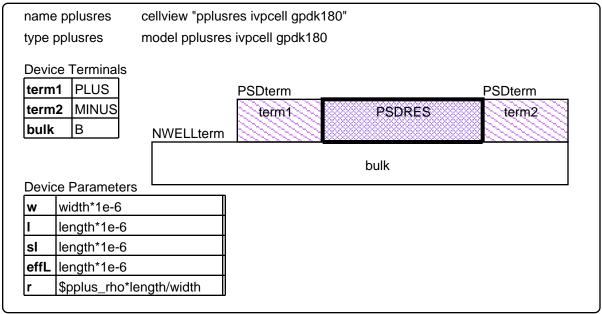


RESISTOR

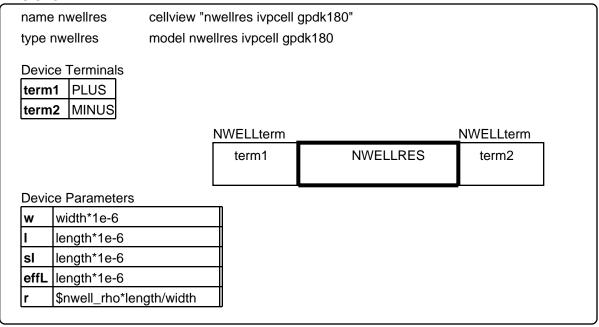


Resistors (cont.)

RESISTOR



RESISTOR



Resistors (cont.)

macro

Macro Table

\$cell	\$rho	\$layer1	\$layer2
m1res	\$m1_rho	M1term	M1res
m2res	\$m2_rho	M2term	M2res
m3res	\$m3_rho	M3term	M3res
m4res	\$m4_rho	M4term	M4res
m5res	\$m5_rho	M5term	M5res
m6res	\$m6_rho	M6term	M6res

RESISTOR

name \$cell cellview "\$cell ivpcell gpdk180" type \$cell model \$cell ivpcell gpdk180

Device Terminals

term1	PLUS
term2	MINUS

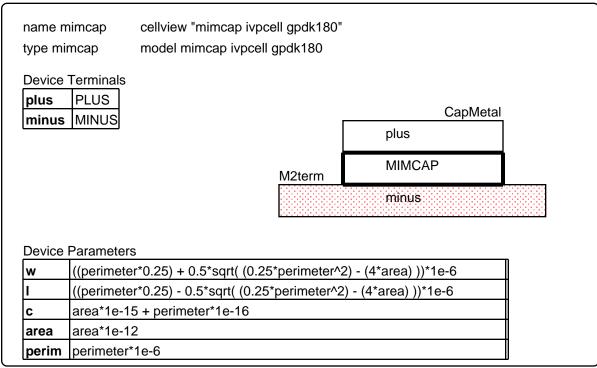
\$layer1		\$layer1	
term1	\$layer2	term2	

Device Parameters

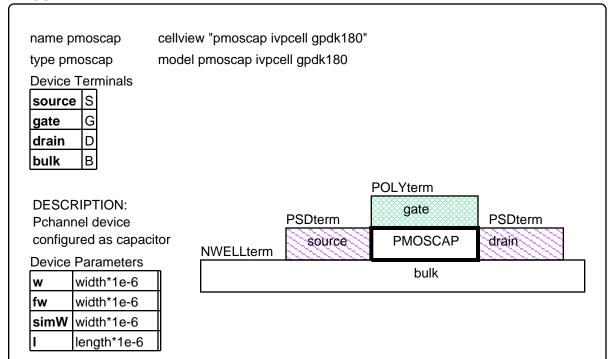
w	width*1e-6
I	length*1e-6
sl	length*1e-6
effL	length*1e-6
r	\$rho*length/width

Capacitors

CAPACITOR

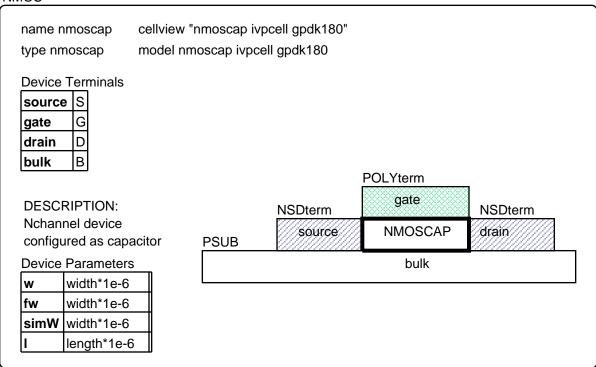


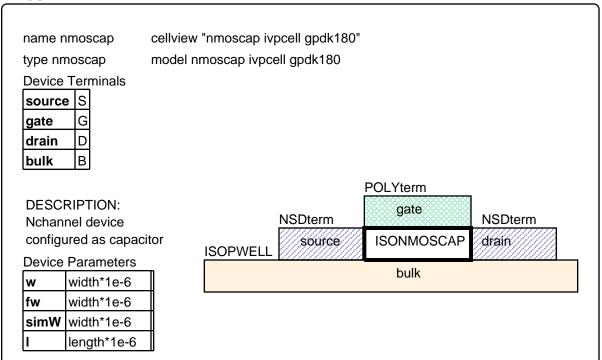
PMOS



Capacitors (cont.)

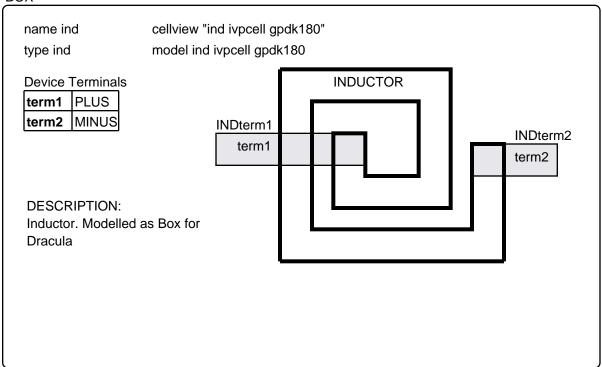
NMOS





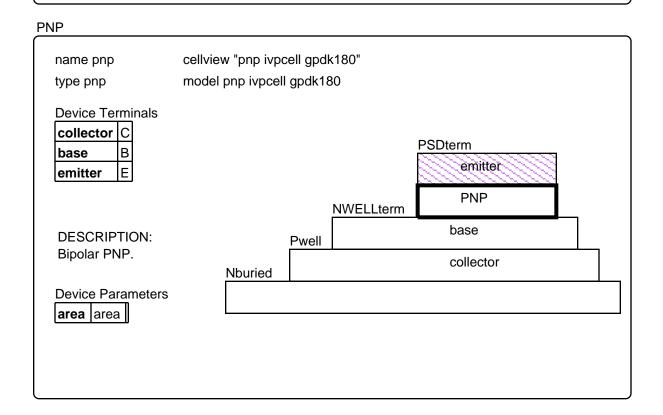
Inductor

BOX

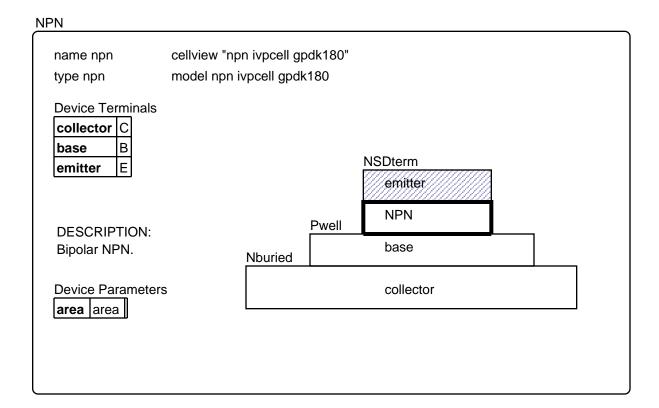


Bipolars

PNP cellview "vpnp ivpcell gpdk180" name vpnp type vpnp model vpnp ivpcell gpdk180 **Device Terminals** collector C base В emitter **PSDterm** emitter **VPNP DESCRIPTION: NWELLterm** Substrate PNP. collector base connects... **PSUB Device Parameters** collector area area

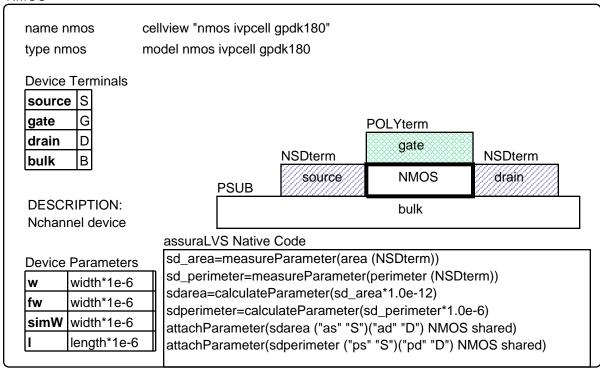


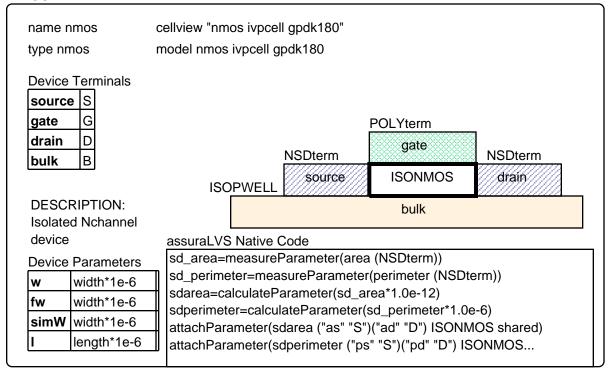
Bipolars (cont.)



Nmos

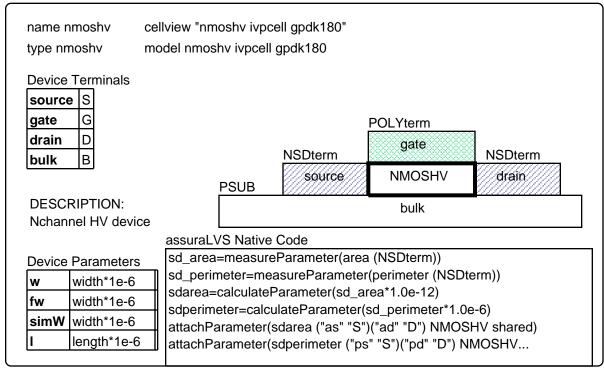
NMOS

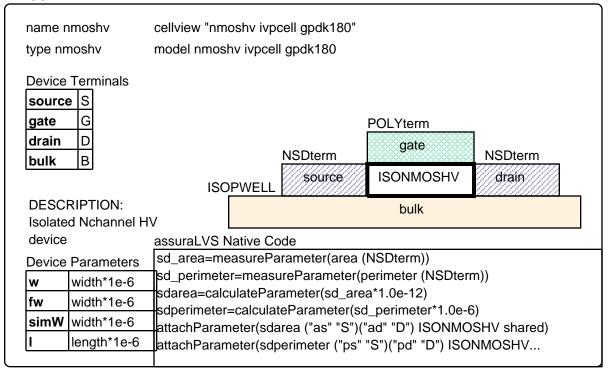




HV Nmos

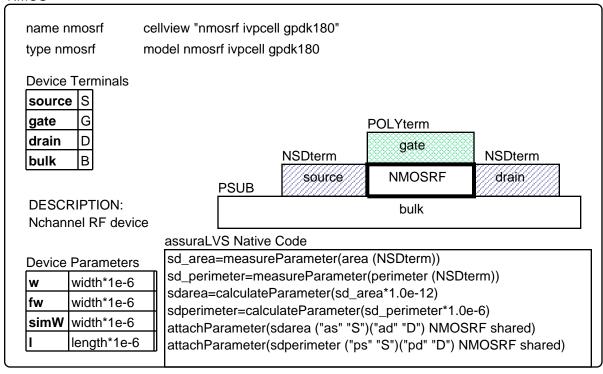
NMOS

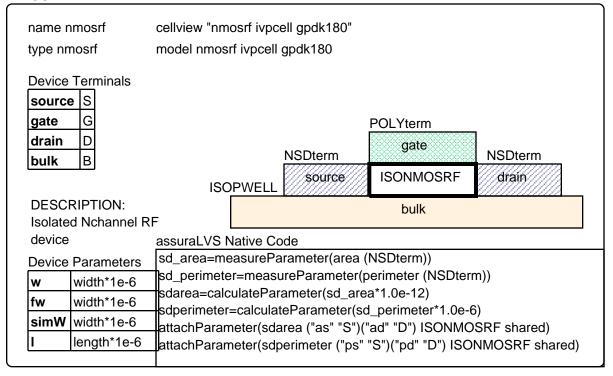




RF Nmos

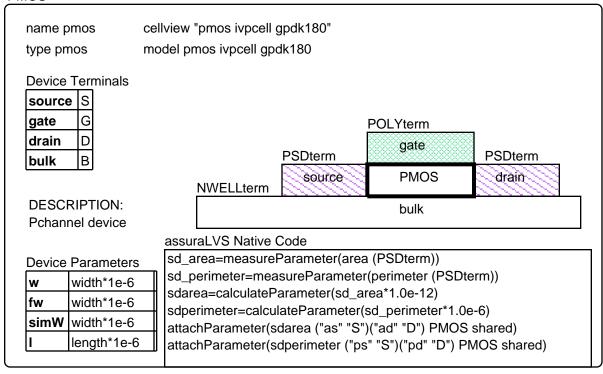
NMOS



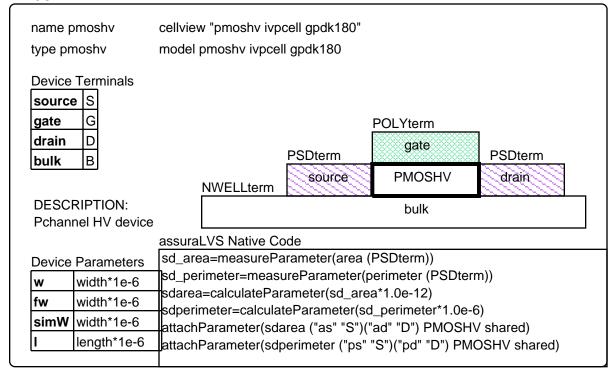


Pmos and HV Pmos

PMOS

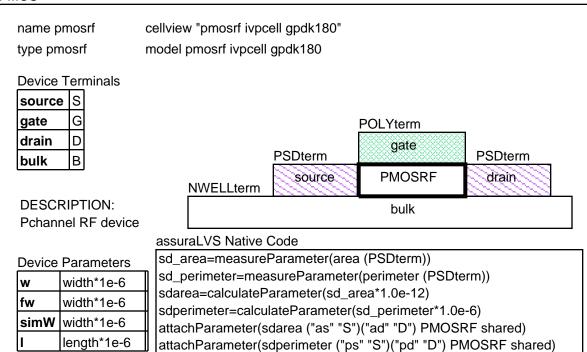


PMOS



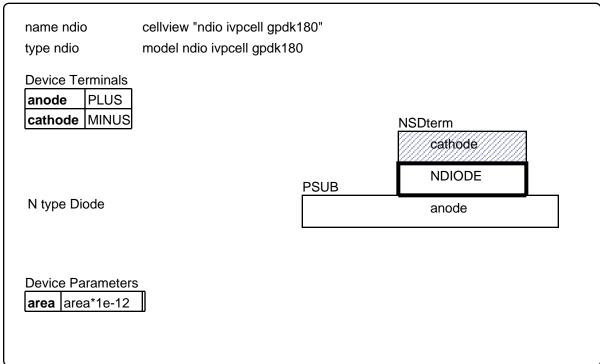
RF Pmos

PMOS

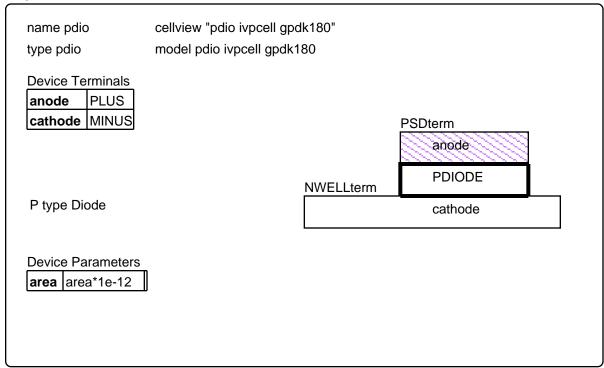


Diodes

DIODE

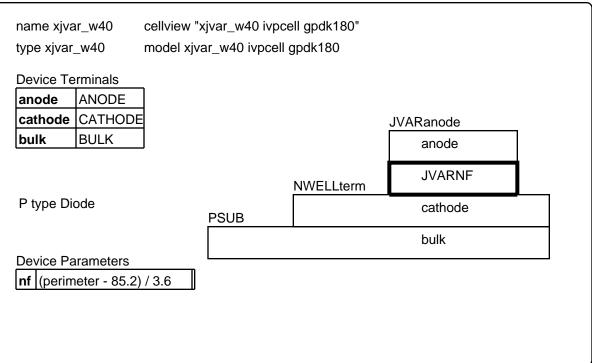


DIODE

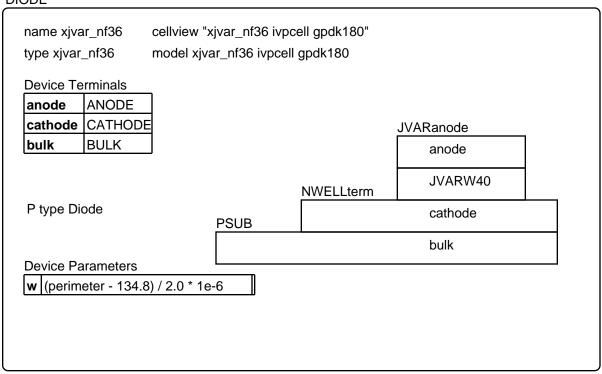


Junction Varactors

DIODE



DIODE



LVS Diva Compare rules

```
diva LVS Compare Code divaLVS.rul
```

```
./***********************
: Title:
        Diva LVS rules for GENERIC PDK Process
.*****
;**** LVS RULES ****
.*******
IvsRules(
printf("\n GENERIC PDK LVS Rules\n")
;*** MOSFETS ***
.*****
;combine parallel MOS devices (mosfets with different I's are not combined)
procedure(parallelMOS(m1 m2)
prog((mt)
mt = ncons(nil)
when(and(m1->l m2->l)
 unless(abs(m1->I - m2->I) < .005e-6
  return("doNotCombine")
when( and(m1->w m2->w)
 unless(m1->m
  m1->m=1
 unless(m2->m
  m2->m=1
 mt->w = m1->w * m1->m + m2->w * m2->m
 mt->m=1
 mt->l = m1->l
return(mt)
```

LVS Assura Compare rules

assura LVS Compare Code assuraLVS.rul

```
: GENERIC PDK Process Assura LVS Rules v2.0 1/10/02
avCompareRules(
fileVer = "2.3"
fileDate = "Aug 2005"
libType = "GENERIC PDK Process"
libVer = "version 2.3"
refDoc1 = "Library Specification No. GPDK, Version 2.3"
printf( " \n" )
printf(
     printf( " \n" )
printf(
 " GENERIC PDK Assura Compare Rules file version %s\n"
 fileVer
printf( " Cadence Design Systems, Inc. \n")
printf( " PDK Technology Center, Melbourne, FL \n")
printf( " %s \n" fileDate )
printf( " Use with %s %s\n" libType libVer )
printf( " \n")
printf( " Reference Documents: \n" )
printf( " %s \n" refDoc1 )
printf( "\n")
printf( " NOTICE: \n" )
printf(
 " Cadence Design Systems shall not be liable for the accuracy \n"
printf(
 of this LVS rule file or its ability to capture errors. \n"
printf(
 " The user is responsible for thoroughly testing and
printf(
 " implementing its features.
                                        \n"
printf( " \n" )
printf(
```

Library CDF Definition

CDF gpdk180 libcdf

CDF device params

basePasswd	"base"	private	Flex PDK private password
masterPasswd	"master"	private	Flex PDK private password

switch !IC61

CDF parameters

name	"lxComponentType"			
prompt	"Virtuoso XL Component Types"			
defValue	"(((\"cells\" (\"gpdk180 nmos3\" \"gpdk180 nmos\")) (\"type\" \"nmos\") (\"IxActiveLayer\" \"Oxide drawing\") (\"IxMOSDeviceType\" \"NMOSHV\") (\"IxDeviceWidth\" \"w\") (\"IxMaxWidth\" 1e-05)) ((\"cells\" (\"gpdk180 pmos3\" \"gpdk180 pmos\")) (\"type\" \"pmos\") (\"IxActiveLayer\" \"Oxide drawing\") (\"IxMOSDeviceType\" \"PMOS\") (\"IxDeviceWidth\" \"w\") (\"IxMaxWidth\" 1e-05)))"			
type	"string"			
display	"nil"			
dontSave	"nil"			

CDF properties

<u> p.opo</u>	
formInitProc	""
doneProc	""
buttonFieldWidth	340
fieldHeight	35
fieldWidth	350
promptWidth	175
instNameType	"schematic"
instDisplayMode	"instName"
netNameType	"schematic"
termSimType	"DC"
termDisplayMode	"netName"
paramSimType	"DC"
paramEvaluate	"t nil nil nil nil"
paramDisplayMode	"parameter"

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Library MOS Dafir	vitiono
Library MOS Defir	intions

Global MOS Parameters

Global Parameters

mos_msDiff	{2B}	Minimum diffusion spacing
mos_mxGate	{5C}	Minimum gate extension
mos_msGate	{5B}	Minimum gate spacing
mos_meDiffGate	{5D}	Minimum diffusion enclosure of gate
mos_msGateCont	{6E}	Minimum gate to contact spacing
mos_mwPoly	{5A}	Minimum poly width on field
mos_msDiffPoly	{5E}	Minimum diffusion to poly spacing
mos_mwCont	{6A}	Minimum & maximum contact width
mos_msCont	{6B}	Minimum contact spacing
mos_masCont	{6B}	Minimum contact array spacing
mos_meDiffCont	{6C}	Minimum diffusion enclosure of contact
mos_meeDiffCont	{6C}	Minimum diffusion end enclosure of contact
mos_mePolyCont	{6D}	Minimum poly enclosure of contact
mos_meePolyCont	{6D}	Minimum poly end enclosure of contact
mos_mwM1	{7A}	Minimum metal 1 width
mos_msM1	{7B}	Minimum metal 1 spacing
mos_meM1Cont	{7C}	Minimum metal 1 enclosure of contact
mos_meeM1Cont	{7C}	Minimum metal 1 end enclosure of contact

Global MOS Parameters (cont.)

Global Parameters

nmos_melmplDiff	{4C}	Minimum implant enclosure of diffusion	
nmos_melmplGate	0.12	Minimum implant enclosure of gates	
nmos_meelmplGate	0.12	Minimum implant end enclosure of gates	
nmos_melmplPoly	0.12	Minimum implant enclosure of field poly	
nmos_melmplCont	0.0	Minimum implant enclosure of contact	
nmos_meVoltDiff	{2.5C}	Minimum thick oxide enclosure of thin oxide diffusion	
nmos_meVoltImpl	nil	Minimum thick oxide enclosure of implant	
nmos_meWellDiff	{2C}	Minimum wellbody enclosure of diffusion	
nmos_meWellVolt	0.0	Minimum wellbody enclosure of thick diffusion	
nmos_meVthDiff	0.0	Minimum high-vt implant enclosure of diffusion	
nmos_meVthGate	0.0	Minimum high-vt implant enclosure of gates	
nmos_meeVthGate	0.0	Minimum implant end enclosure of gates	
nmos_meNtnDiff	0.0	Minimum NTN enclosure of thin oxide diffusion	
nmos_meNtnImpl	0.0	Minimum NTN enclosure of implant	
nmos_meWellNtn	0.0	Minimum wellbody enclosure of NTN	

Global Parameters

pmos_melmplDiff	{4C}	Minimum implant enclosure of diffusion	
pmos_melmplGate	0.12	Minimum implant enclosure of gates	
pmos_meelmplGate	0.12	Minimum implant end enclosure of gates	
pmos_melmplPoly	0.12	Minimum implant enclosure of field poly	
pmos_melmplCont	0.0	Minimum implant enclosure of contact	
pmos_meVoltDiff	{2.5C}	Minimum thick oxide enclosure of thin oxide diffusion	
pmos_meVoltImpI	nil	Minimum thick oxide enclosure of implant	
pmos_meWellDiff	{2C}	Minimum wellbody enclosure of diffusion	
pmos_meWellVolt	0.0	Minimum wellbody enclosure of thick diffusion	
pmos_meVthDiff	0.0	Minimum high-vt implant enclosure of diffusion	
pmos_meVthGate	0.0	Minimum high-vt implant enclosure of gates	
pmos_meeVthGate	0.0	Minimum implant end enclosure of gates	

Global MOS Parameters (cont.)

Global Parameters

tap_nmos_msTapDiff	0.0	Minimum tap diffusion to device diffusion spacing	
tap_nmos_msTapImpI	{3C}	Minimum tap diffusion to device implant spacing	
tap_nmos_meTimpTap	{4C}	Minimum tap implant enclosure of tap diffusion	
tap_nmos_msTimpDiff	0.0	Minimum tap implant to device diffusion spacing	
tap_nmos_msTimpImpI	0.0	Minimum tap implant to device implant spacing	
tap_nmos_maTap	0.16	Minimum tap diffusion area	
tap_nmos_maTimp	0.36	Minimum tap implant area	
tap_nmos_meWellTap	0.0	Minimum well enclosure of tap diffusion	
tap_nmos_msTapVolt	0.25	Minimum tap diffusion to thick oxide spacing	
tap_nmos_mwltap	0.50	Minimum width of integrated tap	
tap_nmos_msltapGate	0.50	Minimum integrated tap spacing to gate	
tap_nmos_maltimp	0.36	Minimum integrated tap implant area	
tap_nmos_meltimpltap	{4C}	Minimum integrated tap implant enclosure of tap	
tap_nmos_meltimpCont	0.10	Minimum integrated tap implant enclosure of contact	
tap_nmos_msTapNtn	0.0	Minimum tap diffusion to NTN spacing	

Global Parameters

tap_pmos_msTapDiff	0.0	Minimum tap diffusion to device diffusion spacing
tap_pmos_msTapImpI	{3C}	Minimum tap diffusion to device implant spacing
tap_pmos_meTimpTap	{4C}	Minimum tap implant enclosure of tap diffusion
tap_pmos_msTimpDiff	0.0	Minimum tap implant to device diffusion spacing
tap_pmos_msTimpImpI	0.0	Minimum tap implant to device implant spacing
tap_pmos_maTap	0.16	Minimum tap diffusion area
tap_pmos_maTimp	0.36	Minimum tap implant area
tap_pmos_meWellTap	{2C}	Minimum well enclosure of tap diffusion
tap_pmos_msTapVolt	0.25	Minimum tap diffusion to thick oxide spacing
tap_pmos_mwltap	0.50	Minimum width of integrated tap
tap_pmos_msltapGate	0.50	Minimum integrated tap spacing to gate
tap_pmos_maltimp	0.36	Minimum integrated tap implant area
tap_pmos_meltimpltap	{4C}	Minimum integrated tap implant enclosure of tap
tap_pmos_meltimpCont	0.10	Minimum integrated tap implant enclosure of contact

Global Parameters

mos_perimGateEdge	t	Use four sides to calculate ps, pd	
mos_aggregateFingers	nil	Aggregate fingers: affects simM, as,ad,ps,pd	

Callbacks

MOS Callback Procedures

CDF Callback

```
; Perforce:
             $File$
             $Revision$
             $DateTime$
             $Change$
             $Author$
; Description: MOS Transistor CDF Callback
; Author:
             Amir Kouchekinia <amir@cadence.com>
; Created:
             Nov 6 16:41 02
             Nov 6 16:41 02 14419 amir
; Modified:
; Language:
             Skill
; Package:
             Primitive_mos
; MainFun:
             gpdk180_mosCB
; Status:
             Experimental (Do Not Distribute)
; (C) Copyright 2002, Cadence Design Systems, all rights reserved.
procedure(gpdk180_mosCB(param "s")
                                           ;_May 15 03 amir 5359
 let(
      cdfId cellName libName procName paramId
      dpt grid scale sGrid doList
      applyThresh mode min max value
      fingers w fw threshold
   ;; set local variable to global cdfgData
   cdfId = cdfgData
   caseq( concat(cdfId~>type)
      ( ( cellData baseCellData userCellData )
       cellName = cdfId~>id~>name
       libName = cdfId~>id~>lib~>name
      ( instData
       cellName = cdfId~>id~>master~>cellName
       libName = cdfId~>id~>master~>libName
      ( t...
```

CDF Macro Definitions

mosParamsA - MOS Parameters (Part A)

CDF macro mosParamsA

CDF parameters

ODI paramete	
name	"model"
prompt	"Model Name"
defValue	gpdk180_mosValue('model ?id cdfld ?returnString t)
type	"string"
display	"gpdk180_mosDisplay('model)"
editable	"nil"
parseAsCEL	"yes"

CDF parameters

OBI Paramotoro	
name	"m"
prompt	"Multiplier"
defValue	"1"
type	"string"
display	"t"
editable	"t"
callback	"gpdk180_mosCB('m)"
parseAsNumber	"yes"
parseAsCEL	"yes"

ODI parameters	
name	"simM"
prompt	"Simulation Multiplier"
defValue	gpdk180_mosValue('simM ?id cdfld ?returnString t)
type	"string"
display	"nil"
editable	"nil"
parseAsNumber	"yes"
parseAsCEL	"yes"

CDF macro mosParamsA

CDF parameters

ODI Paramotoro	
name	" "
prompt	"Length"
units	"lengthMetric"
defValue	gpdk180_mosValue('l ?id cdfld ?returnString t)
type	"string"
display	"t"
callback	"gpdk180_mosCB('I)"
parseAsNumber	"yes"
parseAsCEL	"yes"

CDF parameters

	
name	"w"
prompt	"Total Width"
units	"lengthMetric"
defValue	gpdk180_mosValue('w ?id cdfld ?returnString t)
type	"string"
display	"t"
callback	"gpdk180_mosCB('w)"
parseAsNumber	"yes"
parseAsCEL	"yes"

name	"fw"
prompt	"Finger Width"
units	"lengthMetric"
defValue	gpdk180_mosValue('fw ?id cdfld ?returnString t)
type	"string"
display	"t"
editable	"t"
callback	"gpdk180_mosCB('fw)"
parseAsNumber	"yes"
parseAsCEL	"yes"

CDF macro mosParamsA

CDF parameters

name	"simW"
prompt	"Simulation Width"
units	"lengthMetric"
defValue	gpdk180_mosValue('simW ?id cdfld ?returnString t)
type	"string"
display	"nil"
editable	"nil"
parseAsNumber	"yes"
parseAsCEL	"yes"

CDF parameters

name	"fingers"
prompt	"Fingers"
defValue	gpdk180_mosValue('fingers ?id cdfld ?returnString t)
type	"string"
display	"†"
editable	"†"
callback	"gpdk180_mosCB('fingers)"
parseAsNumber	"yes"
parseAsCEL	"yes"

CDF parameters

name	"threshold"
prompt	"Threshold"
units	"lengthMetric"
defValue	gpdk180_mosValue('threshold ?id cdfld ?returnString t)
type	"string"
display	"t"
callback	"gpdk180_mosCB('threshold)"
parseAsNumber	"yes"
parseAsCEL	"yes"

name	"applyThresh"
prompt	"Apply Threshold"
defValue	nil
type	"boolean"
display	"t"
callback	"gpdk180_mosCB('applyThresh)"

CDF macro mosParamsA

CDF parameters

name	"connectGates"
prompt	"Gate Connection"
defValue	"None"
choices	'("None" "Top" "Bottom" "Both" "Alternate")
type	"radio"
display	"t"

CDF parameters

name	"connectSD"
prompt	"S/D Connection"
defValue	"None"
choices	'("None" "Source" "Drain" "Both")
type	"radio"
display	"gpdk180_mosDisplay('connectSD)"

<u></u>	
name	"sdMtlWidth"
prompt	"S/D Metal Width"
units	"lengthMetric"
defValue	gpdk180_mosValue('sdMtlWidth ?id cdfld ?returnString t)
type	"string"
display	"t"
editable	"t"
callback	"gpdk180_mosCB('sdMtlWidth)"
parseAsNumber	"yes"
parseAsCEL	"yes"

CDF macro mosParamsA

CDF parameters

parameters	
name	"mtlCvg"
prompt	"Metal Coverage"
defValue	""
type	"string"
display	"nil"
callback	"gpdk180_mosCB('mtlCvg)"

CDF parameters

pananiara	
name	"switchSD"
prompt	"Switch S/D"
defValue	nil
type	"boolean"
display	"gpdk180_mosDisplay('switchSD)"
callback	"gpdk180_mosCB('switchSD)"

CDF parameters

F	
name	"leftAbut"
prompt	"Left Abutment"
defValue	0
type	"int"
display	"nil"
editable	"t"
callback	"gpdk180_mosCB('leftAbut)"

name	"rightAbut"
prompt	"Right Abutment"
defValue	0
type	"int"
display	"nil"
editable	"t"
callback	"gpdk180_mosCB('rightAbut)"

mosParamsTap - MOS Tap Parameters

CDF macro mosParamsTap

CDF parameters

-	
name	"tap"
prompt	"Bodytie Type"
defValue	"None"
choices	'("None" "Integrated" "Detached")
type	"cyclic"
display	"t"
callback	"gpdk180_mosCB('tap)"

CDF parameters

name	"leftTap"
prompt	"Left Tap"
defValue	t
type	"boolean"
display	"gpdk180_mosDisplay('leftTap)"

CDF parameters

name	"rightTap"
prompt	"Right Tap"
defValue	nil
type	"boolean"
display	"gpdk180_mosDisplay('rightTap)"

CDF parameters

<u> parametere</u>	
name	"bottomTap"
prompt	"Bottom Tap"
defValue	nil
type	"boolean"
display	"gpdk180_mosDisplay('bottomTap)"

CDF parameters

name	"topTap"
prompt	"Тор Тар"
defValue	nil
type	"boolean"
display	"gpdk180_mosDisplay('topTap)"

CDF parameters

name	"tapExtension"
prompt	"Tap Extension"
defValue	пп
type	"string"
display	"gpdk180_mosDisplay('tapExtension)"
callback	"gpdk180_mosCB('tapExtension)"

name	"tapCntRows"
prompt	"Tap Contact Rows"
defValue	1
type	"int"
display	"gpdk180_mosDisplay('tapCntRows)"

mosParamsB - MOS Parameters (Part B)

CDF macro mosParamsB

CDF parameters

name	"showSimParams"
prompt	"Show Sim Parameters"
defValue	nil
type	"boolean"
display	"gpdk180_mosDisplay('showSimParams)"

CDF parameters

panamiatara	
name	"editAreaPerim"
prompt	"Edit Area & Perim"
defValue	nil
type	"boolean"
display	"gpdk180_mosDisplay('editAreaPerim)"
callback	"gpdk180_mosCB('editAreaPerim)"

CDF parameters

ODI parameters		
name	"ad"	
prompt	"Drain diffusion area"	
defValue	gpdk180_mosValue('ad ?id cdfld ?returnString t)	
type	"string"	
display	"gpdk180_mosDisplay('ad)"	
callback	"gpdk180_mosCB('ad)"	
editable	"cdfgData->editAreaPerim->value"	
parseAsNumber	"yes"	
parseAsCEL	"yes"	

name	"as"	
prompt	"Source diffusion area"	
defValue	gpdk180_mosValue('as ?id cdfld ?returnString t)	
type	"string"	
display	"gpdk180_mosDisplay('as)"	
callback	"gpdk180_mosCB('as)"	
editable	"cdfgData->editAreaPerim->value"	
parseAsNumber	"yes"	
parseAsCEL	"yes"	

CDF macro mosParamsB

CDF parameters

name	"pd"	
prompt	"Drain diffusion periphery"	
units	"lengthMetric"	
defValue	gpdk180_mosValue('pd ?id cdfld ?returnString t)	
type	"string"	
display	"gpdk180_mosDisplay('pd)"	
callback	"gpdk180_mosCB('pd)"	
editable	"cdfgData->editAreaPerim->value"	
parseAsNumber	"yes"	
parseAsCEL	"yes"	

CDF parameters

name	"ps"	
prompt	"Source diffusion periphery"	
units	"lengthMetric"	
defValue	gpdk180_mosValue('ps ?id cdfld ?returnString t)	
type	"string"	
display	"gpdk180_mosDisplay('ps)"	
callback	"gpdk180_mosCB('ps)"	
editable	"cdfgData->editAreaPerim->value"	
parseAsNumber	"yes"	
parseAsCEL	"yes"	

CDF parameters

name	"nrd"
prompt	"Drain diffusion res squares"
defValue	""
type	"string"
display	"gpdk180_mosDisplay('nrd)"
editable	"t"
parseAsNumber	"yes"
parseAsCEL	"yes"

name	"nrs"
prompt	"Source diffusion res squares"
defValue	""
type	"string"
display	"gpdk180_mosDisplay('nrs)"
editable	"t"
parseAsNumber	"yes"
parseAsCEL	"yes"

CDF macro mosParamsB

CDF parameters

	F	
name	"ld"	
prompt	"Drain diffusion length"	
units	"lengthMetric"	
defValue	ш	
type	"string"	
display	"gpdk180_mosDisplay('ld)"	
editable	"t"	
parseAsNumber	"yes"	
parseAsCEL	"yes"	

CDF parameters

name	"Is"
prompt	"Source diffusion length"
units	"lengthMetric"
defValue	""
type	"string"
display	"gpdk180_mosDisplay('ls)"
editable	"t"
parseAsNumber	"yes"
parseAsCEL	"yes"

name	"off"	
prompt	"Device initially off"	
type	"boolean"	
display	"gpdk180_mosDisplay('off)"	

CDF macro mosParamsB

CDF parameters

	\ / - -
name	"Vds"
prompt	"Drain source initial voltage"
units	"voltage"
defValue	""
type	"string"
display	"gpdk180_mosDisplay('Vds)"
parseAsNumber	"yes"
parseAsCEL	"yes"

CDF parameters

name	"Vgs"
prompt	"Gate source initial voltage"
units	"voltage"
defValue	mm .
type	"string"
display	"gpdk180_mosDisplay('Vgs)"
parseAsNumber	"yes"
parseAsCEL	"yes"

CDF parameters

name	"Vbs"
prompt	"Bulk source initial voltage"
units	"voltage"
defValue	""
type	"string"
display	"gpdk180_mosDisplay('Vbs)"
parseAsNumber	"yes"
parseAsCEL	"yes"

name	"trise"	
prompt	"Temp rise from ambient"	
defValue	""	
type	"string"	
display	"gpdk180_mosDisplay('trise)"	
parseAsNumber	"yes"	
parseAsCEL	"yes"	

CDF macro mosParamsB

CDF parameters

name	"rdc"	
prompt	"Additional drain resistance"	
units	"current"	
defValue	""	
type	"string"	
display	"gpdk180_mosDisplay('rdc)"	
parseAsNumber	"yes"	
parseAsCEL	"yes"	

CDF parameters

name	"rsc"	
prompt	"Additional source resistance"	
units	"current"	
defValue	""	
type	"string"	
display	"gpdk180_mosDisplay('rsc)"	
parseAsNumber	"yes"	
parseAsCEL	"yes"	

CDF parameters

name	"dtemp"	
prompt	"Temperature difference"	
defValue	""	
type	"string"	
display	"gpdk180_mosDisplay('dtemp)"	
parseAsNumber	"yes"	
parseAsCEL	"yes"	

name	"geo"	
prompt	"Source/drain selector"	
defValue	""	
type	"string"	
display	"gpdk180_mosDisplay('geo)"	
parseAsNumber	"yes"	
parseAsCEL	"yes"	

CDF macro mosParamsB

CDF parameters

name	"pasUpdateParamList"	
prompt	"PAS Update Param List"	
defValue	" "	
type	"string"	
display	"nil"	
editable	"nil"	

switch !IC61

CDI	= nar:	amet	ers

CDI Parai	neters
name	"IxIgnoredParamsForCAS"
prompt	"IxIgnoredParamsForCAS"
defValue	"connectGates connectSD mtlCvg leftAbut rightAbut showSimParams switchSD ad as pd ps"
type	"string"
display	"nil"
editable	"nil"

CDF properties

formInitProc	"PasCdfFormInit"
doneProc	"PasCdfDone"
buttonFieldWidth	340
fieldHeight	35
fieldWidth	500
promptWidth	175
instNameType	"schematic"
instDisplayMode	"instName"
netNameType	"schematic"
termSimType	"DC"
termDisplayMode	"voltage"
paramSimType	"DC"
paramEvaluate	"nil nil nil t nil"
paramDisplayMode	"parameter"
modelLabelSet	"vto kp gamma"
opPointLabelSet	"id vgs vds gm"
paramLabelSet	"-model w I m"

mosSimInfo - MOS Simulation Information (common)

CDF macro mosSimInfo

CDF siminfo

		_	
simulator	ads		
netlistProcedure	ADSsimCompPrim		
otherParameters	(model)		
instParameters	(Width Length As Ad Ps Pd Nrd Nrs Id Is _M Trise)	T	
componentName	nil	T	
termMapping	(nil D ":P1" G ":P2" S ":P3" B ":P4")	T	
propMapping	(nil Width simW Length I As as Ad ad Ps ps Pd pd Nrd nrd Nrs nrs _M simM Trise trise Region	nlı	region)
typeMapping	nil	T	
uselib	nil	T	

CDF siminfo

simulator	ams	
propMapping	(nil m simM w simW)	
namePrefix	ш	
isPrimitive	t	
instParameters	(w I as ad ps pd nrd nrs ld Is m trise model)	
otherParameters	(model)	

CDF siminfo

simulator	auCdl
netlistProcedure	ansCdlCompPrim
instParameters	(M L W)
propMapping	(nil M m L I W w)
namePrefix	"M"

simulator	auLvs
propMapping	nil
netlistProcedure	ansLvsCompPrim
instParameters	(m I w)
permuteRule	"(p D S)"
namePrefix	"M"

CDF macro mosSimInfo

simulator	spectre	
propMapping	(nil m simM w simW)	
namePrefix	""	
otherParameters	(model)	
instParameters	(w I as ad ps pd nrd nrs ld ls m trise)	
termMapping	(nil D \:d G \:g S \:s B \:b)	

mosSimInfoRF

CDF macro mosSimInfoRF

CDF siminfo

simulator	ads
netlistProcedure	ADSsimCompPrim
otherParameters	(model)
instParameters	(Width Length fingers simM)
componentName	nil
termMapping	(nil D ":P1" G ":P2" S ":P3" B ":P4")
propMapping	(nil Width simW Length I)
typeMapping	nil
uselib	nil

CDF siminfo

simulator	ams
propMapping	(nil w simW)
namePrefix	""
isPrimitive	t
instParameters	(w I fingers simM)
otherParameters	(model)

CDF siminfo

simulator	auCdl
netlistProcedure	ansCdlCompPrim
instParameters	(M L W)
propMapping	(nil M m L I W w)
namePrefix	"M"

simulator	auLvs
propMapping	nil
netlistProcedure	ansLvsCompPrim
instParameters	(m l w)
permuteRule	"(p D S)"
namePrefix	"M"

CDF macro mosSimInfoRF

simulator	spectre	
propMapping	(nil w simW)	
namePrefix	""	
otherParameters	(model)	
instParameters	(w I fingers simM)	
termMapping	(nil D \:d G \:g S \:s B \:b)	

mos3n - 3 terminal NMOS Transistor CDF

CDF macro mos3n

include macro mosParamsA

include macro mosParamsB

include macro mosSimInfo

CDF siminfo

simulator	ads
termOrder	(D G S bulkn)

CDF siminfo

simulator	ams
termOrder	(D G S bulkn)
extraTerminals	((nil name "bulkn" direction "inputOutput" netExpr "[@bulkn:%:gnd!]"))

CDF siminfo		CDF siminto	
simulator	spectre	simulator	auCdl
termOrder	(D.G.S.bulkn)	termOrder	(D.G.S.bulkn)

simulator	auLvs
termOrder	(D G S bulkn)
deviceTerminals	"DGSB"

mos3p - 3 terminal PMOS Transistor CDF

CDF macro mos3p

include macro mosParamsA

include macro mosParamsB

include macro mosSimInfo

CDF siminfo

simulator	ads
termOrder	(D G S bulkp)

CDF siminfo

simulator	ams
termOrder	(D G S bulkp)
extraTerminals	((nil name "bulkp" direction "inputOutput" netExpr "[@bulkp:%:vdd!]"))

CDF siminfo

simulator	spectre
termOrder	(D G S bulkp)

CDF siminfo

simulator	auCdl
termOrder	(D G S bulkp)

simulator	auLvs
termOrder	(D G S bulkp)
deviceTerminals	"D G S B"

mos4 - 4 terminal MOS Transistor CDF

CDF macro mos4

include macro mosParamsA

include macro mosParamsTap

include macro mosParamsB

include macro mosSimInfo

CDF siminfo

simulator	ads
termOrder	(DGSB)

CDF siminfo

simulator	ams
ermOrder	(DGSB)

CDF siminfo

simulator	spectre
termOrder	(DGSB

CDF siminfo

simulator	auCdl	
termOrder	(DGSB)	

simulator	auLvs
termOrder	(DGSB)

mos4RF - 4 terminal RF MOS Transistor CDF

CDF macro mos4RF

include macro mosParamsA

include macro mosParamsTap

include macro mosParamsB

include macro mosSimInfoRF

CDF siminfo

simulator	ads
termOrder	(D G S B)

CDF siminfo

simulator	ams	
termOrder	(DGSB)	

CDF siminfo

simulator	spectre	
termOrder	(DGSB	

CDF siminfo

simulator	auCdl	
termOrder	(DGSB)	

simulator	auLvs
termOrder	(DGSB)

CDF Definitions

MOS Transistor nmos3

CDF gpdk180 nmos3

CDF device params

grid	\$grid	private	MFG Grid
scale	1.0e-6	private	Dimension scale factor
model	"nmos1"	private	Device Model Name
category	"mos"	private	Library Manager Category
perimGateEdge	\$mos_perimGateEdge	private	Use four sides to calculate ps, pd
aggregateFingers	\$mos_aggregateFingers	private	Aggregate fingers: affects simM, as,ad,ps,pd

CDF device params

ODI acvioc pai	<u> </u>			
dwGate	2.0	private	Default channel width	
mwGate	{2A}	private	Minimum channel Width	
xwGate	50.00	private	Maximum channel Width	
mlGate	{5A}	private	Minimum channel Length	
msDiff	\$mos_msDiff	private	Minimum diffusion spacing	
mxGate	\$mos_mxGate	private	Minimum gate extension	
msGate	\$mos_msGate	private	Minimum gate spacing	
meDiffGate	\$mos_meDiffGate	private	Minimum diffusion enclosure of gate	
msGateCont	\$mos_msGateCont	private	Minimum gate to contact spacing	
mwPoly	\$mos_mwPoly	private	MInimum poly width on field	
msDiffPoly	\$mos_msDiffPoly	private	Minimum diffusion to poly spacing	
mwCont	\$mos_mwCont	private	Minimum contact width	
msCont	\$mos_msCont	private	Minimum contact spacing	
masCont	\$mos_masCont	private	Minimum contact array spacing	
meDiffCont	\$mos_meDiffCont	private	Minimum diffusion enclosure of contact	
meeDiffCont	\$mos_meeDiffCont	private	Minimum diffusion end enclosure of contact	
mePolyCont	\$mos_mePolyCont	private	Minimum poly enclusre of contact	
meePolyCont	\$mos_meePolyCont	private	Minimum poly end enclosure of contact	
mwM1	\$mos_mwM1	private	Minimum metal 1 width	
msM1	\$mos_msM1	private	Minimum metal 1 spacing	
meM1Cont	\$mos_meM1Cont	private	Minimum metal 1 enclosure of contact	
meeM1Cont	\$mos_meeM1Cont	private	Minimum metal 1 end enclosure of contact	

melmplDiff	\$nmos_meImplDiff	private	Minimum implant enclosure of diffusion
melmplGate	\$nmos_meImplGate	private	Minimum implant enclosure of gate
meelmplGate	\$nmos_meeImplGate	private	Minimum implant end enclosure of gate
melmplPoly	\$nmos_meImplPoly	private	Minimum implant enclosure of poly on field
melmplCont	\$nmos_meImplCont	private	Minimum implant enclosure of contact
meWellDiff	\$nmos_meWellDiff	private	Minimum well enclosure of diffusion

CDF gpdk180 nmos3

include macro mos3n

CDF siminfo

simulator	auLvs
componentName	"nmos"

CDF siminfo

simulator	auCdl
componentName	nil
modelName	"nmos"

CDF cellview

symbol	pas_std	nmos3	symbol
auCdl	pas_std	nmos3	spectre
auLvs	pas_std	nmos3	spectre
spectre	pas_std	nmos3	spectre
ads	pas_std	nmos3	spectre

nmos

CDF gpdk180 nmos

CDF device params

grid	\$grid	private	MFG Grid
scale	1.0e-6	private	Dimension scale factor
model	"nmos1"	private	Device Model Name
category	"mos"	private	Library Manager Category
perimGateEdge	\$mos_perimGateEdge	private	Use four sides to calculate ps, pd
aggregateFingers	\$mos_aggregateFingers	private	Aggregate fingers: affects simM, as,ad,ps,pd

CDF device params

ODI acvioc pai	<u> </u>		
dwGate	2.0	private	Default channel width
mwGate	{2A}	private	Minimum channel Width
xwGate	50.00	private	Maximum channel Width
mlGate	{5A}	private	Minimum channel Length
msDiff	\$mos_msDiff	private	Minimum diffusion spacing
mxGate	\$mos_mxGate	private	Minimum gate extension
msGate	\$mos_msGate	private	Minimum gate spacing
meDiffGate	\$mos_meDiffGate	private	Minimum diffusion enclosure of gate
msGateCont	\$mos_msGateCont	private	Minimum gate to contact spacing
mwPoly	\$mos_mwPoly	private	MInimum poly width on field
msDiffPoly	\$mos_msDiffPoly	private	Minimum diffusion to poly spacing
mwCont	\$mos_mwCont	private	Minimum contact width
msCont	\$mos_msCont	private	Minimum contact spacing
masCont	\$mos_masCont	private	Minimum contact array spacing
meDiffCont	\$mos_meDiffCont	private	Minimum diffusion enclosure of contact
meeDiffCont	\$mos_meeDiffCont	private	Minimum diffusion end enclosure of contact
mePolyCont	\$mos_mePolyCont	private	Minimum poly enclusre of contact
meePolyCont	\$mos_meePolyCont	private	Minimum poly end enclosure of contact
mwM1	\$mos_mwM1	private	Minimum metal 1 width
msM1	\$mos_msM1	private	Minimum metal 1 spacing
meM1Cont	\$mos_meM1Cont	private	Minimum metal 1 enclosure of contact
meeM1Cont	\$mos_meeM1Cont	private	Minimum metal 1 end enclosure of contact

melmplDiff	\$nmos_meImplDiff	private	Minimum implant enclosure of diffusion
melmplGate	\$nmos_meImplGate	private	Minimum implant enclosure of gate
meelmplGate	\$nmos_meeImplGate	private	Minimum implant end enclosure of gate
melmplPoly	\$nmos_meImplPoly	private	Minimum implant enclosure of poly on field
melmplCont	\$nmos_meImplCont	private	Minimum implant enclosure of contact
meWellDiff	\$nmos_meWellDiff	private	Minimum well enclosure of diffusion

CDF gpdk180 nmos

include macro mos4

CDF siminfo

simulator	auLvs
componentName	"\$cell"

CDF siminfo

simulator	auCdl
componentName	nil
modelName	"nmos"

CDF cellview

symbol	pas_std	nmos4	symbol	
auCdl	pas_std	nmos4	symbol	
auLvs	pas_std	nmos4	symbol	
spectre	pas_std	nmos4	symbol	
ads	pas_std	nmos4	symbol	
ivpcell	\$lib	\$cell	symbol	5

ODI acvioc pai	ue		
msTapDiff	\$tap_nmos_msTapDiff	private	Minimum tap diffusion to device diffusion spacing
msTapImpI	\$tap_nmos_msTapImpI	private	Minimum tap diffusion to device implant spacing
meTimpTap	\$tap_nmos_meTimpTap	private	Minimum tap implant enclosure of tap diffusion
msTimpDiff	\$tap_nmos_msTimpDiff	private	Minimum tap implant to device diffusion spacing
msTimpImpI	\$tap_nmos_msTimpImpI	private	Minimum tap implant to device implant spacing
таТар	\$tap_nmos_maTap	private	Minimum tap diffusion area
maTimp	\$tap_nmos_maTimp	private	Minimum tap implant area
meWellTap	\$tap_nmos_meWellTap	private	Minimum well enclosure of tap diffusion
mwltap	\$tap_nmos_mwltap	private	Minimum width of integrated tap
msItapGate	\$tap_nmos_msItapGate	private	Minimum integrated tap spacing to gate
maltimp	\$tap_nmos_maltimp	private	Minimum integrated tap implant area
meltimpltap	\$tap_nmos_meltimpItap	private	Minimum integrated tap implant enclosure of tap
meltimpCont	\$tap_nmos_meltimpCont	private	Minimum integrated tap enclosure of contact

nmos3hv

CDF gpdk180 nmos3hv

CDF device params

grid	\$grid	private	MFG Grid
scale	1.0e-6	private	Dimension scale factor
model	"nmos25"	private	Device Model Name
category	"mos"	private	Library Manager Category
perimGateEdge	\$mos_perimGateEdge	private	Use four sides to calculate ps, pd
aggregateFingers	\$mos_aggregateFingers	private	Aggregate fingers: affects simM, as,ad,ps,pd

CDF device params

2.0	private	Default channel width
{2A}	private	Minimum channel Width
50.00	private	Maximum channel Width
{5A}	private	Minimum channel Length
\$mos_msDiff	private	Minimum diffusion spacing
\$mos_mxGate	private	Minimum gate extension
\$mos_msGate	private	Minimum gate spacing
\$mos_meDiffGate	private	Minimum diffusion enclosure of gate
\$mos_msGateCont	private	Minimum gate to contact spacing
\$mos_mwPoly	private	MInimum poly width on field
\$mos_msDiffPoly	private	Minimum diffusion to poly spacing
\$mos_mwCont	private	Minimum contact width
\$mos_msCont	private	Minimum contact spacing
\$mos_masCont	private	Minimum contact array spacing
\$mos_meDiffCont	private	Minimum diffusion enclosure of contact
\$mos_meeDiffCont	private	Minimum diffusion end enclosure of contact
\$mos_mePolyCont	private	Minimum poly enclusre of contact
\$mos_meePolyCont	private	Minimum poly end enclosure of contact
\$mos_mwM1	private	Minimum metal 1 width
\$mos_msM1	private	Minimum metal 1 spacing
\$mos_meM1Cont	private	Minimum metal 1 enclosure of contact
\$mos_meeM1Cont	private	Minimum metal 1 end enclosure of contact
	\$\{2A\} \$50.00 \$\{5A\} \$\mos_msDiff \$\mos_msGate \$\mos_msGate \$\mos_msGateCont \$\mos_msDiffPoly \$\mos_msDiffPoly \$\mos_msCont \$\mos_msCont \$\mos_meDiffCont \$\mos_meDiffCont \$\mos_mePolyCont \$\mos_mePolyCont \$\mos_mePolyCont \$\mos_meNos_meNos_meNos_meNos_meNos_meNos_meNos_meNos_meNos_meNos_meNos_meNos_meNos_meNos_meNos_meNos_meNos_meNos_meM1 \$\mos_msM1 \$\mos_msM1 \$\mos_meM1Cont	\$\{2A\} private \$50.00 private \$\{5A\} private

melmplDiff	\$nmos_meImplDiff	private	Minimum implant enclosure of diffusion
melmplGate	\$nmos_meImplGate	private	Minimum implant enclosure of gate
meelmplGate	\$nmos_meeImplGate	private	Minimum implant end enclosure of gate
melmplPoly	\$nmos_meImplPoly	private	Minimum implant enclosure of poly on field
melmplCont	\$nmos_meImplCont	private	Minimum implant enclosure of contact
meWellDiff	\$nmos_meWellDiff	private	Minimum well enclosure of diffusion
meVoltDiff	\$nmos_meVoltDiff	private	Minimum thick oxide enclosure of thin oxide diffusion
meVoltImpl	\$nmos_meVoltImpl	private	Minimum thick oxide enclosure of implant

CDF gpdk180 nmos3hv

include macro mos3n

CDF siminfo

simulator	auLvs
componentName	"nmoshv"

CDF siminfo

simulator	auCdl
componentName	nil
modelName	"nmoshv"

CDF cellview

symbol	pas_std	nmos3_hv	symbol
auCdl	pas_std	nmos3_hv	spectre
auLvs	pas_std	nmos3_hv	spectre
spectre	pas_std	nmos3_hv	spectre
ads	pas_std	nmos3_hv	spectre

nmoshv

CDF gpdk180 nmoshv

CDF device params

grid	\$grid	private	MFG Grid
scale	1.0e-6	private	Dimension scale factor
model	"nmos25"	private	Device Model Name
category	"mos"	private	Library Manager Category
perimGateEdge	\$mos_perimGateEdge	private	Use four sides to calculate ps, pd
aggregateFingers	\$mos_aggregateFingers	private	Aggregate fingers: affects simM, as,ad,ps,pd

CDF device params

ODI acvioc pai	<u> </u>		
dwGate	2.0	private	Default channel width
mwGate	{2A}	private	Minimum channel Width
xwGate	50.00	private	Maximum channel Width
mlGate	{5A}	private	Minimum channel Length
msDiff	\$mos_msDiff	private	Minimum diffusion spacing
mxGate	\$mos_mxGate	private	Minimum gate extension
msGate	\$mos_msGate	private	Minimum gate spacing
meDiffGate	\$mos_meDiffGate	private	Minimum diffusion enclosure of gate
msGateCont	\$mos_msGateCont	private	Minimum gate to contact spacing
mwPoly	\$mos_mwPoly	private	MInimum poly width on field
msDiffPoly	\$mos_msDiffPoly	private	Minimum diffusion to poly spacing
mwCont	\$mos_mwCont	private	Minimum contact width
msCont	\$mos_msCont	private	Minimum contact spacing
masCont	\$mos_masCont	private	Minimum contact array spacing
meDiffCont	\$mos_meDiffCont	private	Minimum diffusion enclosure of contact
meeDiffCont	\$mos_meeDiffCont	private	Minimum diffusion end enclosure of contact
mePolyCont	\$mos_mePolyCont	private	Minimum poly enclusre of contact
meePolyCont	\$mos_meePolyCont	private	Minimum poly end enclosure of contact
mwM1	\$mos_mwM1	private	Minimum metal 1 width
msM1	\$mos_msM1	private	Minimum metal 1 spacing
meM1Cont	\$mos_meM1Cont	private	Minimum metal 1 enclosure of contact
meeM1Cont	\$mos_meeM1Cont	private	Minimum metal 1 end enclosure of contact

melmplDiff	\$nmos_meImplDiff	private	Minimum implant enclosure of diffusion
melmplGate	\$nmos_meImplGate	private	Minimum implant enclosure of gate
meelmplGate	\$nmos_meeImplGate	private	Minimum implant end enclosure of gate
melmplPoly	\$nmos_meImplPoly	private	Minimum implant enclosure of poly on field
melmplCont	\$nmos_meImplCont	private	Minimum implant enclosure of contact
meWellDiff	\$nmos_meWellDiff	private	Minimum well enclosure of diffusion
meVoltDiff	\$nmos_meVoltDiff	private	Minimum thick oxide enclosure of thin oxide diffusion
meVoltImpl	\$nmos_meVoltImpl	private	Minimum thick oxide enclosure of implant

CDF gpdk180 nmoshv

include macro mos4

CDF siminfo

simulator	auLvs
componentName	"\$cell"

CDF siminfo

simulator	auCdl
componentName	nil
modelName	"\$cell"

CDF cellview

symbol	pas_std	nmos4_hv	symbol	
auCdl	pas_std	nmos4_hv	symbol	
auLvs	pas_std	nmos4_hv	symbol	
spectre	pas_std	nmos4_hv	symbol	
ads	pas_std	nmos4_hv	symbol	
ivpcell	\$lib	\$cell	symbol	5

\$tap_nmos_msTapDiff	private	Minimum tap diffusion to device diffusion spacing
\$tap_nmos_msTapImpI	private	Minimum tap diffusion to device implant spacing
\$tap_nmos_meTimpTap	private	Minimum tap implant enclosure of tap diffusion
\$tap_nmos_msTimpDiff	private	Minimum tap implant to device diffusion spacing
\$tap_nmos_msTimpImpI	private	Minimum tap implant to device implant spacing
\$tap_nmos_maTap	private	Minimum tap diffusion area
\$tap_nmos_maTimp	private	Minimum tap implant area
\$tap_nmos_meWellTap	private	Minimum well enclosure of tap diffusion
\$tap_nmos_mwltap	private	Minimum width of integrated tap
\$tap_nmos_msItapGate	private	Minimum integrated tap spacing to gate
\$tap_nmos_maltimp	private	Minimum integrated tap implant area
\$tap_nmos_meltimpItap	private	Minimum integrated tap implant enclosure of tap
\$tap_nmos_meltimpCont	private	Minimum integrated tap enclosure of contact
\$tap_nmos_msTapVolt	public	Minimum tap diffusion to thick oxide spacing
	\$tap_nmos_msTapImpI \$tap_nmos_meTimpTap \$tap_nmos_msTimpDiff \$tap_nmos_msTimpImpI \$tap_nmos_maTap \$tap_nmos_maTimp \$tap_nmos_meWellTap \$tap_nmos_mwltap \$tap_nmos_msItapGate \$tap_nmos_maItimp \$tap_nmos_meltimpItap \$tap_nmos_meltimpItap	\$tap_nmos_msTapImpl private \$tap_nmos_meTimpTap private \$tap_nmos_msTimpDiff private \$tap_nmos_msTimpImpl private \$tap_nmos_maTap private \$tap_nmos_maTimp private \$tap_nmos_meWellTap private \$tap_nmos_meWellTap private \$tap_nmos_mwltap private \$tap_nmos_msItapGate private \$tap_nmos_msItapGate private \$tap_nmos_meltimpl private \$tap_nmos_meltimpl private \$tap_nmos_meltimpl private \$tap_nmos_meltimpl private \$tap_nmos_meltimpCont private

CDF gpdk180 nmosrf

nmosrf

CDF device params

grid	\$grid	private	MFG Grid
scale	1.0e-6	private	Dimension scale factor
model	"nmos_rf"	private	Device Model Name
category	"mos"	private	Library Manager Category
perimGateEdge	\$mos_perimGateEdge	private	Use four sides to calculate ps, pd
aggregateFingers	\$mos_aggregateFingers	private	Aggregate fingers: affects simM, as,ad,ps,pd

CDF device params

CDF device para	anio		
dwGate	2.0	private	Default channel width
mwGate	1.5	private	Minimum channel Width
xwGate	10.00	private	Maximum channel Width
mlGate	0.18	private	Minimum channel Length
xIGate	0.5	private	Maximum channel Length
xnFingers	64	private	Maximum number of fingers
msDiff	\$mos_msDiff	private	Minimum diffusion spacing
mxGate	\$mos_mxGate	private	Minimum gate extension
msGate	\$mos_msGate	private	Minimum gate spacing
meDiffGate	\$mos_meDiffGate	private	Minimum diffusion enclosure of gate
msGateCont	\$mos_msGateCont	private	Minimum gate to contact spacing
mwPoly	\$mos_mwPoly	private	MInimum poly width on field
msDiffPoly	\$mos_msDiffPoly	private	Minimum diffusion to poly spacing
mwCont	\$mos_mwCont	private	Minimum contact width
msCont	\$mos_msCont	private	Minimum contact spacing
masCont	\$mos_masCont	private	Minimum contact array spacing
meDiffCont	\$mos_meDiffCont	private	Minimum diffusion enclosure of contact
meeDiffCont	\$mos_meeDiffCont	private	Minimum diffusion end enclosure of contact
mePolyCont	\$mos_mePolyCont	private	Minimum poly enclusre of contact
meePolyCont	\$mos_meePolyCont	private	Minimum poly end enclosure of contact
mwM1	\$mos_mwM1	private	Minimum metal 1 width
msM1	\$mos_msM1	private	Minimum metal 1 spacing
meM1Cont	\$mos_meM1Cont	private	Minimum metal 1 enclosure of contact
meeM1Cont	\$mos_meeM1Cont	private	Minimum metal 1 end enclosure of contact

melmplDiff	\$nmos_meImplDiff	private	Minimum implant enclosure of diffusion
melmplGate	\$nmos_meImplGate	private	Minimum implant enclosure of gate
meelmplGate	\$nmos_meeImplGate	private	Minimum implant end enclosure of gate
melmplPoly	\$nmos_meImplPoly	private	Minimum implant enclosure of poly on field
melmplCont	\$nmos_meImplCont	private	Minimum implant enclosure of contact
meWellDiff	\$nmos_meWellDiff	private	Minimum well enclosure of diffusion
meVoltDiff	\$nmos_meVoltDiff	private	Minimum thick oxide enclosure of thin oxide diffusion
meVoltImpl	\$nmos_meVoltImpl	private	Minimum thick oxide enclosure of implant

CDF gpdk180 nmosrf

include macro mos4RF

CDF siminfo

simulator	auLvs
componentName	"\$cell"

CDF siminfo

simulator	auCdl
componentName	nil
modelName	"\$cell"

CDF cellview

symbol	baseline_gpdk	nmosrf	symbol	
auCdl	baseline_gpdk	nmosrf	symbol	
auLvs	baseline_gpdk	nmosrf	symbol	
spectre	baseline_gpdk	nmosrf	symbol	
ads	baseline_gpdk	nmosrf	symbol	
ivpcell	\$lib	\$cell	symbol	5

\$tap_nmos_msTapDiff	private	Minimum tap diffusion to device diffusion spacing
\$tap_nmos_msTapImpI	private	Minimum tap diffusion to device implant spacing
\$tap_nmos_meTimpTap	private	Minimum tap implant enclosure of tap diffusion
\$tap_nmos_msTimpDiff	private	Minimum tap implant to device diffusion spacing
\$tap_nmos_msTimpImpI	private	Minimum tap implant to device implant spacing
\$tap_nmos_maTap	private	Minimum tap diffusion area
\$tap_nmos_maTimp	private	Minimum tap implant area
\$tap_nmos_meWellTap	private	Minimum well enclosure of tap diffusion
\$tap_nmos_mwltap	private	Minimum width of integrated tap
\$tap_nmos_msItapGate	private	Minimum integrated tap spacing to gate
\$tap_nmos_maltimp	private	Minimum integrated tap implant area
\$tap_nmos_meltimpItap	private	Minimum integrated tap implant enclosure of tap
\$tap_nmos_meltimpCont	private	Minimum integrated tap enclosure of contact
\$tap_nmos_msTapVolt	public	Minimum tap diffusion to thick oxide spacing
	\$tap_nmos_msTapImpI \$tap_nmos_meTimpTap \$tap_nmos_msTimpDiff \$tap_nmos_msTimpImpI \$tap_nmos_maTap \$tap_nmos_maTimp \$tap_nmos_meWellTap \$tap_nmos_mwltap \$tap_nmos_msItapGate \$tap_nmos_maItimp \$tap_nmos_meltimpItap \$tap_nmos_meltimpItap	\$tap_nmos_msTapImpl private \$tap_nmos_meTimpTap private \$tap_nmos_msTimpDiff private \$tap_nmos_msTimpImpl private \$tap_nmos_maTap private \$tap_nmos_maTimp private \$tap_nmos_meWellTap private \$tap_nmos_meWellTap private \$tap_nmos_mwltap private \$tap_nmos_msItapGate private \$tap_nmos_msItapGate private \$tap_nmos_meltimpl private \$tap_nmos_meltimpl private \$tap_nmos_meltimpl private \$tap_nmos_meltimpl private \$tap_nmos_meltimpCont private

pmos3

CDF gpdk180 pmos3

CDF device params

grid	\$grid	private	MFG Grid
scale	1.0e-6	private	Dimension scale factor
model	"pmos1"	private	Device Model Name
category	"mos"	private	Library Manager Category
perimGateEdge	\$mos_perimGateEdge	private	Use four sides to calculate ps, pd
aggregateFingers	\$mos_aggregateFingers	private	Aggregate fingers: affects simM, as,ad,ps,pd

CDF device params

CET GOVICE PAIN			
dwGate	2.0	private	Default channel width
mwGate	{2A}	private	Minimum channel Width
xwGate	50.00	private	Maximum channel Width
mlGate	{5A}	private	Minimum channel Length
msDiff	\$mos_msDiff	private	Minimum diffusion spacing
mxGate	\$mos_mxGate	private	Minimum gate extension
msGate	\$mos_msGate	private	Minimum gate spacing
meDiffGate	\$mos_meDiffGate	private	Minimum diffusion enclosure of gate
msGateCont	\$mos_msGateCont	private	Minimum gate to contact spacing
mwPoly	\$mos_mwPoly	private	MInimum poly width on field
msDiffPoly	\$mos_msDiffPoly	private	Minimum diffusion to poly spacing
mwCont	\$mos_mwCont	private	Minimum contact width
msCont	\$mos_msCont	private	Minimum contact spacing
masCont	\$mos_masCont	private	Minimum contact array spacing
meDiffCont	\$mos_meDiffCont	private	Minimum diffusion enclosure of contact
meeDiffCont	\$mos_meeDiffCont	private	Minimum diffusion end enclosure of contact
mePolyCont	\$mos_mePolyCont	private	Minimum poly enclusre of contact
meePolyCont	\$mos_meePolyCont	private	Minimum poly end enclosure of contact
mwM1	\$mos_mwM1	private	Minimum metal 1 width
msM1	\$mos_msM1	private	Minimum metal 1 spacing
meM1Cont	\$mos_meM1Cont	private	Minimum metal 1 enclosure of contact
meeM1Cont	\$mos_meeM1Cont	private	Minimum metal 1 end enclosure of contact

melmplDiff	\$pmos_meImplDiff	private	Minimum implant enclosure of diffusion
melmplGate	\$pmos_meImplGate	private	Minimum implant enclosure of gate
meelmplGate	\$pmos_meeImplGate	private	Minimum implant end enclosure of gate
melmplPoly	\$pmos_meImplPoly	private	Minimum implant enclosure of poly on field
melmplCont	\$pmos_meImplCont	private	Minimum implant enclosure of contact
meWellDiff	\$pmos_meWellDiff	private	Minimum well enclosure of diffusion

CDF gpdk180 pmos3

include macro mos3p

CDF siminfo

simulator	auLvs
componentName	"pmos"

CDF siminfo

simulator	auCdl
componentName	nil
modelName	"pmos"

CDF cellview

symbol	pas_std	pmos3	symbol
auCdl	pas_std	pmos3	spectre
auLvs	pas_std	pmos3	spectre
spectre	pas_std	pmos3	spectre
ads	pas_std	pmos3	spectre

pmos

CDF gpdk180 pmos

CDF device params

grid	\$grid	private	MFG Grid
scale	1.0e-6	private	Dimension scale factor
model	"pmos1"	private	Device Model Name
category	"mos"	private	Library Manager Category
perimGateEdge	\$mos_perimGateEdge	private	Use four sides to calculate ps, pd
aggregateFingers	\$mos_aggregateFingers	private	Aggregate fingers: affects simM, as,ad,ps,pd

CDF device params

CDF device par	ams		
dwGate	2.0	private	Default channel width
mwGate	{2A}	private	Minimum channel Width
xwGate	50.00	private	Maximum channel Width
mlGate	{5A}	private	Minimum channel Length
msDiff	\$mos_msDiff	private	Minimum diffusion spacing
mxGate	\$mos_mxGate	private	Minimum gate extension
msGate	\$mos_msGate	private	Minimum gate spacing
meDiffGate	\$mos_meDiffGate	private	Minimum diffusion enclosure of gate
msGateCont	\$mos_msGateCont	private	Minimum gate to contact spacing
mwPoly	\$mos_mwPoly	private	MInimum poly width on field
msDiffPoly	\$mos_msDiffPoly	private	Minimum diffusion to poly spacing
mwCont	\$mos_mwCont	private	Minimum contact width
msCont	\$mos_msCont	private	Minimum contact spacing
masCont	\$mos_masCont	private	Minimum contact array spacing
meDiffCont	\$mos_meDiffCont	private	Minimum diffusion enclosure of contact
meeDiffCont	\$mos_meeDiffCont	private	Minimum diffusion end enclosure of contact
mePolyCont	\$mos_mePolyCont	private	Minimum poly enclusre of contact
meePolyCont	\$mos_meePolyCont	private	Minimum poly end enclosure of contact
mwM1	\$mos_mwM1	private	Minimum metal 1 width
msM1	\$mos_msM1	private	Minimum metal 1 spacing
meM1Cont	\$mos_meM1Cont	private	Minimum metal 1 enclosure of contact
meeM1Cont	\$mos_meeM1Cont	private	Minimum metal 1 end enclosure of contact

melmplDiff	\$pmos_meImplDiff	private	Minimum implant enclosure of diffusion
melmplGate	\$pmos_meImplGate	private	Minimum implant enclosure of gate
meelmplGate	\$pmos_meeImplGate	private	Minimum implant end enclosure of gate
melmplPoly	\$pmos_meImplPoly	private	Minimum implant enclosure of poly on field
melmplCont	\$pmos_meImplCont	private	Minimum implant enclosure of contact
meWellDiff	\$pmos_meWellDiff	private	Minimum well enclosure of diffusion

CDF gpdk180 pmos

include macro mos4

CDF siminfo

simulator	auLvs
componentName	"\$cell"

CDF siminfo

simulator	auCdl
componentName	nil
modelName	"\$cell"

CDF cellview

symbol	pas_std	pmos4	symbol	
auCdl	pas_std	pmos4	symbol	
auLvs	pas_std	pmos4	symbol	
spectre	pas_std	pmos4	symbol	
ads	pas_std	pmos4	symbol	
ivpcell	\$lib	\$cell	symbol	5

\$tap_pmos_msTapDiff	private	Minimum tap diffusion to device diffusion spacing
\$tap_pmos_msTapImpI	private	Minimum tap diffusion to device implant spacing
\$tap_pmos_meTimpTap	private	Minimum tap implant enclosure of tap diffusion
\$tap_pmos_msTimpDiff	private	Minimum tap implant to device diffusion spacing
\$tap_pmos_msTimpImpI	private	Minimum tap implant to device implant spacing
\$tap_pmos_maTap	private	Minimum tap diffusion area
\$tap_pmos_maTimp	private	Minimum tap implant area
\$tap_pmos_meWellTap	private	Minimum well enclosure of tap diffusion
\$tap_pmos_mwltap	private	Minimum width of integrated tap
\$tap_pmos_msItapGate	private	Minimum integrated tap spacing to gate
\$tap_pmos_maltimp	private	Minimum integrated tap implant area
\$tap_pmos_meltimpItap	private	Minimum integrated tap implant enclosure of tap
\$tap_pmos_meltimpCont	private	Minimum integrated tap enclosure of contact
\$tap_pmos_msTapVolt	public	Minimum tap diffusion to thick oxide spacing
	\$tap_pmos_msTapImpI \$tap_pmos_meTimpTap \$tap_pmos_msTimpDiff \$tap_pmos_msTimpImpI \$tap_pmos_maTap \$tap_pmos_maTimp \$tap_pmos_meWellTap \$tap_pmos_mwltap \$tap_pmos_msItapGate \$tap_pmos_maItimp \$tap_pmos_meltimpItap \$tap_pmos_meltimpItap	\$tap_pmos_msTapImpl private \$tap_pmos_meTimpTap private \$tap_pmos_msTimpDiff private \$tap_pmos_msTimpImpl private \$tap_pmos_maTap private \$tap_pmos_maTimp private \$tap_pmos_meWellTap private \$tap_pmos_meWellTap private \$tap_pmos_msItapGate private \$tap_pmos_msItapGate private \$tap_pmos_meltimp private \$tap_pmos_meltimpltap private \$tap_pmos_meltimpltap private \$tap_pmos_meltimpltap private

pmos3hv

CDF gpdk180 pmos3hv

CDF device params

grid	\$grid	private	MFG Grid
scale	1.0e-6	private	Dimension scale factor
model	"pmos25"	private	Device Model Name
category	"mos"	private	Library Manager Category
perimGateEdge	\$mos_perimGateEdge	private	Use four sides to calculate ps, pd
aggregateFingers	\$mos_aggregateFingers	private	Aggregate fingers: affects simM, as,ad,ps,pd

CDF device params

CDF device par	ans		
dwGate	2.0	private	Default channel width
mwGate	{2A}	private	Minimum channel Width
xwGate	50.00	private	Maximum channel Width
mlGate	{5A}	private	Minimum channel Length
msDiff	\$mos_msDiff	private	Minimum diffusion spacing
mxGate	\$mos_mxGate	private	Minimum gate extension
msGate	\$mos_msGate	private	Minimum gate spacing
meDiffGate	\$mos_meDiffGate	private	Minimum diffusion enclosure of gate
msGateCont	\$mos_msGateCont	private	Minimum gate to contact spacing
mwPoly	\$mos_mwPoly	private	MInimum poly width on field
msDiffPoly	\$mos_msDiffPoly	private	Minimum diffusion to poly spacing
mwCont	\$mos_mwCont	private	Minimum contact width
msCont	\$mos_msCont	private	Minimum contact spacing
masCont	\$mos_masCont	private	Minimum contact array spacing
meDiffCont	\$mos_meDiffCont	private	Minimum diffusion enclosure of contact
meeDiffCont	\$mos_meeDiffCont	private	Minimum diffusion end enclosure of contact
mePolyCont	\$mos_mePolyCont	private	Minimum poly enclusre of contact
meePolyCont	\$mos_meePolyCont	private	Minimum poly end enclosure of contact
mwM1	\$mos_mwM1	private	Minimum metal 1 width
msM1	\$mos_msM1	private	Minimum metal 1 spacing
meM1Cont	\$mos_meM1Cont	private	Minimum metal 1 enclosure of contact
meeM1Cont	\$mos_meeM1Cont	private	Minimum metal 1 end enclosure of contact

melmplDiff	\$pmos_meImplDiff	private	Minimum implant enclosure of diffusion
melmplGate	\$pmos_meImplGate	private	Minimum implant enclosure of gate
meelmplGate	\$pmos_meeImplGate	private	Minimum implant end enclosure of gate
melmplPoly	\$pmos_meImplPoly	private	Minimum implant enclosure of poly on field
melmplCont	\$pmos_meImplCont	private	Minimum implant enclosure of contact
meWellDiff	\$pmos_meWellDiff	private	Minimum well enclosure of diffusion
meVoltDiff	\$pmos_meVoltDiff	private	Minimum thick oxide enclosure of thin oxide diffusion
meVoltImpl	\$pmos_meVoltImpl	private	Minimum thick oxide enclosure of implant

CDF gpdk180 pmos3hv

include macro mos3p

CDF siminfo

simulator	auLvs
componentName	"pmoshv"

CDF siminfo

simulator	auCdl
componentName	nil
modelName	"pmoshv"

CDF cellview

symbol	pas_std	pmos3_hv	symbol
auCdl	pas_std	pmos3_hv	spectre
auLvs	pas_std	pmos3_hv	spectre
spectre	pas_std	pmos3_hv	spectre
ads	pas_std	pmos3_hv	spectre

pmoshv

CDF gpdk180 pmoshv

CDF device params

grid	\$grid	private	MFG Grid
scale	1.0e-6	private	Dimension scale factor
model	"pmos25"	private	Device Model Name
category	"mos"	private	Library Manager Category
perimGateEdge	\$mos_perimGateEdge	private	Use four sides to calculate ps, pd
aggregateFingers	\$mos_aggregateFingers	private	Aggregate fingers: affects simM, as,ad,ps,pd

CDF device params

CDF device par	ans		
dwGate	2.0	private	Default channel width
mwGate	{2A}	private	Minimum channel Width
xwGate	50.0	private	Maximum channel Width
mlGate	{5A}	private	Minimum channel Length
msDiff	\$mos_msDiff	private	Minimum diffusion spacing
mxGate	\$mos_mxGate	private	Minimum gate extension
msGate	\$mos_msGate	private	Minimum gate spacing
meDiffGate	\$mos_meDiffGate	private	Minimum diffusion enclosure of gate
msGateCont	\$mos_msGateCont	private	Minimum gate to contact spacing
mwPoly	\$mos_mwPoly	private	MInimum poly width on field
msDiffPoly	\$mos_msDiffPoly	private	Minimum diffusion to poly spacing
mwCont	\$mos_mwCont	private	Minimum contact width
msCont	\$mos_msCont	private	Minimum contact spacing
masCont	\$mos_masCont	private	Minimum contact array spacing
meDiffCont	\$mos_meDiffCont	private	Minimum diffusion enclosure of contact
meeDiffCont	\$mos_meeDiffCont	private	Minimum diffusion end enclosure of contact
mePolyCont	\$mos_mePolyCont	private	Minimum poly enclusre of contact
meePolyCont	\$mos_meePolyCont	private	Minimum poly end enclosure of contact
mwM1	\$mos_mwM1	private	Minimum metal 1 width
msM1	\$mos_msM1	private	Minimum metal 1 spacing
meM1Cont	\$mos_meM1Cont	private	Minimum metal 1 enclosure of contact
meeM1Cont	\$mos_meeM1Cont	private	Minimum metal 1 end enclosure of contact

ODI acvice pare	arrio		
melmplDiff	\$pmos_meImplDiff	private	Minimum implant enclosure of diffusion
melmplGate	\$pmos_meImplGate	private	Minimum implant enclosure of gate
meelmplGate	\$pmos_meeImplGate	private	Minimum implant end enclosure of gate
melmplPoly	\$pmos_meImplPoly	private	Minimum implant enclosure of poly on field
melmplCont	\$pmos_meImplCont	private	Minimum implant enclosure of contact
meWellDiff	\$pmos_meWellDiff	private	Minimum well enclosure of diffusion
meVoltDiff	\$pmos_meVoltDiff	private	Minimum thick oxide enclosure of thin oxide diffusion
meVoltImpl	\$pmos_meVoltImpl	private	Minimum thick oxide enclosure of implant

CDF gpdk180 pmoshv

include macro mos4

CDF siminfo

simulator	auLvs
componentName	"\$cell"

CDF siminfo

simulator	auCdl
componentName	nil
modelName	"\$cell"

CDF cellview

symbol	pas_std	pmos4_hv	symbol	
auCdl	pas_std	pmos4_hv	symbol	
auLvs	pas_std	pmos4_hv	symbol	
spectre	pas_std	pmos4_hv	symbol	
ads	pas_std	pmos4_hv	symbol	
ivpcell	\$lib	\$cell	symbol	5

ODI GOTIOO Pai			
msTapDiff	\$tap_pmos_msTapDiff	private	Minimum tap diffusion to device diffusion spacing
msTapImpI	\$tap_pmos_msTapImpI	private	Minimum tap diffusion to device implant spacing
meTimpTap	\$tap_pmos_meTimpTap	private	Minimum tap implant enclosure of tap diffusion
msTimpDiff	\$tap_pmos_msTimpDiff	private	Minimum tap implant to device diffusion spacing
msTimpImpI	\$tap_pmos_msTimpImpI	private	Minimum tap implant to device implant spacing
таТар	\$tap_pmos_maTap	private	Minimum tap diffusion area
maTimp	\$tap_pmos_maTimp	private	Minimum tap implant area
meWellTap	\$tap_pmos_meWellTap	private	Minimum well enclosure of tap diffusion
mwltap	\$tap_pmos_mwltap	private	Minimum width of integrated tap
msItapGate	\$tap_pmos_msItapGate	private	Minimum integrated tap spacing to gate
maltimp	\$tap_pmos_maltimp	private	Minimum integrated tap implant area
meltimpltap	\$tap_pmos_meltimpItap	private	Minimum integrated tap implant enclosure of tap
meltimpCont	\$tap_pmos_meltimpCont	private	Minimum integrated tap enclosure of contact
msTapVolt	\$tap_pmos_msTapVolt	public	Minimum tap diffusion to thick oxide spacing

CDF gpdk180 pmosrf

pmosrf

CDF device params

grid	\$grid	private	MFG Grid
scale	1.0e-6	private	Dimension scale factor
model	"pmos_rf"	private	Device Model Name
category	"mos"	private	Library Manager Category
perimGateEdge	\$mos_perimGateEdge	private	Use four sides to calculate ps, pd
aggregateFingers	\$mos_aggregateFingers	private	Aggregate fingers: affects simM, as,ad,ps,pd

CDF device params

ans		·
2.0	private	Default channel width
1.5	private	Minimum channel Width
10.0	private	Maximum channel Width
0.18	private	Minimum channel Length
0.5	private	Maximum channel Length
64	private	Maximum number of fingers
\$mos_msDiff	private	Minimum diffusion spacing
\$mos_mxGate	private	Minimum gate extension
\$mos_msGate	private	Minimum gate spacing
\$mos_meDiffGate	private	Minimum diffusion enclosure of gate
\$mos_msGateCont	private	Minimum gate to contact spacing
\$mos_mwPoly	private	MInimum poly width on field
\$mos_msDiffPoly	private	Minimum diffusion to poly spacing
\$mos_mwCont	private	Minimum contact width
\$mos_msCont	private	Minimum contact spacing
\$mos_masCont	private	Minimum contact array spacing
\$mos_meDiffCont	private	Minimum diffusion enclosure of contact
\$mos_meeDiffCont	private	Minimum diffusion end enclosure of contact
\$mos_mePolyCont	private	Minimum poly enclusre of contact
\$mos_meePolyCont	private	Minimum poly end enclosure of contact
\$mos_mwM1	private	Minimum metal 1 width
\$mos_msM1	private	Minimum metal 1 spacing
\$mos_meM1Cont	private	Minimum metal 1 enclosure of contact
\$mos_meeM1Cont	private	Minimum metal 1 end enclosure of contact
	2.0 1.5 10.0 0.18 0.5 64 \$mos_msDiff \$mos_mxGate \$mos_msGate \$mos_meDiffGate \$mos_mwPoly \$mos_msDiffPoly \$mos_msCont \$mos_msCont \$mos_meDiffCont \$mos_meDiffCont \$mos_meDiffCont \$mos_mePolyCont \$mos_mePolyCont \$mos_mePolyCont \$mos_meM1 \$mos_msM1 \$mos_meM1Cont	2.0 private 1.5 private 10.0 private 0.18 private 0.5 private 64 private \$mos_msDiff private \$mos_mxGate private \$mos_msGate private \$mos_msGate private \$mos_msGate private \$mos_msGate private \$mos_msGateCont private \$mos_msGateCont private \$mos_msGateCont private \$mos_msCont private \$mos_msDiffPoly private \$mos_msCont private \$mos_msCont private \$mos_msCont private \$mos_meDiffCont private \$mos_meDiffCont private \$mos_mePolyCont private \$mos_mePolyCont private \$mos_mePolyCont private \$mos_msCont private \$mos_mePolyCont private \$mos_mePolyCont private \$mos_mePolyCont private \$mos_msM1 private \$mos_msM1 private \$mos_meM1Cont private

melmplDiff	\$pmos_meImplDiff	private	Minimum implant enclosure of diffusion
melmplGate	\$pmos_meImplGate	private	Minimum implant enclosure of gate
meelmplGate	\$pmos_meeImplGate	private	Minimum implant end enclosure of gate
melmplPoly	\$pmos_meImplPoly	private	Minimum implant enclosure of poly on field
melmplCont	\$pmos_meImplCont	private	Minimum implant enclosure of contact
meWellDiff	\$pmos_meWellDiff	private	Minimum well enclosure of diffusion
meVoltDiff	\$pmos_meVoltDiff	private	Minimum thick oxide enclosure of thin oxide diffusion
meVoltImpl	\$pmos_meVoltImpl	private	Minimum thick oxide enclosure of implant

CDF gpdk180 pmosrf

include macro mos4RF

CDF siminfo

simulator	auLvs
componentName	"\$cell"

CDF siminfo

simulator	auCdl
componentName	nil
modelName	"\$cell"

CDF cellview

symbol	baseline_gpdk	pmosrf	symbol	
auCdl	baseline_gpdk	pmosrf	symbol	
auLvs	baseline_gpdk	pmosrf	symbol	
spectre	baseline_gpdk	pmosrf	symbol	
ads	baseline_gpdk	pmosrf	symbol	
ivpcell	\$lib	\$cell	symbol	5

\$tap_pmos_msTapDiff	private	Minimum tap diffusion to device diffusion spacing
\$tap_pmos_msTapImpI	private	Minimum tap diffusion to device implant spacing
\$tap_pmos_meTimpTap	private	Minimum tap implant enclosure of tap diffusion
\$tap_pmos_msTimpDiff	private	Minimum tap implant to device diffusion spacing
\$tap_pmos_msTimpImpI	private	Minimum tap implant to device implant spacing
\$tap_pmos_maTap	private	Minimum tap diffusion area
\$tap_pmos_maTimp	private	Minimum tap implant area
\$tap_pmos_meWellTap	private	Minimum well enclosure of tap diffusion
\$tap_pmos_mwltap	private	Minimum width of integrated tap
\$tap_pmos_msItapGate	private	Minimum integrated tap spacing to gate
\$tap_pmos_maltimp	private	Minimum integrated tap implant area
\$tap_pmos_meltimpItap	private	Minimum integrated tap implant enclosure of tap
\$tap_pmos_meltimpCont	private	Minimum integrated tap enclosure of contact
\$tap_pmos_msTapVolt	public	Minimum tap diffusion to thick oxide spacing
	\$tap_pmos_msTapImpI \$tap_pmos_meTimpTap \$tap_pmos_msTimpDiff \$tap_pmos_msTimpImpI \$tap_pmos_maTap \$tap_pmos_maTimp \$tap_pmos_meWellTap \$tap_pmos_mwltap \$tap_pmos_msItapGate \$tap_pmos_maItimp \$tap_pmos_meltimpItap \$tap_pmos_meltimpItap	\$tap_pmos_msTapImpl private \$tap_pmos_meTimpTap private \$tap_pmos_msTimpDiff private \$tap_pmos_msTimpImpl private \$tap_pmos_maTap private \$tap_pmos_maTimp private \$tap_pmos_meWellTap private \$tap_pmos_meWellTap private \$tap_pmos_mwltap private \$tap_pmos_msItapGate private \$tap_pmos_maltimp private \$tap_pmos_meltimpItap private \$tap_pmos_meltimpItap private \$tap_pmos_meltimpCont private

PCells

PCell SKILL Procedures

Skill Procedures MOS Abutment

```
procedure(gpdk180_mosAbut(iA iB pA pB pASide connection event@optional(group nil)
) ;_Jun 25 03 amir 186
  let( (
         apA apB avA avB delta
         ( epsilon 0.0001 )
       )
    case( event
      ( 1 ;; compute abutment offset
        0.0
            ;; adjust pcell for abutment
        apA = pA~>abutParam
        apB = pB~>abutParam
        avA = get(iA apA)
        avB = get(iB apB)
        ;; save current state
        dbReplaceProp( group
                       sprintf(nil "abutInfo_%s" iA~>name)
                       "list"
                       list(
                         list(concat(apA) avA)
                       ) )
        dbReplaceProp( group
                       sprintf(nil "abutInfo_%s" iB~>name)
                       "list"
                       list(
                         list(concat(apB) avB)
                       ))
        case( connection
              ;; no third connection, remove both contacts
            delta = pB~>gateWidth - pA~>gateWidth
            cond(
              ( delta > epsilon
                avA = 6
                avB = 2
              ( delta < -epsilon
                avA = 2
                avB = 6
              ( t
                avA = 4
                avB = 4
            )
          ) . . .
```

Skill Procedures Metal Coverage

```
procedure(gpdk180_mtlCvgUserFunc(info)
                                                         ; Dec 13 02 amir 511
  let((dbId col cols mtlCL inc newV oldV bot top max handle)
    dbId = info->rodObj~>dbId
    col = dbId~>col
    cols = dbId~>cols
    mtlCL = evalstring(sprintf(nil "'( nil %s )" info->paramVal))
    inc = info->increment
    max = dbId~>maxLength - dbId~>minLength
    handle = concat(info->handleName)
    newV = ""
    for(i 0 cols
       oldV = get(mtlCL concat(i))
       if( i==col then
          bot = or(car(oldV) 0.0)
          top = or(cadr(oldV) 0.0)
          caseq( handle
             ( (lowerLeft lowerCenter lowerRight)
               max = max - top
               cond(
                  ( bot - inc > max
                    bot = max
                    ;; fprintf( stderr "Unable to stretch beyond limits!\n" )
                  )
                  ( bot - inc < 0
                    ;; fprintf( stderr "Unable to stretch beyond limits!\n" )
                  )
                    bot = bot - inc
               ) ; ** cond not **
             ( (upperLeft upperCenter upperRight)
               max = max - bot
               cond(
                  ( top - inc > max
                    top = max
                    ;; fprintf( stderr "Unable to stretch beyond limits!\n" )
                  (top - inc < 0)
                    top = 0
                    ;; fprintf( stderr "Unable to stretch beyond limits!\n" )
                  ( t
                    top = top - inc
               ) ; ** cond not **
          ) ; ** caseq handle **
          unless( and(zerop(top) zerop(bot))...
```

PCell Macros

mos (macro)

pcell macro mos

```
deviceProps
1: include
     scale = dpt->scale
     epsilon = grid / 10.0
     meDiffCont = dpt->meDiffCont
     meeDiffCont = dpt->meeDiffCont || meDiffCont
     meDiffGate = dpt->meDiffGate
     msGateCont = dpt->msGateCont
     msDiffPoly = dpt->msDiffPoly
     msDiffGate = dpt->msDiffGate || msDiffPoly
     mePolyCont = dpt->mePolyCont
     meePolyCont = dpt->meePolyCont || mePolyCont
     mxGate = dpt->mxGate
     msGate = dpt->msGate
     mwCont = dpt->mwCont
     msCont = dpt->msCont
     mwM1 = dpt->mwM1
     msM1 = dpt->msM1
     meM1Cont = dpt->meM1Cont
     meeM1Cont = dpt->meeM1Cont || meM1Cont
```

pcell macro mos

```
4: include
    gw = PasCeiling( (fw / scale ) grid t )
    gl = PasCeiling((I/scale) gridt)
    gc = max(fix(fingers)1)
    mlDiff = mwCont + ( 2.0 * meeDiffCont )
    dogbone = (gw + epsilon) < mlDiff
    awSDM = PasCeiling( ( sdMtlWidth / scale ) grid t )
    cntSpan = awSDM - ( 2.0 * meM1Cont )
    lwSDM = awSDM
    rwSDM = awSDM
    tmp = when( cntSpan + epsilon < mwCont
              cntSpan = mwCont
              awSDM = mwCont + ( 2.0 * meM1Cont )
              println("Assigned value for 'sdMtlWidth' is too small; value is overridden")
    leftSource = not( switchSD )
    rightSource = PasXor( evenp( gc ) switchSD )
    leftCnt = nil
    rightCnt = nil
    lwDiff = nil rwDiff = nil
    lhStep = 0 rhStep = 0
```

pcell macro mos

```
6: include
```

2: if dogbone

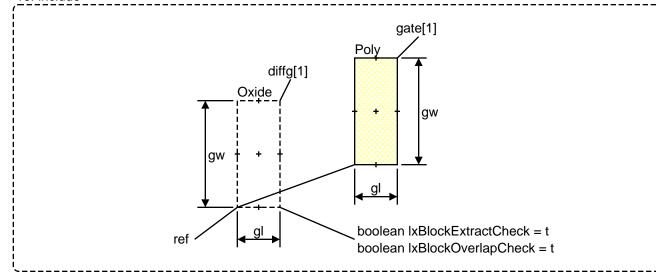
9: if leftCnt && rightCnt

```
connectS = member(connectSD list("Source" "Both"))
connectD = member(connectSD list("Drain" "Both"))
```

pcell macro mos

```
8: include
  tmp = case( leftAbut
              leftCnt = t)
     (0
     ((-1 \ 1) \ \text{leftCnt} = t)
     ( (-2 2 ) lwDiff = max( meDiffGate (msGate - msDiffPoly)) lhStep = bhStep )
     ((-33) laDC = daDC lwDiff = cntSpan + laDC leftCnt = t)
     ((-44) lwDiff = PasCeiling((msGate/2.0) grid t)
                                                       lhStep = bhStep )
     ((-55) lwDiff = daDC lhStep = bhStep)
     ((-66) lwDiff = msDiffPoly lhStep = bhStep)
     ((-77) lwDiff = max( msDiffGate ( msGateCont - meDiffCont )) lhStep = bhStep )
     ( t
              error("leftAbut parameter value out of range!") )
   tmp = case( rightAbut
              rightCnt = t)
     ((-1 1) rightCnt = t)
     ((-22) rwDiff = max(meDiffGate (msGate - msDiffPoly)) rhStep = thStep)
     ((-3 3) raDC = daDC rwDiff = cntSpan + raDC rightCnt = t)
     ( (-4 4 ) rwDiff = PasCeiling( (msGate/2.0) grid t ) rhStep = thStep )
     ((-55) rwDiff = daDC rhStep = thStep)
     ((-66) rwDiff = msDiffPoly rhStep = thStep)
     ( ( -7 7 ) rwDiff = max( msDiffGate ( msGateCont - meDiffCont ) ) rhStep = thStep )
     ( t
              error("rightAbut parameter value out of range!") )
```

10: include

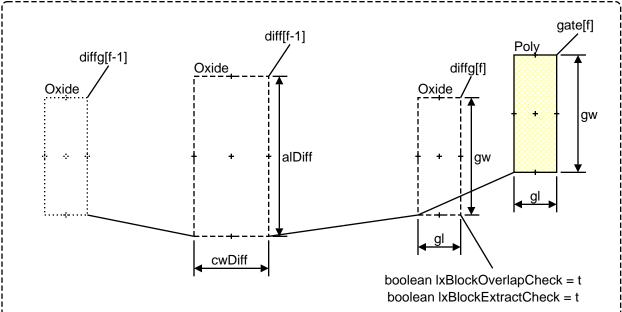


pcell macro mos

11: for f 2 gc

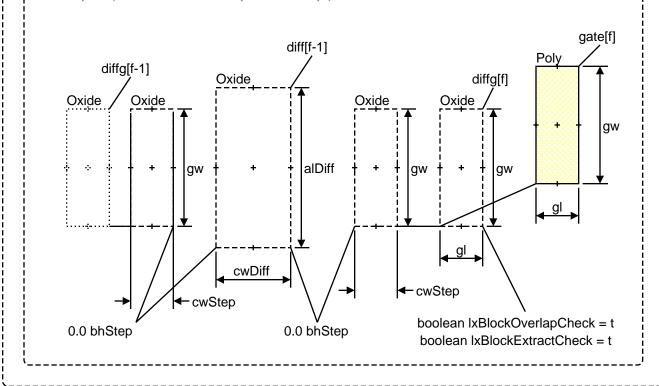
isSource = PasXor(evenp(f) switchSD) cwDiff = if(isSource then swDiff else dwDiff)

1: if not(dogbone)



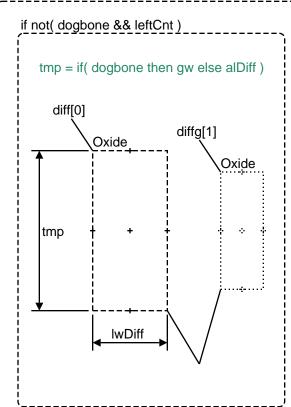
1: if dogbone

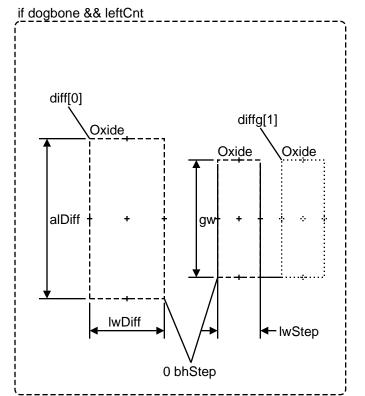
cwStep = if(isSource then swStep else dwStep)



pcell macro mos

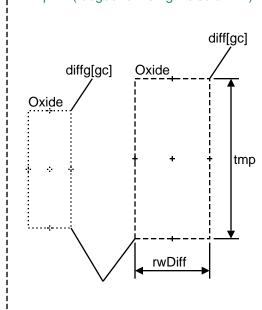
12: include

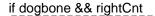


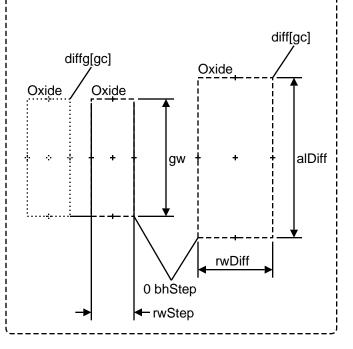


if not(dogbone && rightCnt)

tmp = if(dogbone then gw else alDiff)







diff[0] <

Oxide

Oxide

pcell macro mos

1: include

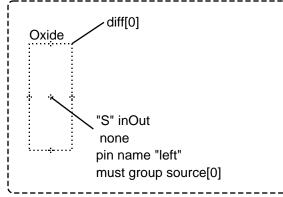
13: include

tmp = dpt->msDiff || 0.0

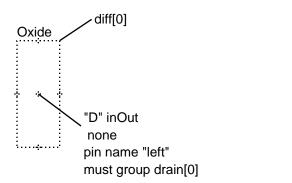
list abutAccessDir=list("left")
string abutFunction="gpdk180_mosAbut"
float gateWidth=gw
string abutParam="leftAbut"
boolean isSource=leftSource
list vxlInstSpacingDir=list("left")
float vxlInstSpacingRule=tmp

list abutAccessDir=list("right")
string abutFunction="gpdk180_mosAbut"
float gateWidth=gw
string abutParam="rightAbut"
boolean isSource=rightSource
list vxlInstSpacingDir=list("right")
float vxlInstSpacingRule=tmp

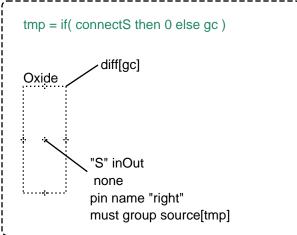
2: if leftSource



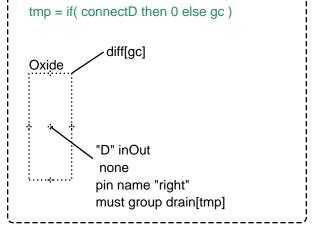




2: if rightSource



2: if not(rightSource)



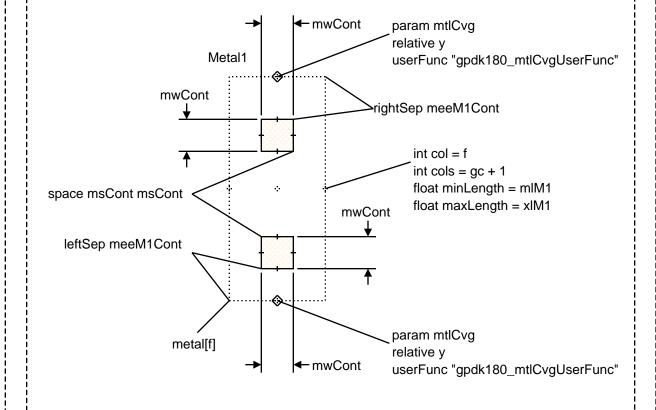
```
pcell macro mos
 14: include
      ySep = meeDiffCont - meeM1Cont
                                                   xIM1 = gw - ySep - ySep
      mIM1 = mwCont + (2.0 * meeM1Cont)
                                           mtlCL = gpdk180_evalMtlCvg( mtlCvg grid )
    for f 0 gc
        1: if ( ( f > 0 && f < gc ) || ( f == 0 && leftCnt ) || ( f == gc && rightCnt ) )
             isSource = PasXor( evenp(f) switchSD )
             delta = or( get( mtlCL concat( f ) ) list( 0.0 0.0 ) )
             botSep = ySep + car( delta )
             topSep = ySep + cadr( delta )
             alM1 = alDiff - topSep - botSep
             xSep = cond( (f == 0 laDC) (f == gc raDC) (isSource saDC) (t daDC)) - meM1Cont
             awM1 = cond((f==0 lwSDM)(f==gc rwSDM)(t awSDM))
           1: if alM1 + epsilon < mlM1
              aIM1 = xIM1
                                    botSep = ySep
              tmp = printf("Overconstraint metal coverage on column %d\n" f)
           2: if f == 0
                                                       2: if f!= 0
                  diff[f]
                                                              diff[f]
                                     ← awM1
                                                                                  -awM1
                                         Oxide
                                                              Oxide
                             Metal1
                                                                          Metal1
                  alM1
                                                                                       alM1
                                                                                metal[f]
                    metal[f]
                                xSep botSep
                                                                xSep botSep
```

pcell macro mos

mtlCvg

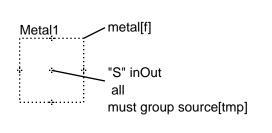
3: include

```
leftSep = cond( (f==0 meM1Cont + lwSDM - awSDM ) ( t meM1Cont ) )
rightSep = cond( (f==gc meM1Cont + rwSDM - awSDM ) ( t meM1Cont ) )
```



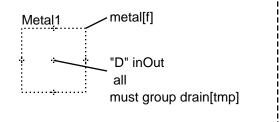
4: if isSource

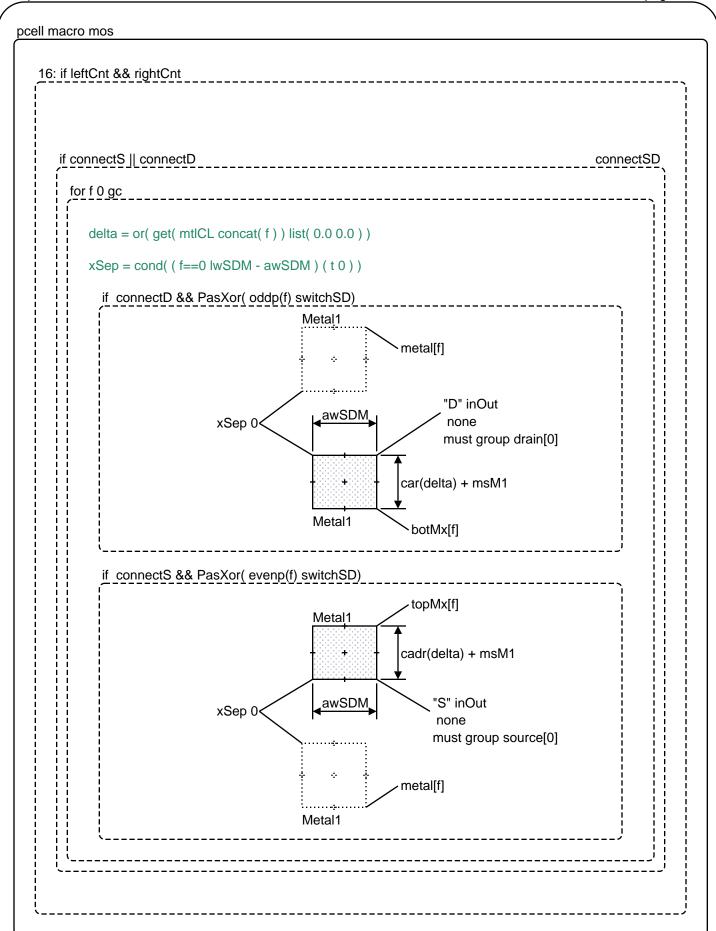
tmp = if(connectS then 0 else f)

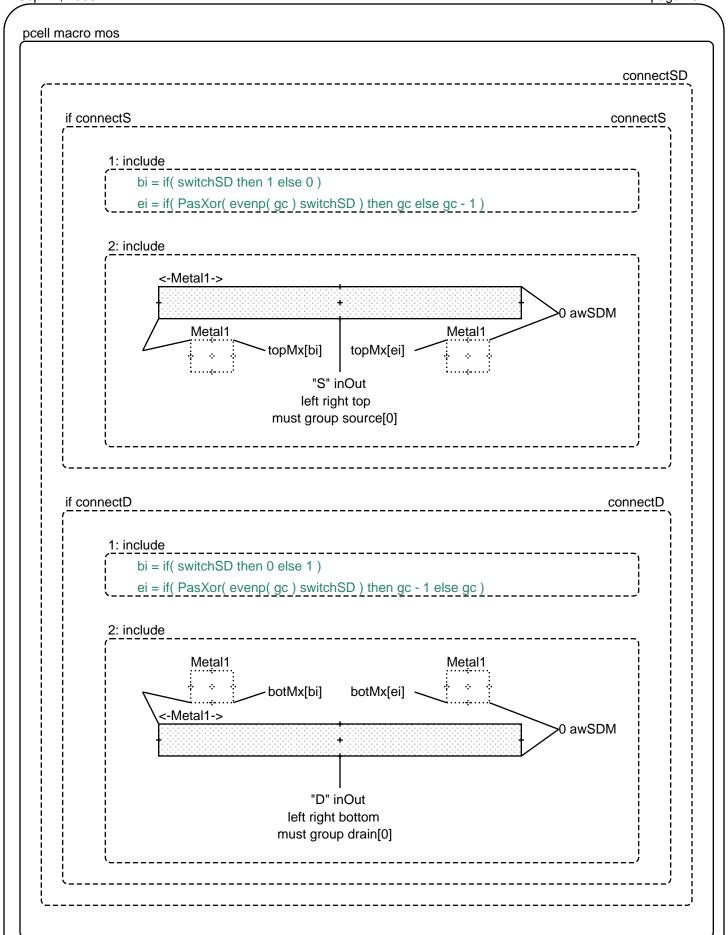


4: if not(isSource)

tmp = if(connectD then 0 else f)



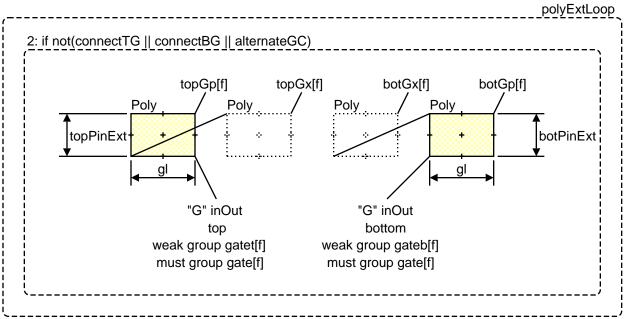




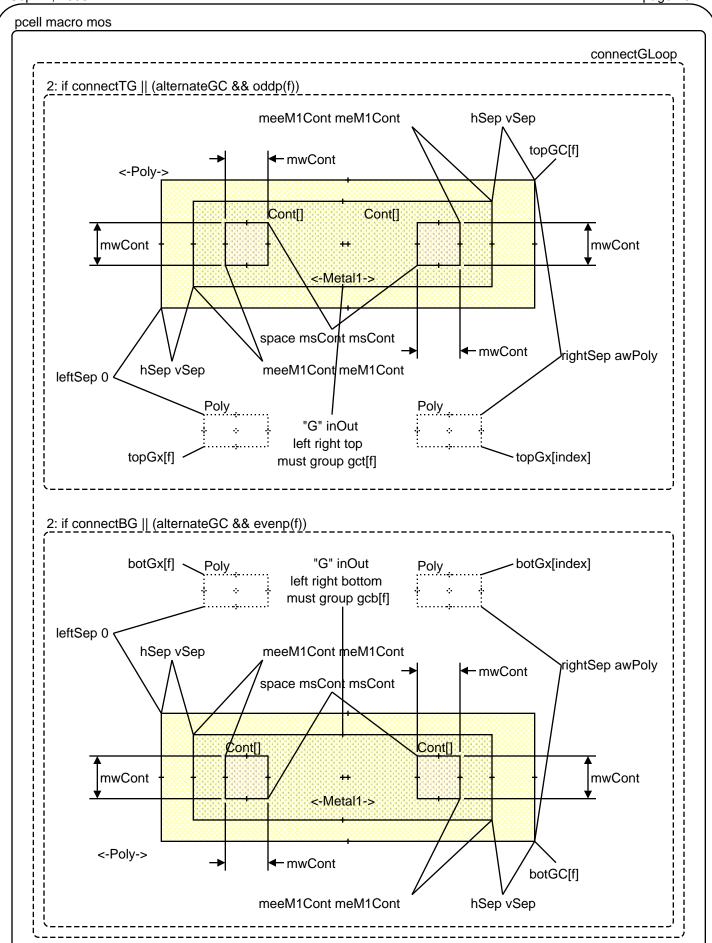
pcell macro mos

```
17: include
                                                                                                polyExt
    connectTG = member( connectGates list( "Top" "Both" ) )
    connectBG = member( connectGates list( "Bottom" "Both" ) )
    alternateGC = equal( connectGates "Alternate" )
    topExt = 0
                     botExt = 0
                                     topPinExt = 0
                                                       botPinExt = 0
    tmp = if( connectTG || alternateGC then
       topExt = msM1 + meM1Cont - mePolyCont + meeM1Cont - meeDiffCont
       when( connectS topExt = topExt + msM1 + awSDM )
       topExt = max(msDiffPoly topExt) + thStep
      else
       if( connectBG || alternateGC then topExt = mxGate
       else topExt = msDiffPoly + thStep topPinExt = max(mxGate - topExt grid))
    tmp = if( connectBG || alternateGC then
       botExt = msM1 + meM1Cont - mePolyCont + meeM1Cont - meeDiffCont
       when( connectD botExt = botExt + msM1 + awSDM )
       botExt = max(msDiffPoly botExt) + bhStep
      else
       if( connectTG || alternateGC then botExt = mxGate
       else botExt = msDiffPoly + bhStep botPinExt = max(mxGate - botExt grid))
   2: include
       polyEnc = PasSizeSpacing( nil ?top ( topExt + topPinExt - thStep )
                                        ?bottom (botExt + botPinExt - bhStep))
                                                                                      _polyExtLoop
   3: for f 1 gc
                  _____
       topSep = if( alternateGC && f == gc && oddp(f) then mxGate else topExt )
       botSep = if( alternateGC && ( f ==1 || ( f == gc && evenp(f) ) ) then mxGate else botExt )
                            topGx[f]
                     Poly
                                                      gate[f]
                                                                                botGx[f]
             topSep
                                                                        Poly
                                                                                      botSep
                             boolean lxBlockOverlapCheck = t
```

pcell macro mos



```
18: if ( connectTG || connectBG || alternateGC)
                                                                                       connectG
    awPoly = mwCont + ( 2.0 * mePolyCont )
    mlPoly = mwCont + ( 2.0 * meePolyCont )
    leftSep = 0
                        rightSep = 0
    tmp = when( gc == 1 && mlPoly > gl
      leftSep = PasCeiling( ( ( mlPoly - gl ) / 2.0 ) grid t )
      rightSep = mIPoly - gl - leftSep
    polyEnc = PasSizeSpacing( polyEnc ?left leftSep ?right rightSep )
    awM1 = mwCont + ( 2.0 * meM1Cont )
    hSep = meePolyCont - meeM1Cont
    vSep = mePolyCont - meM1Cont
    ei = if( alternateGC then max(gc - 1 1) else 1 )
     2: if connectTG || alternateGC
         polyEnc = PasSizeSpacing( polyEnc ?top awPoly )
     2: if connectBG || ( alternateGC && gc > 2 )
         polyEnc = PasSizeSpacing( polyEnc ?bottom awPoly )
     3: for f 1 ei
                                                                             connectGLoop
       index = if( alternateGC then min(f + 1 gc) else gc )
```



mos_nplus (macro) mos_pplus (macro)

pcell macro mos_implant

Pcell Macro Table

\$cell	\$pcLayer1
mos_nplus	Nimp
mos_pplus	Pimp

```
2: include
```

```
meImplDiff = dpt->meImplDiff

meImplPoly = dpt->meImplPoly

meImplGate = dpt->meImplGate

meeImplGate = when( meImplGate dpt->meeImplGate || meImplGate )

meImplCont = dpt->meImplCont
```

20: include

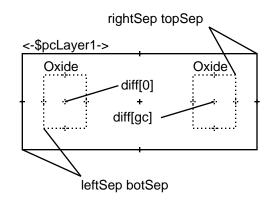
```
1: include preImplant
```

1: include

3: include

```
test = if( onep(gc) then lwDiff + lwStep + leftSep + gl + rwDiff + rwStep + rightSep > epsilon else t )
```

2: if test implant

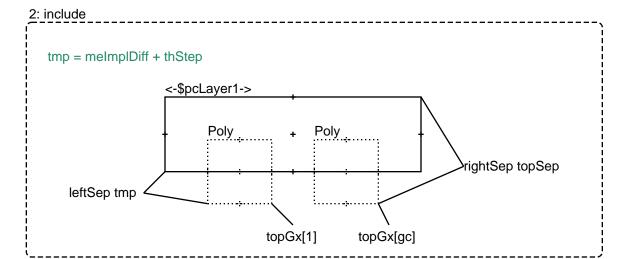


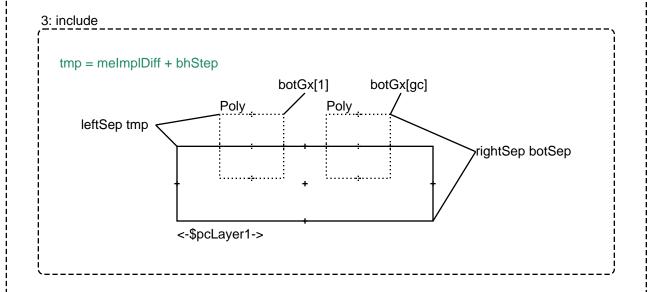
```
pcell macro mos_implant
```

```
implant
2: if meImplGate | meImplPoly
                                                                                   implantGate
    topSep = if( melmplPoly then polyEnc->top + melmplPoly + thStep
                  else meelmplGate)
    botSep = if( melmplPoly then polyEnc->bottom + melmplPoly + bhStep
                  else meelmplGate)
    implEnc = PasConstrainSpacing( implEnc ?top ( topSep - thStep )
                                                    ?bottom ( botSep - bhStep ) )
    leftSep = cond(
       ( leftAbut ==0 polyEnc->left + ( melmplPoly || melmplGate ) )
       (abs(leftAbut) < 3 lwDiff + meImplDiff)
       (abs(leftAbut) > 5 lwDiff - meImplDiff)
       (tlwDiff))
    rightSep = cond(
       ( rightAbut ==0 polyEnc->right + ( melmplPoly || melmplGate ) )
       (abs(rightAbut) < 3 rwDiff + meImplDiff)
       ( abs( rightAbut ) > 5 rwDiff - meImplDiff )
       (trwDiff))
```

pcell macro mos_implant







mos_rf (macro)

```
pcell macro mos rf
```

```
2: include
```

```
meRfDiff = dpt->meVoltDiff
meRfImpl = dpt->meVoltImpl
```

22: include mos_rf

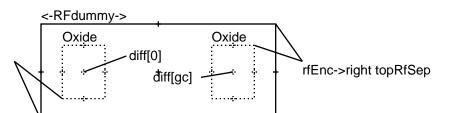
```
rfEnc = PasSizeSpacing( nil ?all meVoltDiff )
```

1: if meRfImpl

2: include

```
botRfSep = rfEnc->bottom + lhStep
topRfSep = rfEnc->top + rhStep
```

rfEnc->left botRfSep



mos_volt (macro)

```
pcell macro mos volt
```

```
2: include
```

```
meVoltDiff = dpt->meVoltDiff
meVoltImpl = dpt->meVoltImpl
```

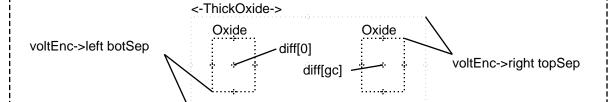
22: include mos_volt

```
voltEnc = PasSizeSpacing( nil ?all meVoltDiff )
```

1: if meVoltImpl

2: include

```
botSep = voltEnc->bottom + lhStep
topSep = voltEnc->top + rhStep
```



mos_volt_tap (macro)

```
pcell macro mos_volt_tap
```

```
include macro mos_volt
```

2: include

```
msTapVolt = dpt->msTapVolt
```

mos_volt

```
3: include
```

mos_nw (macro) mos_pw (macro)

pcell macro mos_well

Pcell Macro Table

\$cell	\$pcLayer1
mos_nw	Nwell
mos_pw	Pwell

2: include

```
meWellDiff = dpt->meWellDiff
```

40: include

```
botSep = meWellDiff + lhStep
topSep = meWellDiff + rhStep

<-$pcLayer1->

Oxide

Oxide

diff[0]

meWellDiff topSep
```

mos_nw_tap (macro) mos_nbl_tap (macro) mos_pw_tap (macro) mos_cap_tap (macro)

pcell macro mos_well_tap

Pcall	Macro	Table
r cell	IVIACIO	Iable

\$cell	\$pcLayer1	\$diffEnc	\$tapEnc	\$name
mos_nw_tap	Nwell	meWellDiff	meWellTap	well
mos_pw_tap	Pwell	meWellDiff	meWellTap	well
mos_nbl_tap	Nburied	meNblDiff	meNblTap	nbl
mos_cap_tap	Capdum	meCapDiff	meCapTap	сар

2: include

\$diffEnc = dpt->\$diffEnc

\$tapEnc = dpt->\$tapEnc

40: include botSep = \$diffEnc + IhStep topSep = \$diffEnc + rhStep diff[0] \$tapEnc \$tapEnc topTap **leftTap** \$tapEnc \$tapEnc \$name [\$pcLayer1] \$diffEnc topSep \$tapEnc \$tapEnc \$tapEnc \$tapEnc <-Oxide Oxide <-Oxide/> <-Oxid/e-> <-Oxide Oxide \$tapEnc/\$tapEnc \$tapEnc \$tapEnc \$diffEnc botSep \$tapEnc \$tapEnc rightTap "B" inOut botTap \$tapEnc \$tapEnc diff[gc]

mosltap (macro)

pcell macro mosltap

Pcell Macro Table

\$cell	\$pcLayer1
mosltap_nplus	Nimp
mosltap_pplus	Pimp

2: include

```
mwltap = dpt->mwltap || 0
msltapGate = dpt->msltapGate
maltimp = dpt->maltimp
meltimpCont = dpt->meltimpCont
meltimpltap = dpt->meltimpItap
leftItap = tap == "Integrated" && leftTap && leftAbut == 0
rightItap = tap == "Integrated" && rightAbut == 0
```

7: if leftItap || rightItap

```
awltap = max( mwltap ( meDiffCont + mwCont + max( meltimpCont (msCont - melmplCont ) )) )

if leftItap

lwDiff = max( ( msltapGate - lwStep ) ( laDC + cntSpan + melmplCont ) ) + awltap

lwSDM = lwDiff - laDC - meDiffCont + meM1Cont + meM1Cont
```

if rightItap

```
rwDiff = max( ( msltapGate - rwStep ) ( raDC + cntSpan + melmplCont ) ) + awltap rwSDM = rwDiff - raDC - meDiffCont + meM1Cont + meM1Cont
```

preImplant

```
2: if leftItap
leftSep = -awItap
```

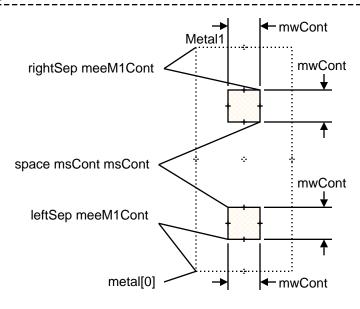
```
2: if rightItap
rightSep = -awItap
```

pcell macro mosltap

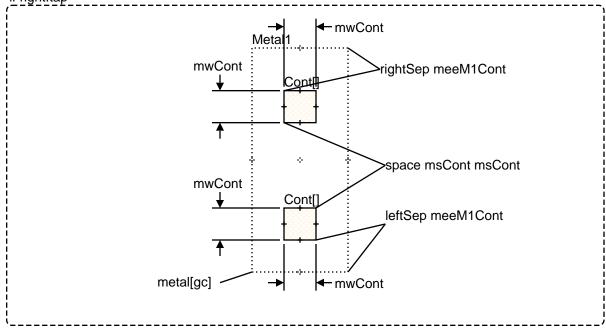
16: if leftItap || rightItap

leftSep = meM1Cont
rightSep = meM1Cont + mwCont

if leftItap

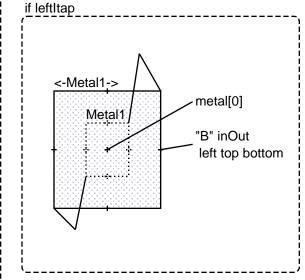


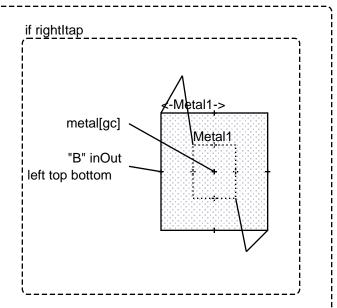
if rightItap



pcell macro mosltap

15: include



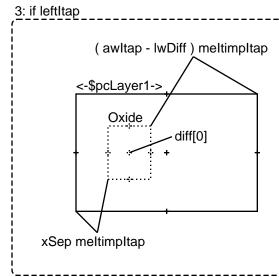


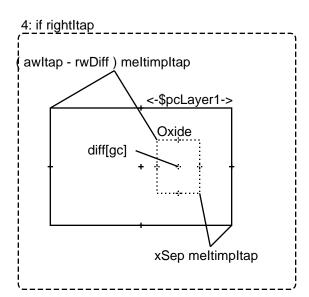
21: if leftltap || rightltap

xSep = meltimpItap

2: if maltimp

xSep = max(meltimpltap PasCeiling((maltimp / (alDiff + (2 * meltimpltap))) - awltap grid t))





mosTap (macro)

```
pcell macro mosTap
```

```
deTap = tap == "Detached" && ( leftTap || rightTap || topTap || bottomTap )

1: if deTap

msTapDiff = dpt->msTapDiff
meTimpTap = dpt->meTimpTap
msTapImpI = dpt->msTapImpI || 0.0
msTimpDiff = dpt->msTimpDiff || 0.0
msTimpImpI = dpt->msTimpImpI || 0.0
msTimpImpI = dpt->msTimpImpI || 0.0
msTimpImpI = dpt->msTimpImpI || 0.0
maTap = dpt->maTap
```

7: if deTap

```
awTap = ( max( 1 tapCntRows ) * ( mwCont + msCont) ) - msCont + (2.0 * meDiffCont)
tapSp = PasConstrainSpacing( nil ?all max( msTapDiff ( meTimpTap + msTimpDiff ) ) )
```

if topTap || bottomTap

```
tmp = msM1 + meM1Cont - meDiffCont + meeM1Cont - meeDiffCont
tapSp = PasConstrainSpacing( tapSp ?top tmp ?bottom tmp )
```

```
pcell macro mosTap
```

```
3: if topTap

tmp = mwM1 + (2 * msM1) + meM1Cont - meDiffCont + meeM1Cont - meeDiffCont

tapSp = PasConstrainSpacing( tapSp ?top tmp )
```

connectD

```
3: if bottomTap
```

```
tmp = mwM1 + ( 2 * msM1 ) + meM1Cont - meDiffCont + meeM1Cont - meeDiffCont
tapSp = PasConstrainSpacing( tapSp ?bottom tmp )
```

30: if deTap

1: include

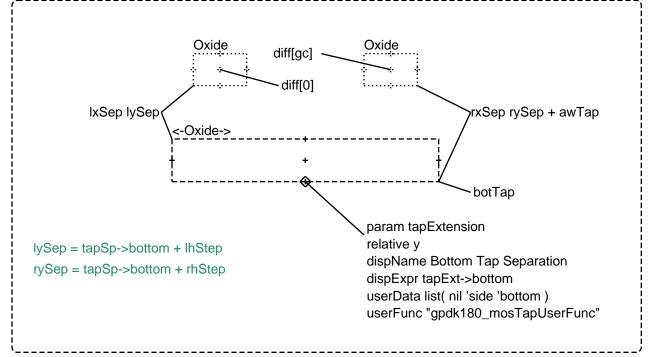
1: include

2: include

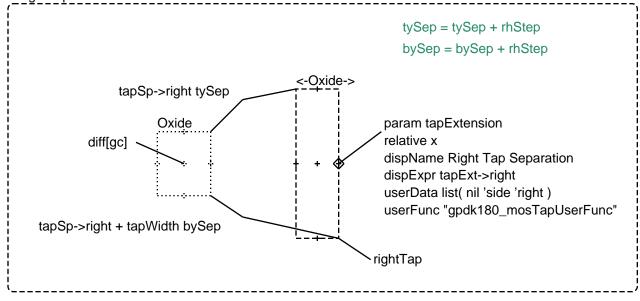
pcell macro mosTap

31: if deTap && (topTap || bottomTap) lxSep = if(leftTap then tapSp->left + awTap else 0.0) rxSep = if(rightTap then tapSp->right + awTap else 0.0) if topTap param tapExtension relative y lySep = tapSp->top + lhStep dispName Top Tap Separation rySep = tapSp->top + rhStep dispExpr tapExt->top userData list(nil 'side 'top) userFunc "gpdk180_mosTapUserFunc" -topTap <-Oxide-> IxSep lySep rxSep rySep + awTap diff[0] diff[gc] if bottomTap





pcell macro mosTap 31: if deTap && (leftTap || rightTap) tySep = if(topTap then tapSp->top else 0.0) bySep = if(bottomTap then tapSp->bottom else 0.0) tapWidth = awTap if maTap && not(topTap || bottomTap) tapWidth = max(tapWidth PasCeiling((maTap / alDiff) grid)) if leftTap tySep = tySep + lhStep bySep = bySep + lhStep <-Oxide-> tapSp->left tySep Oxide param tapExtension diff[0] relative x dispName Left Tap Separation dispExpr tapExt->left userData list(nil 'side 'left) userFunc "gpdk180_mosTapUserFunc" tapSp->left + tapWidth bySep **leftTap** if rightTap tySep = tySep + rhStep bySep = bySep + rhStep <-Oxide-> tapSp->right tySep

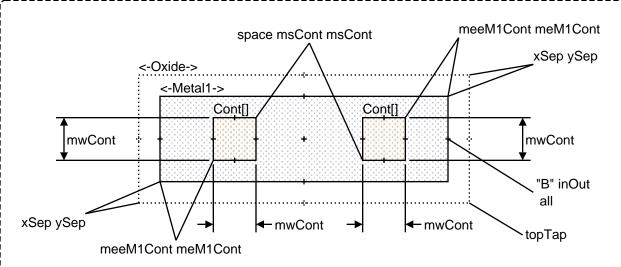


pcell macro mosTap

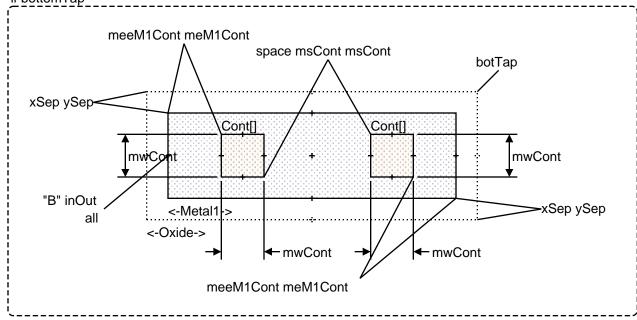
32: if deTap && (topTap || bottomTap)

xSep = meDiffCont - meeM1Cont ySep = meDiffCont - meM1Cont

if topTap



if bottomTap



pcell macro mosTap

32: if deTap && (leftTap || rightTap)

leftRightTap

xSep = meDiffCont - meM1Cont

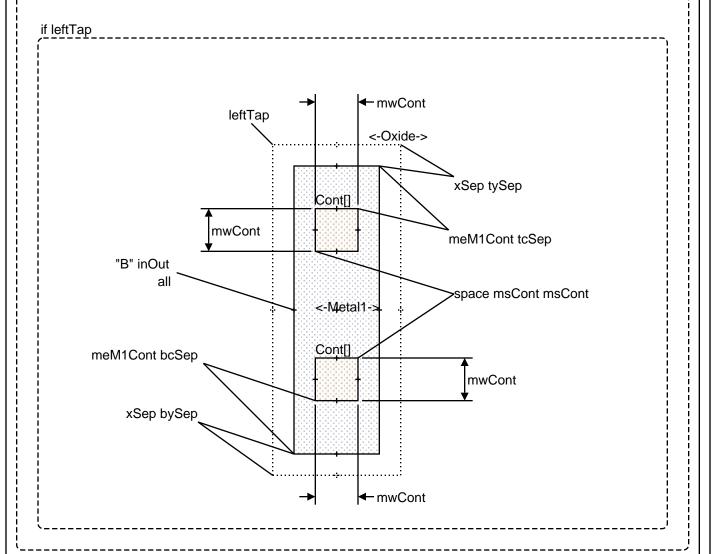
ySep = meDiffCont - meeM1Cont

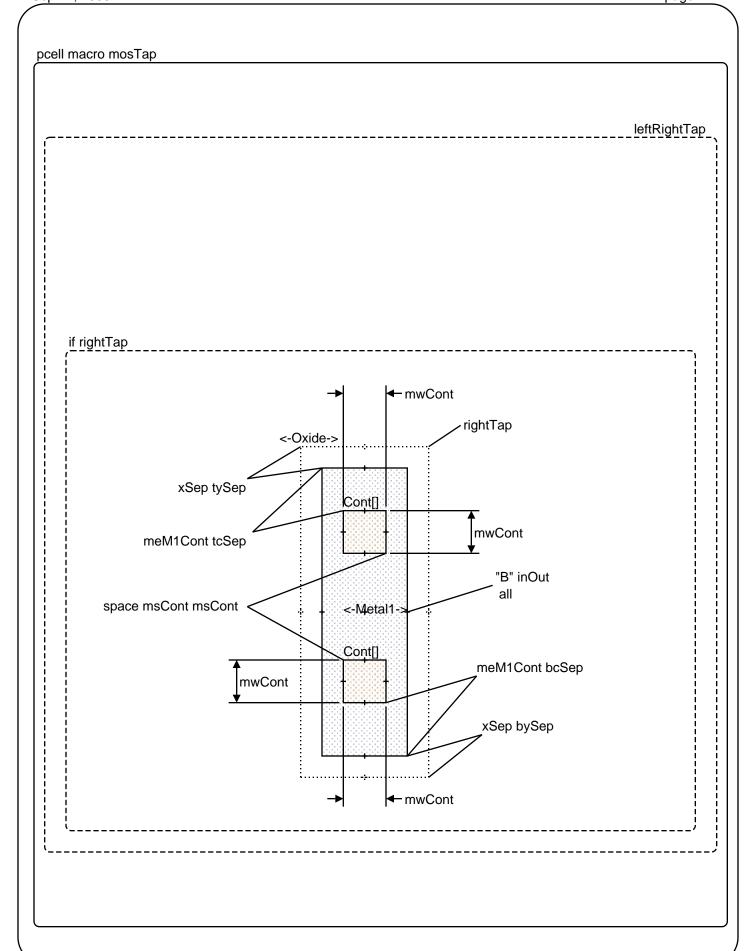
tySep = if(topTap then -xSep else ySep)

bySep = if(bottomTap then -xSep else ySep)

tcSep = if(topTap then msCont - meM1Cont else meeM1Cont)

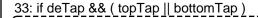
bcSep = if(bottomTap then msCont - meM1Cont else meeM1Cont)

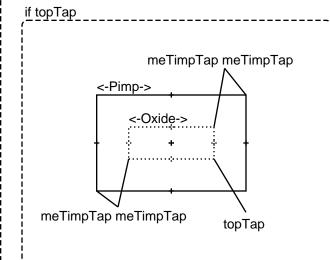


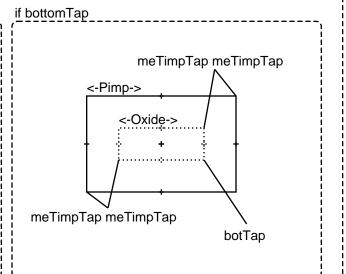


mosTap_pplus (macro)

pcell macro mosTap_pplus



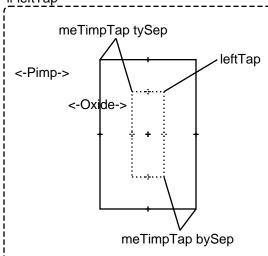




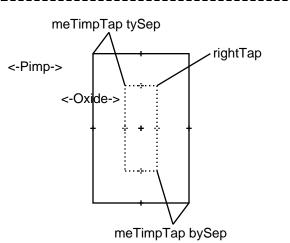
33: if deTap && (leftTap || rightTap)

tySep = if(topTap then -meTimpTap else meTimpTap)
bySep = if(bottomTap then -meTimpTap else meTimpTap)

if leftTap



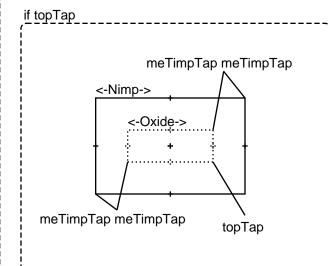
if rightTap

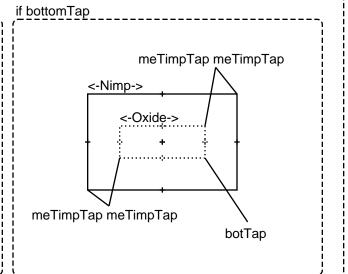


mosTap_nplus (macro)

pcell macro mosTap_nplus



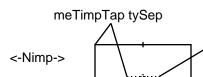


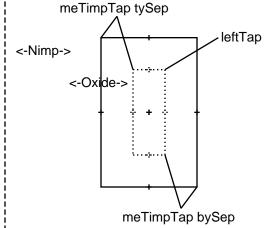


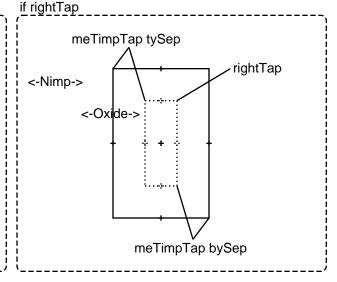
33: if deTap && (leftTap || rightTap)

if leftTap

tySep = if(topTap then -meTimpTap else meTimpTap) bySep = if(bottomTap then -meTimpTap else meTimpTap)







PCell Devices

pcell gpdk180 nmos3

nmos3

PCell formal params

fw	float	CDF
I	float	CDF
fingers	float	CDF
connectGates	string	CDF
connectSD	string	CDF
switchSD	boolean	CDF
mtlCvg	string	CDF
sdMtlWidth	float	CDF
leftAbut	int	CDF
rightAbut	int	CDF

include macro mos include macro mos_nplus

pcell gpdk180 pmos3

pmos3

PCell formal params

fw	float	CDF
I	float	CDF
fingers	float	CDF
connectGates	string	CDF
connectSD	string	CDF
switchSD	boolean	CDF
mtlCvg	string	CDF
sdMtlWidth	float	CDF
leftAbut	int	CDF
rightAbut	int	CDF

include macro mos include macro mos_pplus include macro mos_nw

pcell gpdk180 nmos3hv

nmos3hv

pcell gpdk180 pmos3hv

pmos3hv

PCell formal params

1 Oon formal paramo			
fw	float	CDF	
I	float	CDF	
fingers	float	CDF	
connectGates	string	CDF	
connectSD	string	CDF	
switchSD	boolean	CDF	
mtlCvg	string	CDF	
sdMtlWidth	float	CDF	
leftAbut	int	CDF	
rightAbut	int	CDF	

include macro mos include macro mos_nplus

include macro mos_volt

PCell formal params

fw	float	CDF
I	float	CDF
fingers	float	CDF
connectGates	string	CDF
connectSD	string	CDF
switchSD	boolean	CDF
mtlCvg	string	CDF
sdMtlWidth	float	CDF
leftAbut	int	CDF
rightAbut	int	CDF

include macro mos include macro mos_pplus include macro mos_nw include macro mos_volt

pcell gpdk180 nmos

nmos

PCell formal params

r con torrnar paramo			
fw	float	CDF	
I	float	CDF	
fingers	float	CDF	
connectGates	string	CDF	
connectSD	string	CDF	
switchSD	boolean	CDF	
mtlCvg	string	CDF	
sdMtlWidth	float	CDF	
leftAbut	int	CDF	
rightAbut	int	CDF	
tap	string	CDF	
topTap	boolean	CDF	
bottomTap	boolean	CDF	
leftTap	boolean	CDF	
rightTap	boolean	CDF	
tapExtension	string	CDF	
tapCntRows	int	CDF	

include macro mos include macro mos_nplus

include macro mosTap_pplus include macro mosTap_pplus

pcell gpdk180 pmos

pmos

PCell formal params

fw	float	CDF
I	float	CDF
fingers	float	CDF
connectGates	string	CDF
connectSD	string	CDF
switchSD	boolean	CDF
mtlCvg	string	CDF
sdMtlWidth	float	CDF
leftAbut	int	CDF
rightAbut	int	CDF
tap	string	CDF
topTap	boolean	CDF
bottomTap	boolean	CDF
leftTap	boolean	CDF
rightTap	boolean	CDF
tapExtension	string	CDF
tapCntRows	int	CDF

include macro mos include macro mos_pplus include macro mos_nw_tap include macro mosItap_nplus include macro mosTap include macro mosTap_nplus

pcell gpdk180 nmoshv

nmoshv

PCell formal params

fw	float	CDF
I	float	CDF
fingers	float	CDF
connectGates	string	CDF
connectSD	string	CDF
switchSD	boolean	CDF
mtlCvg	string	CDF
sdMtlWidth	float	CDF
leftAbut	int	CDF
rightAbut	int	CDF
tap	string	CDF
topTap	boolean	CDF
bottomTap	boolean	CDF
leftTap	boolean	CDF
rightTap	boolean	CDF
tapExtension	string	CDF
tapCntRows	int	CDF

include macro mos include macro mos_nplus

include macro mosItap_pplus include macro mosTap include macro mosTap_pplus include macro mos_volt_tap

pcell gpdk180 pmoshv

pmoshv

PCell formal params

fw	float	CDF
I	float	CDF
fingers	float	CDF
connectGates	string	CDF
connectSD	string	CDF
switchSD	boolean	CDF
mtlCvg	string	CDF
sdMtlWidth	float	CDF
leftAbut	int	CDF
rightAbut	int	CDF
tap	string	CDF
topTap	boolean	CDF
bottomTap	boolean	CDF
leftTap	boolean	CDF
rightTap	boolean	CDF
tapExtension	string	CDF
tapCntRows	int	CDF

include macro mos include macro mos_pplus include macro mos_nw_tap include macro mosItap_nplus include macro mosTap include macro mosTap_nplus include macro mos_volt_tap

pcell gpdk180 nmosrf

nmosrf

PCell formal params

fw	float	CDF
I	float	CDF
fingers	float	CDF
connectGates	string	CDF
connectSD	string	CDF
switchSD	boolean	CDF
mtlCvg	string	CDF
sdMtlWidth	float	CDF
leftAbut	int	CDF
rightAbut	int	CDF
tap	string	CDF
topTap	boolean	CDF
bottomTap	boolean	CDF
leftTap	boolean	CDF
rightTap	boolean	CDF
tapExtension	string	CDF
tapCntRows	int	CDF

include macro mos include macro mos_nplus include macro mos_rf include macro mosItap_pplus include macro mosTap include macro mosTap_pplus include macro mos_volt_tap

pcell gpdk180 pmosrf

pmosrf

PCell formal params

fw	float	CDF
I	float	CDF
fingers	float	CDF
connectGates	string	CDF
connectSD	string	CDF
switchSD	boolean	CDF
mtlCvg	string	CDF
sdMtlWidth	float	CDF
leftAbut	int	CDF
rightAbut	int	CDF
tap	string	CDF
topTap	boolean	CDF
bottomTap	boolean	CDF
leftTap	boolean	CDF
rightTap	boolean	CDF
tapExtension	string	CDF
tapCntRows	int	CDF

include macro mos include macro mos_pplus include macro mos_rf include macro mos_nw_tap include macro mosItap_nplus include macro mosTap include macro mosTap_nplus include macro mos_volt_tap

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\		

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Global Resistor Parameters

nplus_rho	50	Sheet Rho for nplusres
pplus_rho	158	Sheet Rho for pplusres
poly_rho	7.5	Sheet Rho for polyres
polyh_rho	352	Sheet Rho for polyhres
nwell_rho	415	Sheet Rho for nwellres
m1_rho	0.01	Sheet Rho for nwellres
m2_rho	0.01	Sheet Rho for nwellres
m3_rho	0.01	Sheet Rho for nwellres
m4_rho	0.01	Sheet Rho for nwellres
m5_rho	0.01	Sheet Rho for nwellres
m6_rho	0.01	Sheet Rho for nwellres

Poly Resistor

Global Parameters

rpoly_mwDevice	0.6	Minimum poly resistor width
rpoly_mlDevice	0.6	Minimum poly resistor length
rpoly_mnSquares	1	Minimum number of squares
rpoly_mwRes	{5A}	Minimum poly width
rpoly_msRes	{5B}	Minimum poly spacing
rpoly_mwCont	{6A}	Minimum & maximum contact width
rpoly_msCont	{6B}	Minimum contact spacing
rpoly_meResCont	{6D}	Minimum poly enclosure of contact
rpoly_meeResCont	{6D}	Minimum poly end enclosure of contact
rpoly_mwM1	{7A}	Minimum metal 1 width
rpoly_msM1	{7B}	Minimum metal 1 spacing
rpoly_meM1Cont	{7C}	Minimum metal 1 enclosure of contact
rpoly_meeM1Cont	{7C}	Minimum metal 1 end enclosure of contact

rnpoly_melmplRes	0.0	Minimum n+ implant enclosure of resistor	
rppoly_melmplRes	0.0	0.0 Minimum p+ implant enclosure of resistor	
rpoly_meldRes	0.05	Minimum recognition layer enclosure of resistor body	
rpoly_meSbRes	0.0	Minimum Sb enclosure of resistor	
rpoly_msSbCont	0.0	Minimum Sb to contact spacing	
rpoly_msSbRes	0.0	Minimum Sb to poly spacing	
rppoly_meWellRes	0.0	Minimum well enclosure of resistor	

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Diffusion Resistor

Global Parameters

rdiff_mwDevice	0.6	Minimum diff resistor width
rdiff_mlDevice	0.6	Minimum diff resistor length
rdiff_mnSquares	1	Minimum number of squares
rdiff_mwRes	{2A}	Minimum diffusion width
rdiff_msRes	{2B}	Minimum diffusion spacing
rdiff_mwCont	{6A}	Minimum & maximum contact width
rdiff_msCont	{6B}	Minimum contact spacing
rdiff_meResCont	{6C}	Minimum diff enclosure of contact
rdiff_meeResCont	{6C}	Minimum diff end enclosure of contact
rdiff_mwM1	{7A}	Minimum metal 1 width
rdiff_msM1	{7B}	Minimum metal 1 spacing
rdiff_meM1Cont	{7C}	Minimum metal 1 enclosure of contact
rdiff_meeM1Cont	{7C}	Minimum metal 1 end enclosure of contact

Olobai i alamotolo			
rndiff_melmplRes	{3C}	Minimum n+ implant enclosure of resistor	
rpdiff_melmplRes	{4C}	Minimum p+ implant enclosure of resistor	
rdiff_meldRes	0.05	Minimum recognition layer enclosure of resistor body	
rdiff_meSbRes	0.0	Minimum Sb enclosure of resistor	
rdiff_msSbCont	0.0	Minimum Sb to contact spacing	
rdiff_msSbRes	0.0	Minimum Sb to diff spacing	
rpdiff_meWellRes	{2C}	Minium well enclosure of resistor	

NWell Resistor

Global Parameters

rnw_mwDevice	1.6	Minimum diff resistor width	
rnw_mlDevice	2.0	Minimum diff resistor length	
rnw_mnSquares	1	Minimum number of squares	
rnw_mwRes	{1A}	Minimum nwell width	
rnw_msRes	{1B}	Minimum nwell spacing	
rnw_mwDiff	{2A}	Minimum diffusion width	
rnw_msDiff	{2B}	Minimum diffusion spacing	
rnw_meResDiff	{2C}	Minimum nwell enclosure of diffusion	
rnw_melmplDiff	{3C}	Minimum implant enclosure of diffusion	
rnw_mwCont	{6A}	Minimum & maximum contact width	
rnw_msCont	{6B}	Minimum contact spacing	
rnw_meDiffCont	{6C}	Minimum diff enclosure of contact	
rnw_meeDiffCont	{6C}	Minimum diff end enclosure of contact	
rnw_mwM1	{7A}	Minimum metal 1 width	
rnw_msM1	{7B}	Minimum metal 1 spacing	
rnw_meM1Cont	{7C}	Minimum metal 1 enclosure of contact	
rnw_meeM1Cont	{7C}	Minimum metal 1 end enclosure of contact	

rnw_meldRes 0.05 Minimum recognition layer enclosure of resistor body	rnw_meldRes	0.05	Minimum recognition layer enclosure of resistor body	
---	-------------	------	--	--

Callbacks

Resistor Callback Procedures

CDF Callback

```
procedure(gpdk180_resCB(param "s")
                                                   ;_Feb 21 03 amir 606
  let(
        cdfId cellName libName procName paramId
        dpt grid scale sGrid epsilon doList
        mode min max commit
        r w sl ssegs psegs cntRows calcDpl
     )
    ;; set local variable to global cdfgData
    cdfId = cdfgData
    caseq( concat(cdfId~>type)
       ( ( cellData baseCellData userCellData )
         cellName = cdfId~>id~>name
         libName = cdfId~>id~>lib~>name
       ( instData
         cellName = cdfId~>id~>master~>cellName
         libName = cdfId~>id~>master~>libName
       )
       ( t
         PasError("gpdk180_resCB: unexpected CDF data type - %L!" cdfId~>type)
         error("\tCallback aborted abnormally!")
    ); ** caseq concat **
    procName = sprintf(nil "%s/%s callback" libName cellName)
    unless( paramId = get(cdfId param)
       PasError("%s: parameter not found in CDF - %s" procName param)
       error("\tCallback aborted abnormally!")
    dpt = PasGetPdkParams(libName cellName)
    grid = dpt->grid
    scale = dpt->scale | | 1e-6
    sGrid = grid * scale
    epsilon = sGrid / 10.0
    doList = tconc(nil nil)
    mode = 'extreme
```

Resistor Spectre netlist procedure

CDF Callback

```
; File:
              ptag_resNetlistProc.il
; Description: Custom resistor netlist routine to handle series and
              parallel resistor configuration correctly.
              This routine expects the following parameter
              to be defined on the resistor CDF.
              segments - int
              connection - cyclic field ("Parallel" && "Series")
              sl - string
              l - string
              w - string
              It will create a resistor instantiation for each
              series or parallel resistor segment.
              The series configuration is a little more trickier
              since it has to create some dummy nets on the
              fly. It will check to make sure those nets do
              not conflict with any existing nets.
              Example of a series configuration:
              One single resistor in schematic:
                connection = "Series"
                sl = 83.335u
                w = 2u
                segments = 4
                model = rnpoly
              Resulting Spectre Netlist:
             //Series configuration of R0
             R0_1__dmy0 a R0_1__dmy486 rnpoly 1=83.335u w=2u dw=0.0 m=1
             R0_2_dmy0 R0_1_dmy486 R0_2_dmy486 rnpoly 1=83.335u w=2u dw=0.0 m=1
             R0_3_dmy0 R0_2_dmy486 R0_3_dmy486 rnpoly 1=83.335u w=2u dw=0.0 m=1
             //End of R0
              Example of a parallel configuration:
              One single resistor in schematic:
                connection = "Parallel"
                s1 = 33.335u
                w = 2u
                segments = 2
                model = rnpoly
              Resulting Spectre Netlist:
                // Parallel configuration of R2...
```

CDF Macro Definitions

resParamsA - Resistor CDF Parameters (Part A)

CDF macro resParamsA

CDF parameters

name	"resType"
prompt	"Resistor type"
defValue	"\$cell"
type	"string"
display	"nil"
editable	"nil"

CDF parameters

est parametere	
name	"calculatedParam"
prompt	"Calculated Parameter"
defValue	"Length"
choices	'("Resistance" "Length")
type	"radio"
display	"t"
callback	"gpdk180_resCB('calculatedParam)"

CDF parameters

name	"model"
prompt	"Model Name"
defValue	gpdk180_resValue('model ?id cdfld ?returnString t)
type	"string"
display	"gpdk180_resDisplay('model)"
editable	"nil"
parseAsCEL	"yes"

CDF parameters

ODI parameters		
name	"r"	
prompt	"Total Resistance"	
units	"resistance"	
defValue	gpdk180_resValue('r ?id cdfld ?returnString t)	
type	"string"	
display	" ["	
editable	"nequal(cdfgData->calculatedParam->value \"Resistance\")"	
callback	"gpdk180_resCB('r)"	
parseAsNumber	"yes"	
parseAsCEL	"yes"	

CDF macro resParamsA

CDF parameters

name	"w"
prompt	"Width"
units	"lengthMetric"
defValue	gpdk180_resValue('w ?id cdfld ?returnString t)
type	"string"
display	"t"
editable	"nequal(cdfgData->calculatedParam->value \"Width\")"
callback	"gpdk180_resCB('w)"
parseAsNumber	"yes"
parseAsCEL	"yes"

resParamsB_uniSeg - Resistor CDF Parameters (Part B)

CDF macro resParamsB_uniSeg

CDF parameters

ODI paramotoro	
name	" "
prompt	"Length"
units	"lengthMetric"
defValue	gpdk180_resValue('I ?id cdfld ?returnString t)
type	"string"
display	"t"
editable	"nequal(cdfgData->calculatedParam->value \"Length\")"
callback	"gpdk180_resCB('I)"
parseAsNumber	"yes"
parseAsCEL	"yes"

CDF parameters

DI parametere	
"m"	
"Multiplier"	
"1"	
"string"	
"t"	
"t"	
"gpdk180_resCB('m)"	
"yes"	
"yes"	

name	"s"
prompt	"S Factor"
defValue	"1"
type	"string"
display	"t"
editable	"t"
callback	"gpdk180_resCB('s)"
parseAsNumber	"yes"
parseAsCEL	"yes"

resParamsB_multiSeg - Resistor CDF Parameters (Part B)

CDF macro resParamsB_multiSeg

CDF device params

segmented t privat	te Segmented Flag (do not modify)
--------------------	-----------------------------------

CDF parameters

CZ: Parameters	
name	" "
prompt	"Total Length"
units	"lengthMetric"
defValue	gpdk180_resValue('I ?id cdfld ?returnString t)
type	"string"
display	"t"
editable	"nil"
callback	"gpdk180_resCB('I)"
parseAsNumber	"yes"
parseAsCEL	"yes"

CDF parameters

name	"sl"
prompt	"Segment Length"
units	"lengthMetric"
defValue	gpdk180_resValue('sl ?id cdfld ?returnString t)
type	"string"
display	"†"
editable	"nequal(cdfgData->calculatedParam->value \"Length\")"
callback	"gpdk180_resCB('sl)"
parseAsNumber	"yes"
parseAsCEL	"yes"

OD: Paramotoro	
name	"effL"
prompt	"Effective Length"
units	"lengthMetric"
defValue	gpdk180_resValue('sl ?id cdfld ?returnString t)
type	"string"
display	"nil"
editable	"nil"
parseAsNumber	"yes"
parseAsCEL	"yes"

CDF macro resParamsB_multiSeg

CDF parameters

name	"segments"
prompt	"Number of Segments"
defValue	gpdk180_resValue('segments ?id cdfld ?returnString nil)
type	"int"
display	"t"
callback	"gpdk180_resCB('segments)"

CDF parameters

name	"connection"
prompt	"Connection"
type	"cyclic"
defValue	gpdk180_resValue('connection ?id cdfld)
choices	gpdk180_resValue('connection ?id cdfld ?type 'choices)
display	"t"
callback	"gpdk180_resCB('connection)"

CDF parameters

parameters	
name	"ssegs"
prompt	"Series Segments"
defValue	gpdk180_resValue('ssegs ?id cdfld ?returnString nil)
type	"int"
display	"nil"
editable	"nil"
callback	"gpdk180_resCB('ssegs)"

name	"psegs"
prompt	"Parallel Segments"
defValue	gpdk180_resValue('psegs ?id cdfld ?returnString nil)
type	"int"
display	"nil"
editable	"nil"
callback	"gpdk180_resCB('psegs)"

resParamsC - Resistor CDF Parameters (Part C)

CDF macro resParamsC

CDF parameters

name	"cntRows"
prompt	"Contact Rows"
defValue	gpdk180_resValue('cntRows ?id cdfld ?returnString nil)
type	"int"
display	"nil"
callback	"gpdk180_resCB('cntRows)"

CDF parameters

name	"leftDummy"
prompt	"Left Dummy"
type	"boolean"
defValue	nil
display	"t"

CDF parameters

name	"rightDummy"
prompt	"Right Dummy"
type	"boolean"
defValue	nil
display	"t"

name	"cntOnDummy"
prompt	"Contact on Dummy"
type	"boolean"
defValue	t
display	"gpdk180_resDisplay('cntOnDummy)"

resParamsTap - Resistor CDF Parameters (Tap)

CDF macro resParamsTap

CDF parameters

name	"showTapProps"
prompt	"Show Tap Properties"
type	"boolean"
defValue	nil
display	"t"

CDF parameters

name	"leftTap"
prompt	"Left Tap"
type	"boolean"
defValue	t
display	"gpdk180_resDisplay('leftTap)"

CDF parameters

name	"rightTap"
prompt	"Right Tap"
type	"boolean"
defValue	nil
display	"gpdk180_resDisplay('rightTap)"

CDF parameters

name	"topTap"
prompt	"Тор Тар"
type	"boolean"
defValue	nil
display	"gpdk180_resDisplay('topTap)"

<u> </u>	
name	"bottomTap"
prompt	"Bottom Tap"
type	"boolean"
defValue	nil
display	"gpdk180_resDisplay('bottomTap)"

resParamsD - Resistor CDF Parameters (Part D)

CDF macro resParamsD

CDF parameters

name	"otherParams"
prompt	"Show Other Parameters"
type	"boolean"
display	"gpdk180_resDisplay('otherParams)"

ODI parameters	
name	"rsh"
prompt	"Sheet Resistivity"
units	"resistance"
defValue	gpdk180_resValue('rsh ?id cdfld ?returnString t)
type	"string"
display	"gpdk180_resDisplay('rsh)"
editable	"nil"
callback	"gpdk180_resCB('rsh)"
parseAsNumber	"yes"
parseAsCEL	"yes"

CDF macro resParamsD

CDF parameters

name	"rbody"
prompt	"Body Resistance"
units	"resistance"
defValue	gpdk180_resValue('rbody ?id cdfld ?returnString t)
type	"string"
display	"gpdk180_resDisplay('rbody)"
editable	"nil"
callback	"gpdk180_resCB('rbody)"
parseAsNumber	"yes"
parseAsCEL	"yes"

CDF parameters

name	"rcont"
prompt	"Contact Resistance"
units	"resistance"
defValue	gpdk180_resValue('rcont ?id cdfld ?returnString t)
type	"string"
display	"gpdk180_resDisplay('rcont)"
editable	"nil"
callback	"gpdk180_resCB('rcont)"
parseAsNumber	"yes"
parseAsCEL	"yes"

ODI Paramotoro		
name	"rend"	
prompt	"End Resistance"	
units	"resistance"	
defValue	gpdk180_resValue('rend ?id cdfld ?returnString t)	
type	"string"	
display	"gpdk180_resDisplay('rend)"	
editable	"nil"	
callback	"gpdk180_resCB('rend)"	
parseAsNumber	"yes"	
parseAsCEL	"yes"	

CDF macro resParamsD

CDF parameters

name	"tc1"	
prompt	"Temperature Coefficient 1"	
defValue	gpdk180_resValue('tc1 ?id cdfld ?returnString t)	
type	"string"	
display	"gpdk180_resDisplay('tc1)"	
editable	"nil"	
parseAsNumber	"yes"	
parseAsCEL	"yes"	

name	"tc2"	
prompt	"Temperature Coefficient 2"	
defValue	gpdk180_resValue('tc2 ?id cdfld ?returnString t)	
type	"string"	
display	"gpdk180_resDisplay('tc2)"	
editable	"nil"	
parseAsNumber	"yes"	
parseAsCEL	"yes"	

CDF macro resParamsD

CDF parameters

name	"vc1"	
prompt	"Voltage Coefficient 1"	
defValue	gpdk180_resValue('vc1 ?id cdfld ?returnString t)	
type	"string"	
display	"gpdk180_resDisplay('vc1)"	
editable	"nil"	
parseAsNumber	"yes"	

CDF parameters

ODI paramotoro		
name	"vc2"	
prompt	"Voltage Coefficient 2"	
defValue	gpdk180_resValue('vc2 ?id cdfld ?returnString t)	
type	"string"	
display	"gpdk180_resDisplay('vc2)"	
editable	"nil"	
parseAsNumber	"yes"	

CDF parameters

name	"dw"	
prompt	"Delta Width"	
units	"lengthMetric"	
defValue	gpdk180_resValue('dw ?id cdfld ?returnString t)	
type	"string"	
display	"gpdk180_resDisplay('dw)"	
editable	"nil"	
callback	"gpdk180_resCB('dw)"	
parseAsNumber	"yes"	
parseAsCEL	"yes"	

name	"dl"	
prompt	"Delta Length"	
units	"lengthMetric"	
defValue	gpdk180_resValue('dl ?id cdfld ?returnString t)	
type	"string"	
display	"gpdk180_resDisplay('dl)"	
editable	"nil"	
callback	"gpdk180_resCB('dl)"	
parseAsNumber	"yes"	
parseAsCEL	"yes"	

resParamsE - Resistor CDF Parameters (Part E)

CDF macro resParamsE

CDF parameters

name	"trise"
prompt	"Temp rise from ambient"
defValue	IIII
type	"string"
display	"nil"
parseAsNumber	"yes"
parseAsCEL	"yes"

CDF parameters

name	"resform"	
prompt	"Resistance Form"	
defValue	""	
type	"string"	
display	"gpdk180_resDisplay('resform)"	
parseAsCEL	"yes"	

CDF parameters

name	"isnoisy"	
prompt	"Generate noise?"	
defValue	"yes"	
choices	'("yes" "no")	
type	"cyclic"	
display	"gpdk180_resDisplay('isnoisy)"	
parseAsCEL	"yes"	

name	"scale"
prompt	"Scale factor"
defValue	""
type	"string"
display	"gpdk180_resDisplay('scale)"
parseAsNumber	"yes"
parseAsCEL	"yes"

CDF macro resParamsE

CDF parameters

name	"ac"
prompt	"AC resistance"
units	"resistance"
defValue	""
type	"string"
display	"gpdk180_resDisplay('ac)"
parseAsNumber	"yes"
parseAsCEL	"yes"

CDF parameters

name	"dtemp"
prompt	"Temperature difference"
defValue	""
type	"string"
display	"gpdk180_resDisplay('dtemp)"
parseAsNumber	"yes"
parseAsCEL	"yes"

CDF parameters

name	"hrc"	
prompt	"Capacitance connected"	
units	"capacitance"	
defValue	""	
type	"string"	
display	"gpdk180_resDisplay('hrc)"	
parseAsNumber	"yes"	
parseAsCEL	"yes"	

name	"source"
prompt	"Source component"
type	"boolean"
display	"gpdk180_resDisplay('source)"

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resParamsF - Resistor CDF Parameters (Part F)

CDF macro resParamsF

CDF parameters

name	"pasUpdateParamList"	
prompt	"CDF Param Update List"	
defValue	"w"	
type	"string"	
display	"nil"	

CDF properties

CDF properties		
formInitProc	"PasCdfFormInit"	
doneProc	"PasCdfDone"	
buttonFieldWidth	340	
fieldHeight	35	
fieldWidth	350	
promptWidth	175	
instNameType	"schematic"	
instDisplayMode	"instName"	
netNameType	"schematic"	
termSimType	"DC"	
termDisplayMode	"voltage"	
paramSimType	"DC"	
paramEvaluate	"nil nil nil t nil"	
paramDisplayMode	"parameter"	
modelLabelSet	"tc1 tc2 coefs"	
opPointLabelSet	"v i pwr"	
paramLabelSet	"-resType r w l"	

resSimInfo2 - 2 terminal Resistor Simulation Information

CDF macro resSimInfo2

CDF siminfo

simulator	auCdl	
netlistProcedure	ansCdlCompPrim	
instParameters	(RWLM)	
componentName	nil	
termOrder	(PLUS MINUS)	
propMapping	(nil R r M m W w L I)	
namePrefix	"R"	

CDF siminfo

simulator	ams	
isPrimitive	t	
otherParameters	(model)	
termOrder	(PLUS MINUS	
propMapping	(nil sl effL)	
instParameters	(model sl w)	

CDF siminfo

simulator	auLvs
netlistProcedure	ansLvsCompPrim
instParameters	(r l w m)
termOrder	(PLUS MINUS)
deviceTerminals	nil
propMapping	nil
permuteRule	"(p PLUS MINUS)"
namePrefix	"R"

resSimInfo3 - 3 terminal Resistor Simulation Information

CDF macro resSimInfo3

CDF siminfo

simulator	auCdl	
netlistProcedure	ansCdlCompPrim	
instParameters	(RWLM)	
componentName	nil	
termOrder	(PLUS MINUS B)	
propMapping	(nil R r M m W w L I)	
namePrefix	"R"	

CDF siminfo

simulator	ams	
isPrimitive	t	
otherParameters	(model)	
termOrder	(PLUS MINUS B	
propMapping	(nil sl effL)	
instParameters	(model sl w)	

CDF siminfo

simulator	auLvs
netlistProcedure	ansLvsCompPrim
instParameters	(r l w m)
termOrder	(PLUS MINUS B)
deviceTerminals	nil
propMapping	nil
permuteRule	"(p PLUS MINUS)'
namePrefix	"R"

res2_uniSeg - 2 terminal Resistor CDF res3_uniSeg - 3 terminal Resistor CDF

CDF macro res2_uniSeg

include macro resParamsA

include macro resParamsB_uniSeg

include macro resParamsC

include macro resParamsD

include macro resParamsE

include macro resParamsF

include macro resSimInfo2

CDF macro res3_uniSeg

include macro resParamsA

include macro resParamsB_uniSeg

include macro resParamsC

include macro resParamsTap

include macro resParamsD

include macro resParamsE

include macro resParamsF

include macro resSimInfo3

res2_multiSeg - 2 terminal Resistor CDFres3_multiSeg - 3 terminal Resistor CDF

CDF macro res2_multiSeg

include macro resParamsA

include macro resParamsB multiSeg

include macro resParamsC

include macro resParamsD

include macro resParamsE

include macro resParamsF

include macro resSimInfo2

CDF macro res3_multiSeg

include macro resParamsA

include macro resParamsB_multiSeg

include macro resParamsC

include macro resParamsD

include macro resParamsE

include macro resParamsF

include macro resSimInfo3

Resistors Schematic Components resPrim

CDF macro resPrim

CDF pa	ıram	eters
--------	------	-------

name	"r"
prompt	"res"
defValue	"pPar(\"r\")"
units	"lengthMetric"
type	"string"
display	"nil"
editable	"nil"
parseAsNumber	"yes"
parseAsCEL	"yes"

CDF parameters

CDF parameters	
name	"w"
prompt	"Width (m)"
defValue	"pPar(\"w\")"
units	"lengthMetric"
type	"string"
parseAsNumber	"yes"
parseAsCEL	"yes"

CDF parameters

" "
"Length (m)"
"pPar(\"I\")"
"lengthMetric"
"string"
"yes"
"yes"

name	"sl"
prompt	"Length (m)"
defValue	"pPar(\"sl\")"
units	"lengthMetric"
type	"string"
parseAsNumber	"yes"
parseAsCEL	"yes"

resPrim2

CDF gpdk180 resPrim2

include macro resPrim

CDF siminfo

simulator	spectre	
otherParameters	(model)	
termMapping	(nil PLUS ":1" MINUS ":2")	
termOrder	(PLUS MINUS)	
instParameters	(sl w)	

CDF siminfo

simulator	ams
isPrimitive	t
otherParameters	(model)
termOrder	(PLUS MINUS
instParameters	(model sl w)

CDF siminfo

simulator	ads
netlistProcedure	ADSsimSubcktCall
otherParameters	(model)
instParameters	(sl w)
componentName	nil
termOrder	(PLUS MINUS)
termMapping	(nil PLUS ":P1" MINUS ":P2")
propMapping	nil
typeMapping	nil
uselib	nil

CDF properties

FF	
formInitProc	""
doneProc	""
buttonFieldWidth	340
fieldHeight	35
fieldWidth	350
promptWidth	175
opPointLabelSet	"res v i"
paramLabelSet	"-model r w l"

symbol	baseline_gpdk	res2	symbol
spectre	baseline_gpdk	res2	symbol
ads	baseline_gpdk	res2	symbol

resPrim3

CDF gpdk180 resPrim3

include macro resPrim

CDF siminfo

simulator	spectre
otherParameters	(model)
termMapping	(nil PLUS ":1" MINUS ":2" B ":3")
termOrder	(PLUS MINUS B)
instParameters	(sl w)

CDF siminfo

simulator	ams
isPrimitive	t
otherParameters	(model)
termOrder	(PLUS MINUS B
instParameters	(model sl w)

CDF siminfo

aime vlatar	a da
simulator	ads
netlistProcedure	ADSsimSubcktCall
otherParameters	(model)
instParameters	(sl w)
componentName	nil
termOrder	(PLUS MINUS B)
termMapping	(nil PLUS ":P1" MINUS ":P2" B ":P3")
propMapping	nil
typeMapping	nil
uselib	nil

CDF properties

""
""
340
35
350
175
"res v i"
"-model r w l"

symbol	baseline_gpdk	res3	symbol
spectre	baseline_gpdk	res3	symbol
ads	baseline_gpdk	res3	symbol

Resistors polyres

CDF gpdk180 polyres

include macro res2_multiSeg

CDF siminfo

simulator	auLvs
componentName	"\$cell"

CDF siminfo

simulator	auCdl
modelName	"\$cell"

CDF device params

CDI device paranis	_		
grid	\$grid	private	Manufacturing Grid Resolution
scale	1u	private	Dimension scale factor
model	"\$lib_\$cell"	private	
rsh	\$poly_rho	private	Sheet resistivity
tc1	0.0	private	Temperature Coefficient 1
tc2	0.0	private	Temperature Coefficient 2
vc1	0.0	private	Voltage Coefficient 1
vc2	0.0		Voltage Coefficient 2
dw 0.0		private	Delta Width
dl 0.0		private	Delta Length
rcont lambda((dpl) 5.0 / (dpl->rows * dpl->cols))		private	Contact Resistance per head
rend 0.0		private	End Resistance per head
category "res res/poly"		private	Library Manager Category
schPcellPrimSymbol '("\$lib" "resPrim2" "symbol")		public	Sch PCell Prim Symbol View
schPcellPinSymbol	'("basic" "iopin" "symbol")	public	Schem PCell Pin Symbol View

CDF gpdk180 polyres

CDF device params

OBI GOTIOO Pai			
mwDevice	\$rpoly_mwDevice	private	Minimum resistor width
mlDevice	\$rpoly_mlDevice	private	Minimum resistor length
mnSquares	\$rpoly_mnSquares	private	Minimum number of squares
mwRes	\$rpoly_mwRes	private	Minimum poly width
msRes	\$rpoly_msRes	private	Minimum spacing
mwCont	\$rpoly_mwCont	private	Minimum contact width
msCont	\$rpoly_msCont	private	Minimum contact spacing
meResCont	\$rpoly_meResCont	private	Minimum poly enclosure of contact
meeResCont	\$rpoly_meeResCont	private	Minimum poly end enclosure of contact
mwM1	\$rpoly_mwM1	private	Minimum metal 1 width
msM1	\$rpoly_msM1	private	Minimum metal 1 spacing
meM1Cont	\$rpoly_meM1Cont	private	Minimum metal 1 enclosure of contact
meeM1Cont	\$rpoly_meeM1Cont	private	Minimum metal 1 end enclosure of contact

CDF device params

melmplRes	\$rnpoly_meImplRes	private	Minimum imlant enclosure of poly
meldRes	\$rpoly_meldRes	private	Minimum recognition layer enclosure of poly
msldCont	<pre>\$rpoly_msSbCont</pre>	private	Minimum recognition layer to contact spacing
msldRes	\$rpoly_msSbRes	private	Minium recognition layer to poly spacing

symbol baseline_gpdk		res2	symbol	
auCdl	baseline_gpdk	res2	symbol	
auLvs	baseline_gpdk	res2	symbol	
ivpcell	\$lib	\$cell	symbol	5

polyhres

CDF gpdk180 polyhres

include macro res2_multiSeg

CDF siminfo

simulator	auLvs
componentName	"\$cell"

CDF siminfo

simulator	auCdl
modelName	"\$cell"

CDF device params

CDF device params		_	
grid	\$grid	private	Manufacturing Grid Resolution
scale	1u	private	Dimension scale factor
model	"\$lib_\$cell"	private	
rsh	\$polyh_rho	private	Sheet resistivity
tc1	0.0	private	Temperature Coefficient 1
tc2	0.0	private	Temperature Coefficient 2
vc1	0.0		Voltage Coefficient 1
vc2	0.0		Voltage Coefficient 2
dw 0.0		private	Delta Width
dl 0.0		private	Delta Length
rcont lambda((dpl) 5.0 / (dpl->rows * dpl->cols))		private	Contact Resistance per head
rend	lambda((dpl) 7.5 / dpl->w)		End Resistance per head
category	tegory "res res/poly"		Library Manager Category
schPcellPrimSymbol '("\$lib" "resPrim2" "symbol")		public	Sch PCell Prim Symbol View
schPcellPinSymbol '("basic" "iopin" "symbol") public Schem PCell Pin S		Schem PCell Pin Symbol View	

CDF gpdk180 polyhres

CDF device params

mwDevice	\$rpoly_mwDevice	private	Minimum resistor width
mlDevice	\$rpoly_mlDevice	private	Minimum resistor length
mnSquares	\$rpoly_mnSquares	private	Minimum number of squares
mwRes	\$rpoly_mwRes	private	Minimum poly width
msRes	\$rpoly_msRes	private	Minimum spacing
mwCont	\$rpoly_mwCont	private	Minimum contact width
msCont	\$rpoly_msCont	private	Minimum contact spacing
meResCont	\$rpoly_meResCont	private	Minimum poly enclosure of contact
meeResCont	\$rpoly_meeResCont	private	Minimum poly end enclosure of contact
mwM1	\$rpoly_mwM1	private	Minimum metal 1 width
msM1	\$rpoly_msM1	private	Minimum metal 1 spacing
meM1Cont	\$rpoly_meM1Cont	private	Minimum metal 1 enclosure of contact
meeM1Cont	\$rpoly_meeM1Cont	private	Minimum metal 1 end enclosure of contact

CDF device params

melmplRes	\$rnpoly_meImplRes	private	Minimum imlant enclosure of poly
meldRes	\$rpoly_meldRes	private	Minimum recognition layer enclosure of poly
msldCont	<pre>\$rpoly_msSbCont</pre>	private	Minimum recognition layer to contact spacing
msldRes	\$rpoly_msSbRes	private	Minium recognition layer to poly spacing

symbol baseline_gpdk		res2	symbol	
auCdl baseline_gpdk		res2	symbol	
auLvs baseline_gpdk		res2	symbol	
ivpcell	\$lib	\$cell	symbol	5

nplusres

CDF gpdk180 nplusres

include macro res3_multiSeg

CDF siminfo

simulator	auLvs
componentName	"\$cell"

CDF siminfo

simulator	auCdl
modelName	"\$cell"

CDF device params

CDF device params		_	
grid	\$grid	private	Manufacturing Grid Resolution
scale	1u	private	Dimension scale factor
model	"\$lib_\$cell"	private	
rsh	\$nplus_rho	private	Sheet resistivity
tc1	0.0	private	Temperature Coefficient 1
tc2	0.0	private	Temperature Coefficient 2
vc1	0.0	private	Voltage Coefficient 1
vc2 0.0		private	Voltage Coefficient 2
dw 0.0		private	Delta Width
dl 0.0		private	Delta Length
rcont lambda((dpl) 7.5 / (dpl->rows * dpl->cols))		private	Contact Resistance per head
rend	end 0.0		End Resistance per head
category "res res/diff"		private	Library Manager Category
schPcellPrimSymbol	'("\$lib" "resPrim3" "symbol")	public	Sch PCell Prim Symbol View
schPcellPinSymbol '("basic" "iopin" "symbol")		public	Schem PCell Pin Symbol View

CDF gpdk180 nplusres

CDF device params

mwDevice	\$rdiff_mwDevice	private	Minimum resistor width	
mlDevice	\$rdiff_mlDevice	private	Minimum resistor length	
mnSquares	\$rdiff_mnSquares	private	Minimum number of squares	
mwRes	\$rdiff_mwRes	private	Minimum diffusion width	
msRes	\$rdiff_msRes	private	Minimum diffusion spacing	
mwCont	\$rdiff_mwCont	private	Minimum contact width	
msCont	\$rdiff_msCont	private	Minimum contact spacing	
meResCont	\$rdiff_meResCont	private	Minimum diffusion enclosure of contact	
meeResCont	<pre>\$rdiff_meeResCont</pre>	private	Minimum diffusion end enclosure of contact	
mwM1	\$rdiff_mwM1	private	Minimum metal 1 width	
msM1	\$rdiff_msM1	private	Minimum metal 1 spacing	
meM1Cont	\$rdiff_meM1Cont	private	Minimum metal 1 enclosure of contact	
meeM1Cont	\$rdiff_meeM1Cont	private	Minimum metal 1 end enclosure of contact	

CDF device params

melmplRes	\$rndiff_meImpIRes	private	Minimum imlant enclosure of diff
meldRes	\$rdiff_meldRes	private	Minimum recognition layer enclosure of diff
msldCont	\$rdiff_msSbCont	private	Minimum recognition layer to contact spacing
msldRes	\$rdiff_msSbRes	private	Minium recognition layer to diff spacing

symbol baseline_gpdk		res3	symbol	
auCdl	baseline_gpdk	res3	symbol	
auLvs	baseline_gpdk	res3	symbol	
ivpcell	\$lib	\$cell	symbol	5

pplusres

CDF gpdk180 pplusres

include macro res3_multiSeg

CDF siminfo

simulator	auLvs
componentName	"\$cell"

CDF siminfo

simulator	auCdl
modelName	"\$cell"

CDF device params

CDF device params			
grid	\$grid	private	Manufacturing Grid Resolution
scale	1u	private	Dimension scale factor
model	"\$lib_\$cell"	private	
rsh	\$pplus_rho	private	Sheet resistivity
tc1	0.0	private	Temperature Coefficient 1
tc2	0.0	private	Temperature Coefficient 2
vc1	0.0	private	Voltage Coefficient 1
vc2 0.0		private	Voltage Coefficient 2
dw 0.0		private	Delta Width
dl 0.0		private	Delta Length
rcont lambda((dpl) 7.5 / (dpl->rows * dpl->cols))		private	Contact Resistance per head
rend	rend 0.0		End Resistance per head
category "res res/diff"		private	Library Manager Category
schPcellPrimSymbol	schPcellPrimSymbol '("\$lib" "resPrim3" "symbol")		Sch PCell Prim Symbol View
schPcellPinSymbol '("basic" "iopin" "symbol")		public	Schem PCell Pin Symbol View

CDF gpdk180 pplusres

CDF device params

mwDevice	\$rdiff_mwDevice	private	Minimum resistor width	
mlDevice	\$rdiff_mlDevice	private	Minimum resistor length	
mnSquares	\$rdiff_mnSquares	private	Minimum number of squares	
mwRes	\$rdiff_mwRes	private	Minimum diffusion width	
msRes	\$rdiff_msRes	private	Minimum diffusion spacing	
mwCont	\$rdiff_mwCont	private	Minimum contact width	
msCont	\$rdiff_msCont	private	Minimum contact spacing	
meResCont	\$rdiff_meResCont	private	Minimum diffusion enclosure of contact	
meeResCont	\$rdiff_meeResCont	private	Minimum diffusion end enclosure of contact	
mwM1	\$rdiff_mwM1	private	Minimum metal 1 width	
msM1	\$rdiff_msM1	private	Minimum metal 1 spacing	
meM1Cont	\$rdiff_meM1Cont	private	Minimum metal 1 enclosure of contact	
meeM1Cont	\$rdiff_meeM1Cont	private	Minimum metal 1 end enclosure of contact	

CDF device params

melmplRes	\$rpdiff_meImplRes	private	Minimum imlant enclosure of diff
meldRes	\$rdiff_meldRes	private	Minimum recognition layer enclosure of diff
msldCont	\$rdiff_msSbCont	private	Minimum recognition layer to contact spacing
msldRes	\$rdiff_msSbRes	private	Minium recognition layer to diff spacing
meWellRes	\$rpdiff_meWellRes	private	Minimum well enclosure of poly

symbol	baseline_gpdk	res3	symbol	
auCdl baseline_gpdk		res3	symbol	
auLvs	baseline_gpdk	res3	symbol	
ivpcell	\$lib	\$cell	symbol	5

nwellres

CDF gpdk180 nwellres

include macro res2_multiSeg

CDF siminfo

simulator	auLvs
componentName	"\$cell"

CDF siminfo

simulator	auCdl	
modelName	"\$cell"	

CDF device params			
grid	\$grid	private	Manufacturing Grid Resolution
scale	1u	private	Dimension scale factor
model	"\$lib_\$cell"	private	
rsh	\$nwell_rho	private	Sheet resistivity
tc1	0.0	private	Temperature Coefficient 1
tc2	0.0	private	Temperature Coefficient 2
vc1	0.0	private	Voltage Coefficient 1
vc2	0.0	private	Voltage Coefficient 2
dw	0.0	private	Delta Width
dl	0.0	private	Delta Length
rcont	lambda((dpl) 7.5 / (dpl->rows * dpl->cols))	private	Contact Resistance per head
rend	lambda((dpl) 160.0 / dpl->w)	private	End Resistance per head
category	"res res/nwell"	private	Library Manager Category
schPcellPrimSymbol	'("\$lib" "resPrim2" "symbol")	public	Sch PCell Prim Symbol View
schPcellPinSymbol	'("basic" "iopin" "symbol")	public	Schem PCell Pin Symbol View

CDF gpdk180 nwellres

CDF device params

mwDevice	\$rnw_mwDevice	private	Minimum resistor width	
mlDevice	\$rnw_mlDevice	private	Minimum resistor length	
mnSquares	\$rnw_mnSquares	private	Minimum number of squares	
mwRes	\$rnw_mwRes	private	Minimum nwell width	
msRes	\$rnw_msRes	private	Minimum nwell spacing	
mwDiff	\$rnw_mwDiff	private	Minimum diffusion width	
msDiff	\$rnw_msDiff	private	Minimum diffusion spacing	
meResDiff	\$rnw_meResDiff	private	Minimum nwell enclosure of diffusion	
melmplDiff	\$rnw_meImpIDiff	private	Minimum implant enclosure of diffusion	
mwCont	\$rnw_mwCont	private	e Minimum contact width	
msCont	\$rnw_msCont	private	Minimum contact spacing	
meDiffCont	\$rnw_meDiffCont	private	Minimum diffusion enclosure of contact	
meeDiffCont	\$rnw_meeDiffCont	private	Minimum diffusion end enclosure of contact	
mwM1	\$rnw_mwM1	private	Minimum metal 1 width	
msM1	\$rnw_msM1	private	e Minimum metal 1 spacing	
meM1Cont	\$rnw_meM1Cont	private	Minimum metal 1 enclosure of contact	
meeM1Cont	\$rnw_meeM1Cont	private	Minimum metal 1 end enclosure of contact	

CDF device params

meldRes \$rnw_meldRes private Minimum recogn	nition layer enclosure of nwell
--	---------------------------------

symbol	baseline_gpdk	res2	symbol	
auCdl baseline_gpdk		res2	symbol	
auLvs	baseline_gpdk	res2	symbol	
ivpcell	\$lib	\$cell	symbol	5

mxres

CDF gpdk180 mxres

include macro res2_multiSeg

CDF siminfo

simulator	auLvs
componentName	"\$cell"

CDF siminfo

simulator	auCdl	
modelName	"\$cell"	

CDF Macro Table

\$cell	\$m_rho	\$minW	\$minS	
m1res	\$m1_rho	{7A}	{7B}	
m2res	\$m2_rho	{9A}	{9B}	
m3res	\$m3_rho	{11A}	{11B}	
m4res	\$m4_rho	{15A}	{15B}	
m5res	\$m5_rho	{17A}	{17B}	
m6res	\$m6_rho	{19A}	{19B}	

CDF device params

CDI device pararris			
grid	\$grid	private	Manufacturing Grid Resolution
scale	1u	private	Dimension scale factor
model	"\$lib_\$cell"	private	
rsh	\$m_rho	private	Sheet resistivity
tc1	0.0	private	Temperature Coefficient 1
tc2	0.0	private	Temperature Coefficient 2
vc1	0.0	private	Voltage Coefficient 1
vc2	0.0	private	Voltage Coefficient 2
dw	0.0	private	Delta Width
dl	0.0	private	Delta Length
rcont	0.0	private	Contact Resistance per head
rend	0.0	private	End Resistance per head
category	"res res/metal"	private	Library Manager Category
schPcellPrimSymbol	'("\$lib" "resPrim2" "symbol")	public	Sch PCell Prim Symbol View
schPcellPinSymbol	'("basic" "iopin" "symbol")	public	Schem PCell Pin Symbol View

CDF gpdk180 mxres

CDF device params

OBI GOTIOG Pai				
mwDevice	\$minW	private	Minimum resistor width	
mlDevice	\$minW	private	Minimum resistor length	
mnSquares	1	private	Minimum number of squares	
mwRes	\$minW	private	Minimum poly width	
msRes	\$minS	private	Minimum spacing	
mwCont	{6A}	private	Minimum contact width	
msCont	{6B}	private	Minimum contact spacing	
meResCont	0.0	private	Minimum poly enclosure of contact	
meeResCont	0.0	private	e Minimum poly end enclosure of contact	
mwM1	\$minW	private	e Minimum metal 1 width	
msM1	\$minS	private	e Minimum metal 1 spacing	
meM1Cont	0.0	private	Minimum metal 1 enclosure of contact	
meeM1Cont	0.0	private	Minimum metal 1 end enclosure of contact	

CDF device params

meldRes	0.05	private	Minimum recognition layer enclosure of metal
msldCont	0.0	private	Minimum recognition layer to contact spacing
msldRes	0.0	private	Minium recognition layer to metal spacing

symbol	baseline_gpdk	res2	symbol	
auCdl	baseline_gpdk	res2	symbol	
auLvs	baseline_gpdk	res2	symbol	
ivpcell	\$lib	\$cell	symbol	5

PCell Macros

res_uniSeg (macro)

pcell macro res_uniSeg

PCell formal params

w	float	CDF
I	float	CDF
leftDummy	boolean	CDF
rightDummy	boolean	CDF
cntOnDummy	boolean	CDF
cntRows	int	CDF

include macro res_connect

connection = "Series" segments = 1

sl = l

res_multiSeg (macro)

mres_multiSeg (macro)

pcell macro res_multiSeg

PCell formal params

w	float	CDF
sl	float	CDF
connection	string	CDF
segments	int	CDF
leftDummy	boolean	CDF
rightDummy	boolean	CDF
cntOnDummy	boolean	CDF
cntRows	int	CDF

include macro res_connect

pcell macro mres_multiSeg

PCell formal params

· • • · · · · · · · · · · · · · · · · ·			
w	float	CDF	
sl	float	CDF	
connection	string	CDF	
segments	int	CDF	
leftDummy	boolean	CDF	
rightDummy	boolean	CDF	
cntRows	int	CDF	

res_poly (macro)

pcell macro res_poly

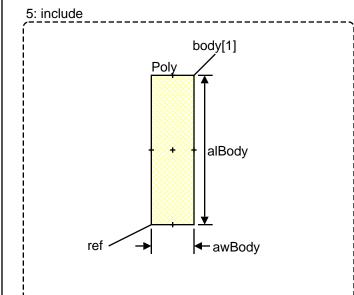
```
1: include
```

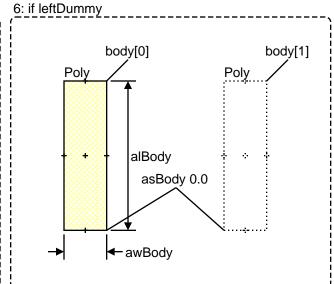
```
dpt = PasGetDeviceProps( cv )
scale = dpt->scale
epsilon = grid / 10.0
mwRes = dpt->mwRes
mwCont = dpt->mwCont
msCont = dpt->msCont
meResCont = dpt->meResCont
meeResCont = dpt->meResCont || meResCont
msIdCont = dpt->msIdCont || 0
msIdRes = dpt->msIdRes
```

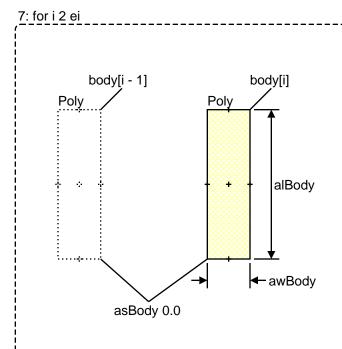
3: include

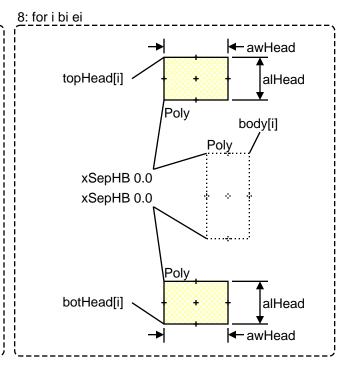
```
alBody = sl / scale
awBody = w / scale
sc = max(1 segments)
awHead = max( awBody mwRes (mwCont + (2.0 * meeResCont)) )
cntSpan = ( max(1 cntRows ) * ( mwCont + msCont ) ) - msCont
alHead = max( mwRes (cntSpan + (2.0 * meResCont)) )
asHead = msRes
asBody = asHead + awHead - awBody
dogbone = awBody + epsilon < awHead
xSepHB = if( dogbone then PasCeiling( ( (awHead - awBody) / 2.0 ) grid t ) else 0.0 )
bi = if( leftDummy then 0 else 1 )
ei = if( rightDummy then sc + 1 else sc )
xSepHM = meeResCont - meeM1Cont
vSepHM = meResCont - meM1Cont
aIM1 = awHead - (2.0 * xSepHM)
awM1 = cntSpan + (2.0 * meM1Cont)
idOffset = msldCont - meResCont
idOffset = if( dogbone && msldRes then max( idOffset msldRes )
               else idOffset )
alBody = alBody + (2.0 * idOffset)
aeeResM1 = xSepHM
```

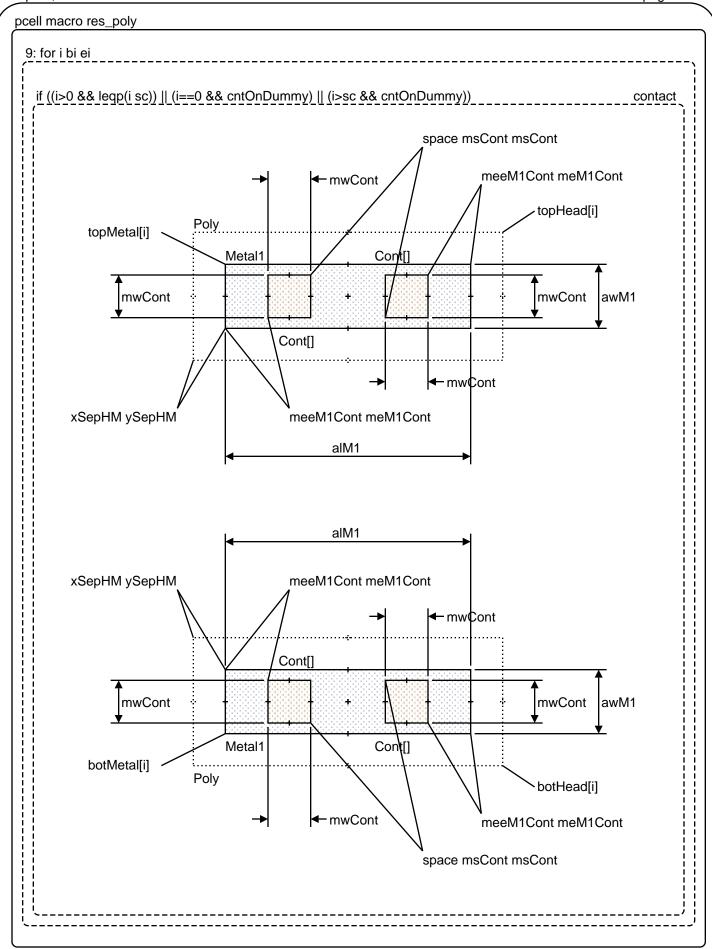
pcell macro res_poly



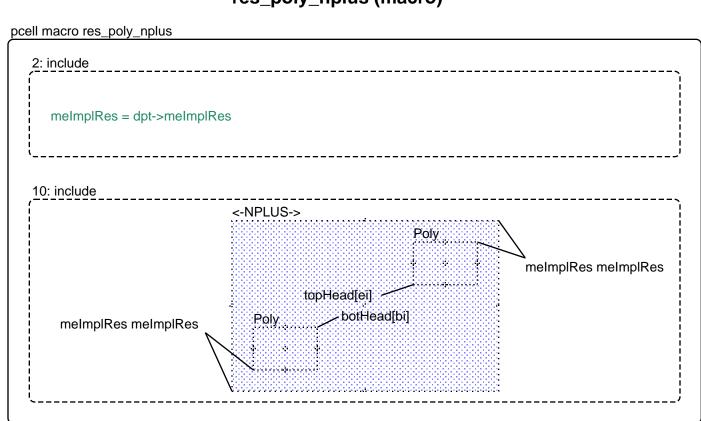




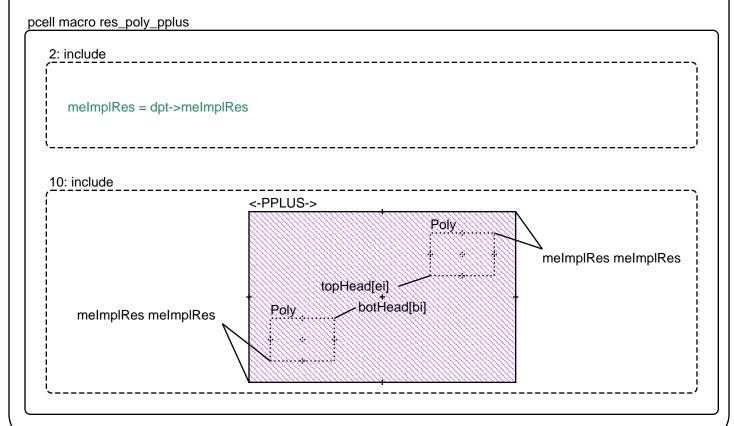




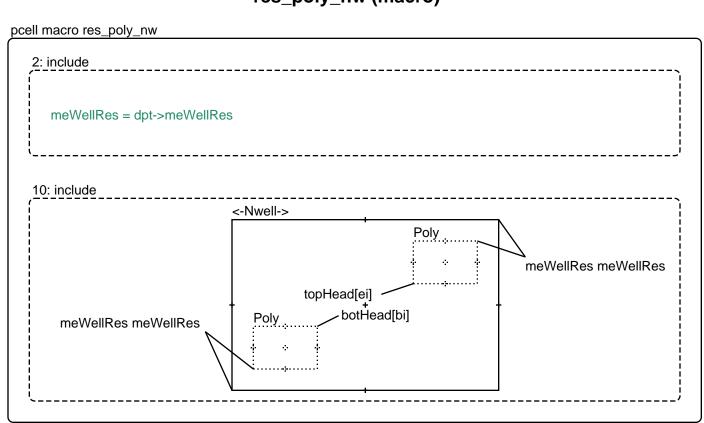
res_poly_nplus (macro)



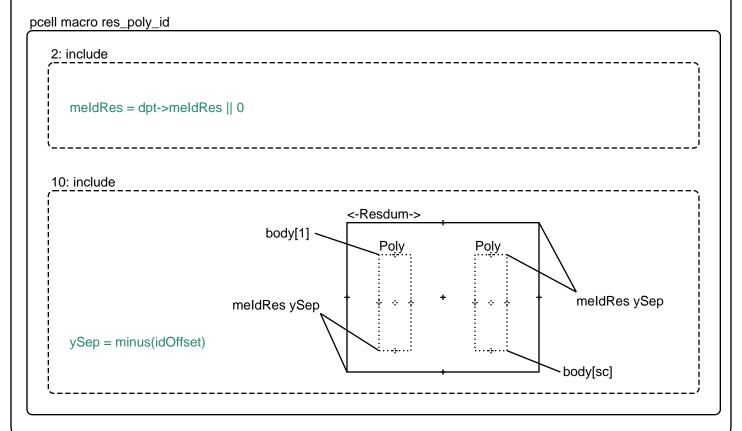
res_poly_pplus (macro)



res_poly_nw (macro)



res_poly_id (macro)



res_poly_sb (macro)

pcell macro res_poly_sb

2: include

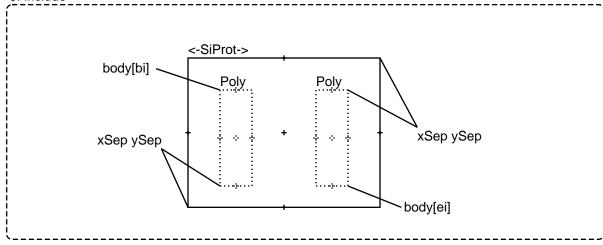
```
\label{eq:mesbres} \begin{split} \text{meSbRes} &= \text{dpt->meSbRes} \mid\mid 0 \\ \text{msSbCont} &= \text{dpt->msSbCont} \mid\mid 0 \\ \end{split} \\ \text{maSb} &= \text{dpt->maSb} \end{split}
```

10: include

1: include

2: if maSb

3: include



res_diff (macro)

pcell macro res diff

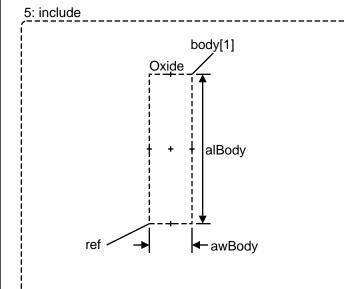
```
1: include
```

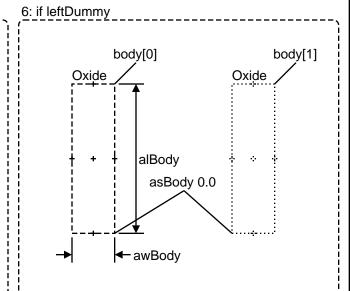
```
dpt = PasGetDeviceProps( cv )
scale = dpt->scale
epsilon = grid / 10.0
mwRes = dpt->mwRes
mwCont = dpt->mwCont
msCont = dpt->msCont
meResCont = dpt->meResCont
meeResCont = dpt->meResCont || meResCont
msIdCont = dpt->msIdCont || 0
msIdRes = dpt->msIdRes
```

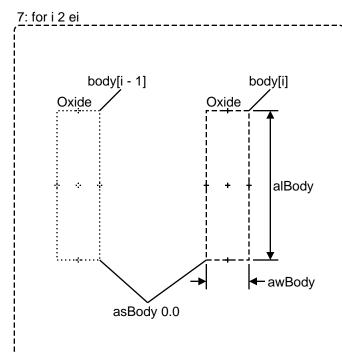
3: include

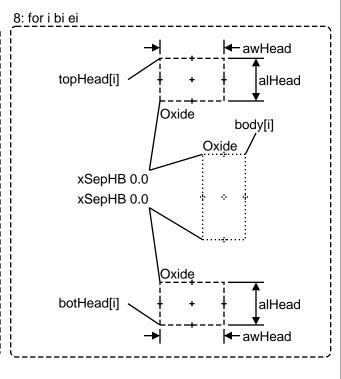
```
alBody = sl / scale
awBody = w / scale
sc = max(1 segments)
awHead = max( awBody mwRes (mwCont + (2.0 * meeResCont)) )
cntSpan = ( max(1 cntRows ) * ( mwCont + msCont ) ) - msCont
alHead = max( mwRes (cntSpan + (2.0 * meResCont)) )
asHead = msRes
asBody = asHead + awHead - awBody
dogbone = awBody + epsilon < awHead
xSepHB = if( dogbone then PasCeiling( ( (awHead - awBody) / 2.0 ) grid t ) else 0.0 )
bi = if( leftDummy then 0 else 1 )
ei = if( rightDummy then sc + 1 else sc )
xSepHM = meeResCont - meeM1Cont
vSepHM = meResCont - meM1Cont
aIM1 = awHead - (2.0 * xSepHM)
awM1 = cntSpan + (2.0 * meM1Cont)
idOffset = msldCont - meResCont
idOffset = if( dogbone && msldRes then max( idOffset msldRes )
               else idOffset )
alBody = alBody + (2.0 * idOffset)
aeeResM1 = xSepHM
```

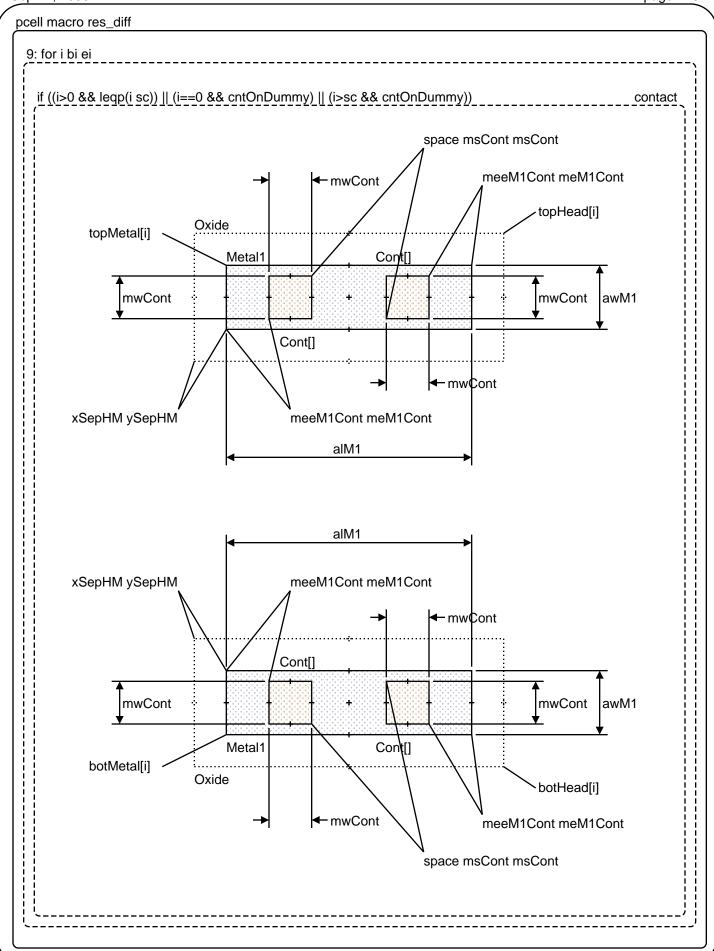
pcell macro res_diff



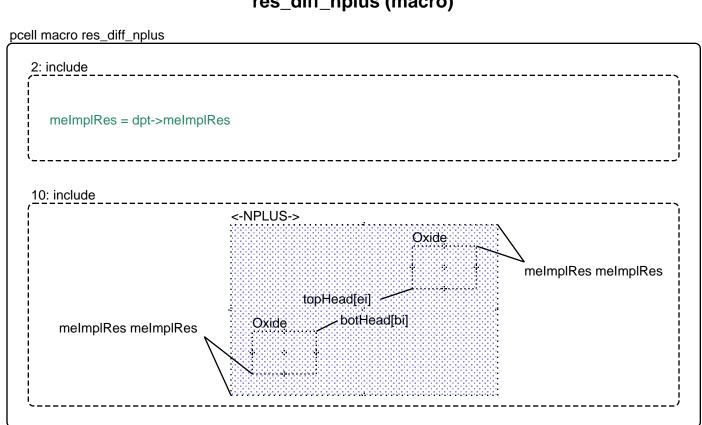




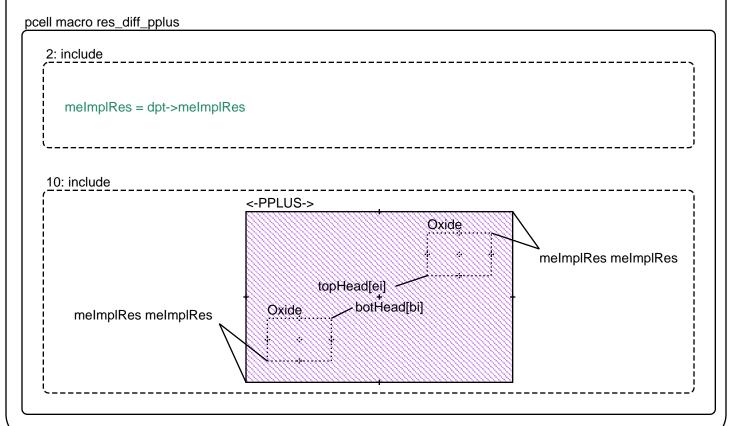




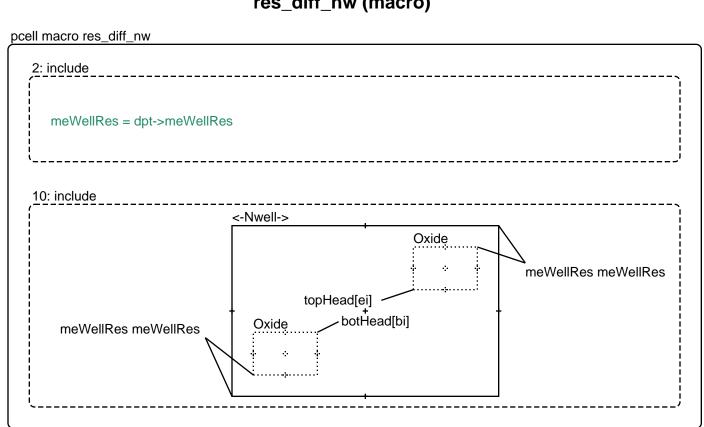
res_diff_nplus (macro)



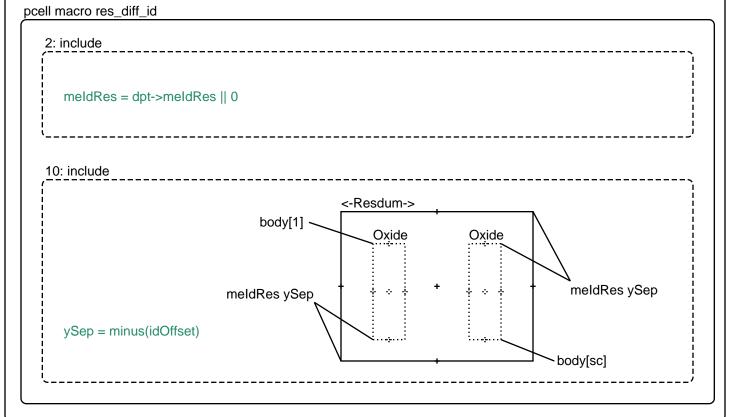
res_diff_pplus (macro)



res_diff_nw (macro)



res_diff_id (macro)



res_nwell (macro)

pcell macro res nwell

```
1: include
```

```
dpt = PasGetDeviceProps( cv )
scale = dpt->scale
epsilon = grid / 10.0
                                                 msRes = dpt->msRes
mwRes = dpt->mwRes
mwDiff = dpt->mwDiff
                                                 msDiff = dpt->msDiff || 0
meResDiff = dpt->meResDiff
                                                 meeResDiff = dpt->meeResDiff || meResDiff
                                                 mwM1 = dpt->mwM1
mwCont = dpt->mwCont
msCont = dpt->msCont
                                                 msM1 = dpt->msM1
meDiffCont = dpt->meDiffCont
                                                 meM1Cont = dpt->meM1Cont
meeDiffCont = dpt->meeDiffCont || meDiffCont
                                                meeM1Cont = dpt->meeM1Cont || meM1Cont
meImplDiff = dpt->meImplDiff
```

```
1: include
    alBody = sl / scale
    awBody = w / scale
    sc = max(1 segments)
    tmp = 2.0 * meeResDiff
    awDiff = max( (awBody - tmp) (mwRes - tmp) (mwCont + (2.0 * meeDiffCont)))
    awHead = awDiff + tmp
    asHead = max( msRes ( msDiff - tmp ) )
    asBody = asHead + awHead - awBody
    dogbone = awBody + epsilon < awHead
    xSepHB = if( dogbone then PasCeiling( ( (awHead - awBody) / 2.0 ) grid t ) else 0.0 )
    bi = if( leftDummy then 0 else 1 )
    ei = if( rightDummy then sc + 1 else sc )
    xSepDM = meeDiffCont - meeM1Cont
    ySepDM = meDiffCont - meM1Cont
    aIM1 = awDiff - (2.0 * xSepDM)
    idOffset = meImplDiff - meResDiff
    alBody = alBody + (2.0 * idOffset)
    aeeResM1 = xSepDM + meeResDiff
```

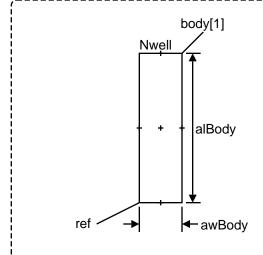
pcell macro res_nwell

```
three
```

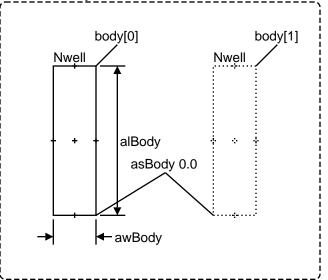
2: include

```
\begin{split} &tmp=2.0 * meResDiff\\ &cntSpan=(\ max(1\ cntRows\ )*(\ mwCont+msCont\ )\ )-msCont\\ &alDiff=max(\ (\ mwRes-tmp\ )\ (cntSpan+(2.0*meDiffCont))\ )\\ &alHead=alDiff+tmp\\ &awM1=cntSpan+(2.0*meM1Cont) \end{split}
```

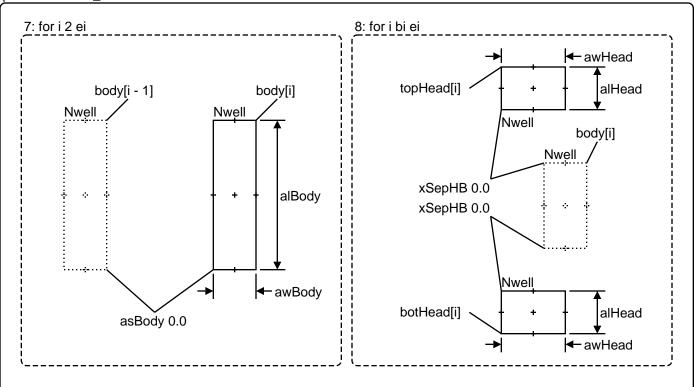
5: include

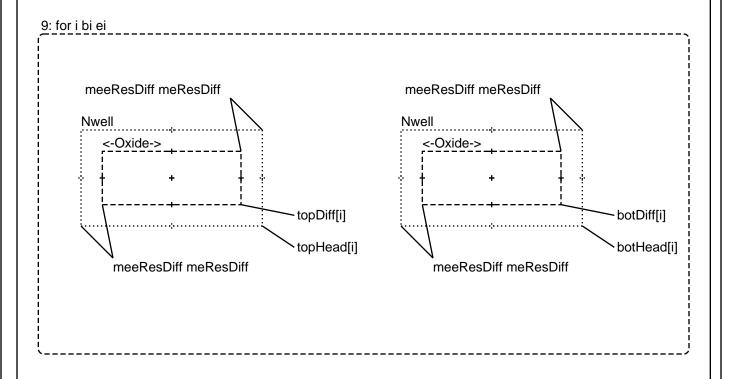


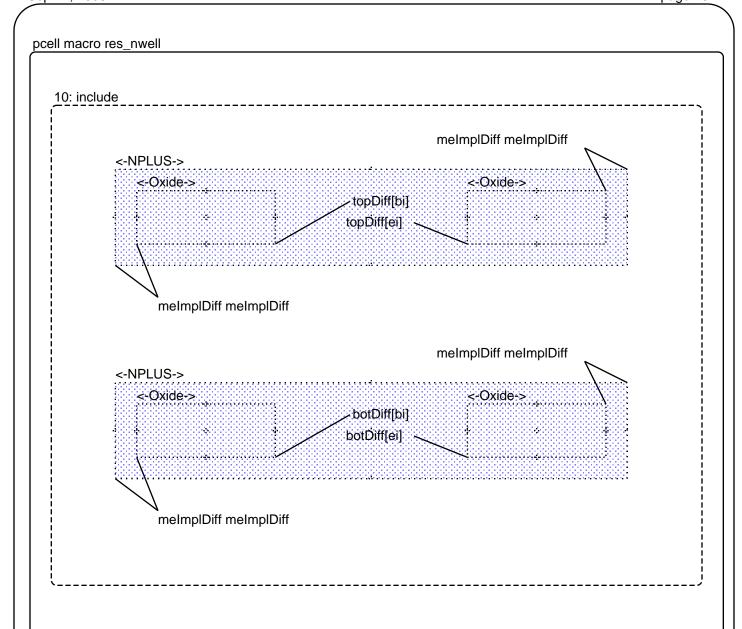
6: if leftDummy

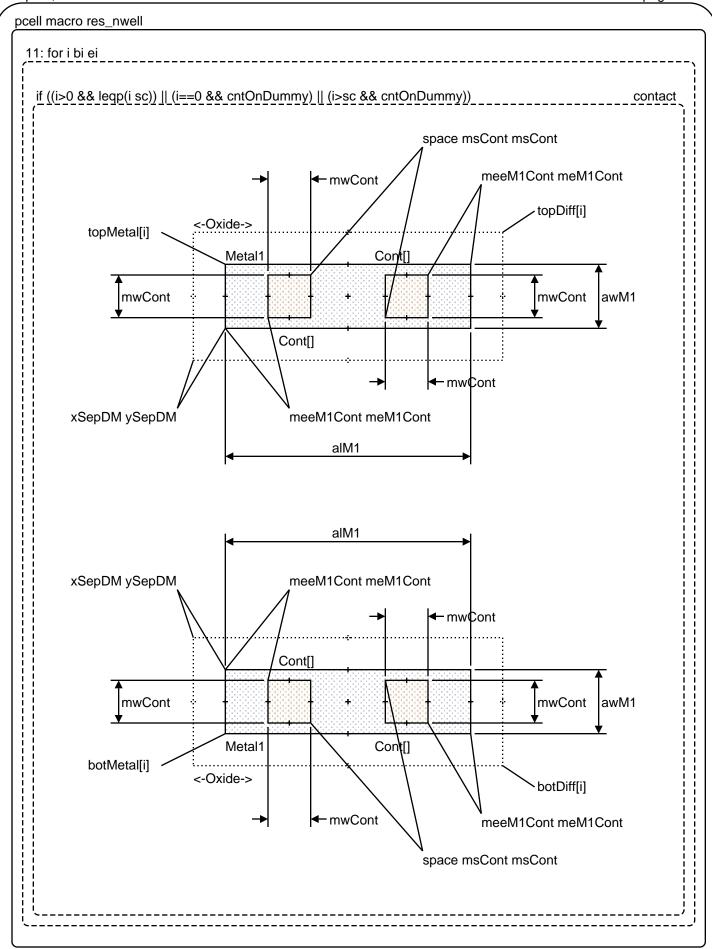


pcell macro res_nwell









res_nwell_id (macro)

pcell macro res_nwell_id

2: include

meldRes = dpt->meldRes || 0

10: include

body[1]

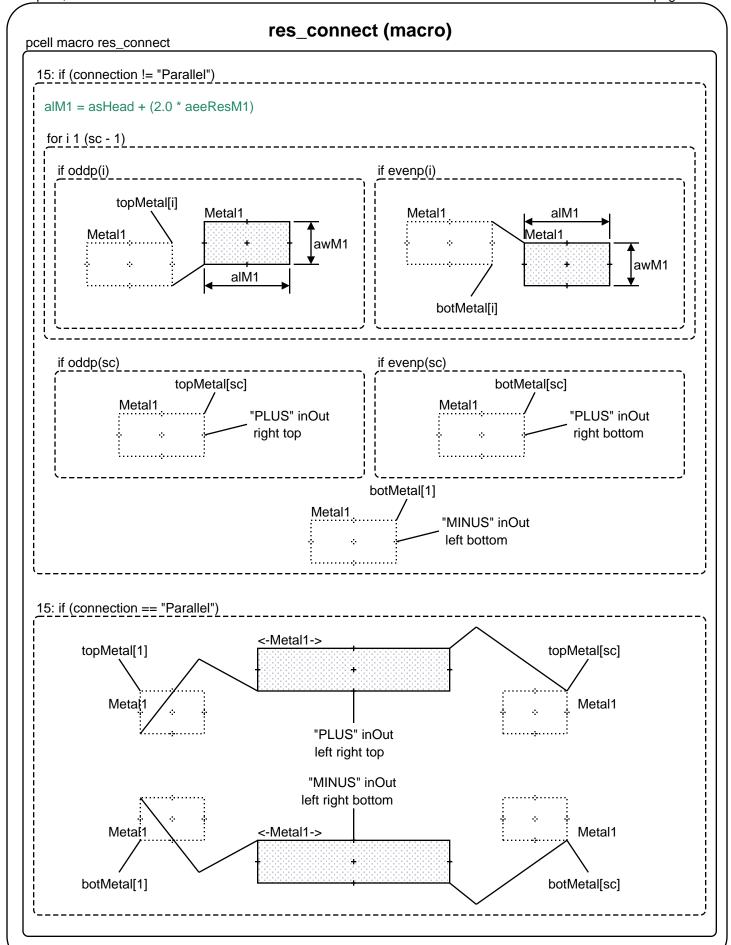
Nwell

Nwell

Nwell

MeldRes ySep

ySep = minus(idOffset)



res_m (macro)

pcell macro res m

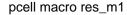
```
1: include
```

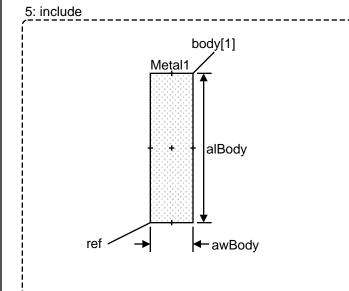
```
dpt = PasGetDeviceProps( cv )
scale = dpt->scale
epsilon = grid / 10.0
mwRes = dpt->mwRes
mwCont = dpt->mwCont
msCont = dpt->msCont
meResCont = dpt->meResCont
meeResCont = dpt->meResCont | meResCont
msIdCont = dpt->msIdCont || 0
msIdRes = dpt->msIdRes
```

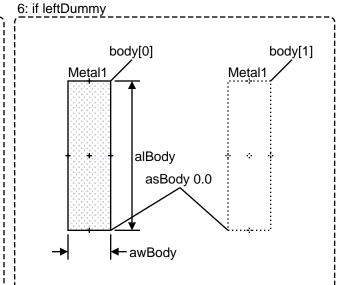
3: include

```
alBody = sl / scale
awBody = w / scale
sc = max(1 segments)
awHead = max( awBody mwRes (mwCont + (2.0 * meeResCont)) )
cntSpan = ( max(1 cntRows ) * ( mwCont + msCont ) ) - msCont
alHead = max( mwRes (cntSpan + (2.0 * meResCont)) )
asHead = msRes
asBody = asHead + awHead - awBody
dogbone = awBody + epsilon < awHead
xSepHB = if( dogbone then PasCeiling( ( (awHead - awBody) / 2.0 ) grid t ) else 0.0 )
bi = if( leftDummy then 0 else 1 )
ei = if( rightDummy then sc + 1 else sc )
xSepHM = meeResCont - meeM1Cont
vSepHM = meResCont - meM1Cont
aIM1 = awHead - (2.0 * xSepHM)
awM1 = cntSpan + (2.0 * meM1Cont)
idOffset = msldCont - meResCont
idOffset = if( dogbone && msldRes then max( idOffset msldRes )
               else idOffset )
alBody = alBody + (2.0 * idOffset)
aeeResM1 = xSepHM
```

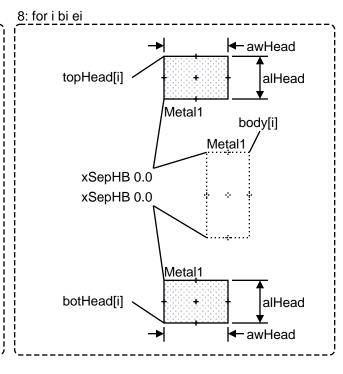
res_m1 (macro)



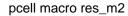


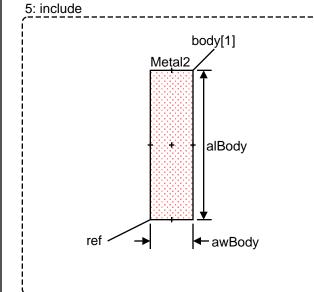


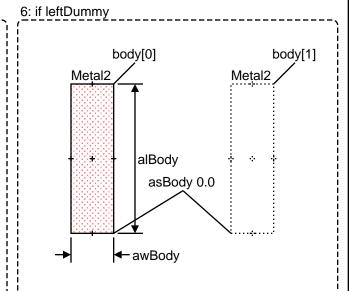
body[i - 1] body[i] Metal1 Metal1 alBody asBody 0.0

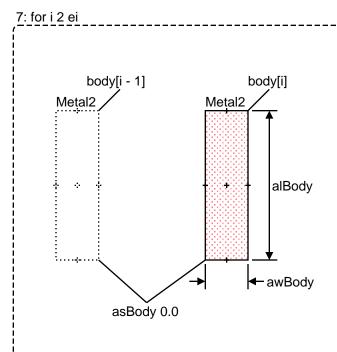


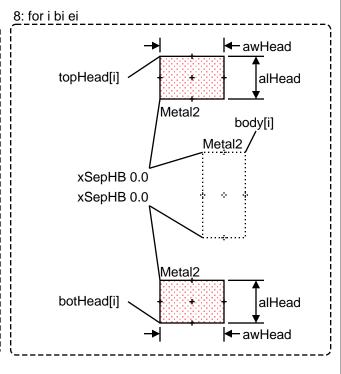
res_m2 (macro)



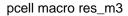


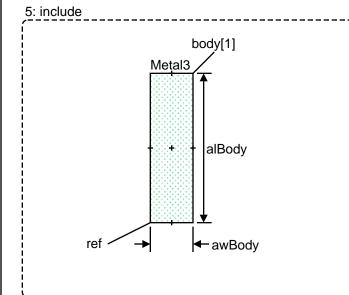


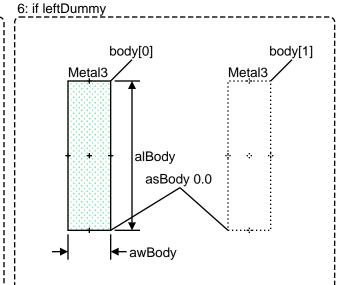


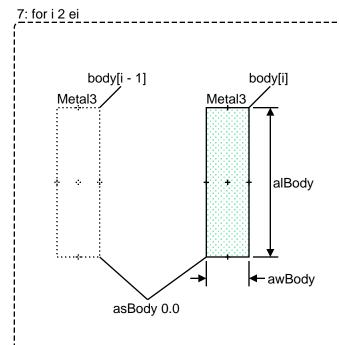


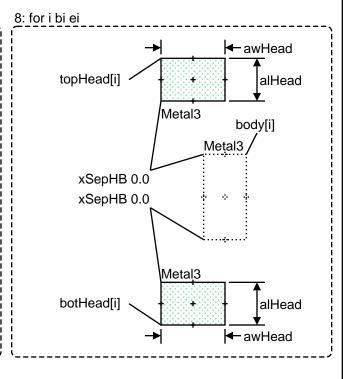
res_m3 (macro)



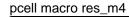


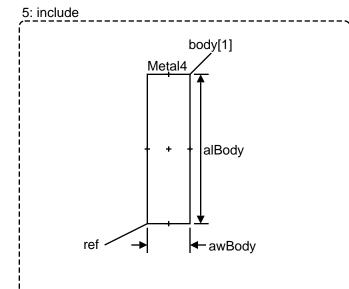


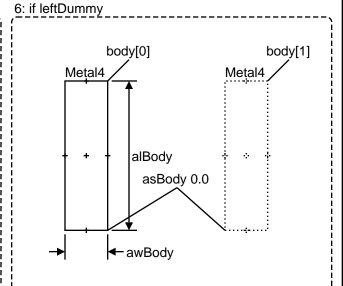


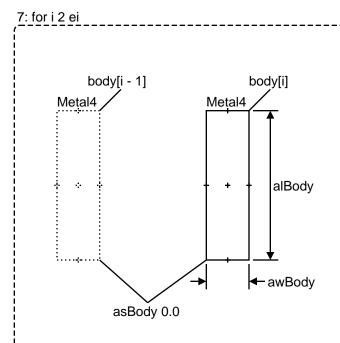


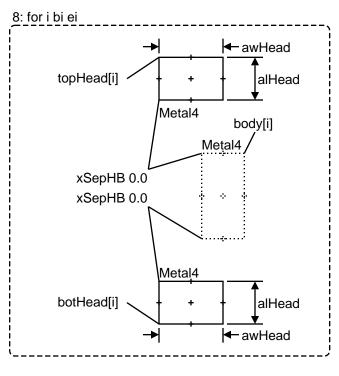
res_m4 (macro)



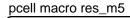


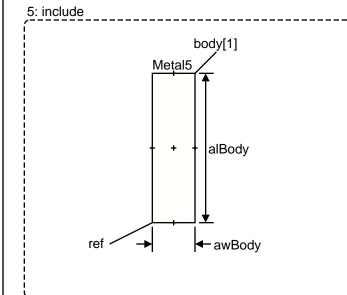


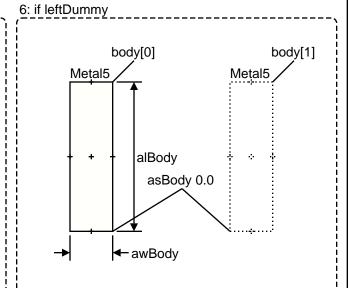


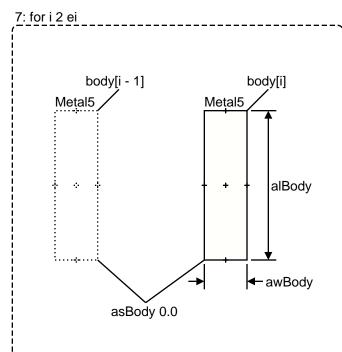


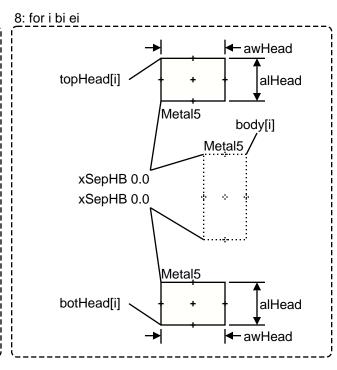
res_m5 (macro)



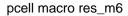


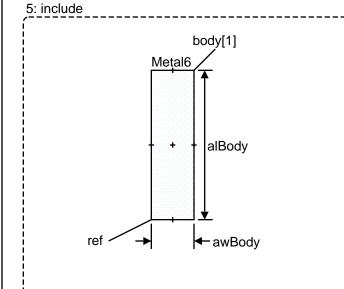


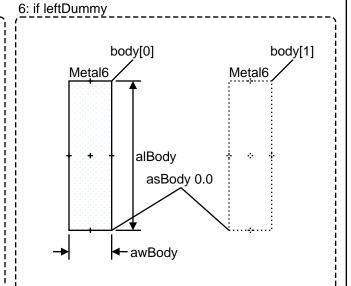


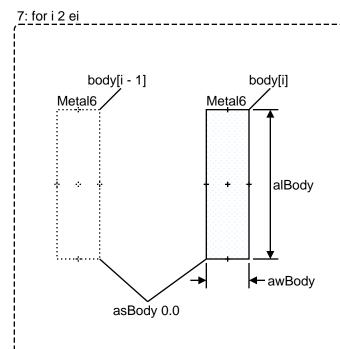


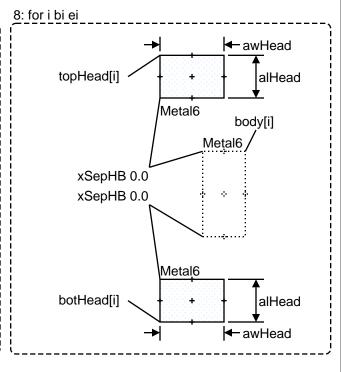
res_m6 (macro)



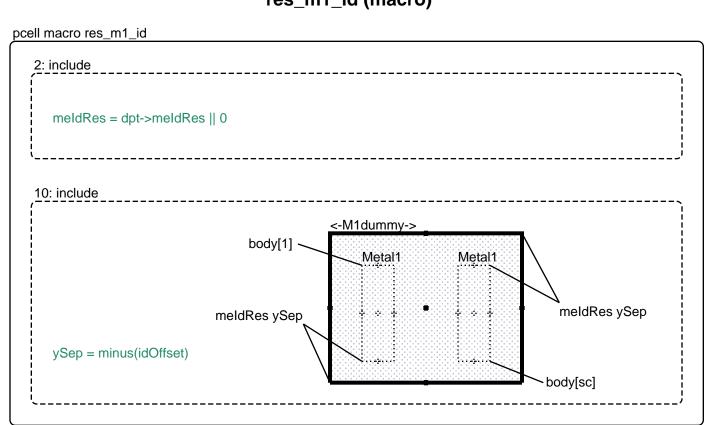




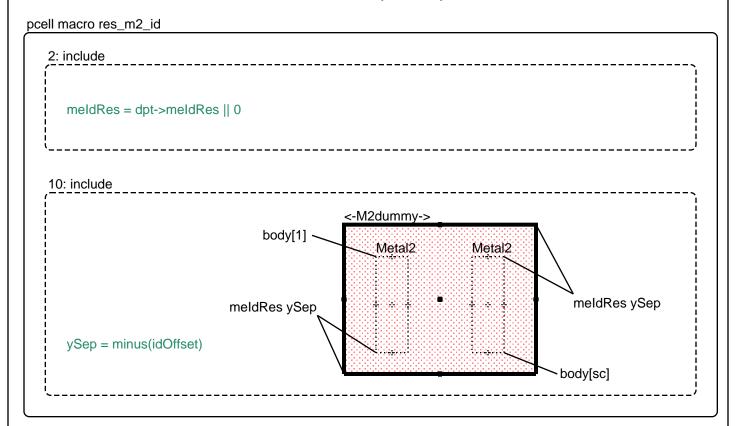




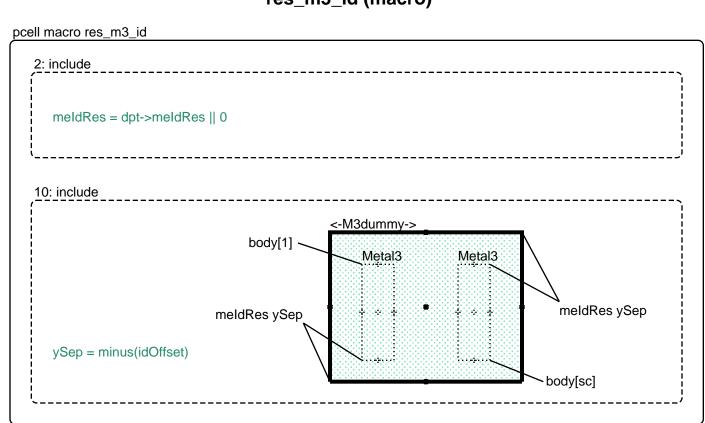
res_m1_id (macro)



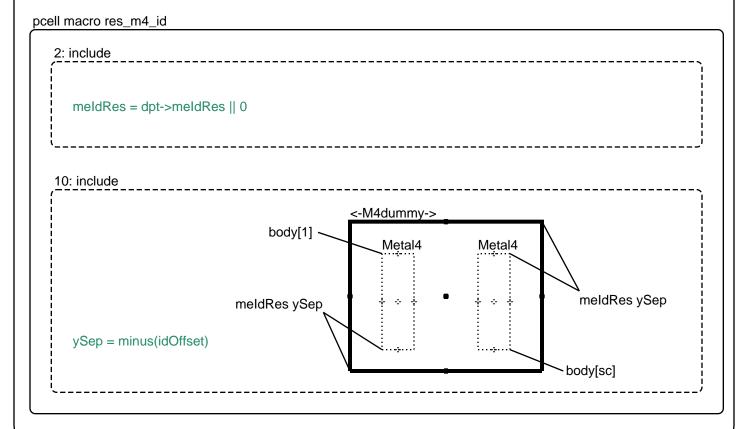
res_m2_id (macro)



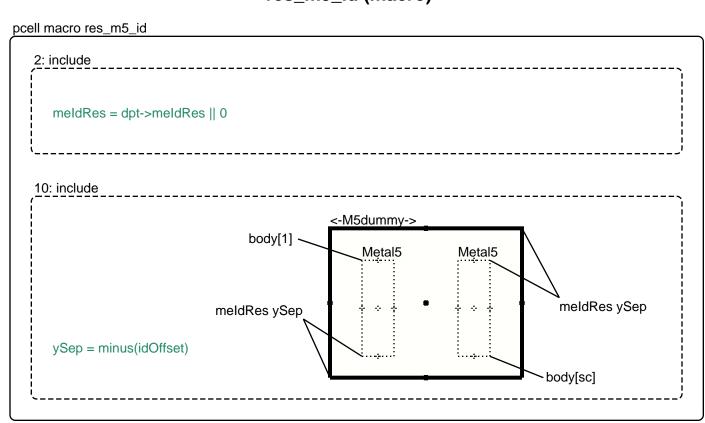
res_m3_id (macro)



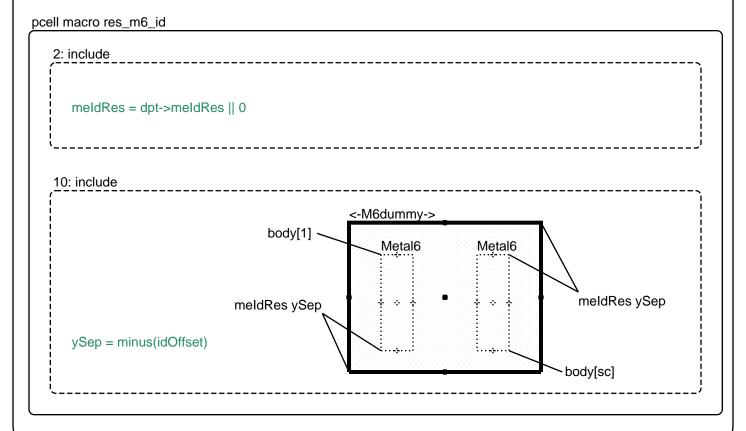
res_m4_id (macro)

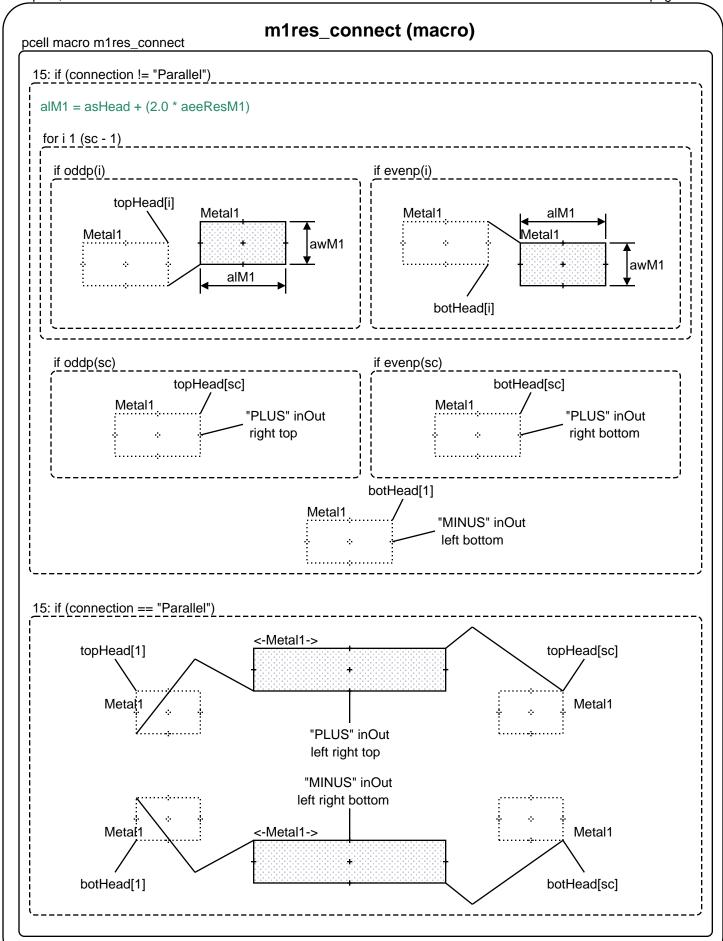


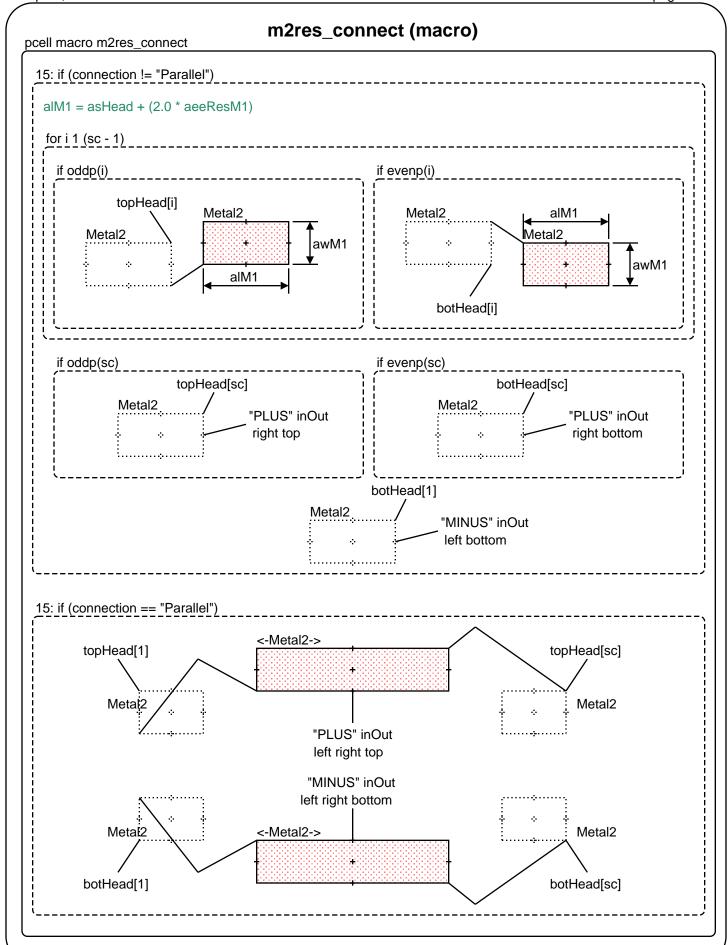
res_m5_id (macro)

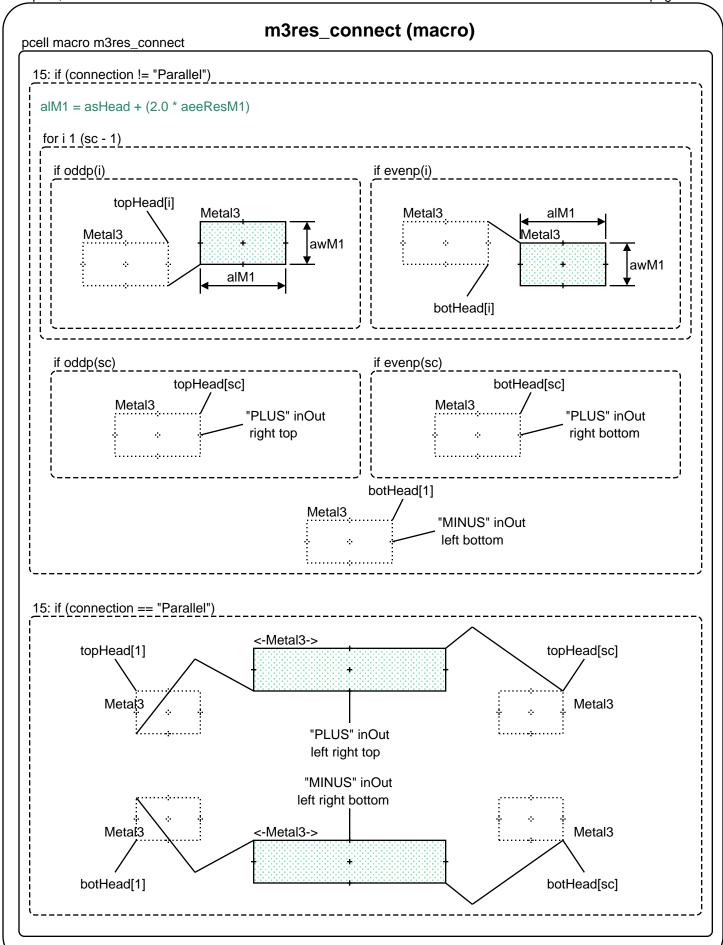


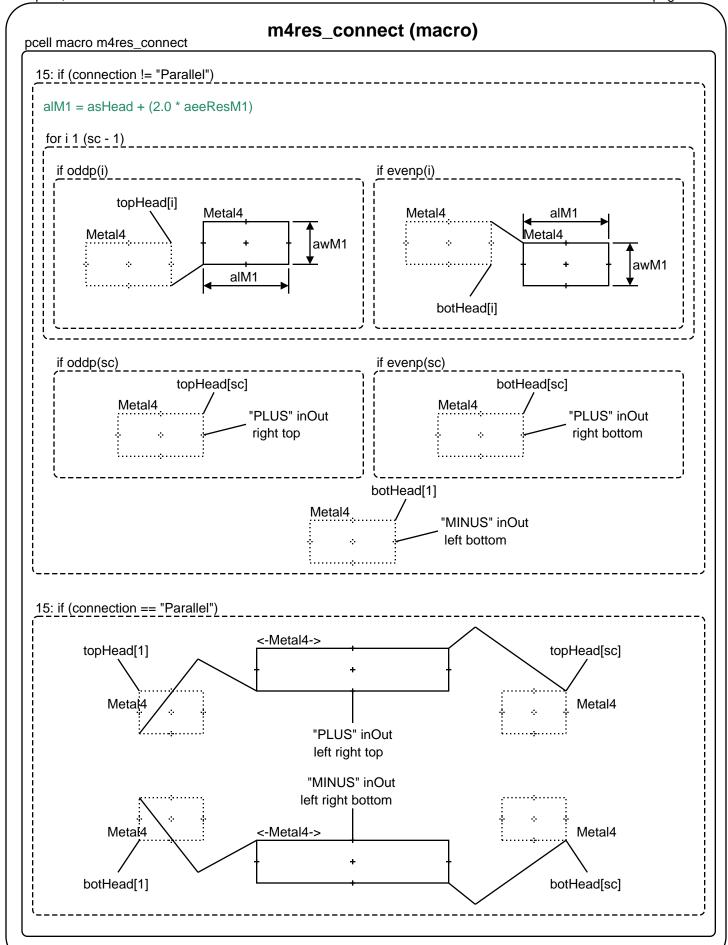
res_m6_id (macro)

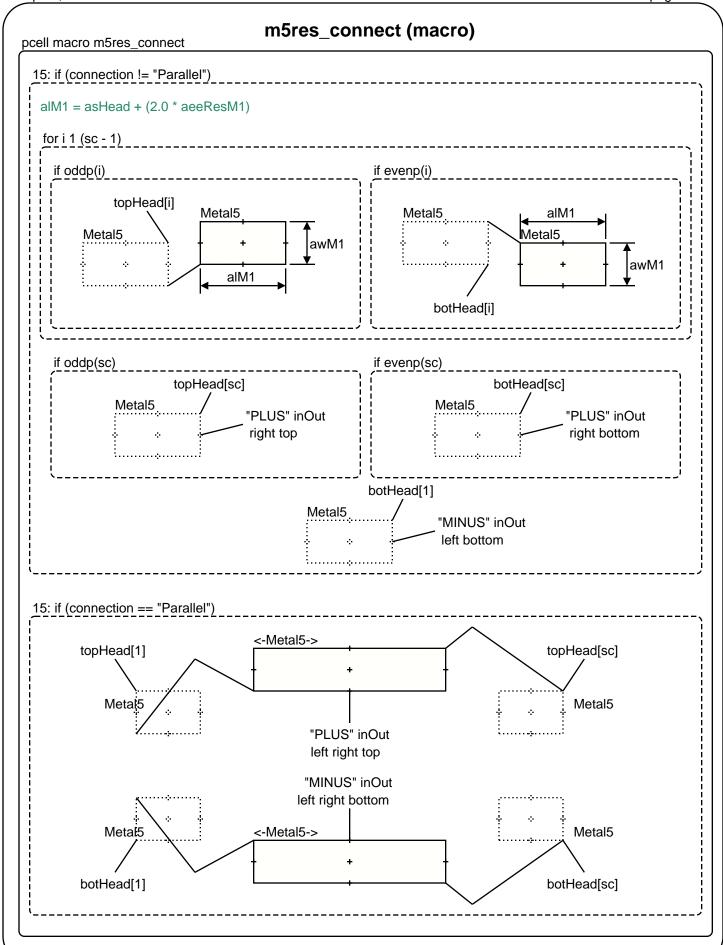


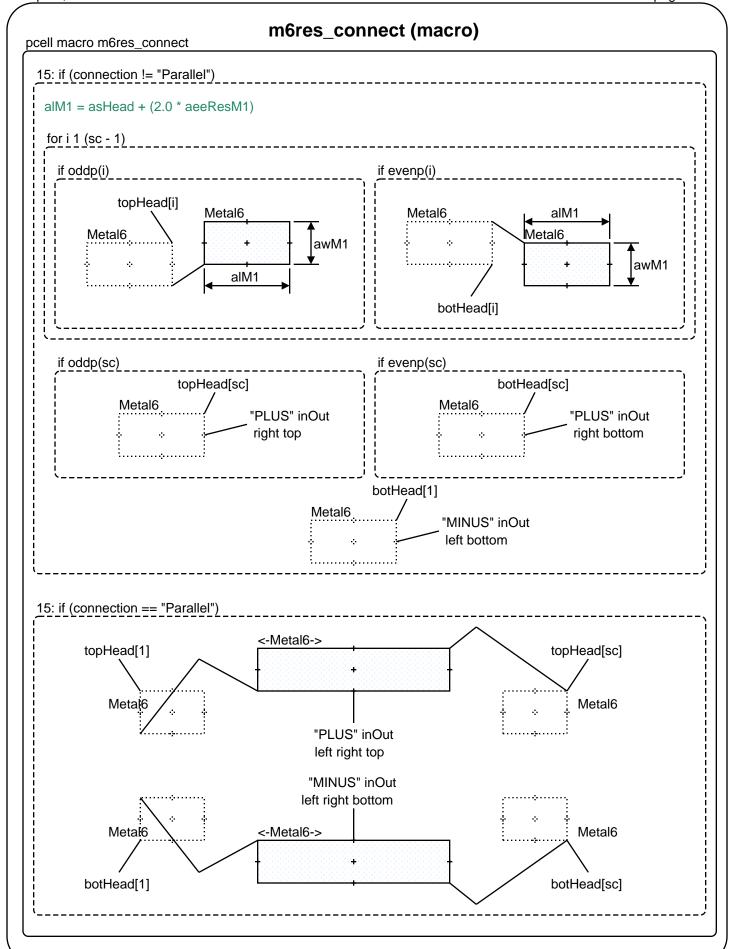












PCell Devices

Poly Resistor

pcell gpdk180 polyres

include macro res_multiSeg include macro res_poly include macro res_poly_id

High Poly Resistor

pcell gpdk180 polyhres

include macro res_multiSeg include macro res_poly include macro res_poly_id include macro res_poly_sb

N+ Diff Resistor

pcell gpdk180 nplusres

include macro res_multiSeg include macro res_diff include macro res_diff_nplus include macro res_diff_id

P+ Diff Resistor

pcell gpdk180 pplusres

include macro res_multiSeg include macro res_diff include macro res_diff_pplus include macro res_diff_id include macro res_diff_nw

NWell Resistor

pcell gpdk180 nwellres

include macro res_multiSeg include macro res_nwell include macro res_nwell_id

Metal Resistor

pcell gpdk180 mxres

include macro mres_multiSeg include macro \$mac0 include macro res_m include macro \$mac1 include macro \$mac2

Pcell Macro Table

\$cell	\$mac0	\$mac1	\$mac2
m1res	m1res_connect	res_m1	res_m1_id
m2res	m2res_connect	res_m2	res_m2_id
m3res	m3res_connect	res_m3	res_m3_id
m4res	m4res_connect	res_m4	res_m4_id
m5res	m5res_connect	res_m5	res_m5_id
m6res	m6res_connect	res_m6	res_m6_id

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L	ibrary CAP Definitions	

CDF Definitions

mimcap

CDF gpdk180 mimcap

CDF device params

grid	\$grid	private	mfg grid
scale	1.0e-6	private	dimensional scale factor
model	"mimcap"	private	device model name
category	"cap"	private	dfII library manager category

include macro cap

CDF device params

acres parame			
mwCap	4.0	private	min CAP width
xwCap	30.0	private	max CAP width
dwCap	4.0	private	default CAP width
mlCap	4.0	private	min CAP length
xlCap	30.0	private	max CAP length
dlCap	4.0	private	default CAP length
са	0.001	private	area capacitance
cf	1.0e-10	private	fringe capacitance

CDF siminfo

simulator	auCdl
componentName	"\$cell"
instParameters	(AREA M)
propMapping	(nil AREA area M m)
modelName	"\$cell"

CDF siminfo

simulator	auLvs
componentName	"\$cell"
instParameters	(area m)

CDF siminfo

simulator	spectre
instParameters	(area perim m scale trise ic tc1 tc2)
componentName	"\$cell"

CDF cellview

symbol	pas_std	cap2	symbol	
spectre	pas_std	cap2	symbol	
auCdl	pas_std	cap2	symbol	
auLvs	pas_std	cap2	symbol	
ads	pas_std	cap2	symbol	
ivpcell	\$lib	\$cell	symbol	5

CDF Macro Definitions

Capacitor

CDF macro cap

CDF parameters

name	"model"
prompt	"Model name"
defValue	gpdk180_capValue('model ?id cdfld ?returnString t)
type	"string"
display	"gpdk180_capDisplay('model)"
editable	"nil"
parseAsCEL	"yes"

CDF parameters

name	"calcParam"	
prompt	pt "Calculate Parameter"	
type	"cyclic"	
choices	list("capacitance" "length" "width")	
defValue	"capacitance"	
display	"t"	

CDF parameters

<u></u>	
name	"c"
prompt	"Capacitance"
units	"capacitance"
defValue	gpdk180_capValue('c ?id cdfld ?returnString t)
type	"string"
display	"t"
editable	"cdfgData->calcParam->value!=\"capacitance\""
parseAsNumber	"yes"
parseAsCEL	"yes"
callback	"gpdk180_capCB('c)"

ODI Paramotoro	
name	"w"
prompt	"Width"
units	"lengthMetric"
defValue	gpdk180_capValue('w ?id cdfld ?returnString t)
type	"string"
display	"t"
editable	"cdfgData->calcParam->value!=\"width\""
parseAsNumber	"yes"
parseAsCEL	"yes"
callback	"gpdk180_capCB('w)"

CDF macro cap

CDF parameters

name	" "		
prompt	"Length"	CDF parameters	
units	"lengthMetric"	name	"m"
defValue	gpdk180_capValue('I ?id cdfld ?returnString t)	prompt	"Multiplier"
type	"string"	defValue	"1"
display	"t"	type	"string"
editable	"cdfgData->calcParam->value!=\"length\""	display	"t"
parseAsNumber	"yes"	parseAsNumber	"yes"
parseAsCEL	"yes"	parseAsCEL	"yes"
callback	"gpdk180_capCB('I)"	-	

CDF parameters

name	"showSimParams"
prompt	"Show Sim Parameters"
defValue	nil
type	"boolean"
display	"gpdk180_capDisplay('showSimParams)"

CDF parameters

name	"cf"
prompt	"Fringe capacitance (F/M)"
defValue	gpdk180_capValue('cf ?id cdfld)
type	"float"
display	"gpdk180_capDisplay('cf)"
editable	"nil"

CDF parameters

name	"area"
prompt	"Area"
defValue	"iPar(\"w\")*iPar(\"I\")"
type	"string"
parseAsNumber	"yes"
parseAsCEL	"yes"
editable	"nil"

CDF parameters

obi parametere		
name	"perim"	
prompt	"Perim"	
defValue	"2*iPar(\"w\")+2*iPar(\"l\")"	
type	"string"	
parseAsNumber	"yes"	
parseAsCEL	"yes"	
editable	"nil"	

name	"ca"
prompt	"Area capacitance (F/M^2)"
defValue	gpdk180_capValue('ca ?id cdfld)
type	"float"
display	"gpdk180_capDisplay('ca)"
editable	"nil"

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CDF macro cap

CDF parameters

name	"ic"
prompt	"Initial condition"
units "voltage"	
defValue	""
type	"string"
display	"gpdk180_capDisplay('ic)"
parseAsNumber	"yes"
parseAsCEL	"yes"

CDF parameters

CDF parameters

ODI parameters		ODI parameters	
name	"scale"	name	"trise"
prompt	"Scale factor"	prompt	"Temp rise from ambient"
defValue	""	defValue	""
type	"string"	type	"string"
display	"gpdk180_capDisplay('scale)"	display	"gpdk180_capDisplay('trise)"
parseAsNumber	"yes"	parseAsNumber	"yes"
parseAsCEL	"yes"	parseAsCEL	"yes"

CDF parameters

CDF parameters

name	"tc1"	name	"tc2"
prompt	"Temperature coefficient 1"	prompt	"Temperature coefficient 2"
defValue	""	defValue	""
type	"string"	type	"string"
display	"gpdk180_capDisplay('tc1)"	display	"gpdk180_capDisplay('tc2)"
parseAsNumber	"yes"	parseAsNumber	"yes"
parseAsCEL	"yes"	parseAsCEL	"yes"

CDF parameters

ODI paramotoro		ODI paran	101010
name	"dtemp"	name	"polyCoef"
prompt	"Temperature difference"	prompt	"Number of Polynomial Coeffs"
defValue	""	defValue	0
type	"string"	type	"int"
display	"gpdk180_capDisplay('dtemp)"	display	"gpdk180_capDisplay('polyCoef)"
parseAsNumber	"yes"		
parseAsCEL	"ves"]	

CDF macro cap

CDF parameters

name	"c1"
prompt	"Poly Coeff 1"
defValue	""
type	"string"
use	"cdfgData->polyCoef->value >= 1"
display	"gpdk180_capDisplay('c1)"
parseAsNumber	"yes"
parseAsCEL	"yes"

CDF parameters

name	"c2"
prompt	"Poly Coeff 2"
defValue	""
type	"string"
use	"cdfgData->polyCoef->value >= 2"
display	"gpdk180_capDisplay('c2)"
parseAsNumber	"yes"
parseAsCEL	"yes"

obi parametere	
name	"c3"
prompt	"Poly Coeff 3"
defValue	""
type	"string"
use	"cdfgData->polyCoef->value >= 3"
display	"gpdk180_capDisplay('c3)"
parseAsNumber	"yes"
parseAsCEL	"yes"

CDF macro cap

CDF parameters

name	"c4"
prompt	"Poly Coeff 4"
defValue	""
type	"string"
use	"cdfgData->polyCoef->value >= 4"
display	"gpdk180_capDisplay('c4)"
parseAsNumber	"yes"
parseAsCEL	"yes"

CDF parameters

name	"c5"
prompt	"Poly Coeff 5"
defValue	""
type	"string"
use	"cdfgData->polyCoef->value >= 5"
display	"gpdk180_capDisplay('c5)"
parseAsNumber	"yes"
parseAsCEL	"yes"

ODI Paramotoro	
name	"c6"
prompt	"Poly Coeff 6"
defValue	""
type	"string"
use	"cdfgData->polyCoef->value >= 6"
display	"gpdk180_capDisplay('c6)"
parseAsNumber	"yes"
parseAsCEL	"yes"

CDF macro cap

CDF parameters

name	"c7"
prompt	"Poly Coeff 7"
defValue	""
type	"string"
use	"cdfgData->polyCoef->value >= 7"
display	"gpdk180_capDisplay('c7)"
parseAsNumber	"yes"
parseAsCEL	"yes"

CDF parameters

name	"c8"
prompt	"Poly Coeff 8"
defValue	""
type	"string"
use	"cdfgData->polyCoef->value >= 8"
display	"gpdk180_capDisplay('c8)"
parseAsNumber	"yes"
parseAsCEL	"yes"

name	"c9"
prompt	"Poly Coeff 9"
defValue	""
type	"string"
use	"cdfgData->polyCoef->value >= 9"
display	"gpdk180_capDisplay('c9)"
parseAsNumber	"yes"
parseAsCEL	"yes"

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CDF macro cap

CDF parameters

name	"c10"
prompt	"Poly Coeff 10"
defValue	""
type	"string"
use	"cdfgData->polyCoef->value >= 10"
display	"gpdk180_capDisplay('c10)"
parseAsNumber	"yes"
parseAsCEL	"yes"

CDF parameters

name	"c11"
prompt	"Poly Coeff 11"
defValue	пп
type	"string"
use	"cdfgData->polyCoef->value >= 11"
display	"gpdk180_capDisplay('c11)"
parseAsNumber	"yes"
parseAsCEL	"yes"

ODI Paramotoro	
name	"c12"
prompt	"Poly Coeff 12"
defValue	""
type	"string"
use	"cdfgData->polyCoef->value >= 12"
display	"gpdk180_capDisplay('c12)"
parseAsNumber	"yes"
parseAsCEL	"yes"

CDF macro cap

CDF parameters

name	"c13"
prompt	"Poly Coeff 13"
defValue	""
type	"string"
use	"cdfgData->polyCoef->value >= 13"
display	"gpdk180_capDisplay('c13)"
parseAsNumber	"yes"
parseAsCEL	"yes"

CDF parameters

name	"c14"
prompt	"Poly Coeff 14"
defValue	""
type	"string"
use	"cdfgData->polyCoef->value >= 14"
display	"gpdk180_capDisplay('c14)"
parseAsNumber	"yes"
parseAsCEL	"yes"

	
name	"c15"
prompt	"Poly Coeff 15"
defValue	""
type	"string"
use	"cdfgData->polyCoef->value >= 15"
display	"gpdk180_capDisplay('c15)"
parseAsNumber	"yes"
parseAsCEL	"yes"

CDF macro cap

CDF parameters

name	"c16"
prompt	"Poly Coeff 16"
defValue	ш
type	"string"
use	"cdfgData->polyCoef->value >= 16"
display	"gpdk180_capDisplay('c16)"
parseAsNumber	"yes"
parseAsCEL	"yes"

CDF parameters

name	"c17"
prompt	"Poly Coeff 17"
defValue	""
type	"string"
use	"cdfgData->polyCoef->value >= 17"
display	"gpdk180_capDisplay('c17)"
parseAsNumber	"yes"
parseAsCEL	"yes"

ODI Paramotoro	
name	"c18"
prompt	"Poly Coeff 18"
defValue	""
type	"string"
use	"cdfgData->polyCoef->value >= 18"
display	"gpdk180_capDisplay('c18)"
parseAsNumber	"yes"
parseAsCEL	"yes"

CDF macro cap

CDF parameters

name	"c19"
prompt	"Poly Coeff 19"
defValue	""
type	"string"
use	"cdfgData->polyCoef->value >= 19"
display	"gpdk180_capDisplay('c19)"
parseAsNumber	"yes"
parseAsCEL	"yes"

CDF parameters

CDF parameters

		_		
name	"c20"	na	me	"source"
prompt	"Poly Coeff 20"	pr	ompt	"Source component"
defValue	""	ty	ре	"boolean"
type	"string"	di	splay	"gpdk180_capDisplay('source)"
use	"cdfgData->polyCoef->value >= 20"			
display	"gpdk180_capDisplay('c20)"			
parseAsNumber	umber "yes"			
parseAsCEL	"yes"			

CDF parameters

	101010
name	"qmode"
prompt	"Quality type"
defValue	"ideal"
choices	'("ideal" "Constant Conductance" "Square Root" "Constant Q")
type	"cyclic"
display	"gpdk180_capDisplay('qmode)"

ODI Paramotoro	
name	"q"
prompt	"Quality"
defValue	""
type	"string"
display	"gpdk180_capDisplay('q)"
parseAsNumber	"yes"
parseAsCEL	"yes"

CDF macro cap

CDF parameters

name	"freq"
prompt	"Frequency"
units	"frequency"
defValue	""
type	"string"
display	"gpdk180_capDisplay('freq)"
parseAsNumber	"yes"
parseAsCEL	"yes"

CDF parameters

name	"pasUpdateParamList"	
prompt	"CDF Param Update List"	
defValue	"c w I"	
type	"string"	
display	"nil"	

CDF siminfo

simulator	auCdl
netlistProcedure	ansCdlCompPrim
instParameters	(C M)
termOrder	(PLUS MINUS)
propMapping	(nil C c M m)
namePrefix	"C"

CDF siminfo

simulator	auLvs
propMapping	nil
netlistProcedure	ansLvsCompPrim
instParameters	(c m)
termOrder	(PLUS MINUS)
permuteRule	"(p PLUS MINUS)"
namePrefix	"C"

CDF siminfo

simulator	ams
instParameters	(c w l m scale trise ic tc1 tc2)
termOrder	(PLUS MINUS)
isPrimitive	t

CDF macro cap

CDF siminfo

simulator	spectre
propMapping	nil
namePrefix	"C"
otherParameters	(model)
instParameters	(c w l m scale trise ic tc1 tc2)
termOrder	(PLUS MINUS)
termMapping	(nil PLUS \:1 MINUS "(FUNCTION minus(root(\"PLUS\")))")
componentName	capacitor

CDF siminfo

simulator	ads
netlistProcedure	ADSsimCompPrim
otherParameters	(model)
instParameters	(Area Periph _M scale Trise ic tc1 tc2)
componentName	nil
termOrder	(PLUS MINUS)
termMapping	(nil PLUS ":P1" MINUS ":minus.P1")
propMapping	(nil Area area Periph perim _M m Trise trise)
typeMapping	nil
uselib	nil

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CDF macro cap

CDF properties

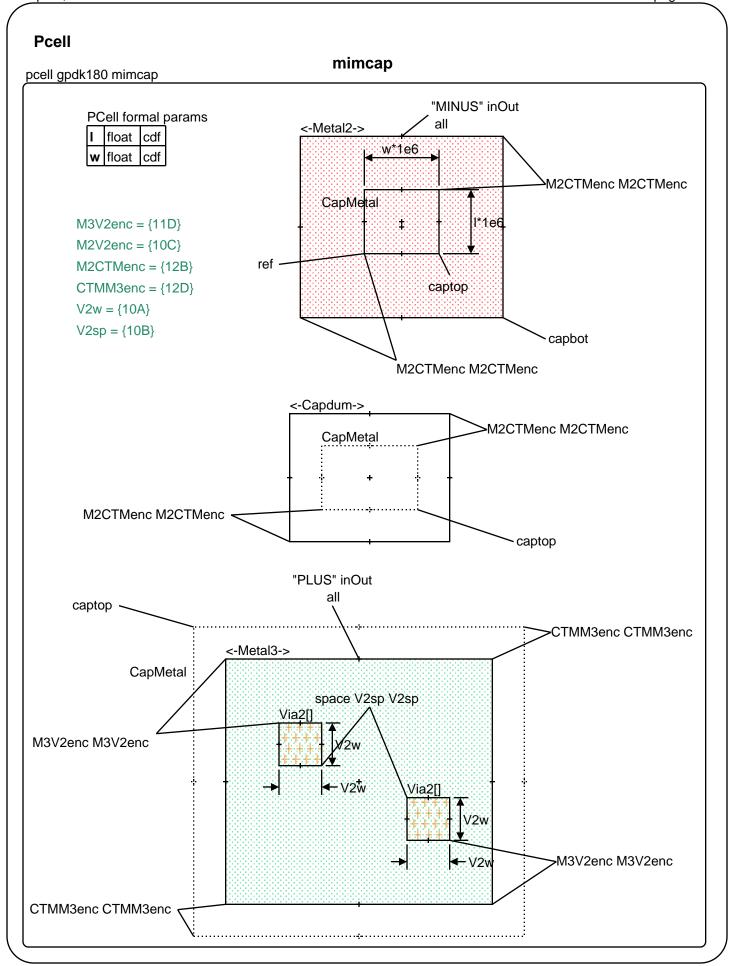
<u> </u>	
formInitProc	"PasCdfFormInit"
doneProc	""
buttonFieldWidth	340
fieldHeight	35
fieldWidth	350
promptWidth	175
instDisplayMode	"instName"
instNameType	"schematic"
netNameType	"schematic"
paramDisplayMode	"parameter"
paramEvaluate	"nil nil nil t nil"
paramSimType	"DC"
termDisplayMode	"netName"
termSimType	"DC"
opPointLabelSet	"j"
paramLabelSet	"-model c m"

Callbacks

mimcap

CDF Callback

```
procedure(gpdk180_capCB(param)
                                                 ;_Feb 13 03 quoc 3858
 let((procName cdfld libName cellName dpt grid scale sGrid epsilon
    ca cf minW maxW minL maxL todo paramld cap width length
    ael_C ael_W ael_L)
  procName = "gpdk180_capCB"
  cdfld = cdfgData
  caseq( concat(cdfld~>type)
    ((cellData baseCellData userCellData)
     libName = cdfld~>id~>lib~>name
     cellName = cdfld~>id~>name
    )
    (instData
    libName = cdfld~>id~>master~>libName
     cellName = cdfld~>id~>master~>cellName
    )
    (t
    PasAttention()
     error("%s: unexpected CDF data type - %L" procName cdfld~>type)
  ); ** caseq concat **
  dpt = PasGetPdkParams(libName cellName)
  grid = dpt->grid
  scale = dpt->scale
  sGrid = grid*scale
  epsilon = sGrid/10.0
  ca = gpdk180_capValue('ca ?dpt dpt)
  cf = gpdk180_capValue('cf ?dpt dpt)
  minW = gpdk180_capValue('w ?dpt dpt ?type 'min)
  maxW = gpdk180_capValue('w ?dpt dpt ?type 'max)
  minL = gpdk180_capValue('l ?dpt dpt ?type 'min)
  maxL = gpdk180_capValue('l ?dpt dpt ?type 'max)
  ;; process param argument
  todo = list(nil)
  caseq( param
    (c
     paramld = cdfld->c
     if( cdfld->calcParam->value == "width" then
      todo = cons('w todo)
     else
      todo = cons('I todo)
    )
     paramld = cdfld->w...
```



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OLD CDF/PCELL/CA	ALLBACK Definitions	

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	Junction Varactors	

Junction Varactor Pcells xjvar_w40 pcell

pcell gpdk180 xjvar_w40

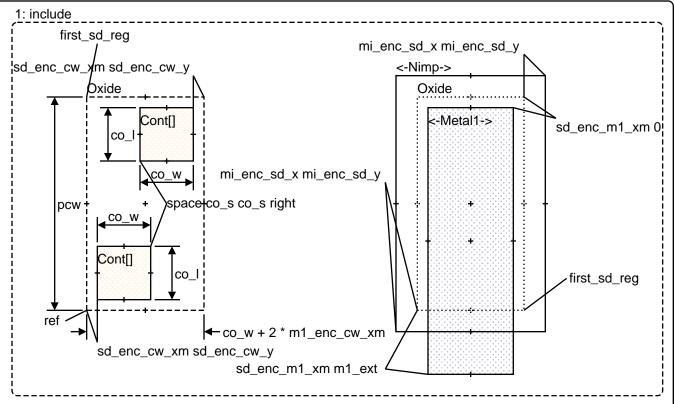
```
pcw = cdfParseFloatString(w) * 1e6
pcl = cdfParseFloatString(I) * 1e6
pcnf = fix(cdfParseFloatString(nf))
dpt = PasGetDeviceProps( cv )
co_w = dpt->CwW
co_I = dpt->CwW
co_s = dpt->CwSp
sd_enc_cw_xm = dpt->M1CwEncXm
sd_enc_cw_xp = gpdk180_PDKsnapToGrid((pcl - co_w) / 2 grid)
sd_enc_cw_x = dpt->SDCwEncY
sd_enc_cw_y = dpt->SDCwEncY
m1_ext = dpt->M1Ext
m1_enc_cw_xm = dpt->M1CwEncXm
m1_enc_cw_xp = dpt->M1CwEncXp
mi_enc_sd_x = dpt->MISDEncX
mi_enc_sd_y = dpt->MISDEncY
sd_enc_m1_xm = sd_enc_cw_xm - m1_enc_cw_xm
sd_enc_m1_xp = sd_enc_cw_xp - m1_enc_cw_xp
pi_sd_enc_x = dpt->PISDEncX
pi_sd_enc_y = dpt->PISDEncY
sd_sp = dpt->SDSp
nw_enc_sd_x = dpt->NWSDEncX
nw_enc_sd_y = dpt->NWSDEncY
ri enc sd x = dpt->RISDEncX
ri_enc_sd_y = dpt->RISDEncY
ri2_enc_sd_x = dpt->RI2SDEncX
ri2_enc_sd_y = dpt->RI2SDEncY
m1_sw = dpt->M1SW
msPwNw = 0.5
mwPwGr = 0.6
```

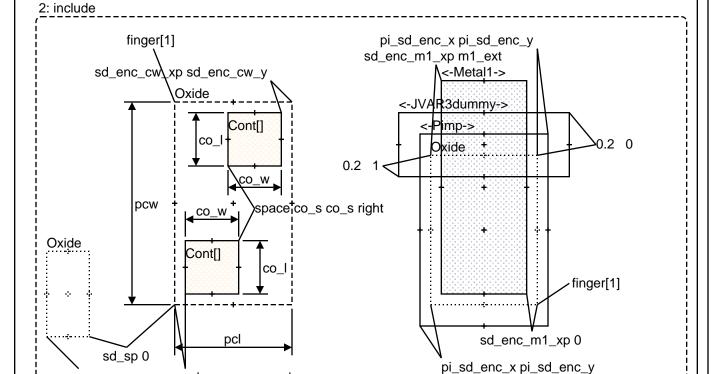
PCell formal params

w	string	cdf
I	string	cdf
nf	string	cdf

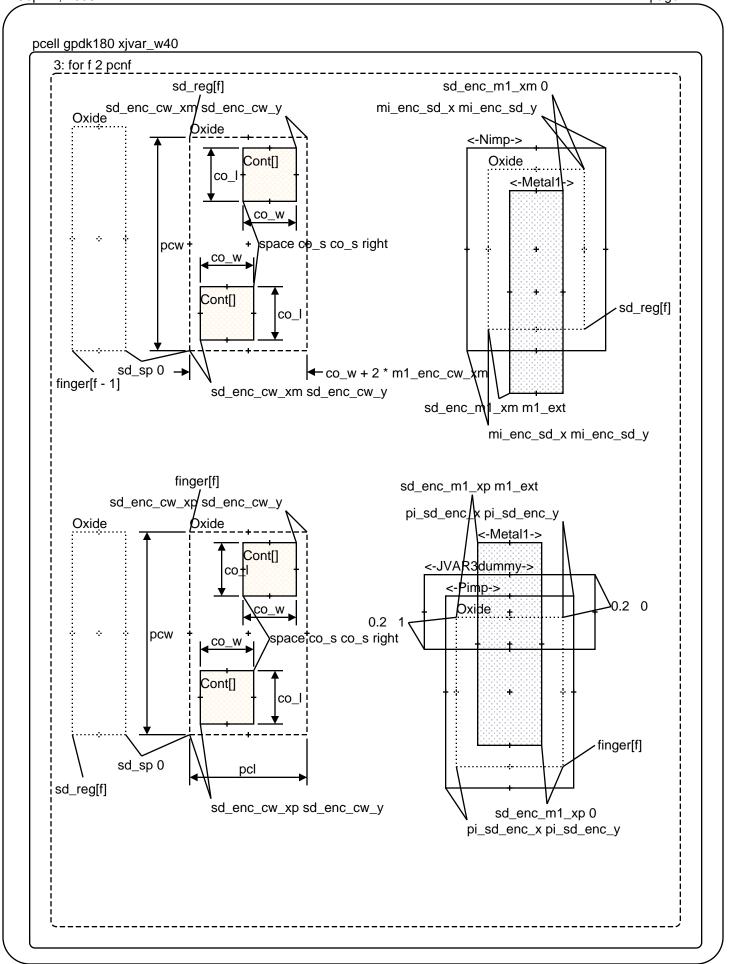
pcell gpdk180 xjvar_w40

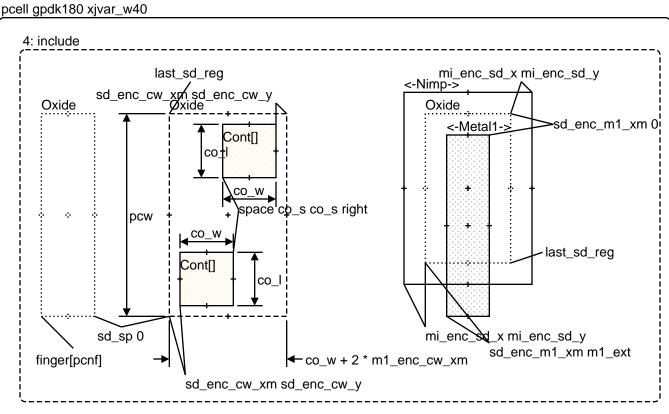
first_sd_reg

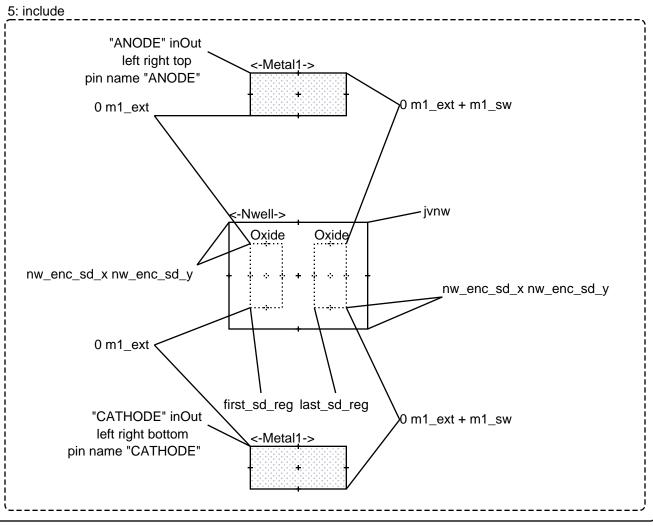


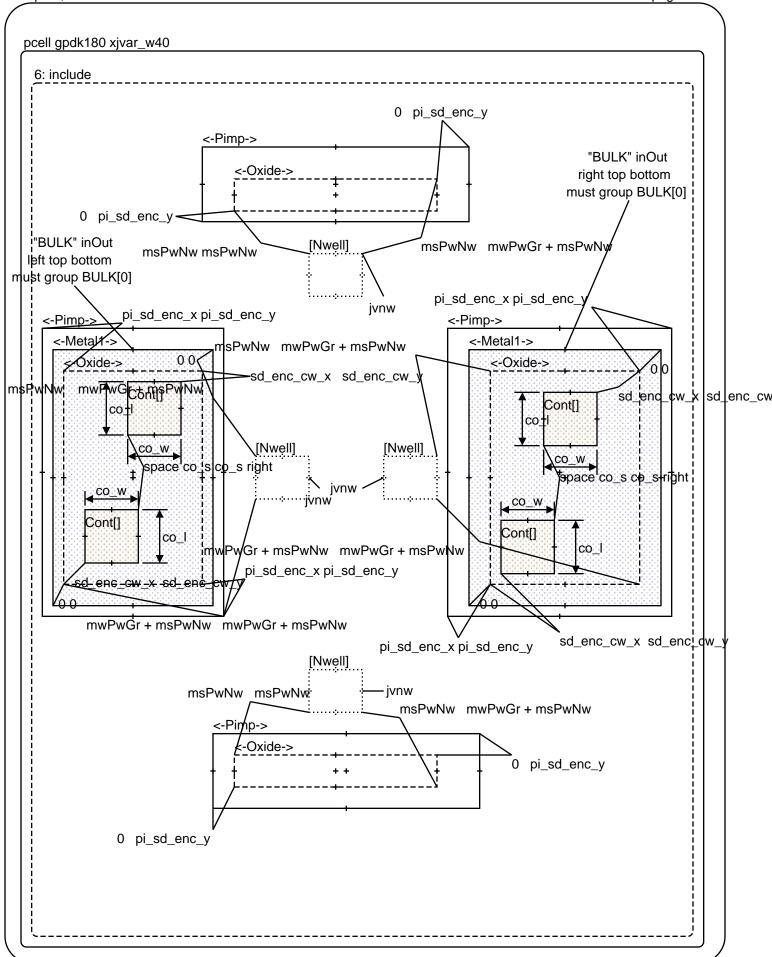


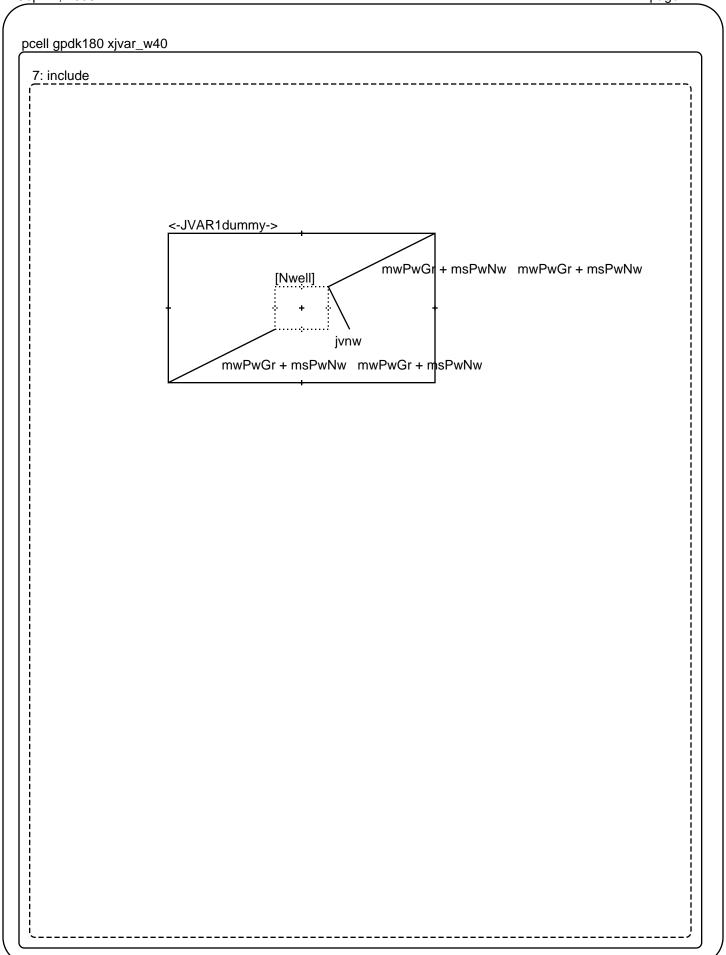
sd_enc_cw_xp sd_enc_cw_y











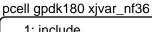
xjvar_nf36 pcell

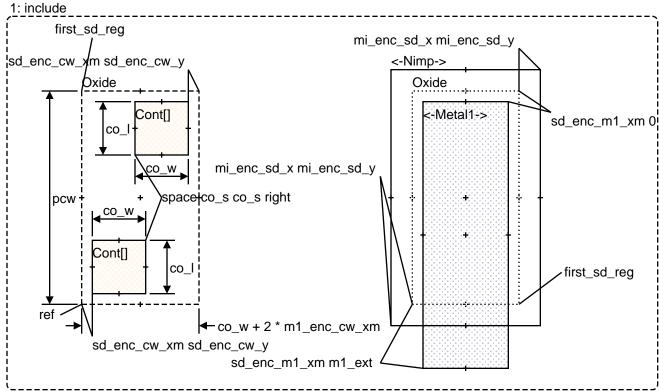
pcell gpdk180 xjvar_nf36

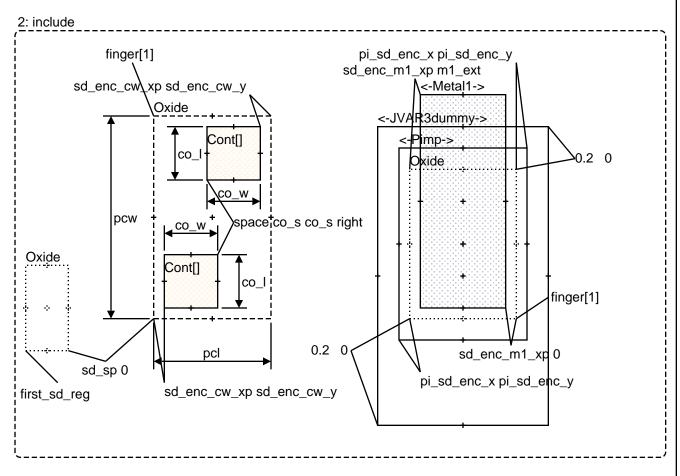
```
pcw = cdfParseFloatString(w) * 1e6
pcl = cdfParseFloatString(I) * 1e6
pcnf = fix(cdfParseFloatString(nf))
dpt = PasGetDeviceProps( cv )
co_w = dpt->CwW
co_I = dpt->CwW
co_s = dpt->CwSp
sd_enc_cw_xm = dpt->M1CwEncXm
sd_enc_cw_xp = gpdk180_PDKsnapToGrid((pcl - co_w) / 2 grid)
sd_enc_cw_x = dpt->SDCwEncY
sd_enc_cw_y = dpt->SDCwEncY
m1_ext = dpt->M1Ext
m1_enc_cw_xm = dpt->M1CwEncXm
m1_enc_cw_xp = dpt->M1CwEncXp
mi_enc_sd_x = dpt->MISDEncX
mi_enc_sd_y = dpt->MISDEncY
sd_enc_m1_xm = sd_enc_cw_xm - m1_enc_cw_xm
sd_enc_m1_xp = sd_enc_cw_xp - m1_enc_cw_xp
pi_sd_enc_x = dpt->PISDEncX
pi_sd_enc_y = dpt->PISDEncY
sd_sp = dpt->SDSp
nw_enc_sd_x = dpt->NWSDEncX
nw_enc_sd_y = dpt->NWSDEncY
ri_enc_sd_x = dpt->RISDEncX
ri_enc_sd_y = dpt->RISDEncY
ri2_enc_sd_x = dpt->RI2SDEncX
ri2_enc_sd_y = dpt->RI2SDEncY
m1_sw = dpt->M1SW
msPwNw = 0.5
mwPwGr = 0.6
```

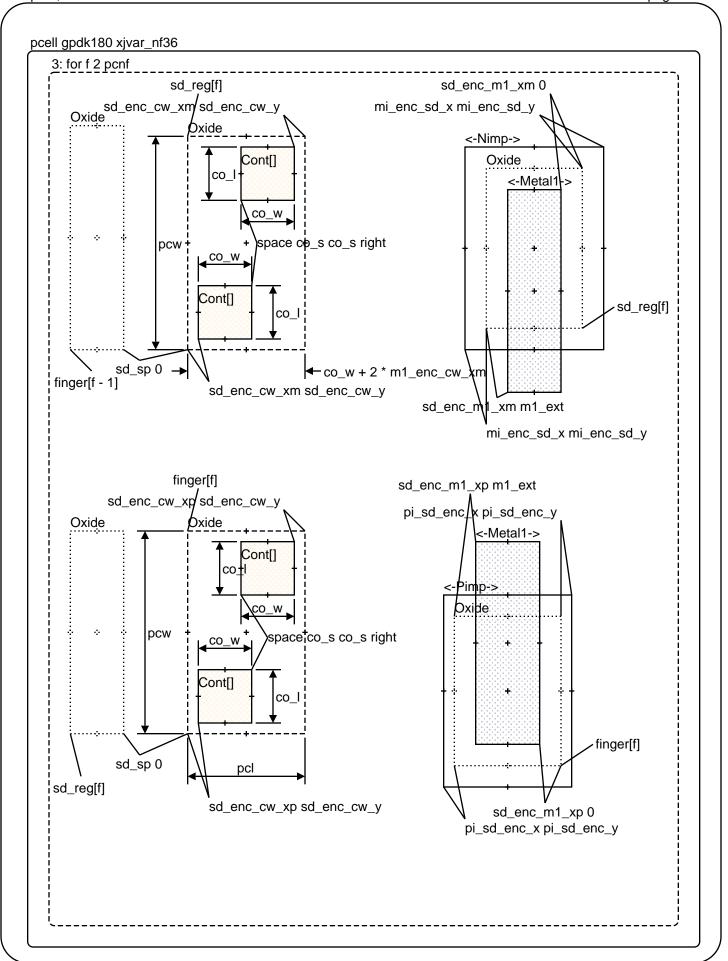
PCell formal params

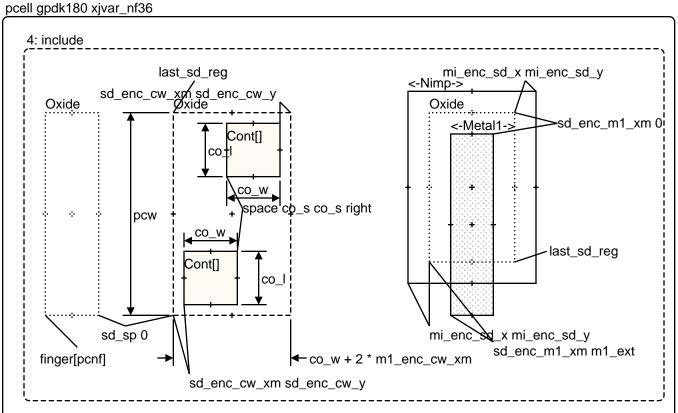
w	string	cdf
I	string	cdf
nf	string	cdf

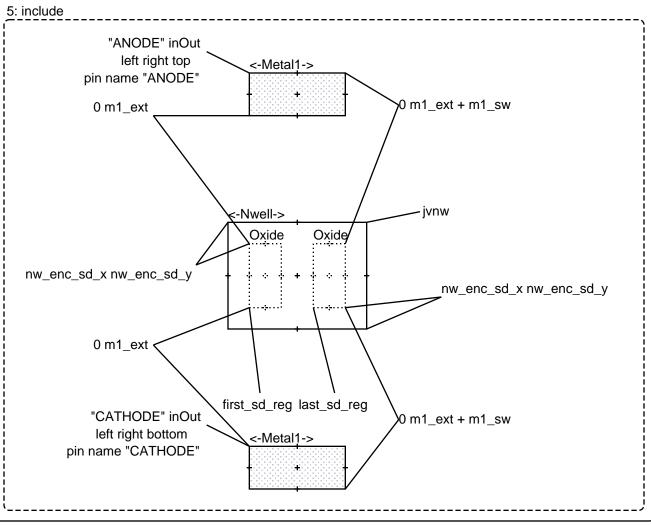


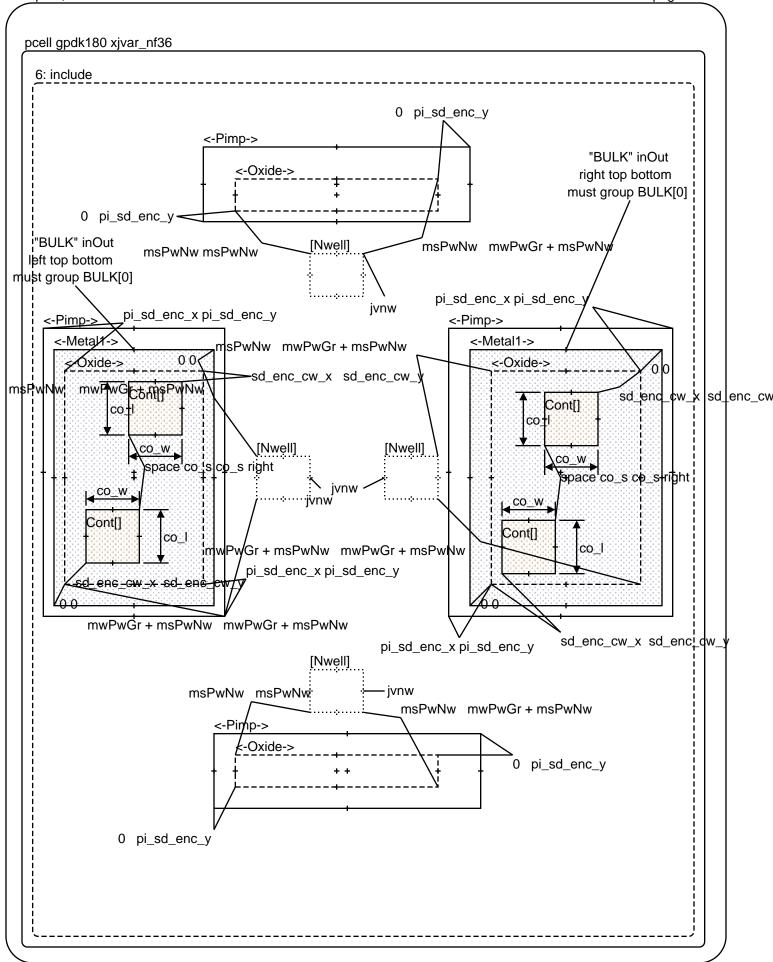


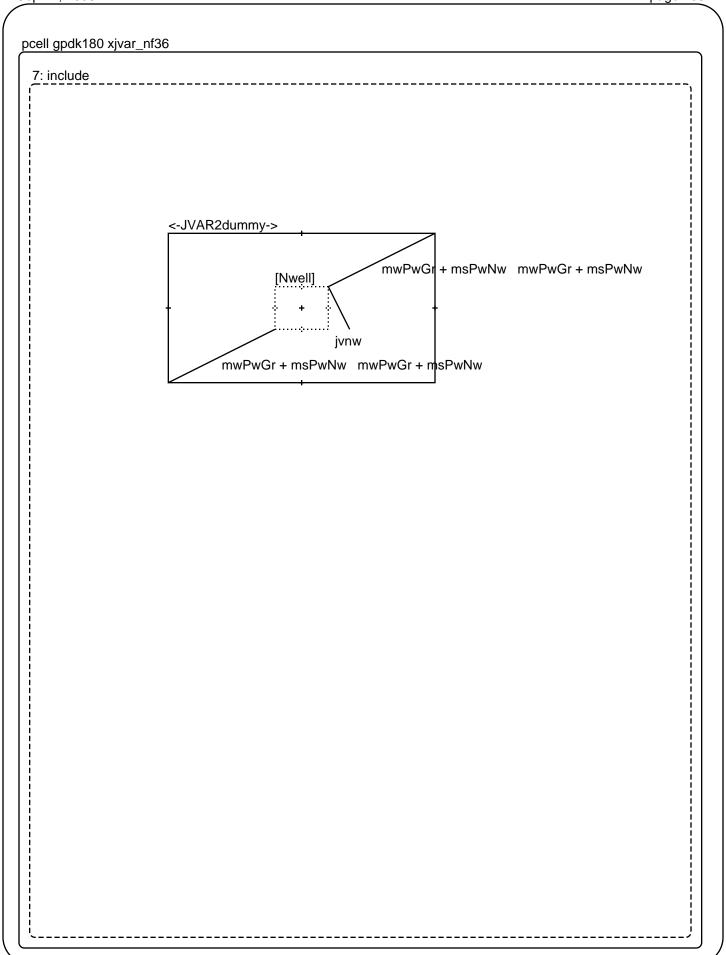












Junction Varactor CDF xjvar_w40 cdf

CDF gpdk180 xjvar_w40

CDF device params

ODI GOVICO PAIGINO			
CwSp	0.2	private	
CwW	0.2	private	
M1CwEncXm	0.2	private	
M1CwEncXp	0.2	private	
M1CwEncY	0.2	private	
M1Ext	0.48	private	
M1SW	1.54	private	
MISDEncX	0.2	private	
MISDEncY	0.2	private	
MaxF	50	public	
MaxW	60e-6	public	
MinF	1	public	
MinW	20e-6	public	

CDF parameters

ODI parameters	
name	"w"
prompt	"Width (M)"
defValue	"4e-05"
type	"string"
editable	"nil"
parseAsNumber	"yes"
parseAsCEL	"yes"

CDF parameters

name	" "	
prompt "Length (N		
defValue	"6e-07"	
type	"string"	
editable	"nil"	
parseAsNumber "yes"		
parseAsCEL	"yes"	

CDF parameters

name	"m"	
prompt	"Multiplier"	
defValue	"1"	
type	"string"	
editable	"deGetEditCellView()->cellViewType != \"maskLayout\""	
callback	"gpdk180_jvCB()"	
parseAsNumber	"yes"	
parseAsCEL	"yes"	

CDF device params

NWSDEncX	0.5	private
NWSDEncY	0.5	private
PIMISp	0.0	private
PISDEncX	0.2	private
PISDEncY	0.2	private
RI2SDEncX	0.2	private
RI2SDEncY	0.2	private
RISDEncX	0.2	private
RISDEncY	0.2	private
SDCwEncX	0.2	private
SDCwEncY	0.2	private
SDSp	0.3	private
category	"varactors"	private

CDF parameters

name	"nf"
prompt	"Number of Fingers"
defValue	"4"
type	"string"
callback	"gpdk180_jvCB()"
parseAsNumber	"yes"
parseAsCEL	"yes"

name	"model"
prompt	"Model Name"
defValue	"xjvar_w40"
type	"string"
editable	"nil"
parseAsCEL	"yes"

CDF gpdk180 xjvar_w40

CDF siminfo

simulator	ams
propMapping	(nil)
instParameters	(model nf m)
otherParameters	(model)
termOrder	(ANODE CATHODE BULK
isPrimitive	t

CDF siminfo

simulator	spectre
instParameters	(nf m)
otherParameters	(model)
termMapping	(nil ANODE ":1" CATHODE ":2" BULK ":3")
termOrder	(ANODE CATHODE BULK)
componentName	subcircuit

CDF cellview

auCdl	baseline_gpdk	jvar	symbol	
auLvs	baseline_gpdk	jvar	symbol	
spectre	baseline_gpdk	jvar	symbol	
symbol	baseline_gpdk	jvar	symbol	
ivpcell	\$lib	\$cell	symbol	5

CDF siminfo

simulator	auCdl
namePrefix	"X"
termOrder	(ANODE CATHODE BULK
instParameters	(model nf m)
netlistProcedure	_ansCdlSubcktCall

CDF siminfo

simulator	auLvs
namePrefix	D
termOrder	(ANODE CATHODE BULK
componentName	"xjvar_w40"
instParameters	(nf m)
netlistProcedure	ansLvsCompPrim

CDF properties

OD: proportion	
formInitProc	""
doneProc	""
buttonFieldWidth	340
fieldHeight	35
fieldWidth	350
promptWidth	175
paramLabelSet	"-model nf m"

xjvar_nf36 cdf

CDF gpdk180 xjvar_nf36

CDF device params

CwSp	0.2	private
CwW	0.2	private
M1CwEncXm	0.2	private
M1CwEncXp	0.2	private
M1CwEncY	0.2	private
M1Ext	0.48	private
M1SW	1.54	private
MISDEncX	0.2	private
MISDEncY	0.2	private
MaxF	50	public
MaxW	60e-6	public
MinF	1	public
MinW	20e-6	public

CDF parameters		С	DF parameters	
name	"w"	n	ame	" "
prompt	"Width (M)"	р	rompt	"Length (M)"
defValue	"2e-05"	d	efValue	"6e-07"
type	"string"	t	уре	"string"
callback	"gpdk180_jvCB()	'e	ditable	"nil"
parseAsNumber	"yes"	р	arseAsNumber	"yes"
parseAsCEL	"yes"	р	arseAsCEL	"yes"

CDF parameters

name	"m"	
prompt	"Multiplier"	
defValue	"1"	
type	"string"	
editable	"deGetEditCellView()->cellViewType != \"maskLayout\""	
callback	"gpdk180_jvCB()"	
parseAsNumber	"yes"	
parseAsCEL	"yes"	L

CDF device params

NWSDEncX	0.5	private
NWSDEncY	0.5	private
PIMISp	0.0	private
PISDEncX	0.2	private
PISDEncY	0.2	private
RI2SDEncX	0.2	private
RI2SDEncY	0.2	private
RISDEncX	0.2	private
RISDEncY	0.2	private
SDCwEncX	0.2	private
SDCwEncY	0.2	private
SDSp	0.3	private
category	"varactors"	private

CDF parameters

name	"nf"
prompt	"Number of Fingers"
defValue	"36"
type	"string"
editable	"nil"
parseAsNumber	"yes"
parseAsCEL	"yes"

name	"model"
prompt	"Model Name"
defValue	"xjvar_nf36"
type	"string"
editable	"nil"
parseAsCEL	"yes"
•	•

CDF gpdk180 xjvar_nf36

CDF siminfo

simulator	ams
propMapping	(nil)
instParameters	(model w m)
otherParameters	(model)
termOrder	(ANODE CATHODE BULK
isPrimitive	t

CDF siminfo

simulator	spectre
instParameters	(w m)
otherParameters	(model)
termMapping	(nil ANODE ":1" CATHODE ":2" BULK ":3")
termOrder	(ANODE CATHODE BULK)
componentName	subcircuit

CDF cellview

auCdl	baseline_gpdk	jvar	symbol	
auLvs	baseline_gpdk	jvar	symbol	
spectre	baseline_gpdk	jvar	symbol	
symbol	baseline_gpdk	jvar	symbol	
ivpcell	\$lib	\$cell	symbol	5

CDF siminfo

simulator	auCdl
namePrefix	"X"
termOrder	(ANODE CATHODE BULK
instParameters	(model w m)
netlistProcedure	_ansCdlSubcktCall

CDF siminfo

simulator	auLvs
namePrefix	D
termOrder	(ANODE CATHODE BULK
componentName	"xjvar_nf36"
instParameters	(w m)
netlistProcedure	ansLvsCompPrim

CDF properties

""
""
340
35
350
175
"-model w m"

Junction Varactor Callback

CDF Callback

```
* File
        : jv.cb
* Purpose : CDF Callback for junction varactor
* Author(s) : Padman S
* Created : 02/20/02
* Notes :
* Modified: 07/23/03 johnag Updates for PAS
* Copyright (C) 2004 Cadence Design Systems, Inc.
          All Rights Reserved.
*/
procedure(gpdk180_jvCB()
let((bagld libName cellName m nf minF maxF nfVar wVal wVar minW maxW)
libName = or(cdfgData~>id~>libName cdfgData~>id~>lib~>name )
cellName = or(cdfgData~>id~>cellName cdfgData~>id~>name)
bagId = PasGetPdkParams(libName cellName)
minF = bagId->MinF
maxF = bagId->MaxF
minW = bagId->MinW
maxW = bagId->MaxW
; check "m"
  if(rexMatchp("^[]*$" cdfgData->m->value) then
   artError("Multiplier value must be a positive integer - set to default")
   m = cdfParseFloatString(cdfgData->m->defValue)
  else
   m = cdfParseFloatString(cdfgData->m->value)
  unless((m && floatp(m))
   artError("Multiplier value must be a number - setting to default")
   m = cdfParseFloatString(cdfgData->m->defValue)
  if(m < 1 then
   artError("Multiplier must be a positive integer - setting to default")
   m = cdfParseFloatString(cdfgData->m->defValue)
  cdfgData->m->value = sprintf(nil "%d" fix(m))
; check "nf"...
```

Diode Macro

CDF macro diode

CDF parameters

ODI parameters	
name	"model"
prompt	"Model name"
defValue	"diode"
type	"string"
editable	"nil"
parseAsCEL	"yes"

CDF parameters

name	"macro"
prompt	"Model name"
defValue	"iPar(\"model\")"
type	"string"
display	"nil"
parseAsCEL	"yes"

CDF parameters

name	"w"
prompt	"Width (M)"
units	"lengthMetric"
defValue	sprintf(nil "%g" PasGetPdkParams("\$lib" "\$cell")~>MinW)
type	"string"
callback	"gpdk180_diodeCB()"
parseAsNumber	"yes"
parseAsCEL	"yes"

CDF parameters

name	"I"
prompt	"Length (M)"
units	"lengthMetric"
defValue	sprintf(nil "%g" PasGetPdkParams("\$lib" "\$cell")~>MinL)
type	"string"
callback	"gpdk180_diodeCB()"
parseAsNumber	"yes"
parseAsCEL	"yes"

CDF parameters

name	"m"
prompt	"Multiplier"
defValue	"1"
type	"string"
editable	"deGetEditCellView()~>cellViewType != \"maskLayout\""
callback	"gpdk180_diodeCB()"
parseAsNumber	"yes"
parseAsCEL	"yes"

CDF device params

CDF macro diode

CDF	parameters
-----	------------

name	"area"		
prompt	"Device area"		
defValue	sprintf(nil "%g" PasGetPdkParams("\$lib" "\$cell")~>MinW*PasGetPdkParams("\$lib" "\$cell")-	N<-	inL)
type	"string"		
editable	"nil"		
parseAsNumber	"yes"		
parseAsCEL	"yes"		

CDF parameters

CDI parameters			
name	"pj"		
prompt	"Periphery of junction"		
defValue	sprintf(nil "%g" 2*PasGetPdkParams("\$lib" "\$cell")~>MinW+2*PasGetPdkParams("\$lib" "\$cell")	ell"	~>MinL
type	"string"		
editable	"nil"		
parseAsNumber	"yes"		
parseAsCEL	"ves"		

CDF siminfo

simulator	auCdl
instParameters	(area)
netlistProcedure	ansCdlCompPrim
componentName	diode
termOrder	(PLUS MINUS)
namePrefix	"D"
modelName	"\$cell"

CDF parameters

ODI paramotoro		
name	"calcParam"	
prompt	"Calculate Parameter"	
type	"cyclic"	
choices	list("area" "width" "length")	
defValue	"area"	
display	"nil"	

CDF siminfo

simulator	auLvs
namePrefix	"D"
termOrder	(PLUS MINUS)
componentName	"\$cell"
instParameters	(area m)
netlistProcedure	ansLvsCompPrim

CDF siminfo

simulator	spectre
otherParameters	(model)
instParameters	(area pj m)
termOrder	(PLUS MINUS)
termMapping	(nil PLUS \:1 MINUS \:2)
componentName	pdio

CDF siminfo

<u> </u>	
simulator	ams
otherParameters	(model)
instParameters	(model area pj m)
termOrder	(PLUS MINUS)
isPrimitive	t

CDF macro diode

CDF siminfo

CDI SIIIIIIII	
simulator	ads
netlistProcedure	ADSsimCompPrim
otherParameters	(model)
instParameters	(Area Periph _M)
componentName	nil
termOrder	(PLUS MINUS)
termMapping	(nil PLUS ":P1" MINUS ":P2")
propMapping	(nil Area area Periph pj _M m)
typeMapping	nil
uselib	nil

CDF properties

CDF properties	
formInitProc	""
doneProc	""
buttonFieldWidth	340
fieldHeight	35
fieldWidth	350
promptWidth	175
modelLabelSet	"is rs n"
opPointLabelSet	"i v region"
paramLabelSet	"-model area m"

ndio

CDF gpdk180 ndio

include macro diode

CDF parameters

name	"model"	
defValue	"\$cell"	

CDF device params

MinW	0.6u	private
MaxW	20.0u	private
MinL	0.6u	private
MaxL	20.0u	private

CDF device params

grid	\$grid	private	MFG Grid
scale	1.0e-6	private	Dimension scale factor

CDF cellview

symbol	pas_std	diode2	symbol	
spectre	pas_std	diode2	symbol	
auLvs	pas_std	diode2	symbol	
auCdl	pas_std	diode2	symbol	
ads	pas_std	diode2	symbol	
ivpcell	gpdk180	\$cell	symbol	40

pdio

CDF gpdk180 pdio

include macro diode

CDF parameters

name	"model"	
defValue	"\$cell"	

CDF device params

MinW	0.6u	private
MaxW	20.0u	private
MinL	0.6u	private
MaxL	20.0u	private

CDF device params

grid	\$grid	private	MFG Grid
scale	1.0e-6	private	Dimension scale factor

CDF cellview

symbol	pas_std	diode2	symbol	
spectre	pas_std	diode2	symbol	
auLvs	pas_std	diode2	symbol	
auCdl	pas_std	diode2	symbol	
ads	pas_std	diode2	symbol	
ivpcell	gpdk180	\$cell	symbol	40

inductor

CDF gpdk180 ind

CDF parameters	
name	"ind"
prompt	"Inductance (H)"
defValue	sprintf(nil "%g" PasGetPdkParams("\$lib" "\$cell")~>DefInd)
type	"string"
editable	"nil"

CDF parameters

parseAsNumber parseAsCEL

"yes"

"yes"

name	"nr"
prompt	"Number Of Turns"
defValue	sprintf(nil "%g" PasGetPdkParams("\$lib" "\$cell")~>DefNR)
choices	'("1.5" "2.5" "3.5" "4.5" "5.5" "6.5" "7.5" "8.5" "9.5")
type	"cyclic"
callback	"gpdk180_indCB()"

CDF device params

DefInd	5.95156e-10	private
DefNR	2.5	private
MinR	20.0u	private
MaxR	100.0u	private
MinS	2.0u	private
MaxS	10.0u	private
MinW	5.0u	private
MaxW	20.0u	private

CDF parameters

name	"rad"
prompt	"Inner Radius"
units	"lengthMetric"
defValue	sprintf(nil "%g" PasGetPdkParams("\$lib" "\$cell")~>MinR)
type	"string"
callback	"gpdk180_indCB()"
parseAsNumber	"yes"
parseAsCEL	"yes"

CDF parameters

name	"width"
prompt	"Inductor Width"
units	"lengthMetric"
defValue	sprintf(nil "%g" PasGetPdkParams("\$lib" "\$cell")~>MinW)
type	"string"
callback	"gpdk180_indCB()"
parseAsNumber	"yes"
parseAsCEL	"yes"

name	"space"
prompt	"Inductor Space"
units	"lengthMetric"
defValue	sprintf(nil "%g" PasGetPdkParams("\$lib" "\$cell")~>MinS)
type	"string"
callback	"gpdk180_indCB()"
parseAsNumber	"yes"
parseAsCEL	"yes"

CDF gpdk180 ind

CDF parameters

name	"model"	
prompt	"Model"	
defValue	"inductor"	
type	"string"	
display	"nil"	
parseAsCEL	"yes"	

CDF device params

category	"ind"	private	Library Manager Category
----------	-------	---------	--------------------------

CDF device params

grid	\$grid	private	MFG Grid	
scale	1.0e-6	private	Dimension scale factor	

CDF parameters

name	"m"	
prompt	"multiplier"	
defValue	"1"	
type	"string"	
display	"nil"	
editable	"nil"	
parseAsNumber	"yes"	
parseAsCEL	"yes"	

CDF parameters

name	"macro"	
prompt	"Model"	
defValue	"iPar(\"model\")"	
type	"string"	
display	"nil"	
parseAsCEL	"yes"	

CDF siminfo

simulator	auCdl	
modelName	"\$cell"	
componentName	ind	
namePrefix	"I"	
propMapping	(nil I ind)	
termOrder (PLUS MINU		
instParameters	(1)	
netlistProcedure	ansCdlCompPrim	

CDF siminfo

simulator	auLvs	
netlistProcedure	ansLvsCompPrim	
instParameters	(ind)	
componentName	"\$cell"	
termOrder	(PLUS MINUS)	
namePrefix	"I"	

CDF siminfo

simulator	spectre
propMapping	(nil I ind)
termMapping	(nil PLUS \:1 MINUS \:2)
instParameters	(I)
termOrder	(PLUS MINUS)
otherParameters	(model)
componentName	ind

simulator	ams
isPrimitive	t
termOrder	(PLUS MINUS
instParameters	ind

CDF gpdk180 ind

CDF siminfo

simulator	ads
netlistProcedure	ADSsimCompPrim
otherParameters	(model)
instParameters	(L)
componentName	nil
termOrder	(PLUS MINUS)
termMapping	(nil PLUS ":P1" MINUS ":P2")
propMapping	(nil L ind)
typeMapping	nil
uselib	nil

CDF properties

est proportion		
formInitProc	""	
doneProc	""	
buttonFieldWidth	340	
fieldHeight	35	
fieldWidth	350	
promptWidth 175		
paramLabelSet	"-model nr rad"	

CDF cellview

symbol	pas_std	ind2	symbol	
spectre pas_std		ind2	symbol	
auLvs	pas_std	ind2	symbol	
auCdl pas_std		ind2	symbol	
ads pas_std		ind2	symbol	
ivpcell	gpdk180	ind	symbol	40

vpnp

CDF gpdk180 vpnp

CDF parameters

CDF parameters	
name	"model"
prompt	"Model name"
defValue	"vpnp"
type	"string"
editable	"nil"
parseAsCEL	"yes"

CDF parameters

name	"macro"
prompt	"Model name"
defValue	"iPar(\"model\")"
type	"string"
display	"nil"
parseAsCEL	"yes"

CDF parameters

name	"EmitterSize"
prompt	"Emitter Size"
defValue	"1.3x1.3"
choices	'("1.3x1.3" "5.0x5.0")
type	"cyclic"
callback	"gpdk180_vpnpCB()"

CDF parameters

name	"area"
prompt	"Area"
defValue	"1.69"
type	"string"
editable	"nil"
parseAsNumber	"yes"
parseAsCEL	"yes"

CDF device params

grid	\$grid	private	MFG Grid
scale	1.0e-6	private	Dimension scale factor (not used)

CDF parameters

name	"m"
prompt	"Multiplier"
defValue	"1"
type	"string"
editable	"deGetEditCellView()->cellViewType != \"maskLayout\""
callback	"gpdk180_vpnpCB()"
parseAsNumber	"yes"
parseAsCEL	"yes"

CDF device params

CDF gpdk180 vpnp

CDF siminfo

simulator	auCdl
netlistProcedure	ansCdlCompPrim
propMapping	(nil M m EA area)
instParameters	(EA M)
namePrefix	"Q"
componentName	vpnp
termOrder	(CBE)
modelName	"\$cell"

CDF siminfo

simulator	auLvs
netlistProcedure	ansLvsCompPrim
instParameters	(area m)
componentName	"\$cell"
termOrder	(CBE)
namePrefix	"Q"

CDF siminfo

simulator	ams
isPrimitive	t
termOrder	(CBE)
instParameters	(area m)
otherParameters	(model)

simulator	spectre
termMapping	(nil C \:c B \:b E \:e)
termOrder	(CBE)
instParameters	(area m)
otherParameters	(model)

CDF gpdk180 vpnp

CDF siminfo

simulator	ads
netlistProcedure	ADSsimCompPrim
otherParameters	(model)
instParameters	(Area _M)
componentName	nil
termOrder	(CBE)
termMapping	(nil C ":P1" B ":P2" E ":P3")
propMapping	(nil _M m)
typeMapping	nil
uselib	nil

CDF properties

02. p.opo00	
formInitProc	""
doneProc	""
buttonFieldWidth	340
fieldHeight	35
fieldWidth	350
promptWidth	175
modelLabelSet	"bf is vaf"
opPointLabelSet	"betadc ic vce"
paramLabelSet	"-model m area"
P	araa

CDF cellview

symbol	pas_std	pnp3	symbol	
spectre	pas_std	pnp3	symbol	
auLvs	pas_std	pnp3	symbol	
auCdl pas_std		pnp3	symbol	
ads	pas_std	pnp3	symbol	
ivpcell gpdk180		vpnp	symbol	40

npn

CDF gpdk180 npn

CDF parameters

ODI parameters	
name	"model"
prompt	"Model name"
defValue	"npn"
type	"string"
editable	"nil"
parseAsCEL	"yes"

CDF device params

MinE	0.6	private
MaxE	10.0	private

CDF device params

	grid	\$grid	private	MFG Grid
ſ	scale	1.0e-6	private	Dimension scale factor (not used)

CDF parameters

· · · · · · · · · · · · · · · · · · ·	
name	"Ewidth"
prompt	"Emitter width"
defValue	sprintf(nil "%g" PasGetPdkParams("\$lib" "\$cell")~>MinE)
type	"string"
callback	"gpdk180_bjtCB()"
parseAsNumber	"yes"
parseAsCEL	"yes"

CDF parameters

name	"area"		
prompt	"Area"		
defValue	sprintf(nil "%g" PasGetPdkParams("\$lib" "\$cell")~>MinE*PasGetPdkParams("\$lib" "\$cell")~>Mi	nE)
type	"string"		
editable	"nil"		
parseAsNumber	"yes"		
parseAsCEL	"yes"		

CDF parameters

· · · · · · · · · · · · · · · · · · ·	
name	"m"
prompt	"Multiplier"
defValue	"1"
type	"string"
editable	"deGetEditCellView()->cellViewType != \"maskLayout\""
callback	"gpdk180_bjtCB()"
parseAsNumber	"yes"
parseAsCEL	"yes"

CDF parameters

name	"scaleF"
prompt	"Scale Factor"
defValue	"1"
type	"string"
editable	"nil"
parseAsNumber	"yes"
parseAsCEL	"yes"

CDF device params

category "bip" private Library Manager Ca
--

CDF gpdk180 npn

CDF parameters

name	"macro"
prompt	"Model name"
defValue	"iPar(\"model\")"
type	"string"
display	"nil"
parseAsCEL	"yes"

CDF siminfo

simulator	auCdl
modelName	"\$cell"
termOrder	(CBE)
componentName	npn
namePrefix	"Q"
instParameters	(EA)
propMapping	(nil EA area)
netlistProcedure	ansCdlCompPrim

CDF siminfo

simulator	auLvs
netlistProcedure	ansLvsCompPrim
instParameters	(area m)
componentName	"\$cell"
termOrder	(CBE)
namePrefix	"Q"

CDF siminfo

simulator	spectre
propMapping	(nil area scaleF)
termMapping	(nil C \:c B \:b E \:e)
termOrder	(CBE)
instParameters	(area m)
otherParameters	(model)

OBI OIIIIIIIO		
simulator	ams	
isPrimitive	t	
termOrder	(CBE)	
instParameters	(area m)	
propMapping	(nil area scaleF)	
otherParameters	(model)	

CDF gpdk180 npn

CDF siminfo

simulator	ads
netlistProcedure	ADSsimCompPrim
otherParameters	(model)
instParameters	(Area _M)
componentName	nil
termOrder	(CBE)
termMapping	(nil C ":P1" B ":P2" E ":P3")
propMapping	(nil Area scaleF _M m)
typeMapping	nil
uselib	nil

CDF properties

ODI Proportios	
formInitProc	""
doneProc	""
buttonFieldWidth	340
fieldHeight	35
fieldWidth	350
promptWidth	175
modelLabelSet	"bf is vaf"
opPointLabelSet	"betadc ic vce"
paramLabelSet	"-model m area"

CDF cellview

symbol	pas_std	npn3	symbol	
spectre	pas_std	npn3	symbol	
auLvs	pas_std	npn3	symbol	
auCdl	pas_std	npn3	symbol	
ads	pas_std	npn3	symbol	
ivpcell	gpdk180	npn	symbol	40

pnp

CDF gpdk180 pnp

CDF parameters

name	"model"
prompt	"Model name"
defValue	"pnp"
type	"string"
editable	"nil"
parseAsCEL	"yes"

CDF device params

MinE	0.6	private
MaxE	10.0	private

CDF device params

grid	\$grid	private	MFG Grid
scale	1.0e-6	private	Dimension scale factor (not used)

CDF parameters

ODI parameters	
name	"Ewidth"
prompt	"Emitter width"
defValue	sprintf(nil "%g" PasGetPdkParams("\$lib" "\$cell")~>MinE)
type	"string"
callback	"gpdk180_bjtCB()"
parseAsNumber	"yes"
parseAsCEL	"yes"

CDF parameters

name	"area"		
prompt	"Area"		
defValue	sprintf(nil "%g" PasGetPdkParams("\$lib" "\$cell")~>MinE*PasGetPdkParams("\$lib" "\$cell")~	>M	nE)
type	"string"		
editable	"nil"		
parseAsNumber	"yes"		
parseAsCEL	"yes"		

CDF parameters

name	"m"	CDF pa	rameters	
prompt	"Multiplier"	name		"scaleF"
defValue	"1"	promp	t	"Scale Factor"
type	"string"	defVal	ue	"1"
editable	"deGetEditCellView()->cellViewType != \"maskLayout\"	type		"string"
callback	"gpdk180_bjtCB()"	editab	е	"nil"
parseAsNumber	"yes"	parse/	sNumber	"yes"
parseAsCEL	"yes"	parse/	sCEL	"yes"

CDF device params

I calcacty I bib Ibilyale ILibialy Maliadel Calcacty	category	"did"	private	Library Manager Category
---	----------	-------	---------	--------------------------

CDF gpdk180 pnp

CDF parameters

<u> </u>	I	
name	"macro"	
prompt	"Model name"	
defValue	"iPar(\"model\")"	
type	"string"	
display	"nil"	
parseAsCEL	"yes"	

CDF siminfo

simulator	auLvs	
netlistProcedure	ansLvsCompPrim	
instParameters	(area m)	
componentName	"\$cell"	
termOrder	(CBE)	
namePrefix	"Q"	

CDF siminfo

ODI SIIIIIIIO		
simulator	auCdl	
modelName	"\$cell"	
termOrder	(CBE)	
componentName	pnp	
namePrefix	"Q"	
instParameters	(EA)	
propMapping	(nil EA area)	
netlistProcedure	ansCdlCompPrim	

CDF siminfo

simulator	spectre	
propMapping	(nil area scaleF)	
termMapping	(nil C \:c B \:b E \:e)	
termOrder	(C B E)	
instParameters	(area m)	
otherParameters	(model)	

simulator	ams	
isPrimitive	t	
termOrder	(CBE)	
instParameters	(area m)	
propMapping	(nil area scaleF)	
otherParameters	(model)	

CDF gpdk180 pnp

CDF siminfo

simulator	ads
netlistProcedure	ADSsimCompPrim
otherParameters	(model)
instParameters	(Area _M)
componentName	nil
termOrder	(CBE)
termMapping	(nil C ":P1" B ":P2" E ":P3")
propMapping	(nil Area scaleF _M m)
typeMapping	nil
uselib	nil

CDF properties

ODI Properties		
formInitProc	""	
doneProc	""	
buttonFieldWidth	340	
fieldHeight	35	
fieldWidth	350	
promptWidth	175	
modelLabelSet	"bf is vaf"	
opPointLabelSet	"betadc ic vce"	
paramLabelSet	"-model m area"	

CDF cellview

symbol	pas_std	pnp3	symbol	
spectre	pas_std	pnp3	symbol	
auLvs	pas_std	pnp3	symbol	
auCdl	pas_std	pnp3	symbol	
ads	pas_std	pnp3	symbol	
ivpcell	gpdk180	pnp	symbol	40

moscap macro

CDF macro moscap

CDF device params

ODI acvice pai				
dwGate	2.0	private	Default channel width	
mwGate	{2A}	private	Minimum channel Width	
xwGate	50.0	private	Maximum channel Width	
mlGate	{5A}	private	Minimum channel Length	
mxGate	\$mos_mxGate	private	Minimum gate extension	
msGate	\$mos_msGate	private	Minimum gate spacing	
meDiffGate	\$mos_meDiffGate	private	Minimum diffusion enclosure of gate	
msGateCont	\$mos_msGateCont	private	Minimum gate to contact spacing	
mwPoly	\$mos_mwPoly	private	MInimum poly width on field	
msDiffPoly	\$mos_msDiffPoly	private		
mwCont	\$mos_mwCont	private	Minimum contact width	
msCont	\$mos_msCont	private	te Minimum contact spacing	
masCont	\$mos_masCont	private	e Minimum contact array spacing	
meDiffCont	\$mos_meDiffCont	private	Minimum diffusion enclosure of contact	
meeDiffCont	\$mos_meeDiffCont	private	Minimum diffusion end enclosure of contact	
mePolyCont	\$mos_mePolyCont	private	Minimum poly enclusre of contact	
meePolyCont	\$mos_meePolyCont	private	Minimum poly end enclosure of contact	
mwM1	\$mos_mwM1	private	Minimum metal 1 width	
msM1	\$mos_msM1	private	Minimum metal 1 spacing	
meM1Cont	\$mos_meM1Cont	private	Minimum metal 1 enclosure of contact	
meeM1Cont	\$mos_meeM1Cont	private	Minimum metal 1 end enclosure of contact	

CDF macro moscap

CDF parameters

ODI paramotoro		
name	"model"	
prompt	"Model name"	
type	"string"	
display	"nil"	
editable	"nil"	
parseAsCEL	"yes"	

CDF device params

DefC	2.88f	private
MinW	0.6u	private
MaxW	100.0u	private
MinL	0.18u	private
MaxL	20.0u	private
MaxF	1000	private
CapA	0.008	private
CapP	0.0	private

CDF parameters

ODI parameters	parameters				
name	"C"				
prompt	"Capacitance (F)"				
units	"capacitance"				
defValue	sprintf(nil "%g" PasGetPdkParams("\$lib" "\$cell")~>DefC)				
type	"string"				
editable	"cdfgData->entryModeC->value != \"Length&Width\""				
callback	"gpdk180_moscapCB()"				
parseAsNumber	"yes"				
parseAsCEL	"yes"				

CDF parameters

name	"totalC"				
prompt	"Total Capacitance (F)"				
units	"capacitance"				
defValue	sprintf(nil "%g" PasGetPdkParams("\$lib" "\$cell")~>DefC)				
type	"string"				
editable	"nil"				
parseAsNumber	"yes"				
parseAsCEL	"yes"				

name	"entryModeC"	
prompt	"Capacitance Entry Mode"	
defValue	"Capacitance"	
choices	'("Capacitance" "Cap&Length" "Cap&Width" "Length&Width")	
type	"cyclic"	
callback	"gpdk180_moscapCB()"	

CDF macro moscap

CDF parameters

name	" "	
prompt	"Length (m)"	
units	"lengthMetric"	
defValue	sprintf(nil "%g" PasGetPdkParams("\$lib" "\$cell")~>MinL)	
type	"string"	
editable		
callback	"gpdk180_moscapCB()"	
parseAsNumber	"yes"	
parseAsCEL	"yes"	

CDF parameters

name	"entryModeW"	
prompt	"Width Entry Mode"	
defValue	"TotalWidth"	
choices	'("TotalWidth" "WidthPerFinger")	
type	"cyclic"	
display	"cdfgData->entryModeC->value == \"Cap&Width\" cdfgData->entryModeC->value == \"Length&Width\""	
callback	"gpdk180_moscapCB()"	

CDF parameters

name	"w"			
prompt	"Width (m)"			
units	"lengthMetric"			
defValue	sprintf(nil "%g" PasGetPdkParams("\$lib" "\$cell")~>MinW)			
type	"string"			
editable	"(cdfgData->entryModeC->value == \"Cap&Width\" cdfgData->entryModeC->value == \"Length&Width\") && cdfgData->entryModeW->value == \"TotalWidth\""			
callback	"gpdk180_moscapCB()"			
parseAsNumber	"yes"			
parseAsCEL	"yes"			

DDI parameters				
name	"fw"			
prompt	"Width Per Finger (m)"			
units	"lengthMetric"			
defValue	sprintf(nil "%g" PasGetPdkParams("\$lib" "\$cell")~>MinW)			
type	"string"			
editable	"(cdfgData->entryModeC->value == \"Cap&Width\" cdfgData->entryModeC->value == \"Length&Width\") && cdfgData->entryModeW->value == \"WidthPerFinger\""			
callback	"gpdk180_moscapCB()"			
parseAsNumber	"yes"			
parseAsCEL	"yes"			

CDF macro moscap

CDF parameters

name	"fingers"
prompt	"Number of Fingers"
defValue	"1"
type	"string"
callback	"gpdk180_moscapCB()"
parseAsNumber	"yes"
parseAsCEL	"yes"

CDF parameters

name	"m"		
prompt "Multiplier"			
defValue	"1"		
type	"string"		
display	"deGetEditCellView()->cellViewType != \"maskLayout\""		
editable	"deGetEditCellView()->cellViewType != \"maskLayout\""		
callback	"gpdk180_moscapCB()"		
parseAsNumber	"yes"		
parseAsCEL	"yes"		

CDF parameters

name	"simM"		
prompt	"Simulation Multiplier"		
defValue	"iPar(\"m\") * iPar(\"fingers\")"		
type	"string"		
display	"nil"		
parseAsNumber	"yes"		
parseAsCEL	"yes"		

Parametere				
name	"macro"			
prompt	"macro model name"			
defValue	"iPar(\"model\")"			
type	"string"			
display	"nil"			
parseAsCEL	"yes"			

CDF macro moscap

CDF parameters

name "connectGates"		CDF parameters		CDF parameters		
name		na	me	"mtlCvg"	name	"rightAbut"
prompt	"Gate Connection"					
defValue	"None"	r-	· ·		prompt	"Right Abutment"
choices	'("None" "Top" "Bottom" "Both" "Alternate")	de	fValue		defValue	0
type	"radio"	–		"string"	type	"int"
display	"t"	di	splay	nil"	display	nil"

CDF parameters

name	"showTapParams"	
prompt	"Show Tap Parameters"	
defValue	nil	
type	"boolean"	
display	"nil"	

CDF parameters

oz: paramitoro	
name	"switchSD"
prompt	"Switch S/D"
defValue	nil
type	"boolean"
display	"nil"

CDF parameters

name	"leftAbut"	
prompt	"Left Abutment'	
defValue	0	
type	"int"	
display	"nil"	

CDF parameters

ODI parameters	
"sdMtlWidth"	
"S/D Metal Width"	
"lengthMetric"	
".4u"	
"string"	
"nil"	
"yes"	
"yes"	

CDF parameters

ODI parameters		
name	"tapCntRows"	
prompt	"Tap Contact Rows"	
defValue	1	
type	"int"	
display	"nil"	

CDF parameters

name	"leftTap"
prompt	"Left Tap"
defValue	t
type	"boolean"
display	"t"

CDF parameters

name	"connectSD"	
prompt	"S/D Connection"	
defValue	"None"	
choices	'("None" "Source" "Drain" "Both")	
type	"radio"	
display	"gpdk180_mosDisplay('connectSD)"	

CDF parameters

name	"bottomTap"
prompt	"Bottom Tap"
defValue	nil
type	"boolean"
display	"t"

CDF parameters

name	"tap"	
prompt	"Bodytie Type"	
defValue	"Detached"	
choices	'("None" "Detached")	
type	"cyclic"	
display	"nil"	

CDF parameters

name	"rightTap"
prompt	"Right Tap"
defValue	nil
type	"boolean"
display	"t"
type	"boolean"

CDF parameters

name	"tapExtension"	
prompt	"Tap Extension"	
defValue	""	
type	"string"	
display	"nil"	

name	"topTap"
prompt	"Top Tap"
defValue	nil
type	"boolean"
display	"t"

CDF macro moscap

CDF siminfo

simulator	auLvs
permuteRule	""
propMapping	nil
deviceTerminals	ш
namePrefix	"M"
termOrder	(D G S B)
instParameters	(w l m)
netlistProcedure	ansLvsCompPrim

CDF siminfo

simulator	auCdl	
netlistProcedure	ansCdlCompPrim	
instParameters	(LWM)	
termOrder	(D G S B)	
propMapping	(nil L I W w M m)	
namePrefix	"M"	

CDF siminfo

simulator	spectre	
opParamExprList	(("cap" "OP(inst() \"cgg\")"))	
termMapping	(nil D \:d G \:g S \:s B \:b)	
instParameters	(w I m)	
termOrder	(D G S B)	
propMapping	(nil m simM w fw)	
otherParameters	(model)	

CDF siminfo

simulator	ams	
otherParameters	(model)	
instParameters	(model w l m)	
termOrder	(D G S B)	
propMapping	(nil m simM w fw)	
isPrimitive	t	

CDF siminfo

simulator	ads
netlistProcedure	ADSsimCompPrim
otherParameters	(model)
instParameters	(W L _M)
componentName	nil
termOrder	(D G S B)
termMapping	(nil D ":P1" G ":P2" S ":P3" B ":P4")
propMapping	(nil W fw L I _M simM)
typeMapping	nil
uselib	nil

CDF properties

ODI Properties		
formInitProc	""	
doneProc	""	
buttonFieldWidth	340	
fieldHeight	35	
fieldWidth	350	
promptWidth	175	
paramLabelSet	"-totalC I w fingers"	
opPointLabelSet	"ids vgs vds vth vdsat"	
modelLabelSet	"vtho kf beta0"	

nmoscap

CDF gpdk180 nmoscap

include macro moscap

CDF parameters

name	"model"	
defValue	"nmos1"	

CDF device params

grid	\$grid	private	MFG Grid	
scale	1e-6	private	rivate Dimension scale factor	
model	"nmos1"	private	e Device Model Name	
category	"cap"	private	Library Manager Category	

CDF device params

ODI GOVIGO PAIL				
melmplDiff	\$nmos_meImplDiff	private	Minimum implant enclosure of diffusion	
melmplGate	\$nmos_meImplGate	private	e Minimum implant enclosure of gate	
meelmplGate	\$nmos_meeImplGate	private	Minimum implant end enclosure of gate	
melmplPoly	\$nmos_meImplPoly	private	Minimum implant enclosure of poly on field	
meNbIDiff	.93	private	Minimum Nbl enclosure of diff	
meCapDiff	.93	private	Minimum Cap enclosure of diff	
meNblTap	.93	private	Minimum Nbl enclosure of Tap	
meCapTap	.93	private	Minimum Cap enclosure of Tap	

CDF device params

msTapDiff	\$tap_nmos_msTapDiff	private	Minimum tap diffusion to device diffusion spacing
msTapImpI	\$tap_nmos_msTapImpI	private	Minimum tap diffusion to device implant spacing
meTimpTap	\$tap_nmos_meTimpTap	private	Minimum tap implant enclosure of tap diffusion
msTimpDiff	\$tap_nmos_msTimpDiff	private	Minimum tap implant to device diffusion spacing
msTimpImpI	\$tap_nmos_msTimpImpI	private	Minimum tap implant to device implant spacing
таТар	\$tap_nmos_maTap	private	Minimum tap diffusion area
maTimp	\$tap_nmos_maTimp	private	Minimum tap implant area

CDF cellview

symbol	baseline_gpdk	nmoscap4n	symbol	
spectre	baseline_gpdk	nmoscap4n	symbol	
auLvs	baseline_gpdk	nmoscap4n	symbol	
auCdl	baseline_gpdk	nmoscap4n	symbol	
ads	baseline_gpdk	nmoscap4n	symbol	
ivpcell	gpdk180	nmoscap	symbol	40

CDF siminfo

simulator	auCdl
componentName	nmoscap
modelName	"\$cell"

simulator	auLvs
componentName	nmoscap

nmoscap3

CDF gpdk180 nmoscap3

include macro moscap

CDF parameters

name	"model"
defValue	"nmos1"

CDF device params

grid	\$grid	private	MFG Grid
scale	1e-6	private	Dimension scale factor
model	"nmos1"	private	Device Model Name
category	"cap"	private	Library Manager Category

CDF device params

ODI acvice pare			
melmplDiff	\$nmos_meImplDiff	private	Minimum implant enclosure of diffusion
melmplGate	\$nmos_meImplGate	private	Minimum implant enclosure of gate
meelmplGate	\$nmos_meeImplGate	private	Minimum implant end enclosure of gate
melmplPoly	\$nmos_meImplPoly	private	Minimum implant enclosure of poly on field
meNbIDiff	.93	private	Minimum Nbl enclosure of diff
meCapDiff	.93	private	Minimum Cap enclosure of diff
meNblTap	.93	private	Minimum Nbl enclosure of Tap
meCapTap	.93	private	Minimum Cap enclosure of Tap

CDF device params

msTapDiff	\$tap_nmos_msTapDiff	private	Minimum tap diffusion to device diffusion spacing
msTapImpI	\$tap_nmos_msTapImpI	private	Minimum tap diffusion to device implant spacing
meTimpTap	\$tap_nmos_meTimpTap	private	Minimum tap implant enclosure of tap diffusion
msTimpDiff	\$tap_nmos_msTimpDiff	private	Minimum tap implant to device diffusion spacing
msTimpImpI	\$tap_nmos_msTimpImpI	private	Minimum tap implant to device implant spacing
таТар	\$tap_nmos_maTap	private	Minimum tap diffusion area
maTimp	\$tap_nmos_maTimp	private	Minimum tap implant area

CDF cellview

symbol	baseline_gpdk	nmoscap4n3	symbol	
spectre	baseline_gpdk	nmoscap4n3	symbol	
auLvs	baseline_gpdk	nmoscap4n3	symbol	
auCdl	baseline_gpdk	nmoscap4n3	symbol	
ads	baseline_gpdk	nmoscap4n3	symbol	
ivpcell	gpdk180	nmoscap3	symbol	40

CDF siminfo

simulator	auCdl	
componentName	nmoscap	
modelName	nmoscap	

simulator	auLvs
componentName	nmoscap

pmoscap

CDF gpdk180 pmoscap

include macro moscap

CDF parameters

name	"model"
defValue	"pmos1"

CDF device params

grid	\$grid	private	MFG Grid
scale	1e-6	private	Dimension scale factor
model	"pmos1"	private	Device Model Name
category	"cap"	private	Library Manager Category

CDF device params

melmplDiff	\$pmos_meImplDiff	private	Minimum implant enclosure of diffusion
melmplGate	\$pmos_meImplGate	private	Minimum implant enclosure of gate
meelmplGate	\$pmos_meeImplGate	private	Minimum implant end enclosure of gate
melmplPoly	\$pmos_meImplPoly	private	Minimum implant enclosure of poly on field
meWellDiff	\$pmos_meWellDiff	private	Minimum well enclosure of diffusion
meCapDiff	\$pmos_meWellDiff	private	Minimum cap enclosure of diffusion

CDF device params

msTapDiff	\$tap_pmos_msTapDiff	private	Minimum tap diffusion to device diffusion spacing
msTapImpI	\$tap_pmos_msTapImpl	private	Minimum tap diffusion to device implant spacing
meTimpTap	\$tap_pmos_meTimpTap	private	Minimum tap implant enclosure of tap diffusion
msTimpDiff	\$tap_pmos_msTimpDiff	private	Minimum tap implant to device diffusion spacing
msTimpImpI	\$tap_pmos_msTimpImpI	private	Minimum tap implant to device implant spacing
таТар	\$tap_pmos_maTap	private	Minimum tap diffusion area
maTimp	\$tap_pmos_maTimp	private	Minimum tap implant area
meWellTap	\$tap_pmos_meWellTap	private	Minimum well enclosure of tap diffusion
теСарТар	\$tap_pmos_meWellTap	private	Minimum capenclosure of tap diffusion

CDF cellview

symbol	baseline_gpdk	pmoscap4n	symbol	
spectre	baseline_gpdk	pmoscap4n	symbol	
auLvs	baseline_gpdk	pmoscap4n	symbol	
auCdl	baseline_gpdk	pmoscap4n	symbol	
ads	baseline_gpdk	pmoscap4n	symbol	
ivpcell	gpdk180	pmoscap	symbol	40

CDF siminfo

simulator	auCdl
componentName	pmoscap
modelName	"\$cell"

simulator	auLvs
componentName	pmoscap

pmoscap3

CDF gpdk180 pmoscap3

include macro moscap

CDF parameters

name	"model"	
defValue	"pmos1"	

CDF device params

grid	\$grid	private	MFG Grid
scale	1e-6	private	Dimension scale factor
model	"pmos1"	private	Device Model Name
category	"cap"	private	Library Manager Category

CDF device params

melmplDiff	\$pmos_meImplDiff	private	Minimum implant enclosure of diffusion
melmplGate	\$pmos_meImplGate	private	Minimum implant enclosure of gate
meelmplGate	\$pmos_meeImplGate	private	Minimum implant end enclosure of gate
melmplPoly	\$pmos_meImplPoly	private	Minimum implant enclosure of poly on field
meWellDiff	\$pmos_meWellDiff	private	Minimum well enclosure of diffusion
meCapDiff	\$pmos_meWellDiff	private	Minimum cap enclosure of diffusion

CDF device params

msTapDiff	\$tap_pmos_msTapDiff	private	Minimum tap diffusion to device diffusion spacing
msTapImpI	\$tap_pmos_msTapImpl	private	Minimum tap diffusion to device implant spacing
meTimpTap	\$tap_pmos_meTimpTap	private	Minimum tap implant enclosure of tap diffusion
msTimpDiff	\$tap_pmos_msTimpDiff	private	Minimum tap implant to device diffusion spacing
msTimpImpI	\$tap_pmos_msTimpImpI	private	Minimum tap implant to device implant spacing
таТар	\$tap_pmos_maTap	private	Minimum tap diffusion area
maTimp	\$tap_pmos_maTimp	private	Minimum tap implant area
meWellTap	\$tap_pmos_meWellTap	private	Minimum well enclosure of tap diffusion
теСарТар	\$tap_pmos_meWellTap	private	Minimum capenclosure of tap diffusion

CDF cellview

symbol	baseline_gpdk	pmoscap4n3	symbol	
spectre	baseline_gpdk	pmoscap4n3	symbol	
auLvs	baseline_gpdk	pmoscap4n3	symbol	
auCdl	baseline_gpdk	pmoscap4n3	symbol	
ads	baseline_gpdk	pmoscap4n3	symbol	
ivpcell	gpdk180	pmoscap3	symbol	40

CDF siminfo

simulator	auCdl
componentName	pmoscap
modelName	pmoscap

simulator	auLvs
componentName	pmoscap

Component Callback Definitions

Common Callback Procedures

```
......
;;; gpdk180_PDKblankValueCheck()
;;; Purpose:
;;; Determine whether a test value is blank
;;; Parameters:
  paramName: name of parameter being checked which is used in the error
        statement if the value is blank
  testValue: value which is being compared for a blank entry
  defValue: default value of parameter being checked which is used for the
        new value of the parameter being checked if the test value is
       determined to be blank
;;; Outputs:
  returnValue: testValue that is adjusted depending on whether it is blank
procedure(gpdk180_PDKblankValueCheck(paramName testValue defValue)
prog((stringCondition numberCondition returnValue)
 ;;; determine whether the test value is a string or non string and whether it
 ;;; it has a blank value
 stringCondition = stringp(testValue) &&
          (testValue == "" || testValue == " ")
 numberCondition = !stringp(testValue) && !testValue
 ;;; print an error message if a blank value is encountered
 ;;;
 if(stringCondition || numberCondition then
  printf("*Error* %s must have a value - setting to default\n"
      paramName)
  returnValue = defValue
  else
  returnValue = testValue...
```

diode

```
procedure(gpdk180_diodeCB()
let((MinL MinW MaxL MaxW
cell grid scale sGrid libName deviceName device
IVal wVal mult areaVal IVar wVar multVar perimVal
)
setq(cell or(cdfgData->id~>master cdfgData->id))
 libName = or(cdfgData~>id~>libName cdfgData~>id~>lib~>name )
deviceName = or(cdfgData~>id~>cellName cdfgData~>id~>name )
 setq(device PasGetPdkParams(libName deviceName))
 grid = device->grid
 scale = device->scale || 1e-6
sGrid = grid * scale
 MinL = device->MinL
 MinW = device->MinW
 MaxL = device->MaxL
MaxW = device->MaxW
IVar = nil
 wVar = nil
multVar = nil
if( (mult = cdfParseFloatString(cdfgData->m->value)) &&
     typep(mult) != 'flonum then
   multVar = t
if( mult == " " || mult == "" then
 artError("Multiplier value must be a positive integer - set to default")
  cdfgData->m->value = "1"
 mult = 1
 multVar = nil
if(!multVar && (mult = fix(mult)) < 1 then
 artError("Multiplier value must be a positive integer - set to default")
  cdfgData->m->value = "1"
 mult = 1
 multVar = nil
IVal = cdfParseFloatString(cdfgData->I->value)
wVal = cdfParseFloatString(cdfgData->w->value)
if(typep(IVal) != 'flonum then
 IVar = t
if(IVar && deGetEditCellView()~>cellViewType == "maskLayout" then...
```

inductor

```
procedure(gpdk180_indCB()
 prog((
     cell grid scale sGrid libName deviceName device
     wVal sVal rVal indVal
     MinR MaxR MinS MaxS MinW MaxW
     k1 k2 u0 nr s w r)
 setq(cell or(cdfgData->id~>master cdfgData->id))
 libName = or(cdfgData~>id~>libName cdfgData~>id~>lib~>name )
 deviceName = or(cdfgData~>id~>cellName cdfgData~>id~>name )
 setq(device PasGetPdkParams(libName deviceName))
 grid = device->grid
 scale = device->scale || 1e-6
 sGrid = grid * scale
 MinR = device->MinR
 MaxR = device->MaxR
 MinS = device->MinS
 MaxS = device->MaxS
 MinW = device->MinW
 MaxW = device->MaxW
: Check Radius Values
   rVal = cdfParseFloatString(cdfgData->rad->value)
   unless( rVal && (typep(rVal) == 'flonum)
    artError("Inductor Radius value must be a number - set to default")
    cdfgData->rad->value = cdfgData->rad->defValue
   rVal = evalstring(cdfgData->rad->value)
   if(rVal < MinR then
    artError("Radius is smaller than minimum - setting to min")
    rVal = MinR
   if(rVal > MaxR then
    artError("Radius is larger than maximum - setting to max")
    rVal = MaxR
   r = gpdk180_PDKsnapToGrid(rVal sGrid)
; Check Space Values
   sVal = cdfParseFloatString(cdfgData->space->value)
   unless( sVal && (typep(sVal) == 'flonum)
    artError("Inductor Space value must be a number - set to default")
    cdfgData->space->value = cdfgData->space->defValue
   sVal = evalstring(cdfgData->space->value)
   if(sVal < MinS then
    artError("Space is smaller than minimum - setting to min")
    sVal = MinS...
```

bipolar

```
procedure(gpdk180_bjtCB()
let((
   cell grid scale sGrid libName deviceName device
   Ewidth area MinE MaxE mult multVar)
get cdf data
 setq(cell or(cdfgData->id~>master cdfgData->id))
 libName = or(cdfgData~>id~>libName cdfgData~>id~>lib~>name )
 deviceName = or(cdfgData~>id~>cellName cdfgData~>id~>name )
 setq(device PasGetPdkParams(libName deviceName))
 grid = device->grid
 scale = device->scale || 1e-6
 sGrid = grid * scale
 MinE = device->MinE
 MaxE = device->MaxE
get parameter values
 if( (mult = cdfParseFloatString(cdfgData->m->value)) &&
      typep(mult) != 'flonum then
   multVar = t
 if( mult == " " || mult == "" then
  artError("Multiplier value must be a positive integer - set to default")
  cdfgData->m->value = "1"
  mult = 1
  multVar = nil
 if(!multVar && (mult = fix(mult)) < 1 then
  artError("Multiplier value must be a positive integer - set to default")
  cdfgData->m->value = "1"
  mult = 1
  multVar = nil
 )
 Ewidth = cdfParseFloatString(cdfgData->Ewidth->value)
 unless( Ewidth && (typep(Ewidth) == 'flonum)
  artError("Emitter Width value must be a number - set to default")
  cdfgData->Ewidth->value = cdfgData->Ewidth->defValue
  Ewidth = cdfParseFloatString( cdfgData->Ewidth->value )
calculate and check min E -> this is also the value of E length;
 if((Ewidth - MinE) < -.001 then
     artError("Emitter Width is below minimum - setting to min")...
```

nmoscap / pmoscap

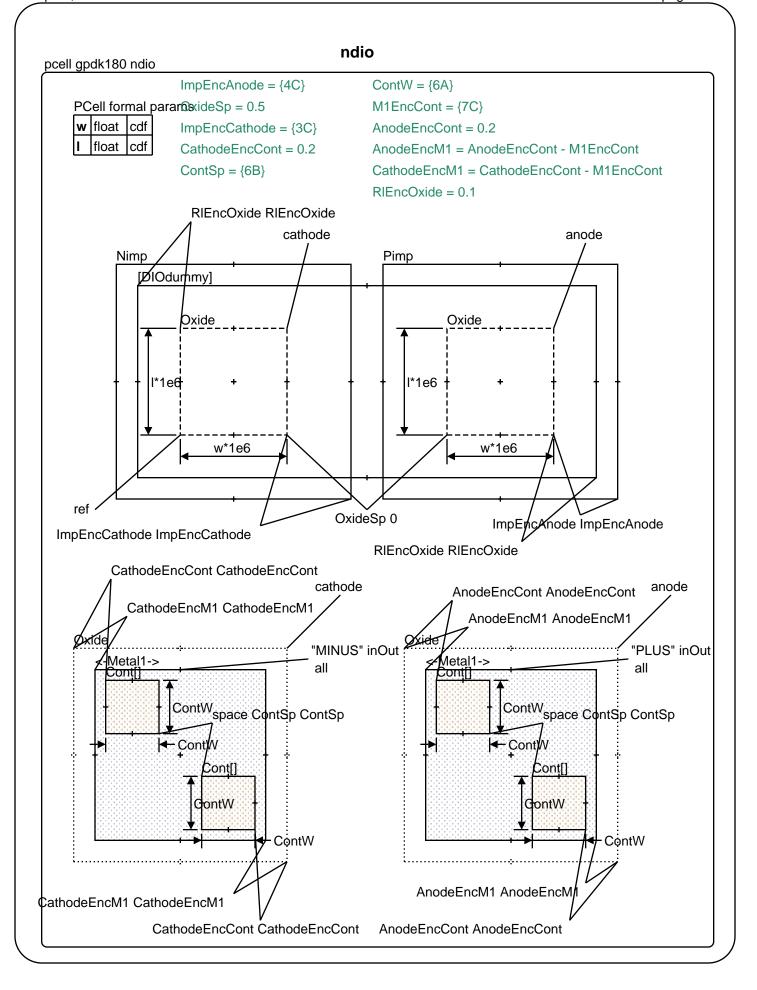
```
procedure(gpdk180_moscapCB()
let((
   cell grid scale sGrid libName deviceName device
   MinL MaxL MinW MaxW MaxF CapA CapP
   cVar wVar fwVar IVar fingersVar
   m fingers c totalC I w wtemp fw entryModeC entryModeW)
 ;;; retrieve process information necessary for callback procedure
 setq(cell or(cdfgData->id~>master cdfgData->id))
 libName = or(cdfgData~>id~>libName cdfgData~>id~>lib~>name )
 deviceName = or(cdfgData~>id~>cellName cdfgData~>id~>name )
 setq(device PasGetPdkParams(libName deviceName))
 grid = device->grid
 scale = device->scale || 1e-6
 sGrid = grid * scale
 MinL = device->MinL
 MinW = device->MinW
 MaxL = device->MaxL
 MaxW = device->MaxW
 MaxF = device->MaxF
 CapA = device->CapA
 CapP = device->CapP
 scale = device->scale
 setq(sGrid times(grid scale))
 ;;; set initial variable status of capacitance, width, length, and fingers
 cVar = nil
  wVar = nil
  fwVar = nil
  IVar = nil
  fingersVar = nil
 ;;; determine entry mode for capacitance and width values
 ;;;...
```

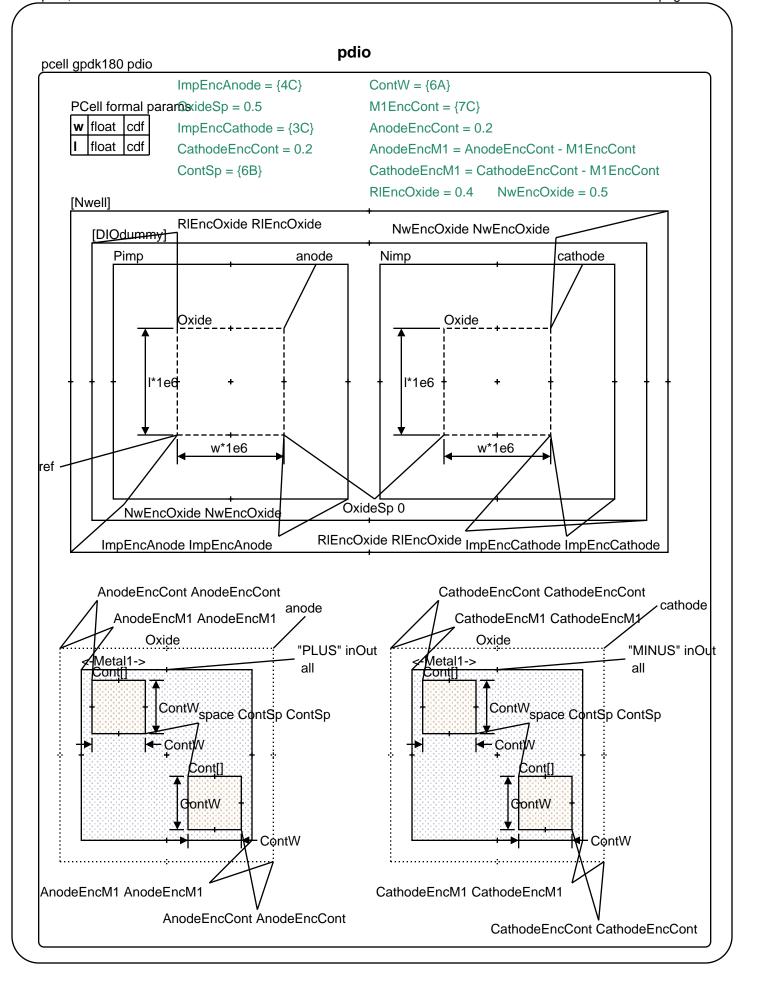
Component PCell Code

Common Pcell Procedures

Skill Procedures

```
procedure( FpCeiling( value grid "ff" )
   ;; Returns the smallest multiple of grid not smaller than the given value
   ceiling((value - grid/1000)/grid) * grid
)
procedure( Xor( a b "gg" )
   (not(a) && b) || (a && not(b))
)
```



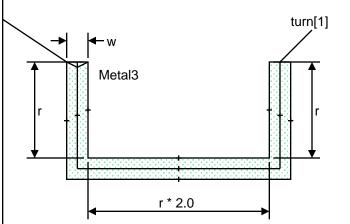


inductor

pcell gpdk180 ind

PCell formal params					
nr string cdf					
rad	float	cdf			
space	float	cdf			
width	float	cdf			

ref



w = width * 1e6

s = space * 1e6

r = rad * 1e6

n = cdfParseFloatString(nr)

numHalfTurns = fix(n * 2.0)

MinusTabL = RIEncInd

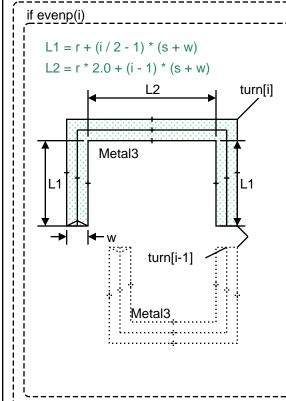
RIEncInd = 10.0

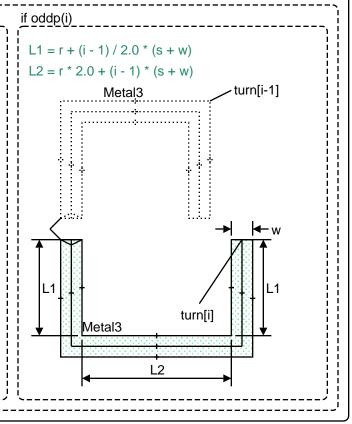
R12EncInd = 0.0

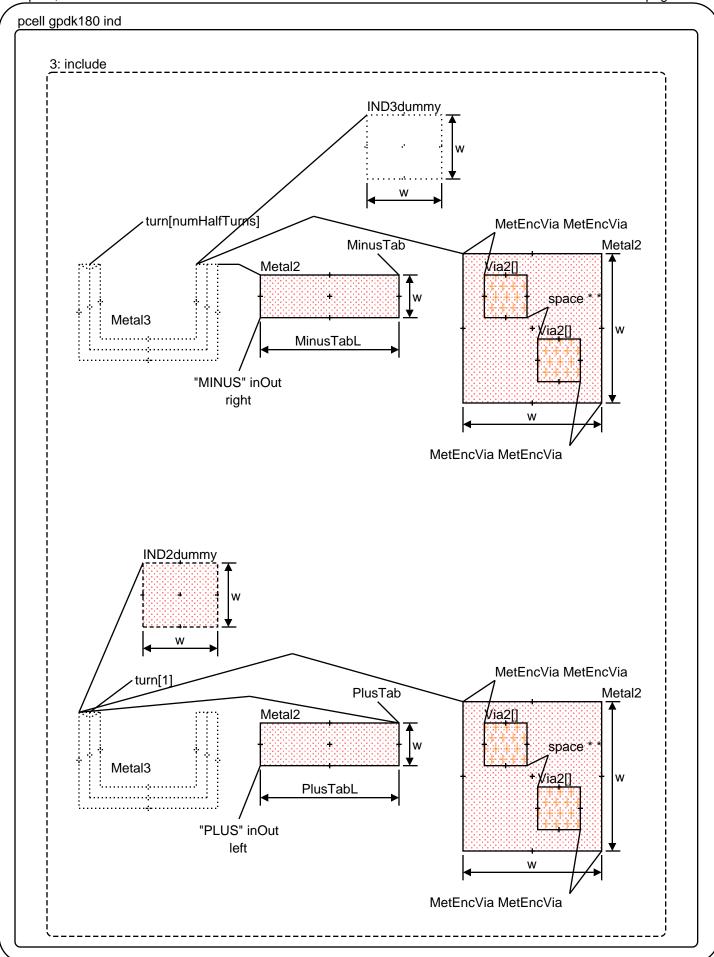
MetEncVia = 0.9

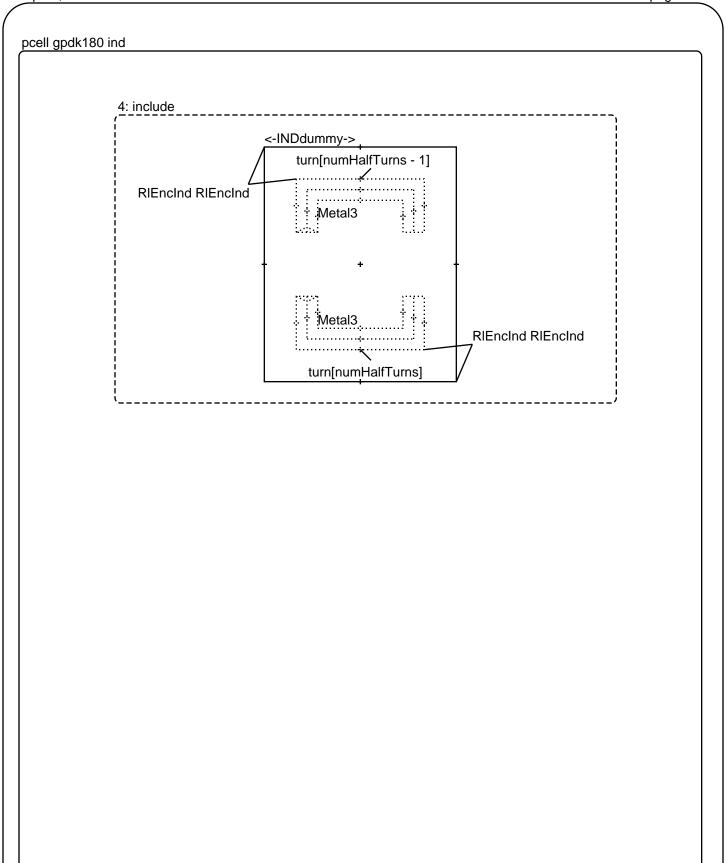
PlusTabL = fix(n) * (s + w) + RIEncInd

2: for i 2 numHalfTurns









npn

pcell gpdk180 npn

PCell formal params **Ewidth** float cdf

alEmit = 0.6

 $meImpIDiff = {3C}$

msEmitBase = 0.5

msCollBase = 1.0

meWellDiff = {2C}

 $msCont = \{6B\}$

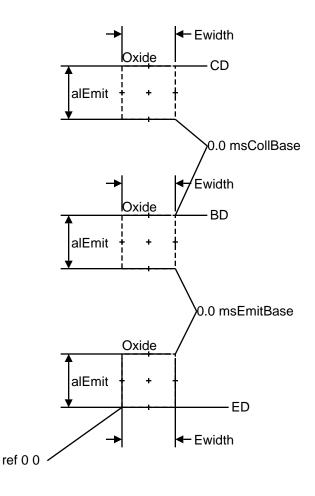
 $mwCont = \{6A\}$

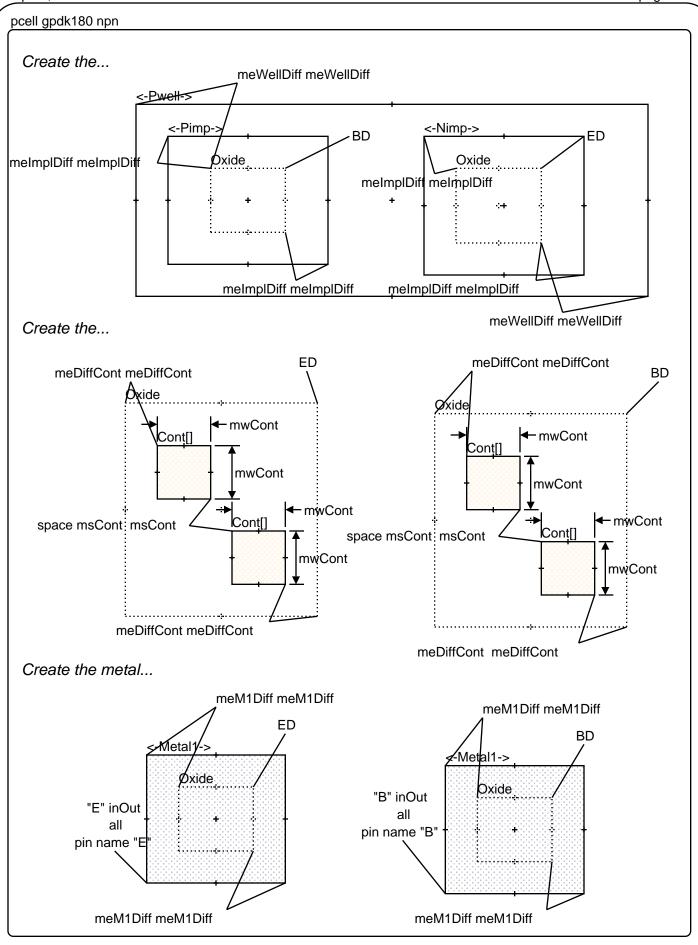
meDiffCont = {6C}

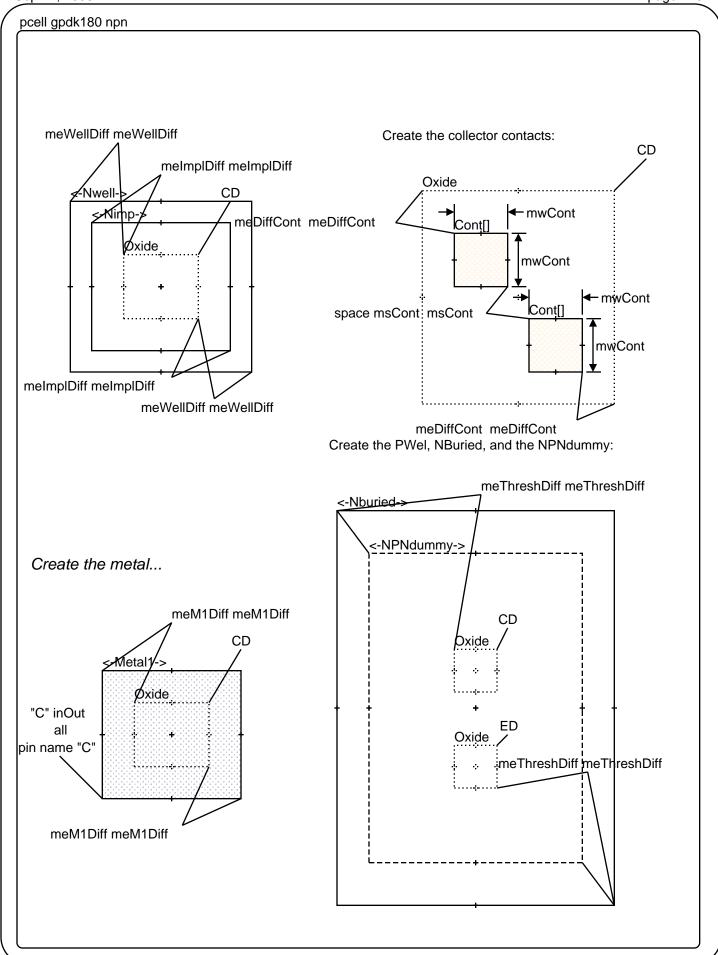
 $meM1Cont = \{7C\}$

meThreshDiff = 0.8

meM1Diff = meM1Cont - meDiffCont







pnp

pcell gpdk180 pnp

PCell formal params

Ewidth | float | cdf |

alEmit = 0.6

meImplDiff = {3C}

msEmitBase = 0.5

msCollBase = 1.0

meWellDiff = {2C}

msCont = {6B}

mwCont = {6A}

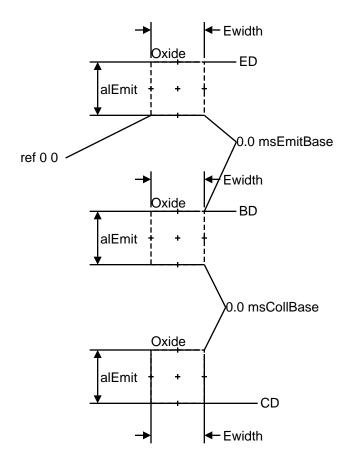
meDiffCont = {6C}

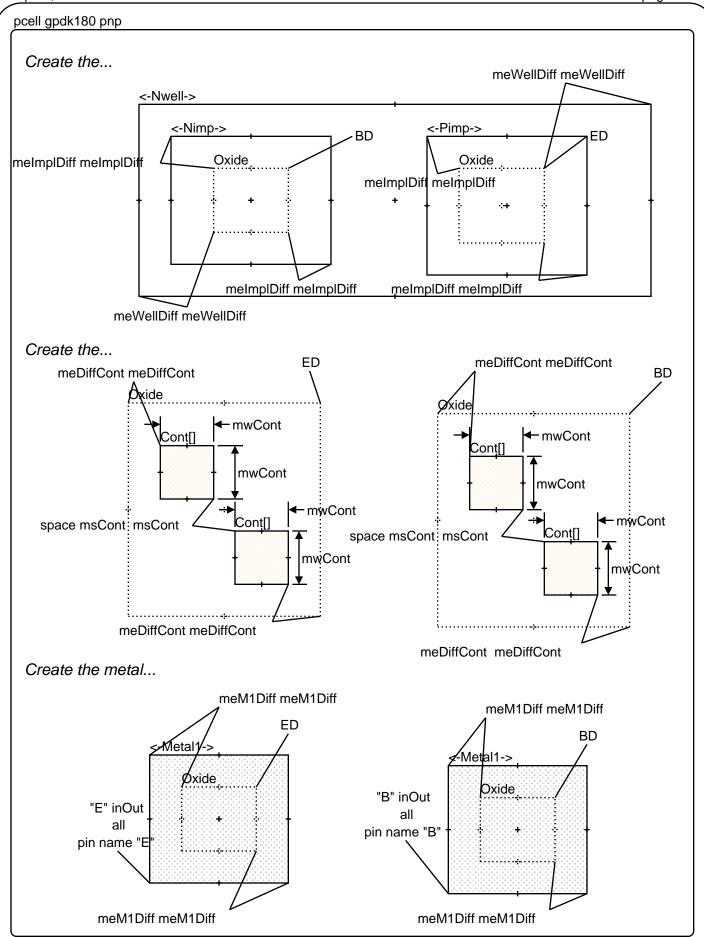
meM1Cont = {7C}

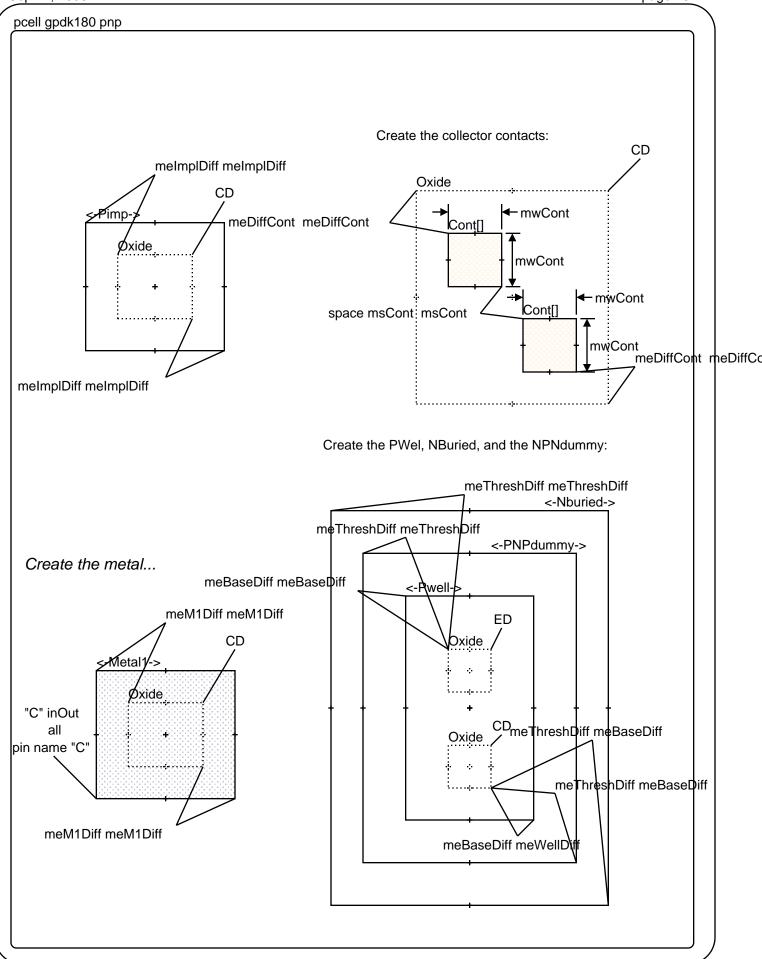
meBaseDiff = 0.8

meThreshDiff = 1.1

meM1Diff = meM1Cont - meDiffCont







vpnp

pcell gpdk180 vpnp

```
PCell formal params

EmitterSize string cdf
```

Elength = evalstring(substring(index(EmitterSize "x") 2))

```
\label{eq:mepimpDiff} \begin{tabular}{ll} mePimpDiff = \{4C\} & msCont = \{6B\} \\ meNimpDiff = \{3C\} & mwCont = \{6A\} \\ msPimpNimp = 0.4 & meDiffCont = \{6C\} \\ msNimpPimp = 0.4 & meM1Cont = \{7C\} \\ meWellDiff = \{2C\} & meBJTDum = 0.05 \\ \end{tabular}
```

msWellDiff = {2D}

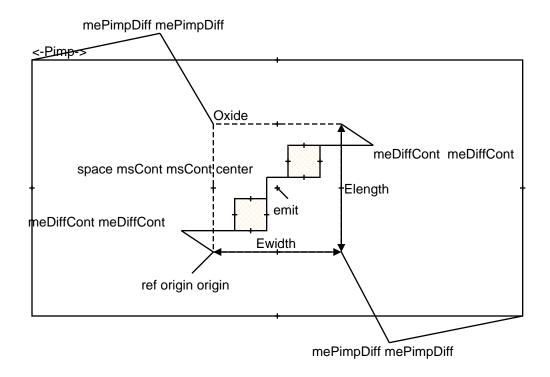
```
meWellPimp = max( difference( meWellDiff mePimpDiff ) 0.0 )
meWellNimp = max( difference( meWellDiff meNimpDiff ) 0.0 )
msWellPimp = max( difference( msWellDiff mePimpDiff ) mePimpDiff )
msWellNimp = max( difference( msWellDiff meNimpDiff ) meNimpDiff )
msEmitBase = mePimpDiff + msPimpNimp + meNimpDiff
msBaseColl = meNimpDiff + meWellNimp + msWellPimp + mePimpDiff
meDiffM1 = meDiffCont - meM1Cont
mwBase = mwCont + 2.0 * meDiffCont
mwColl = mwCont + 2.0 * meDiffCont
mwM1 = mwCont + 2.0 * meM1Cont
mwNimp = mwBase + 2.0 * meNimpDiff
mwPimp = mwBase + 2.0 * mePimpDiff
mwPimp = mwBase + 2.0 * mePimpDiff
```

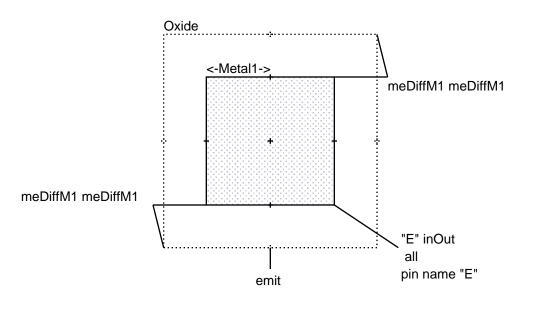
These are the variables to control the pcell. The first section will map to DRC rules and the second section are derived values.

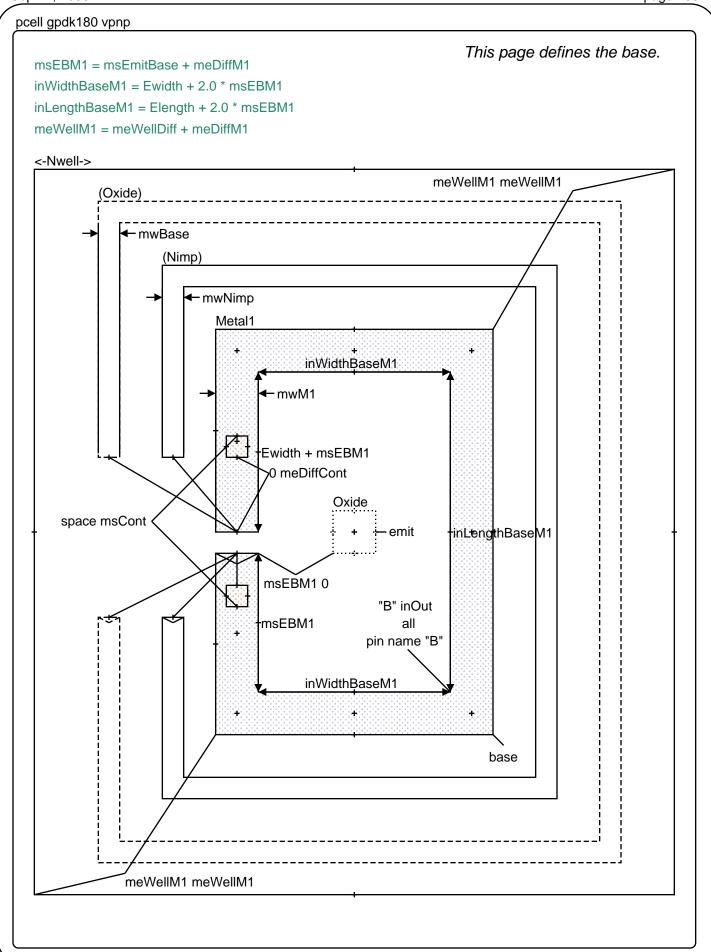
pcell gpdk180 vpnp

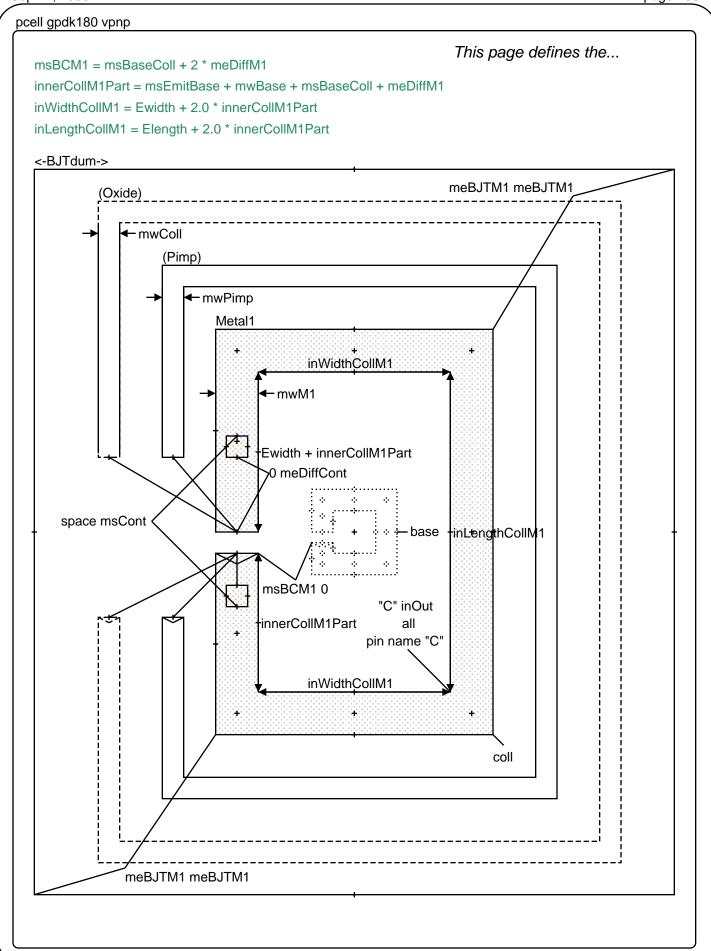
This page defines the...

origin = msEmitBase + mwBase + msBaseColl + mwColl + mePimpDiff + meBJTDum









moscap (macro) pcell macro moscap 999: include <-Metal1-> Metal1 Metal1 metal[0] metal[gc] "D" inOut all must group drain

nmoscap (uses nmos pcell)

pcell gpdk180 nmoscap

PCell formal params

r con ronnar paramo				
fw	float	CDF		
I	float	CDF		
fingers	float	CDF		
connectGates	string	CDF		
connectSD	string	CDF		
switchSD	boolean	CDF		
mtlCvg	string	CDF		
sdMtlWidth	float	CDF		
leftAbut	int	CDF		
rightAbut	int	CDF		
tap	string	CDF		
topTap	boolean	CDF		
bottomTap	boolean	CDF		
leftTap	boolean	CDF		
rightTap	boolean	CDF		
tapExtension	string	CDF		
tapCntRows	int	CDF		

include macro mos include macro mos_nplus include macro mos_nbl_tap include macro mos_cap_tap include macro mosTap include macro mosTap_pplus include macro mosItap_pplus

nmoscap3 (uses nmos pcell)

pcell gpdk180 nmoscap3

PCell formal params

fw	float	CDF
I	float	CDF
fingers	float	CDF
connectGates	string	CDF
connectSD	string	CDF
switchSD	boolean	CDF
mtlCvg	string	CDF
sdMtlWidth	float	CDF
leftAbut	int	CDF
rightAbut	int	CDF
tap	string	CDF
topTap	boolean	CDF
bottomTap	boolean	CDF
leftTap	boolean	CDF
rightTap	boolean	CDF
tapExtension	string	CDF
tapCntRows	int	CDF

include macro mos include macro mos_nplus include macro mos_nbl_tap include macro mos_cap_tap include macro mosTap include macro mosTap_pplus include macro mosItap_pplus include macro moscap

pmoscap (uses pmos pcell)

pcell gpdk180 pmoscap

PCell formal params

r con ronnar parame				
fw	float	CDF		
I	float	CDF		
fingers	float	CDF		
connectGates	string	CDF		
connectSD	string	CDF		
switchSD	boolean	CDF		
mtlCvg	string	CDF		
sdMtlWidth	float	CDF		
leftAbut	int	CDF		
rightAbut	int	CDF		
tap	string	CDF		
topTap	boolean	CDF		
bottomTap	boolean	CDF		
leftTap	boolean	CDF		
rightTap	boolean	CDF		
tapExtension	string	CDF		
tapCntRows	int	CDF		

include macro mos include macro mos_pplus include macro mos_nw_tap include macro mos_cap_tap include macro mosTap include macro mosTap_nplus include macro mosItap_nplus

pmoscap3 (uses pmos pcell)

pcell gpdk180 pmoscap3

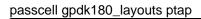
PCell formal params

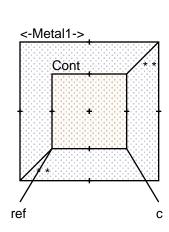
C	(1 1	
fw	float	CDF
I	float	CDF
fingers	float	CDF
connectGates	string	CDF
connectSD	string	CDF
switchSD	boolean	CDF
mtlCvg	string	CDF
sdMtlWidth	float	CDF
leftAbut	int	CDF
rightAbut	int	CDF
tap	string	CDF
topTap	boolean	CDF
bottomTap	boolean	CDF
leftTap	boolean	CDF
rightTap	boolean	CDF
tapExtension	string	CDF
tapCntRows	int	CDF

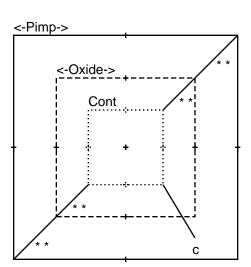
include macro mos include macro mos_pplus include macro mos_nw_tap include macro mos_cap_tap include macro mosTap include macro mosTap_nplus include macro mosItap_nplus include macro moscap

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Basic Test Stru	ıctures

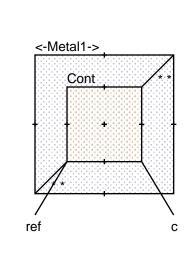
Taps

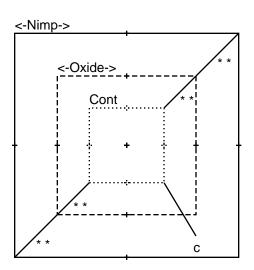






passcell gpdk180_layouts ntap



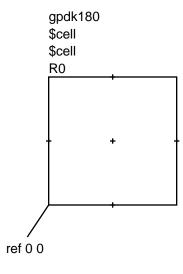


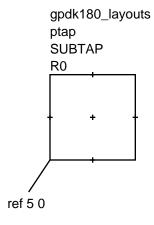
Create Layouts

passcell gpdk180_layouts placer

Pcell Macro Table

\$lib	\$cell
gpdk180_layouts	nmos
gpdk180_layouts	nmos3
gpdk180_layouts	pmos
gpdk180_layouts	pmos3
gpdk180_layouts	nplusres
gpdk180_layouts	pplusres
gpdk180_layouts	nwellres
gpdk180_layouts	polyres
gpdk180_layouts	polyhres
gpdk180_layouts	mimcap
gpdk180_layouts	nmoscap
gpdk180_layouts	pmoscap
gpdk180_layouts	ind
gpdk180_layouts	ndio
gpdk180_layouts	pdio
gpdk180_layouts	npn
gpdk180_layouts	pnp
gpdk180_layouts	vpnp





CDB	layers
-----	--------

ResWdum	0	ResWdum
Oxide	1	OXIDE
Nwell	2	NWELL
Poly	3	POLY
Nimp	4	NIMP
Pimp	5	PIMP
Cont	6	CONT
Metal1	7	METAL1
Via1	8	VIA1
Metal2	9	METAL2
Via2	10	VIA2
Metal3	11	METAL3
Capdum	12	CAPDUM
Resdum	13	RESDUM
CapMetal	14	САРМЕТА
BJTdum	15	BJTDUM
INDdummy	16	INDdumm
IND2dummy	17	IND2dum

CDB layers

18	PWELL
19	NBURIED
20	NPNDUMM
21	PNPDUMM
22	DIODUMM
23	SiProt
24	ThickOxide
30	VIA3
31	METAL4
32	VIA4
33	METAL5
34	VIA5
35	METAL6
36	Bondpad
50	WELLBOD
	19 20 21 22 23 24 30 31 32 33 34 35 36

CDB layers

51	PSUBISO
66	scaP
67	scaNW
68	scaNB
69	scaSel
70	IND3dum
71	M1dum
72	M2dum
73	M3dum
74	M4dum
75	M5dum
76	M6dum
100	allGeo
101	OVERLAP
	66 67 68 69 70 71 72 73 74 75 76

CDB layers

CDB layers		
Unrouted	200	Unroute
Row	201	Row
Group	202	Group
Cannotoccupy	203	noOcupy
Canplace	204	Canplac
hardFence	205	hardFnc
softFence	206	softFnc
y0	207	y0
y1	208	y1
y2	209	y2
у3	210	у3
y4	211	y4
y5	212	у5
y6	213	y6
у7	214	у7
y8	215	y8
y9	216	у9

CDB layers

designFlow	217	dsnFlow
stretch	218	stretch
edgeLayer	219	edgeLyr
changedLayer	220	chngLyr
unset	221	unset
unknown	222	unknown
spike	223	spike
hiz	224	hiz
resist	225	resist
drive	226	drive
supply	227	supply
wire	228	wire
pin	229	pin

CDB lavers

CDD layers		
text	230	text
device	231	device
border	232	border
snap	233	snap
align	234	align
prBoundary	235	prBndry
instance	236	instnce
annotate	237	anotate
marker	238	marker
select	239	select
grid	251	grid
axis	252	axis
hilite	253	hilite
background	254	bkground

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CDB purposes

CDB burbo	562	
GeoShare	1	GeoShare
warning	234	wng
tool1	235	tl1
tool0	236	tIO
label	237	lbl
flight	238	flt
error	239	err
annotate	240	ant
drawing1	241	dr1
drawing2	242	dr2
drawing3	243	dr3
drawing4	244	dr4
drawing5	245	dr5
drawing6	246	dr6
drawing7	247	dr7
drawing8	248	dr8
drawing9	249	dr9
boundary	250	bnd
pin	251	pin
drawing	252	drw
net	253	net
cell	254	cel
all	255	all

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\$layer1	dummy	
\$layer2	dummy	
\$pcLayer1	dummy	
ALL_GATES	(Poly and NPLUS andnot Nwell) or (Poly and PPLUS	device_recognition
	and Nwell)	
BJTdum	input 15;0 df2order 104 packet zbip	
Ei Bondpad	input 36;0 df2order 96 packet pass	
Bondpad_boundary	input (Bondpad boundary) df2order 97 packet	
	pbaseBnd notChg notValid	
Cannotoccupy	input df2order 208 notChg notValid	
Cannotoccupy_bounda	input (Cannotoccupy boundary) df2order 209 packet	
ry	CannotoccupyBnd notChg notValid	
Canplace	input df2order 210 notChg notValid	
CapMetal	input 14;0 df2order 66 packet m4	
CapMetal_boundary	input (CapMetal boundary) df2order 68 packet m4	
	notChg notValid	
CapMetal_net	input (CapMetal net) df2order 67 packet m4 notChg	
	notValid	
Capdum	input 12;0 df2order 98 packet zcap	
Cont	input 6;0 df2order 69 packet cw	via
Cont_boundary	input (Cont boundary) df2order 72 packet cwBnd	
-	notChg notValid	
Cont_net	input (Cont net) df2order 71 packet cwNet notChg	
	notValid	
Cont_pin	input (Cont pin) df2order 70 packet cwPin notChg	
	notValid	
Contdum	Cont and Metal1	
DIOdummy	input 22;0 df2order 107 packet zdiode	
FIELD_POLY1	Poly andnot ALL_GATES	
FIELD POLY1 CONNE	GFIELD_POLY1 and Poly	via
т	_ ,	
Group	input df2order 206 notChg notValid	
Group_label	input (Group label) df2order 207 packet GroupLbl	
	notChg notValid	
IND2dummy	input 17;0 df2order 100 packet zind2	
IND3dummy	input 70;0 df2order 101 packet zind3	
INDUCTOR	(Metal3 or IND2dummy or IND3dummy) and INDdummy	device_recognition
INDdummy	input 16;0 df2order 99 packet zind	
INDdummy_net	input (INDdummy net) packet zindnet notChg notValid	
INDterm1	INDdummy and IND2dummy	
INDterm1Cont	Metal2 and INDdummy and IND2dummy	via
INDterm2	INDdummy and IND3dummy	
INDterm2Cont	Metal2 and INDdummy and IND3dummy	via
ISONMOS	POLYterm andnot Nwell and NPLUS andnot ThickOxide	device_recognition
	andnot Capdum and Nburied	1 22 9
ISONMOSCAP	POLYterm and Nell and NPLUS and Capdum and	device_recognition
	Nburied	
ISONMOSHV	POLYterm andnot Nwell and NPLUS and ThickOxide	device_recognition
	andnot Capdum and Nburied andnot RFdummy	
ISONMOSRF	POLYterm and Novell and NPLUS and ThickOxide	device_recognition
1001 WICON	andnot Capdum and Nburied and RFdummy	acvice_recognition
ISONSDRES	NSD and Resdum and Nburied	device_recognition
ISOPWELL	((Nburied enclose (holes Nwell)) and (holes	
ISOF WELL		
IV/AD4 dummi:	Nwell))andnot Pwell	dovice recognition
JVAR1dummy	input 43;0 packet zjvar1	device_recognition

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JVAR1dummy_net	input (JVAR1dummy net) packet zjvar1 notChg notValid	
JVAR2dummy	input 44;0 packet zjvar2	device_recognition
JVAR3dummy	input 48;0 packet zjvar3	device_recognition
JVARNF	JVAR1dummy and Nwell	device_recognition
JVARW40	JVAR2dummy and Nwell	device_recognition
JVARanode	(JVAR1dummy or JVAR2dummy) andnot NSDterm	
JVARterm	PSDterm and (JVAR1dummy or JVAR2dummy) and	soft_via
	JVAR3dummy	
M1dummy	input 37;0 df2order 103 packet m1dum	
M1res	Metal1 and M1dummy	device_recognition
M1term	Metal1 andnot M1dummy	
M2dummy	input 38;0 df2order 103 packet m2dum	
M2res	Metal2 and M2dummy	device_recognition
M2term	Metal2 andnot M2dummy	
M3dummy	input 39;0 df2order 103 packet m3dum	
M3res	Metal3 and M3dummy	device_recognition
M3term	Metal3 andnot M3dummy	
M4dummy	input 40;0 df2order 103 packet m4dum	
M4res	Metal4 and M4dummy	device recognition
M4term	Metal4 andnot M4dummy	
M5dummy	input 41;0 df2order 103 packet m5dum	
M5res	Metal5 and M5dummy	device_recognition
M5term	Metal5 andnot M5dummy	<u> </u>
M6dummy	input 42;0 df2order 103 packet m6dum	
M6res	Metal6 and M6dummy	device_recognition
M6term	Metal6 andnot M6dummy	<u> </u>
MIMCAP	CapMetal and Metal2 and Capdum	device_recognition
Metal1	input 7;0 df2order 2 packet m1	dovice_reeegrineeri
	Attach Text: 7;3 (Metal1 label)	
Metal1_boundary	input (Metal1 boundary) df2order 5 packet m1Bnd	
Wetari_boardary	notChg notValid	
Metal1_drawing4	input 7;4 (Metal1 drawing4) df2order 1 packet m1	
Metall_drawing+	notChg notValid	
Metal1_label	input 7;3 (Metal1 label) df2order 0 packet m1 notChg	
Wetari_laser	notValid	
Metal1_net	input 7;2 (Metal1 net) df2order 4 packet m1Net	
ivietari_net	notChg notValid	
Metal1_pin	input 7;1 (Metal1 pin) df2order 3 packet m1Pin notChg	
Wetarr_piir	notValid fillStyle X	
Metal2	input 9;0 df2order 14 packet m2	
ivietaiz	Attach Text: 9;3 (Metal2 label)	
Metal2_boundary	input (Metal2 boundary) df2order 17 packet m2Bnd	
ivietaiz_bouridary	notChg notValid	
Metal2_drawing4	input 9;4 (Metal2 drawing4) df2order 13 packet m2	
ivietaiz_drawing4	notChg notValid	
Metal2_label	input 9;3 (Metal2 label) df2order 12 packet m2 notChg	
ivietaiz_iabei	notValid	
Metal2_net	input 9;2 (Metal2 net) df2order 16 packet m2Net	
	notChg notValid	
Metal2_pin	input 9;1 (Metal2 pin) df2order 15 packet m2Pin	
-	notChg notValid fillStyle X	
1	protong not valid illistyle A	
Metal3	input 11;0 df2order 26 packet m3	

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Metal3_boundary	input (Metal3 boundary) df2order 29 packet m3Bnd notChg notValid	
Metal3_drawing4	input 11;4 (Metal3 drawing4) df2order 25 packet m3	
<u> </u>	notChg notValid	
Metal3_label	input 11;3 (Metal3 label) df2order 24 packet m3	
	notChg notValid	
Metal3_net	input 11;2 (Metal3 net) df2order 28 packet m3Net	
	notChg notValid	
Metal3_pin	input 11;1 (Metal3 pin) df2order 27 packet m3Pin	
	notChg notValid fillStyle X	
Metal4	input 31;0 df2order 38 packet m4	
	Attach Text: 31;3 (Metal4 label)	
Metal4_boundary	input (Metal4 boundary) df2order 41 packet m4Bnd	
	notChg notValid	
Metal4_drawing4	input 31;4 (Metal4 drawing4) df2order 37 packet m4	
	notChg notValid	
Metal4_label	input 31;3 (Metal4 label) df2order 36 packet m4	
	notChg notValid	
Metal4_net	input 31;2 (Metal4 net) df2order 40 packet m4Net	
	notChg notValid	
Metal4_pin	input 31;1 (Metal4 pin) df2order 39 packet m4Pin	
	notChg notValid fillStyle X	
Metal5	input 33;0 df2order 50 packet m5	
	Attach Text: 33;3 (Metal5 label)	
Metal5_boundary	input (Metal5 boundary) df2order 53 packet m5Bnd	
TT Marrie de la la contra	notChg notValid	
Metal5_drawing4	input 33;4 (Metal5 drawing4) df2order 49 packet m5	
Motal Label	notChg notValid	
Metal5_label	input 33;3 (Metal5 label) df2order 48 packet m5	
MotolE not	notChg notValid	
Metal5_net	input 33;2 (Metal5 net) df2order 52 packet m5Net notChg notValid	
Metal5_pin	input 33;1 (Metal5 pin) df2order 51 packet m5Pin	+
ivietaio_pii i	notChg notValid fillStyle X	
Metal6	input 35;0 df2order 62 packet m6	
ivietalo	Attach Text: 35;3 (Metal6 label)	
Metal6_boundary	input (Metal6 boundary) df2order 65 packet m6Bnd	
Wetalo_bourlaary	notChg notValid	
Metal6_drawing4	input 35;4 (Metal6 drawing4) df2order 61 packet m6	
motalo_araming r	notChg notValid	
Metal6_label	input 35;3 (Metal6 label) df2order 60 packet m6	
	notChg notValid	
Metal6_net	input 35;2 (Metal6 net) df2order 64 packet m6Net	
	notChg notValid	
Metal6_pin	input 35;1 (Metal6 pin) df2order 63 packet m6Pin	
_ -	notChg notValid fillStyle X	
NBVIA	NWELLterm and Nburied	soft_via
NDIODE	DIOdummy and (NSDterm andnot Nwell)	device_recognition
NMOS	POLYterm andnot Nwell and NPLUS andnot ThickOxide	device_recognition
<u> </u>	andnot Capdum andnot Nburied	
NMOSCAP	POLYterm andnot Nwell and NPLUS and Capdum	device_recognition
	andnot Nburied	
NMOSHV	POLYterm andnot Nwell and NPLUS and ThickOxide	device_recognition
•	andnot Capdum andnot Nburied andnot RFdummy	1

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NMOSRF	POLYterm andnot Nwell and NPLUS and ThickOxide	device_recognition
	andnot Capdum andnot Nburied and RFdummy	
NPLUS NPLUS	Nimp and Oxide	
NPN	NPNdummy and Pwell and Nburied and NSDterm	device_recognition
NPNdummy	input 20;0 df2order 105 packet znpn	
☑ NSD	NPLUS andnot Poly	
NSDRES	NSD and Resdum andnot Nburied	device_recognition
NSDarea	(NPLUS cut Poly) andnot Poly	
NSDcont	Cont and NPLUS	via
✓ NSDterm	NSD andnot Resdum	
NWELLRES	Nwell and ResWdum	device_recognition
NWELLterm	Nwell andnot ResWdum	
NWVIA	NSDterm and NWELLterm	soft_via
Nburied	input 19;0 df2order 88 packet npblk	
Nburied_boundary	input (Nburied boundary) df2order 90 packet npblkBnd	
	notChg notValid	
☐ Nburied_net	input (Nburied net) df2order 89 packet npblk notChg	
	notValid	
Nimp	input 4;0 df2order 91 packet nplus	
Nimp_boundary	input (Nimp boundary) df2order 92 packet nplusBnd	
	notChg notValid	
Nimp_net	input (Nimp net) packet nplusBnd notChg notValid	
Nwell	input 2;0 df2order 79 packet nwell	
Nwell_boundary	input (Nwell boundary) df2order 81 packet nwellBnd	
_	notChg notValid	
Nwell_net	input (Nwell net) df2order 80 packet nwellNet notChg	
_	notValid	
OVERLAP	input 101;0 df2order 116 packet ovlap notSel notValid	
OVERLAP_boundary	input 101;5 (OVERLAP boundary) df2order 118 packet	1
,	ovlap notSel notChg	
OVERLAP_label	input 101;3 (OVERLAP label) df2order 117 packet ovlap	
_	notSel notChg notValid	
i Oxide	input 1;0 df2order 85 packet tox	
Oxide_boundary	input (Oxide boundary) df2order 87 packet toxBnd	
,	notChg notValid	
Oxide_net	input (Oxide net) df2order 86 packet toxBnd notChg	1
	notValid	
PDIODE	DIOdummy and PSDterm and Nwell	device_recognition
PMOS	POLYterm and Nwell and PPLUS andnot ThickOxide	device_recognition
	andnot Capdum	
PMOSCAP	POLYterm and Nwell and PPLUS and Capdum	device_recognition
PMOSHV	POLYterm and Nwell and PPLUS and ThickOxide	device_recognition
	andnot Capdum andnot RFdummy	actico_icocgimien
PMOSRF	POLYterm and Nwell and PPLUS and ThickOxide	device_recognition
1 Meerti	andnot Capdum and RFdummy	device_recegnicer
PNP	PNPdummy and Nwell and Pwell and Nburied and	device_recognition
1 141	PSDterm	device_recognition
PNPdummy	input 21;0 df2order 106 packet zpnp	1
POLYHRES	Poly and Resdum and SiProt	device_recognition
POLYRES	Poly and Resdum and SiProt Poly and Resdum andnot SiProt	device_recognition
POLYcont	Cont and Poly	via
POLYterm	Poly andnot Resdum	via
PPLUS	Pimp and Oxide	+
S PSD	PPLUS andnot Poly	+
	•	dovice recognition
PSDRES	PPLUS and Resdum and Nwell	device_recognition

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National PSDarea	(PPLUS cut Poly) andnot Poly	
PSDcont	Cont and PPLUS	via
S PSDterm	PSD andnot Resdum	
☐ PSUB	(bulk andnot (Nburied or Nwell or Pwell)) or BJTdum	
PWNBVIA	(PSDterm and ISOPWELL)	soft_via
PWVIA	(PSDterm and Pwell) andnot Nwell	soft_via
Pimp	input 5;0 df2order 93 packet pplus	
Pimp_boundary	input (Pimp boundary) df2order 94 packet pplusBnd	
	notChg notValid	
Pimp_net	input (Pimp net) packet pplusBnd notChg notValid	
Poly	input 3;0 df2order 75 packet poly1	
Poly_boundary	input (Poly boundary) df2order 78 packet poly1Bnd	
	notChg notValid	
Poly_drawing4	input 3;4 (Poly drawing4) df2order 74 packet poly1	
	notChg notValid	
Poly_label	input 3;3 (Poly label) df2order 73 packet poly1 notChg	
	notValid	
Poly_net	input 3;2 (Poly net) df2order 77 packet poly1Net	
,-	notChg notValid	
Poly_pin	input 3;1 (Poly pin) df2order 76 packet poly1Pin	
	notChg notValid	
Psubiso	input 51;0 df2order 109	
Pwell	input 18;0 df2order 82 packet pwell	
Pwell_boundary	input (Pwell boundary) df2order 84 packet pwellBnd	
eseamaany	notChg notValid	
Pwell_net	input (Pwell net) df2order 83 packet pwellNet notChg	
	notValid	
RFdummy	input 69;0 df2order 102 packet zrf	
ResWdum	input 71;0 df2order 102 packet zrwell	
Resdum	input 13;0 df2order 102 packet zrpoly	
Row	input df2order 204 packet prBoundaryLbl notChg	
Row_label	input (Row label) df2order 205 packet RowLbl notChg	
I Now_label	notValid	
SUBVIA	(PSUB and PSDterm) andnot Nburied andnot Nwell	soft_via
SiProt	input 72;0 df2order 95 packet siprot	SOIT_VIA
ThickOxide	input 72,0 di20rder 95 packet siprot	
Unrouted	input df2order 211 notChg notValid	
Unrouted_drawing1	input (Unrouted drawing1) df2order 212 packet	
Officuled_drawing i	• • • • • • • • • • • • • • • • • • • •	
Libraritad drawing?	Unrouted1 notChg notValid input (Unrouted drawing2) df2order 213 packet	
Unrouted_drawing2	, ,	
Librarita di dispusia si	Unrouted2 notChg notValid	
Unrouted_drawing3	input (Unrouted drawing3) df2order 214 packet	
	Unrouted3 notChg notValid	
Unrouted_drawing4	input (Unrouted drawing4) df2order 215 packet	
	Unrouted4 notChg notValid	
Unrouted_drawing5	input (Unrouted drawing5) df2order 216 packet	
	Unrouted5 notChg notValid	
Unrouted_drawing6	input (Unrouted drawing6) df2order 217 packet	
	Unrouted6 notChg notValid	
Unrouted_drawing7	input (Unrouted drawing7) df2order 218 packet	
	Unrouted7 notChg notValid	
Unrouted_drawing8	input (Unrouted drawing8) df2order 219 packet	
	Unrouted8 notChg notValid	
Unrouted_drawing9	input (Unrouted drawing9) df2order 220 packet	
	Unrouted9 notChg notValid	

VPNP	page Ab
Via1 input 8:0 df2crder 9 packet v1 via via Via1 boundary input (Via1 boundary) df2crder 11 packet v1Bnd notChg notValid input 8:4 (Via1 drawing4) df2crder 8 packet v1 notChg notValid input 8:3 (Via1 label) df2crder 6 packet v1 notChg notValid input 8:3 (Via1 label) df2crder 6 packet v1 notChg notValid input 8:1 (Via1 pin) df2crder 10 packet v1Net notChg notValid input 8:1 (Via1 pin) df2crder 7 packet v1 notChg notValid input 8:1 (Via1 pin) df2crder 7 packet v1 notChg notValid via2 input 10:0 df2crder 21 packet v2 via via2Cap via2 and CapMetal via2Cap via2 and Metala2 and not (CapMetal or INDdummy) via via2Cap via2 and Metala2 and not (CapMetal or INDdummy) via via2_drawing4 input (Via2 boundary) df2crder 23 packet v2Bnd notChg notValid input 10:3 (Via2_drawing4) df2crder 20 packet v2 notChg notValid via2_label input 10:3 (Via2_label) df2crder 19 packet v2 notChg notValid via2_net input (Via2 pin) df2crder 19 packet v2 notChg notValid via2_pin input 10:1 (Via2 pin) df2crder 18 packet v2 notChg notValid input 10:1 (Via2 pin) df2crder 33 packet v3 via3 input 30:0 df2crder 33 packet v3 input 30:0 df2crder 35 packet v3 input 30:0 df2crder 37 packet v3 notChg notValid via3_drawing4 input 30:0 (Via3 remi) df2crder 31 packet v3 notChg notValid via3_label input 30:1 (Via3 pin) df2crder 31 packet v3 notChg notValid via3_net input 30:1 (Via3 pin) df2crder 34 packet v4 notChg notValid input 30:1 (Via3 pin) df2crder 34 packet v4 notChg notValid input 30:1 (Via4 pin) df2crder 45 packet v4 notChg notValid input 30:3 (Via4 pin) df2crder 45 packet v4 notChg notValid input 30:3 (Via4 pin) df2crder 45 packet v4 notChg notValid input 30:3 (Via4 pin) df2crder 45 packet v4 notChg notValid input 30:3 (Via4 pin) df2crder 45 packet v4 notChg notValid input 30:1 (Vi	ion
Via1_boundary input (Via1 boundary) df2order 11 packet v1Bnd notChg notValid input 8:4 (Via1 drawing4) df2order 8 packet v1 notChg notValid input 8:3 (Via1 label) df2order 6 packet v1 notChg notValid input 8:3 (Via1 label) df2order 6 packet v1 notChg notValid input 8:3 (Via1 label) df2order 10 packet v1Net notChg notValid input 8:1 (Via1 net) df2order 7 packet v1 notChg notValid input 8:1 (Via1 pin) df2order 7 packet v1 notChg notValid input 8:1 (Via1 pin) df2order 7 packet v2 via input 10:0 df2order 21 packet v2 via via2 input 10:0 df2order 21 packet v2 via via2 via2 via2 and CapMetal via2 via2 via2 and CapMetal via2 via3 via4 vi	
notChg notValid Via1_drawing4	
Via1_drawing4 input 8.4 (Via1 drawing4) df2order 8 packet v1 notChg notValid input 8.3 (Via1 label) df2order 6 packet v1 notChg notValid input 8.3 (Via1 label) df2order 6 packet v1 notChg notValid input 8.1 (Via1 net) df2order 10 packet v1Net notChg notValid input 8.1 (Via1 net) df2order 7 packet v1 notChg notValid input 8.1 (Via1 pin) df2order 7 packet v2 via notValid via2 input 10:0 df2order 21 packet v2 via via2 via2 via2 and Metal2 andnot (CapMetal or INDdummy) via input 8.2 boundary input (Via2 boundary) df2order 23 packet v2Bnd input 10:4 (Via2 drawing4) df2order 20 packet v2 notChg notValid input 10:4 (Via2 drawing4) df2order 20 packet v2 notChg notValid input 10:3 (Via2 label) df2order 19 packet v2 notChg notValid input 10:1 (Via2 pin) df2order 19 packet v2 notChg notValid input 10:1 (Via2 pin) df2order 18 packet v2 notChg notValid input 10:1 (Via2 pin) df2order 18 packet v2 notChg notValid input 30:0 df2order 33 packet v3 via input 30:0 df2order 33 packet v3 via input 30:0 df2order 33 packet v3 via input 30:3 (Via3 boundary) df2order 32 packet v3 via input 30:3 (Via3 drawing4) df2order 32 packet v3 notChg notValid input 30:3 (Via3 drawing4) df2order 32 packet v3 notChg notValid input 30:3 (Via3 drawing4) df2order 31 packet v3 notChg notValid via3 input 30:3 (Via3 drawing4) df2order 31 packet v3 notChg notValid input 30:3 (Via3 pin) df2order 34 packet v3 notChg notValid via4 input 30:3 (Via4 pondary) df2order 47 packet v4 via input 30:3 (Via4 pondary) df2order 47 packet v4 via input 30:4 (Via4 drawing4) df2order 49 packet v4 via input 32:1 (Via4 boundary) df2order 49 packet v4 via input 32:1 (Via4 pondary) df2order 49 packet v4 notChg notValid input 32:1 (Via4 pin) df2order 49 packet v4 notChg notValid input 32:1 (Via4 pin) df2order 49 packet v4 notChg notValid input 32:1 (Via4 pin) df2order 49 packet v4 notChg notValid input 32:1 (Via4 pin) df2order 49 packet	
NotValid Input 8;3 (Via1 label) df2order 6 packet v1 notChg notValid Input 8;3 (Via1 label) df2order 10 packet v1Net notChg notValid Input 8;1 (Via1 pin) df2order 7 packet v1 notChg notValid Via1_pin Input 8;1 (Via1 pin) df2order 7 packet v1 notChg notValid Via2 Input 10;0 df2order 21 packet v2 Via2 Via2 Via2 and CapMetal Via2 and Metal2 and fot (CapMetal or INDdummy) Via Via2 boundary Input (Via2 boundary) df2order 23 packet v2Bnd notChg notValid Input 10;4 (Via2 drawing4) df2order 20 packet v2 notChg notValid Input 10;4 (Via2 drawing4) df2order 19 packet v2 notChg notValid Input 10;3 (Via2 label) df2order 19 packet v2 notChg notValid Input 10;3 (Via2 label) df2order 18 packet v2 notChg notValid Input 10;1 (Via2 pin) df2order 18 packet v2 notChg notValid Input 10;1 (Via2 pin) df2order 18 packet v2 notChg notValid Input 10;1 (Via2 pin) df2order 18 packet v2 notChg notValid Input 10;3 (Via2 label) df2order 35 packet v3 Via3 Input 30;0 df2order 35 packet v3 Via3 Input 30;0 df2order 35 packet v3 Via1 Input 30;4 (Via3 boundary) df2order 35 packet v3 Via1 Input 30;4 (Via3 drawing4) df2order 32 packet v3 NotChg notValid Input 30;4 (Via3 drawing4) df2order 32 packet v3 NotChg notValid Input 30;4 (Via3 label) df2order 31 packet v3 notChg notValid Input 30;4 (Via3 label) df2order 34 packet v3 notChg notValid Input 30;4 (Via3 pin) df2order 34 packet v3 notChg notValid Input 30;4 (Via3 pin) df2order 34 packet v4 NotChg notValid Input 30;4 (Via4 boundary) df2order 45 packet v4 Via1 Input 40;4 (Via4 boundary) df2order 47 packet v4Bnd notChg notValid Input 30;4 (Via4 drawing4) df2order 47 packet v4Net notChg notValid Input 32;4 (Via4 drawing4) df2order 48 packet v4 notChg notValid Input 32;4 (Via4 drawing4) df2order 49 packet v4Net notChg notValid Input 32;4 (Via4 pet) df2order 49 packet v4Net notChg notValid Input 32;4 (Via4 pet) df2order 49 packet v4 notChg notValid Input 32;4 (Via4 pet) df2order 49 packet v4 notChg notValid I	
Via1_label input 8;3 (Via1 label) df2order 6 packet v1 notChg notValid input 8;1 (Via1 net) df2order 10 packet v1Net notChg notValid input 8;1 (Via1 pin) df2order 7 packet v1 notChg notValid input 8;1 (Via1 pin) df2order 7 packet v1 notChg notValid input 10;0 df2order 21 packet v2 via Via2 input 10;0 df2order 21 packet v2 via Via2NoCaplnd Via2 and Metal2 via2NoCaplnd via input 10;4 (Via2 boundary) df2order 23 packet v2Bnd notChg notValid input 10;4 (Via2 boundary) df2order 23 packet v2Bnd notChg notValid input 10;4 (Via2 drawing4) df2order 20 packet v2 notChg notValid input 10;3 (Via2 label) df2order 19 packet v2 notChg notValid input 10;3 (Via2 label) df2order 19 packet v2 notChg notValid input 10;1 (Via2 pin) df2order 18 packet v2 notChg notValid input 10;1 (Via2 pin) df2order 18 packet v2 notChg notValid input 10;1 (Via2 pin) df2order 33 packet v3 via input 30;0 df2order 33 packet v3 via input 30;0 df2order 33 packet v3 via input 30;0 df2order 34 packet v3 via input 30;3 (Via3 label) df2order 32 packet v3 notChg notValid input 30;3 (Via3 label) df2order 31 packet v3 notChg notValid input 30;3 (Via3 label) df2order 34 packet v3 notChg notValid input 30;3 (Via3 label) df2order 34 packet v3 notChg notValid input 30;3 (Via3 pin) df2order 34 packet v4 notChg notValid input 30;3 (Via3 pin) df2order 34 packet v4 hotChg notValid input 32;4 (Via4 boundary) df2order 47 packet v4Bnd notChg notValid input 32;4 (Via4 boundary) df2order 47 packet v4Bnd notChg notValid input 32;4 (Via4 drawing4) df2order 47 packet v4Net notChg notValid input 32;4 (Via4 drawing4) df2order 47 packet v4Net notChg notValid input 32;4 (Via4 pin) df2order 45 packet v4Net notChg notValid input 32;4 (Via4 pin) df2order 45 packet v4Net notChg notValid input 32;4 (Via4 pin) df2order 45 packet v4Net notChg notValid input 32;4 (Via4 pin) df2order 45 packet v4Net notChg notValid input 32;4 (Via4 pin) df2order 45 packet v4Net notChg notValid input 32;4 (Via4 pin)	
NotValid Input (Via1 net) df2order 10 packet v1Net notChg notValid Input (Via1 pin) df2order 7 packet v1 notChg notValid Via1 pin Input 8:1 (Via1 pin) df2order 7 packet v1 notChg notValid Via2 Input 10:0 df2order 21 packet v2 Via Via2Cap Via2 and CapMetal Via2Cap Via2 and Metal2 Via2Cap Via2 and Metal2 Via2 poundary Via2 poundary Input (Via2 boundary) Via2 packet v2Bnd notChg notValid Input 10:3 (Via2 drawing4) df2order 23 packet v2Bnd notChg notValid Input 10:3 (Via2 label) df2order 19 packet v2 notChg notValid Input 10:3 (Via2 label) df2order 19 packet v2 notChg notValid Input 10:3 (Via2 label) df2order 19 packet v2 notChg notValid Via2_net Input (Via2 pet) df2order 22 packet v2Net notChg notValid Via2_pin Input 10:1 (Via2 pin) df2order 18 packet v2 notChg notValid Via2_NotCapInd and Metal3 Input 30:0 df2order 33 packet v3 Via3 Via3_boundary Input 30:0 df2order 33 packet v3 Via3_boundary Input (Via3 boundary) df2order 35 packet v3Bnd notChg notValid Via3_drawing4 Input 30:4 (Via3 drawing4) df2order 31 packet v3 notChg notValid Via3_net Input 30:3 (Via3 label) df2order 31 packet v3 notChg notValid Via3_net Input 30:3 (Via3 label) df2order 34 packet v3 notChg notValid Via3_net Input 30:3 (Via3 label) df2order 34 packet v3 notChg notValid Via3_net Input 30:3 (Via3 pin) df2order 34 packet v3 notChg notValid Via4 Input 32:3 (Via4 boundary) df2order 47 packet v4Bnd notChg notValid Via4_boundary Input 32:4 (Via4 drawing4) df2order 47 packet v4Bnd notChg notValid Via4_label Input 32:3 (Via4 label) df2order 43 packet v4 notChg notValid Input 32:3 (Via4 label) df2order 45 packet v4Net notChg notValid Input 32:4 (Via4 drawing4) df2order 45 packet v4Net notChg notValid Input 32:4 (Via4 drawing4) df2order 45 packet v4Net notChg notValid Input 32:1 (Via4 pin) df2order 45 packet v4Net notChg notValid Input 32:1 (Via4 pin) df2order 45 packet v4Net notChg notValid Input 32:1 (Via4 pin) df2order 45 packet v4Net notChg	
Via1_net	
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notValid input (Via3 net) df2order 34 packet v3Net notChg notValid Via3_pin input 30;1 (Via3 pin) df2order 30 packet v3 notChg notValid Via3 and Metal4 Via4 input 32;0 df2order 45 packet v4 via Via4_boundary input (Via4 boundary) df2order 47 packet v4Bnd notChg notValid Via4_drawing4 input 32;4 (Via4 drawing4) df2order 44 packet v4 notChg notValid Via4_label input 32;3 (Via4 label) df2order 43 packet v4 notChg notValid Via4_net input (Via4 net) df2order 46 packet v4Net notChg notValid Via4_pin input 32;1 (Via4 pin) df2order 42 packet v4 notChg notValid Via4_and Metal5	
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Via4_label input 32;3 (Via4 label) df2order 43 packet v4 notChg notValid Via4_net input (Via4 net) df2order 46 packet v4Net notChg notValid Via4_pin input 32;1 (Via4 pin) df2order 42 packet v4 notChg notValid Via4dum Via4 and Metal5	
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Via4_pin input 32;1 (Via4 pin) df2order 42 packet v4 notChg notValid Via4 and Metal5	
notValid Via4dum Via4 and Metal5	
Uia4 and Metal5	
Viab Illiput 54,0 dizbidei 57 backet v5 Ivia Ivia	
☐ Via5_boundary input (Via5 boundary) df2order 59 packet v5Bnd	
notChg notValid	

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☐ Via5_drawing4	input 34;4 (Via5 drawing4) df2order 56 packet v5 notChg notValid	
Via5_label	input 34;3 (Via5 label) df2order 55 packet v5 notChg	
	notValid	
Via5_net	input (Via5 net) df2order 58 packet v5Net notChg notValid	
☐ Via5_pin	input 34;1 (Via5 pin) df2order 54 packet v5 notChg notValid	
I : Vio E duna		
Via5dum	Via5 and Metal6	
WellBody	input df2order 108 notVis notSel notChg notValid	
align	input df2order 137 notChg notValid	
allGeoShare	input 100;1 df2order 114 packet ovlap notVis notSel notChg notValid	
allGeoShare_GeoShare	input 100;0 (allGeoShare GeoShare) df2order 115	
	packet ovlap	
annotate annotate	input df2order 122 packet annotate notChg notValid	
annotate_drawing1	input (annotate drawing1) df2order 123 packet	
	annotate1 notChg notValid	
annotate_drawing2	input (annotate drawing2) df2order 124 packet	
	annotate2 notChg notValid	
annotate_drawing3	input (annotate drawing3) df2order 125 packet	
amotato_arawingo	annotate3 notChg notValid	
annotate_drawing4	input (annotate drawing4) df2order 126 packet	
annotate_drawing4	• • • • • • • • • • • • • • • • • • • •	
Danatata dravinas	annotate4 notChg notValid	
annotate_drawing5	input (annotate drawing5) df2order 127 packet	
	annotate5 notChg notValid	
annotate_drawing6	input (annotate drawing6) df2order 128 packet	
	annotate6 notChg notValid	
annotate_drawing7	input (annotate drawing7) df2order 129 packet annotate7 notChg notValid	
annotate_drawing8	input (annotate drawing8) df2order 130 packet	
	annotate8 notChg notValid	
annotate_drawing9	input (annotate drawing9) df2order 131 packet	
	annotate9 notChg notValid	
axis	input df2order 156 packet axis notSel notChg notValid	
background	input df2order 119 packet background notSel notChg	
sacrigicana	notDrg notValid	
border	input df2order 144 packet border notChg notValid	
bulk bulk	substrate	
changedLayer_tool0	input (changedLayer tool0) df2order 200 packet	
ChangedLayer_toolo	changedLayerTl0 notVis notSel notChg notDrg	
	notValid	
changedLayer_tool1	input (changedLayer tool1) df2order 201 packet	+
ChangedLayer_toom	changedLayerTl1 notVis notSel notChg notDrg	
	InotValid	
decign Flour		
designFlow	input df2order 190 packet designFlow notSel notChg	
de alema Classon de la Caración	notDrg	
designFlow_drawing1	input (designFlow drawing1) df2order 191 packet	
de alone Classes directions C	designFlow1 notSel notChg notDrg	
designFlow_drawing2	input (designFlow drawing2) df2order 192 packet	
	designFlow2 notSel notChg notDrg	
designFlow_drawing3	input (designFlow drawing3) df2order 193 packet	
	designFlow3 notSel notChg notDrg	
designFlow_drawing4	input (designFlow drawing4) df2order 194 packet	
	designFlow4 notSel notChg notDrg	

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designFlow_drawing5	input (designFlow drawing5) df2order 195 packet	
	designFlow5 notSel notChg notDrg	
designFlow_drawing6	input (designFlow drawing6) df2order 196 packet designFlow6 notSel notChg notDrg	
designFlow_drawing7	input (designFlow drawing7) df2order 197 packet	
designFlow_drawling/	designFlow7 notSel notChg notDrg	
designFlow_drawing8	input (designFlow drawing8) df2order 198 packet	
designi low_drawingo	designFlow8 notSel notChg notDrg	
designFlow_drawing9	input (designFlow drawing9) df2order 199 packet	
assign is n_ananings	designFlow9 notSel notChg notDrg	
device	input df2order 145 packet device notChg notValid	
device_annotate	input (device annotate) df2order 149 packet deviceAnt	
	notChg notValid	
device_drawing1	input (device drawing1) df2order 147 packet device1	
	notChg notValid	
device_drawing2	input (device drawing2) df2order 148 packet device2	
	notChg notValid	
device_label	input (device label) df2order 146 packet deviceLbl	
	notChg notValid	
drive	input df2order 183 packet drive notChg notValid	
edgeLayer	input df2order 157 packet edgeLayer notChg notValid	
edgeLayer_pin	input (edgeLayer pin) df2order 158 packet	
	edgeLayerPin notChg notValid	
grid	input df2order 120 packet grid notSel notChg notDrg	
	notValid	
grid_drawing1	input (grid drawing1) df2order 121 packet grid1 notSel	
	notChg notDrg notValid	
hardFence	input df2order 138 packet hardFence notChg notValid	
hilite	input df2order 172 packet hilite notChg notValid	
hilite_drawing1	input (hilite drawing1) df2order 173 packet hilite1	
Lilita daguda ao	notChg notValid	+
hilite_drawing2	input (hilite drawing2) df2order 174 packet hilite2 notChg notValid	
hilite_drawing3	input (hilite drawing3) df2order 175 packet hilite3	
	notChg notValid	
hilite_drawing4	input (hilite drawing4) df2order 176 packet hilite4	_
	notChg notValid	
hilite_drawing5	input (hilite drawing5) df2order 177 packet hilite5	
	notChg notValid	
hilite_drawing6	input (hilite drawing6) df2order 178 packet hilite6	
	notChg notValid	
hilite_drawing7	input (hilite drawing7) df2order 179 packet hilite7	
	notChg notValid	
hilite_drawing8	input (hilite drawing8) df2order 180 packet hilite8	
	notChg notValid	
hilite_drawing9	input (hilite drawing9) df2order 181 packet hilite9	
	notChg notValid	
hiz	input df2order 184 packet hiz notChg notValid	
instance	input df2order 132 packet instance notChg notValid	
instance_label	input (instance label) df2order 133 packet instanceLbl	
	notChg notValid	
marker_error	input (marker error) df2order 203 packet markerErr	
morker wereing	notChg notValid	
marker_warning	input (marker warning) df2order 202 packet markerWarn notChg notValid	
L	Imarkeryvani notony notvanu	

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pin	input df2order 154 packet pin notChg notValid	
pin_annotate	input (pin annotate) df2order 155 packet pinAnt	
	notChg notValid	
pin_label	input (pin label) df2order 153 packet pinLbl notChg	
·	notValid	
prBoundary	input 235;0 df2order 134 packet prBoundary notChg	
prBoundary_boundary	input 235;5 (prBoundary boundary) df2order 135	
	packet prBoundaryBnd notChg notValid	
prBoundary_label	input (prBoundary label) df2order 136 packet	
	prBoundaryLbl notChg notValid	
resist	input df2order 185 packet resist notChg notValid	
scaNburied_net	input (scaNburied net) df2order 111 notSel notChg	
	notDrg notValid noTranslate	
scaNwell_net	input (scaNwell net) df2order 112 notSel notChg	
	notDrg notValid noTranslate	
scaPort_net	input (scaPort net) df2order 113 notSel notChg notDrg	
	notValid noTranslate	
scaSelect	input df2order 110 notChg noTranslate	
select	input df2order 182 packet select notChg notValid	
snap	input df2order 159 packet snap notChg notValid	
snap_boundary	input (snap boundary) df2order 160 packet snap	
snap_boundary	notChg notValid	
softFence	•	
=	input df2order 139 packet softFence notChg notValid	
spike	input df2order 186 packet spike notChg notValid	
stretch	input df2order 161 packet stretch notChg notValid	
supply	input df2order 187 packet supply notChg notValid	
text	input 230;0 df2order 140 packet notChg	
text_drawing1	input (text drawing1) df2order 142 packet text1	
<u></u>	notChg notValid	
text_drawing2	input (text drawing2) df2order 143 packet text2	
	notChg notValid	
text_label	input 230;3 (text label) df2order 141 packet notChg	
unknown	input df2order 188 packet unknown notChg notValid	
unset	input df2order 189 packet unset notChg notValid	
wire	input df2order 150 packet wire notChg notValid	
[i wire_flight	input (wire flight) df2order 152 packet wireFlt notChg	
	notValid	
wire_label	input (wire label) df2order 151 packet wireLbl notChg	
	notValid	
[] y0	input df2order 162 packet y0 notChg notValid	
[y1	input df2order 163 packet y1 notChg notValid	
i y2	input df2order 164 packet y2 notChg notValid	
[] y3	input df2order 165 packet y3 notChg notValid	
y4	input df2order 166 packet y4 notChg notValid	
[_] y5	input df2order 167 packet y5 notChg notValid	
y6	input df2order 168 packet y6 notChg notValid	
y7	input df2order 169 packet y7 notChg notValid	
y8	input df2order 170 packet y8 notChg notValid	
y9	input df2order 171 packet y9 notChg notValid	
	1 7 9	

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0A	Nburied width must be >= 1.0 um
0B	Nburied to Nburied spacing must be >= 1.0 um
1A	Nwell width must be >= 1.0 um
1B	Nwell to Nwell spacing must be >= 1.0 um
1C	Nburied to Nwell enclosure must be >= 0.3 um
1D	Pwell width must be >= 1.0 um
1E	Pwell to Pwell spacing must be >= 1.0 um
1F	Nburied to Pwell enclosure must be >= 0.3 um
2.5A	ThickOxide width must be >= 0.5 um
2.5B	ThickOxide to ThickOxide spacing must be >= 0.4 um
2.5C	ThickOxide to Oxide enclosure must be >= 0.25 um
2.5D	ThickOxide to Oxide spacing must be >= 0.25 um
2.5E	ThickOxide to Poly spacing must be >= 0.4 um
2.5F	ThickOxide to Poly enclosure must be >= 0.4 um
2A	Oxide width must be >= 0.4 um
2B	Oxide to Oxide spacing must be >= 0.3 um
2C	Nwell to Oxide enclosure must be >= 0.5 um
2C	Pwell to Oxide enclosure must be >= 0.5 um
2D	Nwell to Oxide spacing must be >= 0.5 um
2D	Pwell to Oxide spacing must be >= 0.5 um
3A	Nimp width must be >= 0.4 um
3B	Nimp to Nimp spacing must be >= 0.4 um
3C	Nimp to Oxide enclosure must be >= 0.2 um
3D	Nburied to Nimp enclosure must be >= 0.6 um
4A	Pimp width must be >= 0.4 um
4B	Pimp to Pimp spacing must be >= 0.4 um
4C	Pimp to Oxide enclosure must be >= 0.2 um
4D	Nburied to Pimp enclosure must be >= 0.6 um
5A	Poly width must be >= 0.18 um
5B	Poly to Poly spacing must be >= 0.3 um
5C	Poly to Oxide enclosure must be >= 0.2 um
5D	Oxide to Poly enclosure must be >= 0.4 um
5E	Oxide to Poly spacing must be >= 0.2 um
6A	Cont shapes must be 0.2x0.2 rectangles
6B	Cont to Cont spacing must be >= 0.2 um
6C	Oxide to Cont enclosure must be >= 0.2 um
6D	Poly to Cont enclosure must be >= 0.2 um
6E	Poly to Cont spacing must be >= 0.2 um
6F	Pimp to Cont enclosure must be >= 0.1 um
6G	Nimp to Cont enclosure must be >= 0.1 um
6H	Oxide to Cont spacing must be >= 0.2 um
7A	Metal1 width must be >= 0.3 um
7B	Metal1 to Metal1 spacing must be >= 0.3 um
7C	Metal1 to Cont enclosure must be >= 0.1 um
8A	Via1 shapes must be 0.2x0.2 rectangles
8B	Via1 to Via1 spacing must be >= 0.3 um
8C	Metal1 to Via1 enclosure must be >= 0.1 um
9A	Metal2 width must be >= 0.3 um
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9B	Metal2 to Metal2 spacing must be >= 0.3 um	
9C	Metal2 to Via1 enclosure must be >= 0.1 um	
10A	Via2 shapes must be 0.2x0.2 rectangles	
10B	Via2 to Via2 spacing must be >= 0.3 um	
10C	Metal2 to Via2 enclosure must be >= 0.1 um	
11A	Metal3 width must be >= 0.3 um	
11B	Metal3 to Metal3 spacing must be >= 0.3 um	
11C	Metal3 to Via2NoCapInd enclosure must be >= 0.1 um	
11D	Metal3 to Via2Cap enclosure must be >= 0.1 um	
12A	CapMetal width must be >= 0.5 um	
12B	Metal2 to Via2Cap enclosure must be >= 0.1 um	
12C	CapMetal to Via2Cap enclosure must be >= 0.2 um	
12D	CapMetal to Metal3 enclosure must be >= 0.3 um	
13A1	P+SD to NW tap spacing must be <= 10.0 um	
13A2	N+SD to Iso Psub tap spacing must be <= 10.0 um	
13A3	N+SD to Psub tap spacing must be <= 10.0 um	
13A3	N+SD to PW tap spacing must be <= 10.0 um	
14A	Via3 shapes must be 0.2x0.2 rectangles	
14B	Via3 to Via3 spacing must be >= 0.3 um	
14C	Metal3 to Via3 enclosure must be >= 0.1 um	
15A	Metal4 width must be >= 0.3 um	
15B	Metal4 to Metal4 spacing must be >= 0.3 um	
15C	Metal4 to Via3 enclosure must be >= 0.1 um	
16A	Via4 shapes must be 0.2x0.2 rectangles	
16B	Via4 to Via4 spacing must be >= 0.3 um	
16C	Metal4 to Via4 enclosure must be >= 0.1 um	
17A	Metal5 width must be >= 0.3 um	
17B	Metal5 to Metal5 spacing must be >= 0.3 um	
17C	Metal5 to Via4 enclosure must be >= 0.1 um	
18A	Via5 shapes must be 0.2x0.2 rectangles	
18B	Via5 to Via5 spacing must be >= 0.3 um	
18C	Metal5 to Via5 enclosure must be >= 0.1 um	
19A	Metal6 width must be >= 0.3 um	
19B	Metal6 to Metal6 spacing must be >= 0.3 um	
19C	Metal6 to Via5 enclosure must be >= 0.1 um	
20A	Minimum Pad width = 45.0	
20B	Minimum Pad spacing to Pad = 10.0	
20C	Metal1 to Bondpad enclosure must be == 3.0 um	
20D	Metal2 to Bondpad enclosure must be == 3.0 um	
20E	Metal3 to Bondpad enclosure must be == 3.0 um	
20F	Metal4 to Bondpad enclosure must be == 3.0 um	
20G	Metal5 to Bondpad enclosure must be == 3.0 um	
20H	Metal6 to Bondpad enclosure must be == 3.0 um	
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