2x2 Multi-Project Support for Caravel (MPC) features

The user_project_wrapper is divided into 4 independent projects. One project can be activated at a time using a configuration register that is implemented inside the muli_user_project_wrapper.

Each user_project has the following **features**:
- Area of **1300 um x 1600 um**
- 36 IO ports
- 32 logic analyzer probes
- Wishbone port connection to the management SoC wishbone bus.
- 1 digital power domain

Limitations:
- Analog IOs are not supported.
- Metal5 cannot be used in user projects
- User projects have fixed PDN configuration

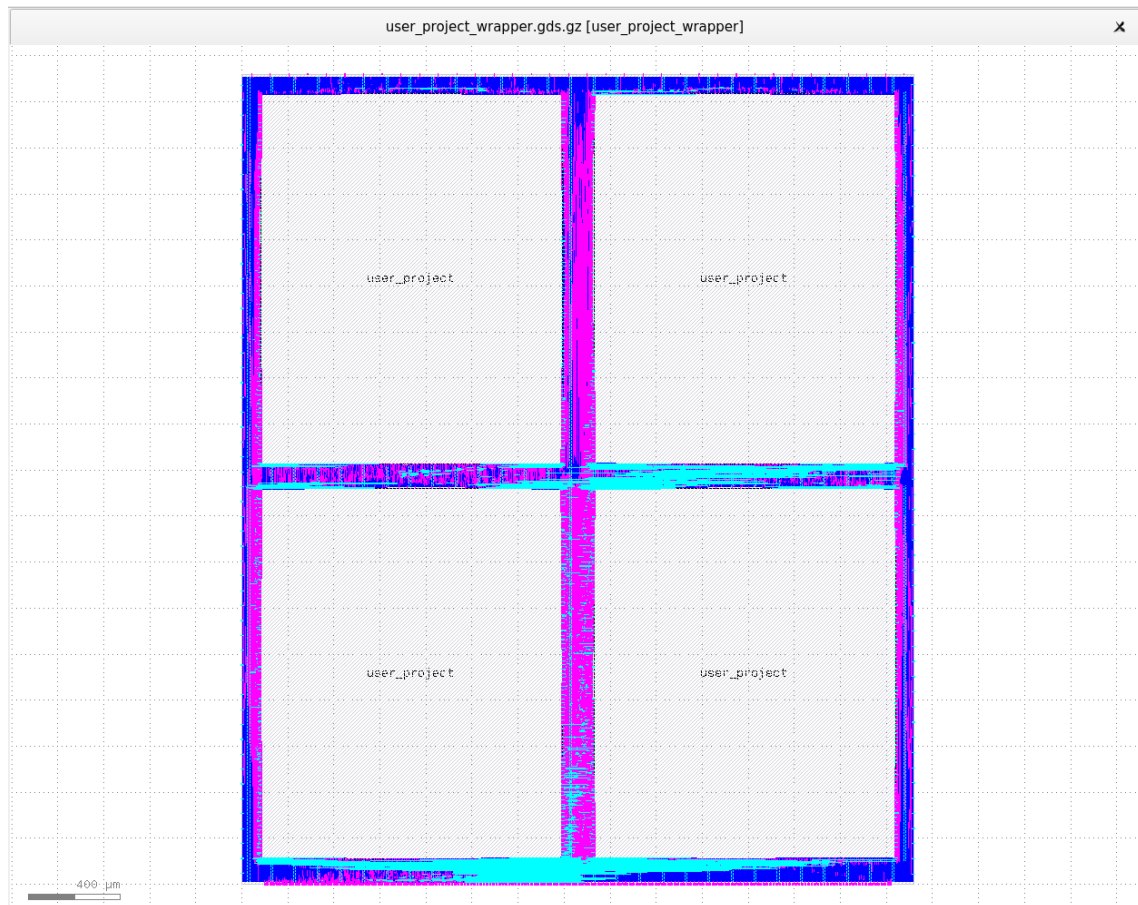Figure 1 shows a routed implementation for the mpc user_project_wrapper.

Fig 1. Routed MPC user_project_wrapper.