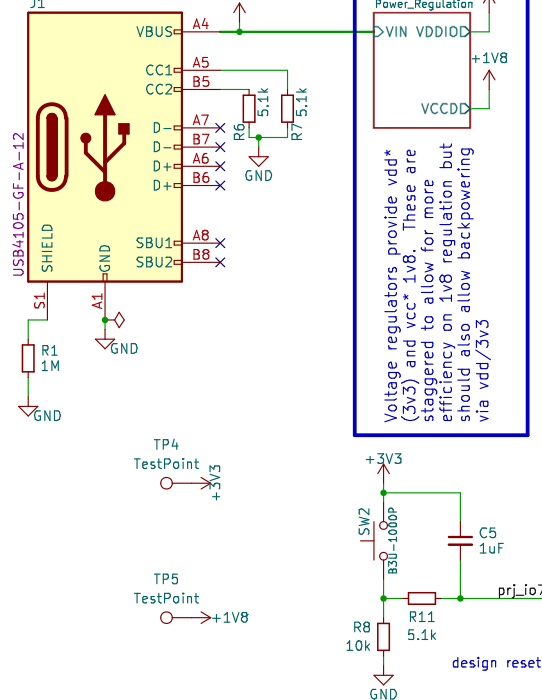


## Power Supply



## TT3p5 VGA Clock PCB

I/O

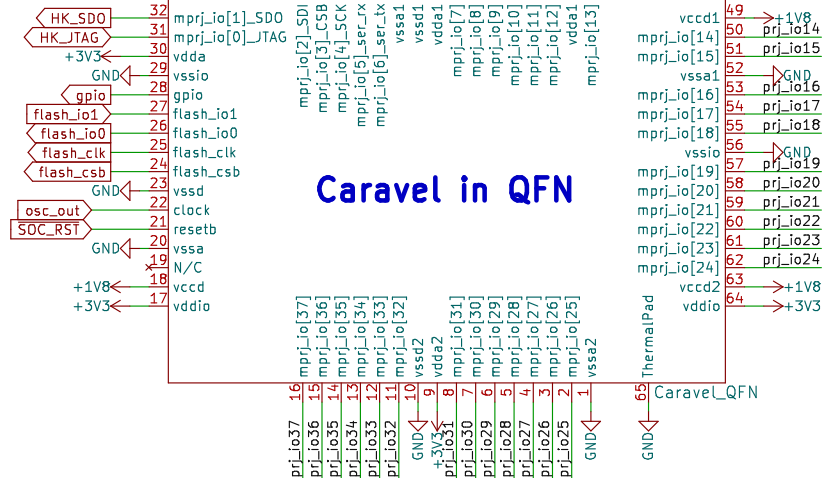
inputs

06 clock  
07 rst\_n  
08 hours  
09 mins  
10 sec

outputs

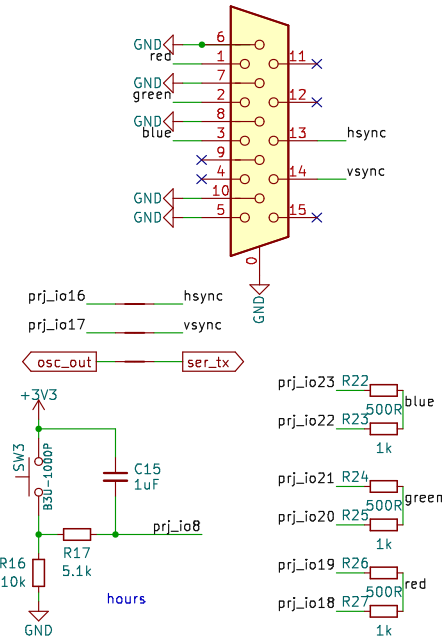
16 hsync  
17 vsync  
18:23 rrrgbb

U1

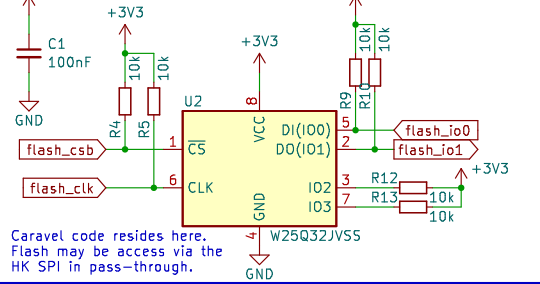


## Caravel in QFN

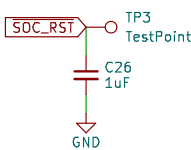
J5 DE15\_Receptacle\_HighDensity\_MountingHoles



## Flash Memory

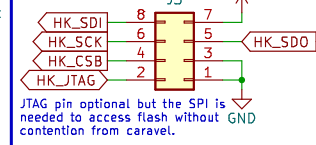


SoC Caravel reset

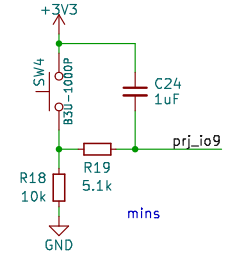
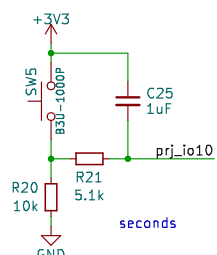
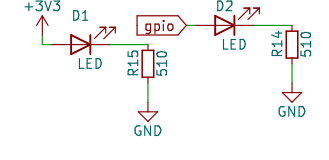


## Housekeeping SPI

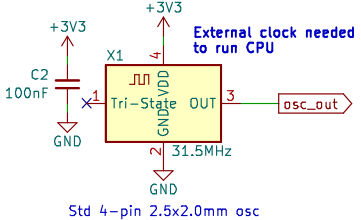
prj\_io 0 to 4, special purpose, use to program flash



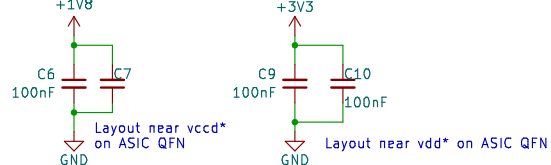
## Power Good and GPIO LED



## CMOS Clock Osc



## Bypass Caps



Fids for PnP

- FID1 Fiducial
- FID2 Fiducial
- FID3 Fiducial

ref chip pinout: [https://github.com/TinyTapeout/tinytapeout-03p5/blob/main/verilog/rtl/user\\_defines.v](https://github.com/TinyTapeout/tinytapeout-03p5/blob/main/verilog/rtl/user_defines.v)  
vga clock design: <https://github.com/TinyTapeout/tt03p5-vga-clock>

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Psychogenic Technologies INC

Sheet: /

File: tt-vga-clock.kicad\_sch

Title: Caravel Minimum Viable PCB Example

Size: A4 Date: 2023-10-27

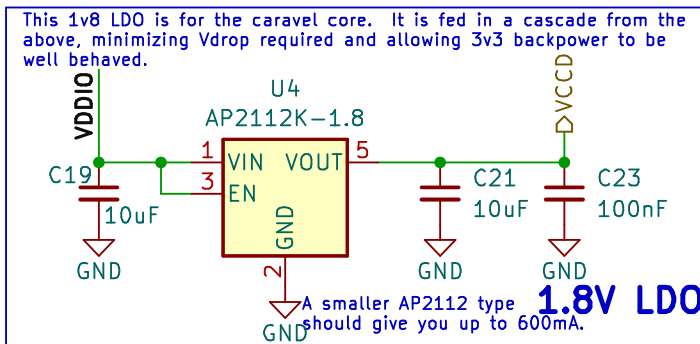
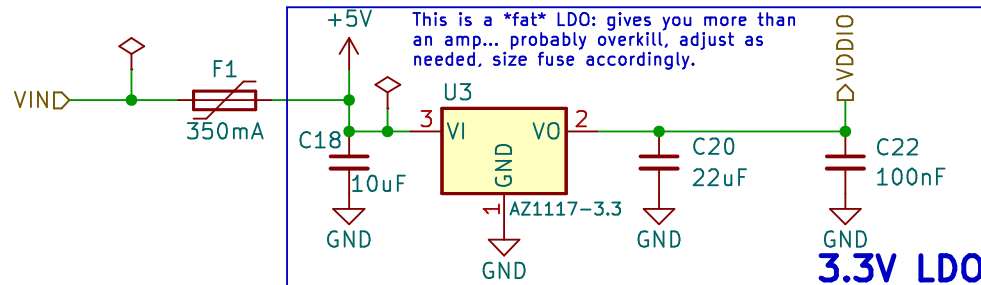
KiCad E.D.A. kicad 7.0.9-7.0.9-ubuntu20.04.1

Rev: 1.2

Id: 1/2

# Voltage Regulators

Simple voltage regulation for logic and core. In a distinct sheet to allow you to easily do fancy stuff, like use switchers or whatever is needed.



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**Psychogenic Technologies INC**

Sheet: /Power\_Regulation/

File: power\_reg.kicad\_sch

**Title: Voltage Regulation**

Size: User

Date: 2023-09-30

**Rev: 1.1**

KiCad E.D.A. kicad 7.0.9-7.0.9~ubuntu20.04.1

Id: 2/2