AT2 Language
Reference Manual

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Introduction

Here's a complete documentation about the AT2 language. All the information provided is accurate, agreed and reviewed by all the team members. You can use this document to create your own .aop files.

SYSTEM

Registers

The Virtual Processor is composed of 8 registers of 16 bits each (value max of 0xFFFF), rg0 to rg7 (rg3 is reserved to the clock and he's in read only).

Lexical Conventions

Comments

A comment can be appended to a statement. The comment consists of the double slash character (//)(ASCII 0x2F 0x2F) followed by the text of the comment. The comment is terminated by the newline that terminates the statement.

Labels

Labels are subroutines that contain instructions. They can be called at any moment during the execution of code by goto and call mnemonics. To declare them, the syntax is *lab name*.

Variables

A variable is a named container for a particular set of bits or type of data. It can be declared anywhere in the code. It needs to have a name and a value : *var name, value*.

Strings

A string is a sequence of characters, used to represent text. You can declare them using: " ".

Characters

A character refers to a single unit of text or symbol. You can declare it using: ‘ ‘.

Hexadecimal

The lexical convention for representing hexadecimal numbers in most programming languages, including AT2 Language, is to prefix the hexadecimal value with "0x" or "0X".

Binary

The lexical convention for binary values in the AT2 Assembly Language is represented by a sequence of digits, where each digit can be either 0 or 1.

INSTRUCTIONS

General-Purpose Instructions

Binary Arithmetic Instructions

The binary arithmetic instructions perform basic integer computations on operands in memory or the general-purpose registers.

AT2 Mnemonic	Description	Example
+	addition	+ rg0, 2
-	subtraction	- rg0, 2
/	division	/ rg0, 2
*	multiplication	* rg0, 2
%	modulo	% rg0, 2
|	bitwise OR	| rg0, 2
&	bitwise AND	& rg0, 2
^	bitwise XOR	^ rg0, 2
!	bitwise NOT	! rg0, 2
neg	negate the value	neg rg0
++	increment	++ rg0
--	decrement	-- rg0
if	first compare	if =, rg0, 2
else	second compare	else
end	close if and else compare statement	end

Logical Instructions

The logical instructions perform basic logical operations on their operands.

AT2 Mnemonic	Description	Example
&&	AND	if &&, rg0, rg1
||	OR	if ||, rg0, rg1
^^	XOR	if ^, rg0, rg1
<	inferior	if <, rg0, rg1
>	superior	if >, rg0, rg1
<=	inferior or equal	if <=, rg0, rg1
>=	superior or equal	if >=, rg0, rg1
==	equal	if =, rg0, rg1
!=	not equal	if !=, rg0, rg1

Shift Instructions

Shift instructions move the bits of a binary number to the left or right within a register or memory location.

AT2 Mnemonic	Description	Example
>>	shift the bits to the right	>> rg0
<<	shift the bits to the left	<< rg0

Control Transfer Instructions

The control transfer instructions control the flow of program execution.

AT2 Mnemonic	Description	Example	Note
call	call subroutine	call label	
goto	go to subroutine	goto label	
ret	return where previous call was use	ret	only when call is use

String Instructions

String instructions perform operations on strings.

AT2 Mnemonic	Description	Example
draw	print the string on the display	draw "Hello, World!"

I/O Instructions

The input/output instructions transfer data between the processor's I/O ports, registers, and memory.

AT2 Mnemonic	Description	Example	Note
get	get external file content	get "filename.aop"	get instructions need to be at the header of file

Operating System Support Instructions

These instructions provide support for interfacing with the operating system.

AT2 Mnemonic	Description	Example
clok	get the current execution time of program (only with register 3)	clok
ngr	exit the program and return control to the operating system	ngr

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