

Project : Virtual Processor

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graph TD; Root[Project : Virtual Processor] --> Planning[Planning]; Root --> Programming[Programming]; Root --> Testing[Testing]; Root --> PM[Project Management]; Planning --> FS[Functional Specification]; Planning --> TS[Technical Specification]; Programming --> AS[ASSEMBLY]; Programming --> CL[C Language]; Testing --> TP[Test Plan]; PM --> WR[Weekly Report]; PM --> PC[Project Charter];
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Planning

Functional Specification

Technical Specification

Programming

ASSEMBLY

C Language

Testing

Test Plan

Project Management

Weekly Report

Project Charter