

The background of the slide features a stylized, light blue and grey illustration of an FPGA (Field-Programmable Gate Array) chip. The chip is depicted with a central square area containing a complex circuit pattern, surrounded by a grid of lines representing the chip's pins and internal connections. The overall design is clean and technical.

# **User Manual Web FPGA By ALGOSUP Team 3**

Author: Paul NOWAK

# Summary

INTRODUCTION .....	3
--------------------	---

## Let's get started !

GitHub visit .....	3
Setup .....	3
Extract files .....	4
Launch (Windows / macOS version) .....	4
Launch (LINUX version) .....	5

## Create a new simulation

Opening simulator .....	6
Uploading file .....	7

## Testing the simulation

Interface presentation .....	8
Play simulation .....	9
Controlling zoom level .....	9
Panning .....	9
Signals .....	10
Simulation logs .....	11
Switch animation .....	11

Safety measures .....	12
-----------------------	----

Troubleshooting.....	13
----------------------	----

Credits .....	14
---------------	----



# INTRODUCTION

Welcome to Web FPGA!

In this project, you have access to a web interface that allows you to simulate various FPGA apps. It's simple, efficient, and especially designed to be informative. Whether you are an IT student or a teacher, this website makes FPGA learning more accessible than ever before.

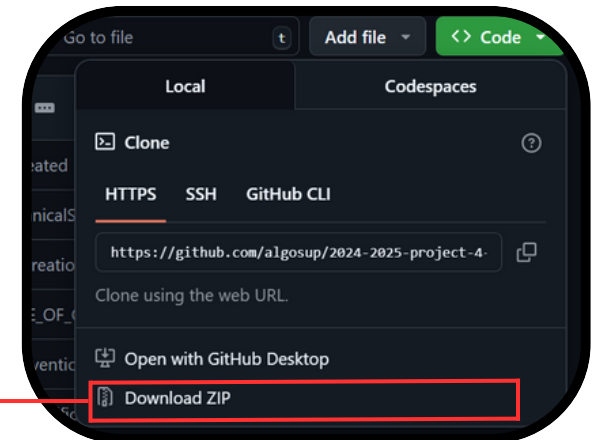
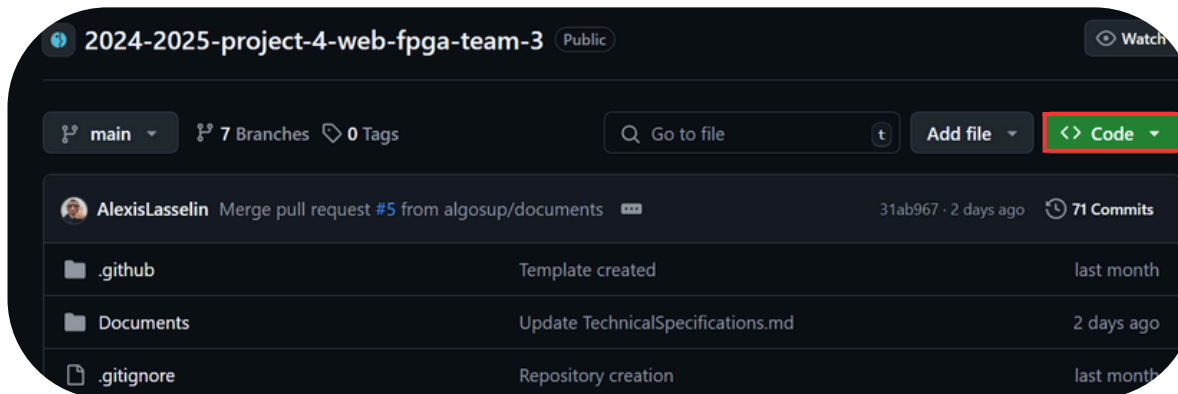
## Let's get started !

### GitHub visit

First, you need to go to our [Github](#) page:



### Setup

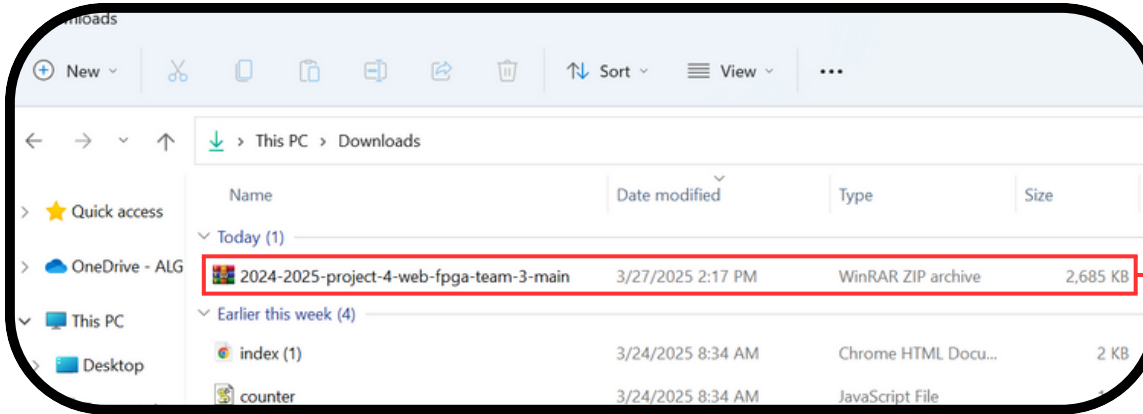


### Download

Press the **Code** button and select **Download Zip** to obtain the repository.

# Let's get started !

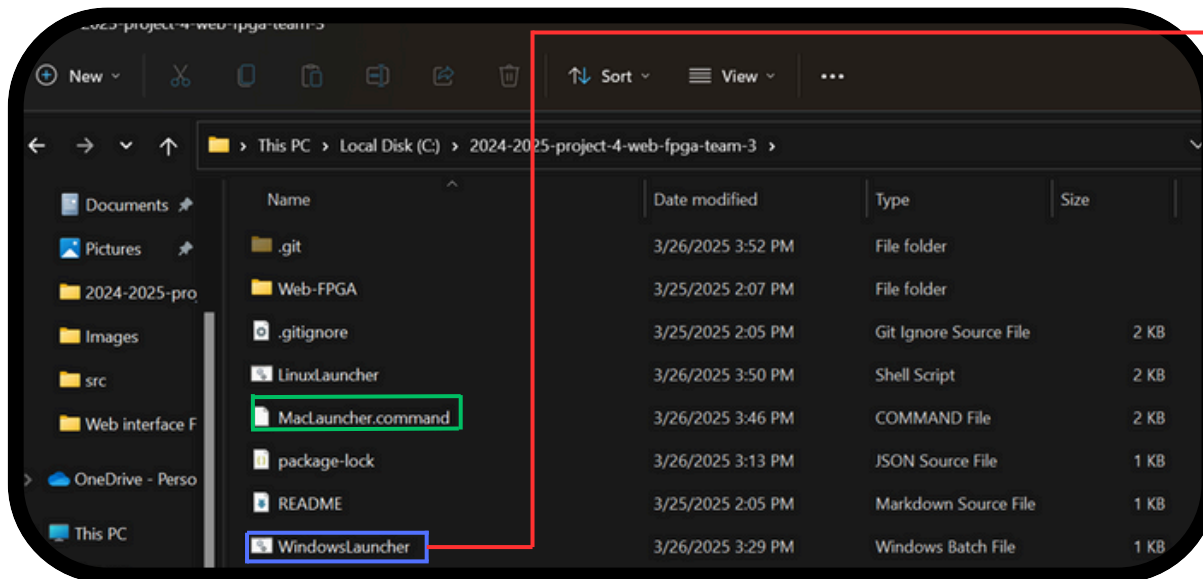
## Extract files



### Decompress

Once you have your .ZIP file, double-click here to extract its contents to access the necessary file.

## Launch (Windows / macOS version)



### Start

Open the launcher depending on your operating system, either **Windows** or **macOS**.

### Address

You can access the website on your browser by entering the following address: **<http://localhost:5173>**



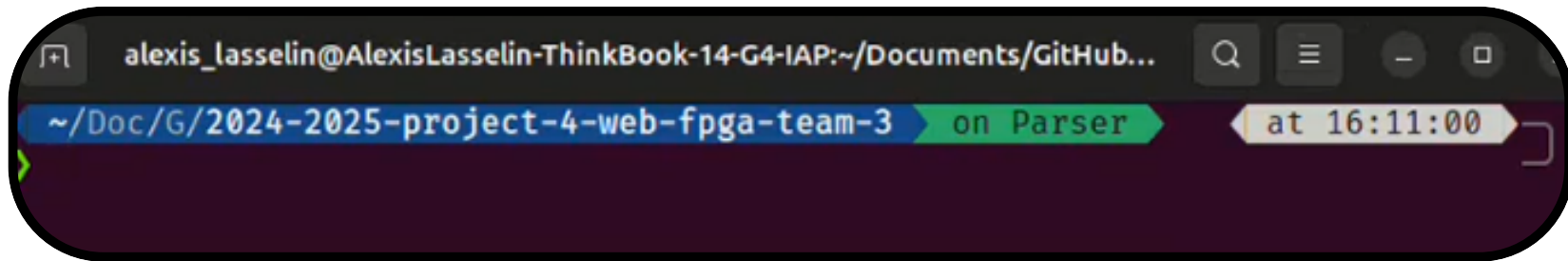
### Warning

*Do not modify or relocate any files within the folders. This could result in potential malfunctions.*

# Let's get started !

## Launch (LINUX version)

1. Launch your **Command prompt**.
2. Browse for the **repository path** (.../2024-2025-project-4-web-fpga-team-3).



3. Enter the following command:  
**`chmod -x ./LinuxLauncher.sh`**
4. Enter the command **`./LinuxLauncher.sh`** to execute the Launcher.
5. Enter the following address in your browser:  
**`http://localhost:XXXX`**

### **Note**

*If you're experiencing any issues or the launcher fails to execute, check our **Troubleshooting** section.*



# Create a new simulation

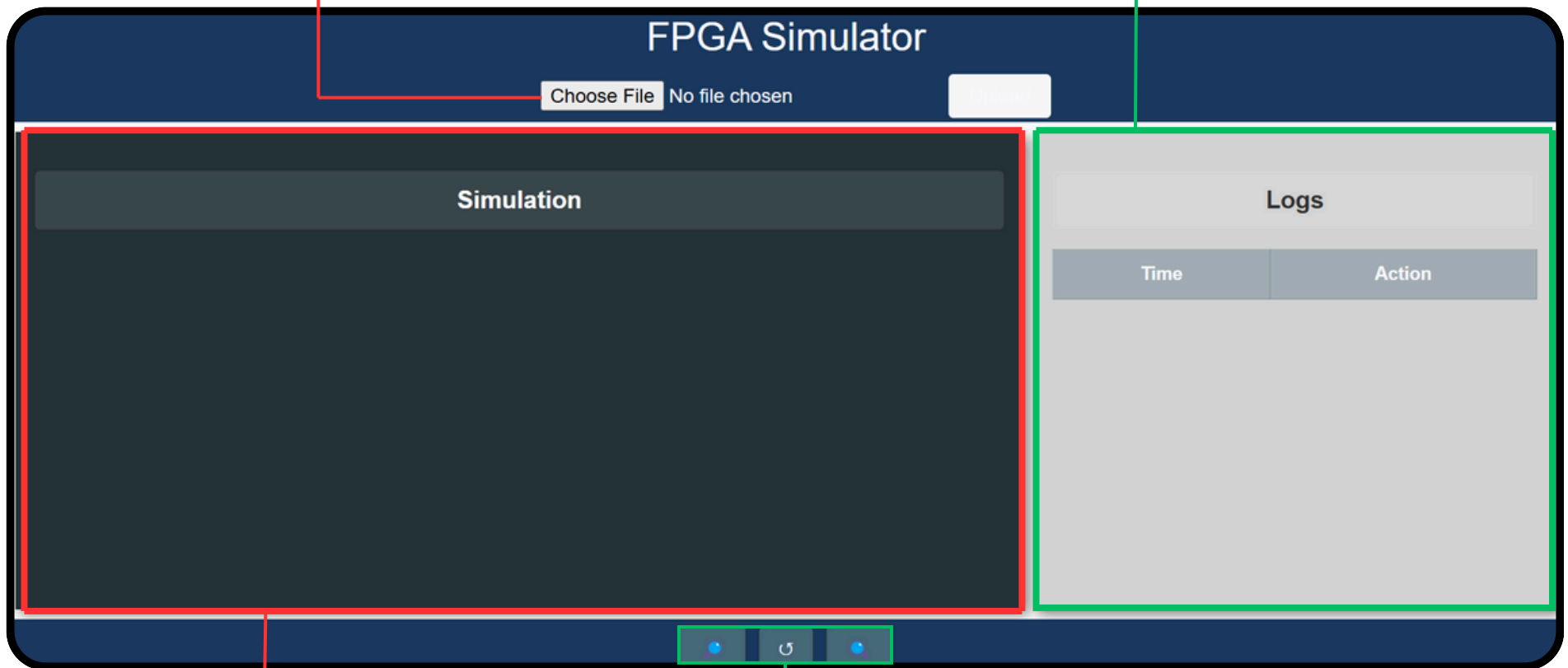
## Opening simulator

### Choose file

To upload the file necessary for the simulation.

### Logs

To display information about the simulation based on the time and step.



### Main area

FPGA structure visualisation where the user can interact with the simulation.

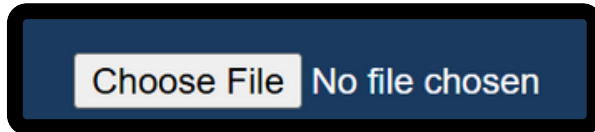
### Zoom view

To control the Zoom level of the FPGA simulation.

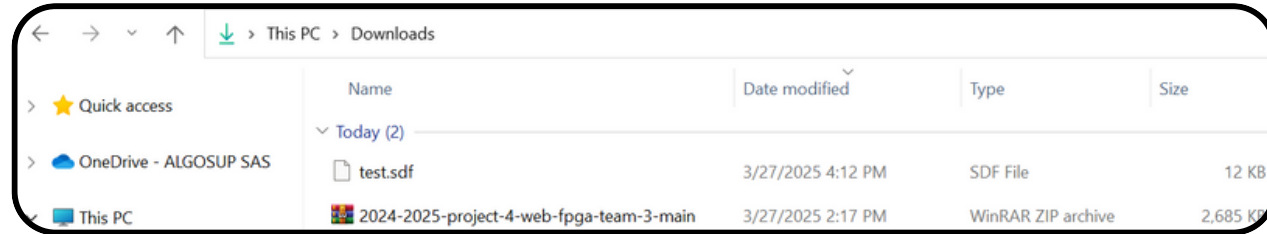
# Create a new simulation

## Uploading file

1. Click on **Choose File**:



2. Choose **.sdf** file on your file explorer:



3. Click on **Upload** to confirm:



The uploaded file will be converted into a JSON file for the machine to use and saved in its local cache.

Then, a newly rendered visualization of the original file will appear.

**Important**

***Make sure the file uses the .sdf extension.***

# Testing the simulation

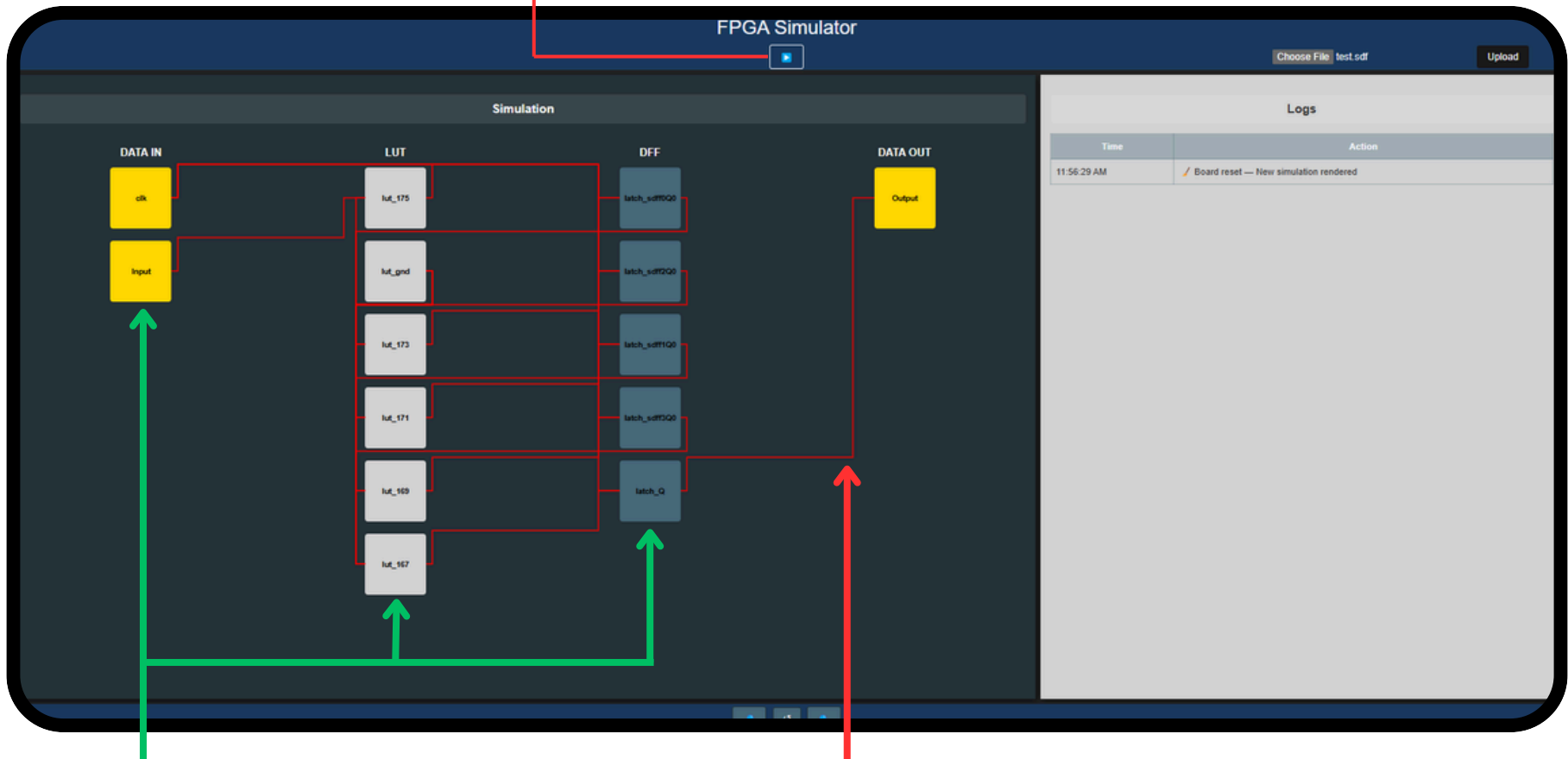
## Interface presentation

### Note \*

Only the **Play button** is available in the actual version of this project.

### Button Panels\*

Used to interact with the simulation.



### Basic elements

The primary components of the circuit, including **LUTs** and **Flip-Flops**.

### Signals routes

Routes that connect the basic elements of the circuit.






# Testing the simulation

## Play simulation

To use the simulation, click the **Play/Pause**  **button** to start or pause its execution.

## Controlling zoom level

The zoom level of the simulation can be controlled with the following buttons:

	<b>Zoom In</b>	Increase the zoom level of the visualization.
	<b>Zoom Out</b>	Decrease the zoom level of the visualization.
	<b>View Reset</b>	Return to the original zoom level.

## Panning

Perform a **left click and drag the mouse** to move through the visualization's **main area**.

The panning stops when **the mouse is released**.

# Testing the simulation

## Signals

When you start playing the simulation, **activated signals will be animated**. Indeed, they become thicker and have **dots** circulating through the routes connecting the different components of the FPGA circuit.

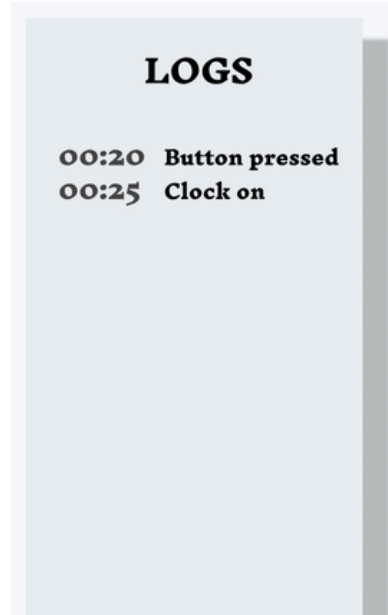


# Testing the simulation

## Simulation logs \*

Logs on the sidebar are useful for displaying simulation information based on time and steps.

Displayed in chronological order like in the following image\*\*, logs are shown in the current simulation time and are updated based on the current step.



### Previous log

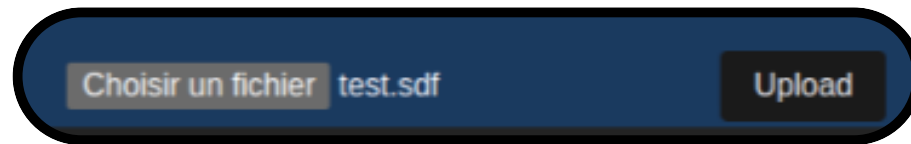
Click the **Prev Button** to show the simulation and its logs from the previous step.

### Next log

Click the **Next Button** to update the simulation from the next step.

## Switch animation

If you want to do another animation, click on **Choose File** to select another file.



Once you click **Upload**, the current simulation will be erased and replaced with a new one.

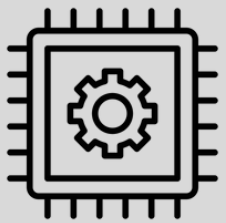



### Note \*

This feature will only be available in the version 2 of our project.

### Note \*\*

This image isn't part of our project and only **11** represents the concept art of an upcoming feature.

# Safety measures

	<b>Hardware compatibility</b>	<p>Make sure you are using a computer that meets the minimum hardware requirements.</p> <p><b>Web Browsers:</b> <b>Chrome</b> (134.0.6998.178), <b>Firefox</b> (136.0.4), <b>Microsoft Edge</b> ( 134.0.3124.85)</p> <p><b>Operating System:</b> <b>Windows</b> (11 Pro), <b>macOS</b> (), <b>LINUX</b> ()</p>
	<b>Data file handling</b>	<p>Avoid renaming or modifying any files used by the application unless you are certain of your actions, as this could affect its functionality.</p>
	<b>Error handling</b>	<p>If an error occurs, please consult the troubleshooting section.</p>
	<b>Performance optimization</b>	<p>Avoid running other resource-intensive applications simultaneously to maintain optimal performance. If the page becomes unresponsive or slows down, try reloading it or rebooting your computer.</p>

# Troubleshooting

Problem	Possible symptoms	Solution
The software takes too much time to execute.	<ul style="list-style-type: none"> <li>It takes significantly longer than expected to load or complete tasks.</li> </ul>	<ul style="list-style-type: none"> <li>Close other apps or programs running in the background.</li> </ul>
The software does not generate the simulation correctly.	<ul style="list-style-type: none"> <li>Error message: “Invalid format”</li> <li>The .sdf file contains syntax errors or is empty.</li> </ul>	<ul style="list-style-type: none"> <li>Ensure the file has the required format <b>.sdf</b> and check its correctness.</li> </ul>
The software encounters an unpredictable behavior.	<ul style="list-style-type: none"> <li>Possible new update released by the moderators</li> <li>Features that previously worked now cause crashes or errors.</li> <li>Unexpected UI glitches or missing options.</li> </ul>	<ul style="list-style-type: none"> <li>Adjust the window size of your page. If the issue persists, either reload the page or restart your computer.</li> </ul>
Error when launching the launchers (especially windows)	<ul style="list-style-type: none"> <li>“Vite” Is not recognized as a program and needs update</li> </ul>	<ul style="list-style-type: none"> <li>Run <b><i>npm audit fix</i></b> in the command prompt</li> </ul>

# Credits

This software and its related documentation are protected by copyright laws and international treaties. Any unauthorized reproduction, modification, or distribution of the software, partially or entirely, is strictly forbidden without prior written consent from the copyright holder.

If you're still experiencing any issues, don't hesitate to contact **Support**.

**Mail: [contact@algosup.com](mailto:contact@algosup.com)**