Project Name

Refrigerator Temperature Controller Circuit

Description

We will design a digital logic circuit which will control the temperature of a refrigerator. We assume that our refrigerator has 4 buttons, each button can be used to increase and decrease the temperature of the refrigerator. The maximum temperature of the refrigerator will be 9-Degree Celsius and the minimum temperature will be 0-Degree Celsius. And the more the button is raised from 0, the higher the temperature of the refrigerator will be. Suppose when the button is at number 0 then the temperature inside the refrigerator will be 0-Degree Celsius, on the other hand if the button is at number 2 then the temperature will be 6-Degree Celsius. We will calculate the temperature change across the button using an equation. The equation is,

Temperature =
$$(3x)$$
 °C Here, $x = 0,1,2,3$ (Four Buttons)

To implement our project, we will create a truth table first, from the table we will generate K-map and with the help of K-map we get some equations and finally we will design our logical circuit with these equations.

Calculations

Temperature =
$$(3x)$$
 °C Here, $x = 0,1,2,3$ (Four Buttons)

when the button is at number 0,

when the button is at number 1,

 $= (3 \times 1)$ °C

 $= 3 \, ^{\circ}\text{C}$

Temperature =
$$(3x)$$
 °C
$$= (3 \times 0)$$
 °C
$$= 0$$
 °C
$$= (3 \times 1)$$

$$= 3$$
 °C

when the button is at number 2,

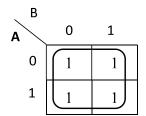
when the button is at number 3,

Temperature =
$$(3x)$$
 °C
$$= (3 \times 2)$$
 °C
$$= 6$$
 °C
$$= 9$$
 °C
$$= 9$$
 °C

Truth Table

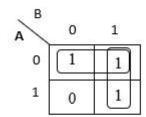
Input			Output							
Index (Buttons)	A	В	a	b	c	d	e	f	g	T°C in Binary Digit
0	0	0	1	1	1	1	1	1	0	0 °C
1	0	1	1	1	1	1	0	0	1	3 °C
2	1	0	1	0	1	1	1	1	1	6 °C
3	1	1	1	1	1	1	0	1	1	9 °C

K-Map for a



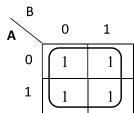
Equation for a = None

K-map for b



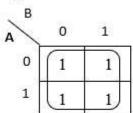
Equation for $b = \overline{A} + B$

K-Map for c



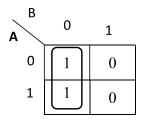
Equation for c = None

K-Map for d



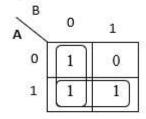
Equation for d = None

K-Map for e



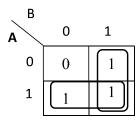
Equation for $e = \overline{B}$

K-Map for f



Equation for $f = A + \overline{B}$

K-Map for g



Equation for g = A + B

Logical Expressions

Logical expression of a = 1

Logical expression of $b = \overline{A} + B$

Logical expression of c = 1

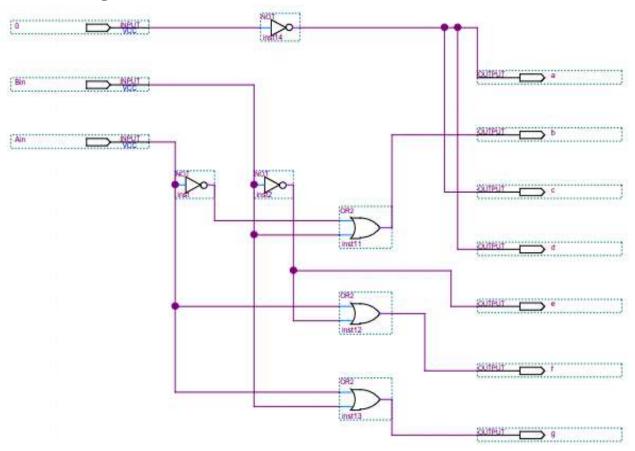
Logical expression of d = 1

Logical expression of $e = \overline{B}$

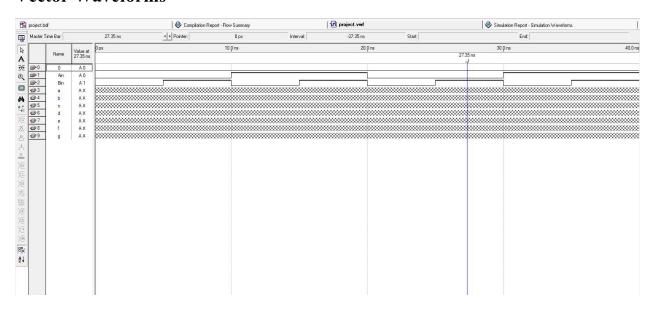
Logical expression of $f = A + \overline{B}$

Logical expression of g = A+B

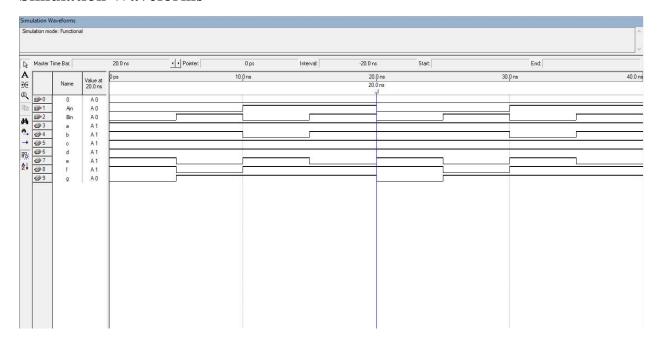
Circuit Diagram

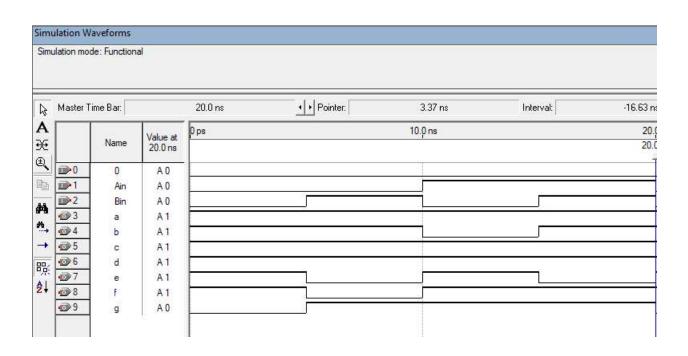


Vector Waveforms

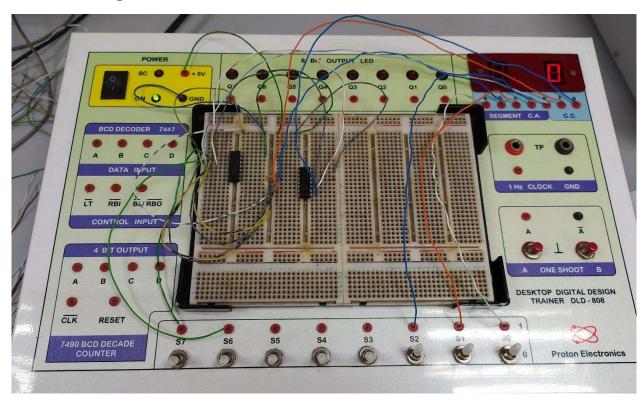


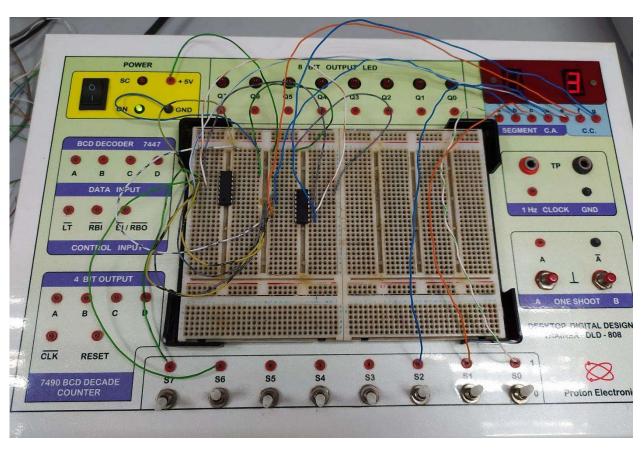
Simulation Waveforms

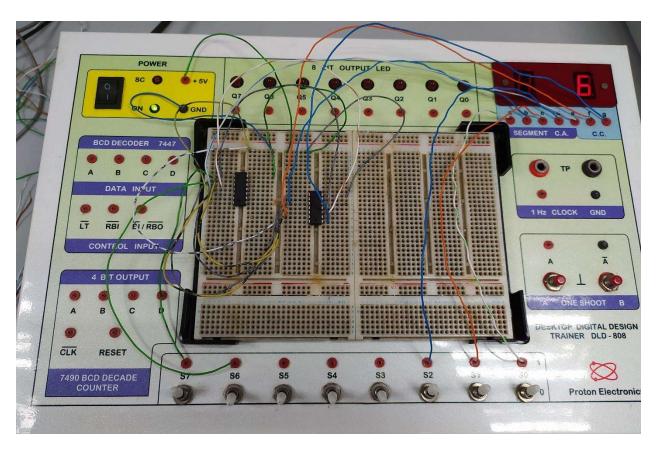


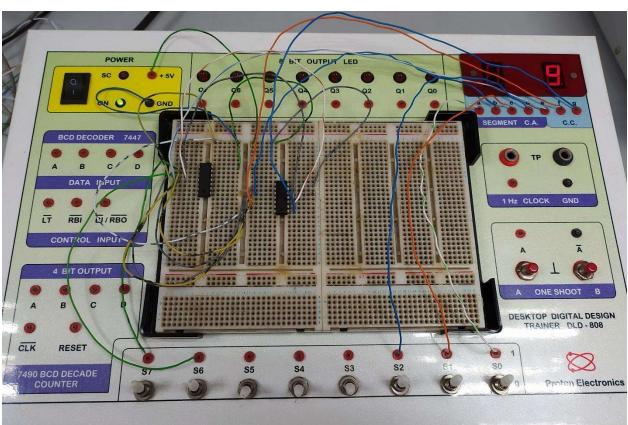


Hardware Implementation







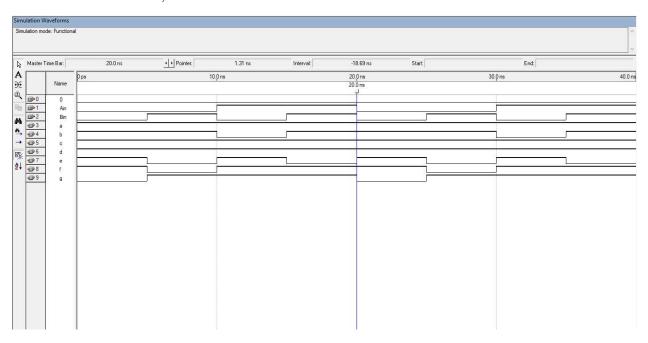


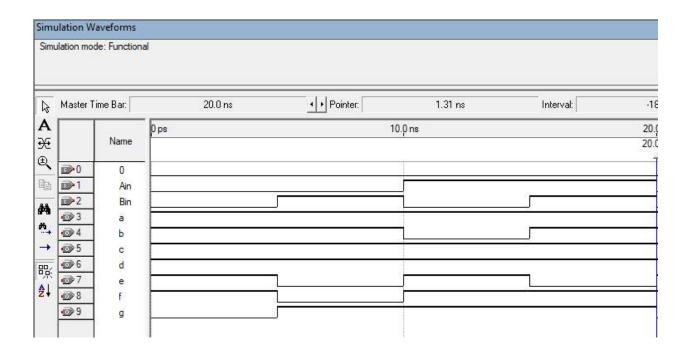
Verilog Code and Simulation

Structural Verilog Code,

```
■module verilog1 (input A, B, O,
 1
 2
                 output a, b, c, d, e, f, g);
 3
                 wire w1, w2, w3, w4, w5;
 4
                 not g1(w1, 0),
 5
                      g2(w2, B);
 6
                  or g3(w3, ~A, B),
 7
                     g4(w4, A, ~B),
 8
                     g5(w5, A, B);
9
                     q6(a, w1),
                     g7(b, w3),
10
                     g8(c, w1),
11
12
                     g9(d, w1),
13
                     g10(e, w2),
14
                     g11(f, w4),
                     g12(g, w5);
15
16
     endmodule
```

Simulation Waveforms,





Conclusion

For implementing our refrigerator temperature controller circuit project, we first create a truth table. From the table we generate seven K-map and with the help of K-map we get seven equations. With these equations, we design our logical circuit. We also simulate our circuit with Quartus II software. However, we used our previous knowledge to implement this project properly. Besides, we got to learn how to implement seven-segment display through this project.