

Comment	Description	Designator	Footprint	LibRef	Quantity
S_POT_VOLUME	Besonption	Dosignator	F_POT_VOLUME	S_POT_VOLUME	1
S TDA1308		*1	F_TDA1308_so8	S TDA1308	1
S zener		*2, *9	F_SMD_ZENR	S_zener	2
sod_223_5822diod		*3, PP1	sod223-diod-5822	sod_223_5822diod	2
S_key6pin		*4	F_key6pin	S_key6pin	1
S_jack3.5		*5	F_JACK3.5	S_jack3.5	1
S_LED_smd		*6, *7	F_LED_805	S_LED_smd	2
S_LM1117		*8	F_LM1117	S LM1117	1
S_OLED		*10	F_OLED	S_OLED	1
S_encoder		*11, *12	F_encoder	S encoder	2
Res Thermal	Thermistor	1, res_led_1, RT1, RT2,		_	
		RT3, RT4, RT5, RT6, RT7, RT8, RT9	6-0805_N	Res Thermal	11
buzzer		buzzer1	buzzer	buzzer	1
Cap Pol1	Polarized Capacitor (Radial)	C1, C10	cap2	Cap Pol1	2
Cap Pol3	Polarized Capacitor (Surface Mount)	C2, C3, C4, C5, C6, C7, C8, C9, C12, C13, C14, C15, C16, C17	C0805	Cap Pol3	14
Cap Pol1	Polarized Capacitor (Radial)	C11	cap1	Cap Pol1	1
fuse_smd_805	<u> </u>	f1	fuse_smd_805	fuse_smd_805	1
Fuse 2	Fuse	F2, F3	PIN-W2/E2.8	Fuse 2	2
Inductor Adj	Adjustable Inductor	L1	12_12_INDUCTOR_S MD	Inductor Adj	1
MOSFET_N		mosn1	mosfet_n_sot23a	MOSFET_N	1
MHDR1X2	Header, 2-Pin	P1	JST XH B2B-XH-A	MHDR1X2	1
Header 3X2	Header, 3-Pin, Dual row	P2	HDR2X3	Header 3X2	1
Header 6	Header, 6-Pin	P3	HDR1X6	Header 6	1
Header 7	Header, 7-Pin	P4	HDR1X7	Header 7	1
Header 5	Header, 5-Pin	P?	HDR1X5	Header 5	1
Res Semi	Semiconductor Resistor	R1, R2, R3, R4, R5, R6, R7, R8	AXIAL-0.5	Res Semi	8
Res3	Resistor	R9, R10	C0805	Res3	2
RPot	Potentiometer	R?	VR5	RPot	1
SPST-NO	i otontionietei	S1, S2, S3	TE FSMSMTR	SPST-NO 2 PIN	3
S_SELECTOR		selector1, selector?	F_SELECTOR	S_SELECTOR	2
TL2575-33IKTTR	Buck Step Down Regulator with 4.75 to 40 V Input and 3.3 V Output, -40 to 125 degC, 5-Pin DDPAK/TO- 263 (KTT), Green (RoHS & no Sb/Br)	U1	KTT5_N	TL2575-33IKTTR	1
STM32F103C8T6	ARM Cortex-M3 32- bit MCU, 64 KB Flash, 20 KB Internal RAM, 37 I/Os, 48-pin LQFP, - 40 to 85 degC, Tray	U2	F_STM32f103c8t6_blu e_pill	STM32F103C8T6	1