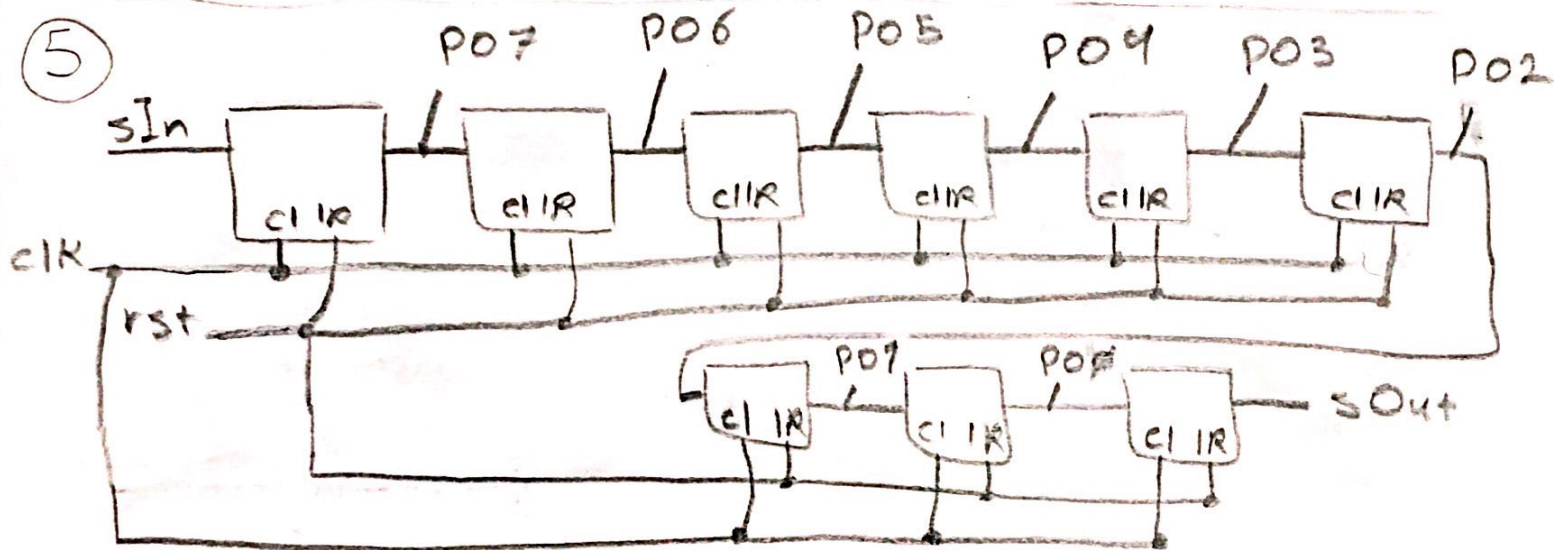
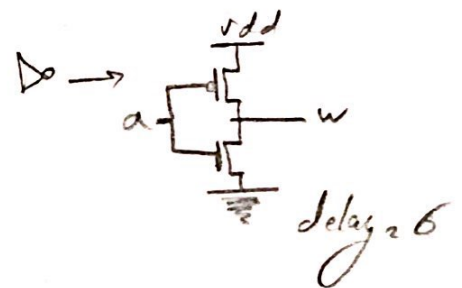
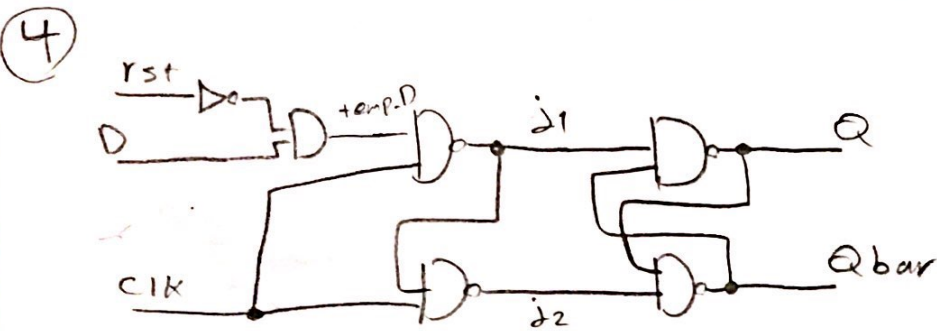
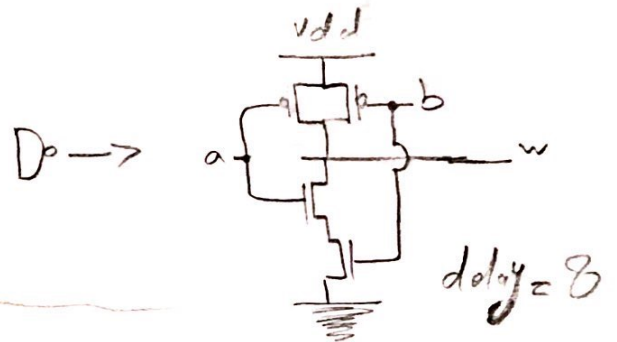
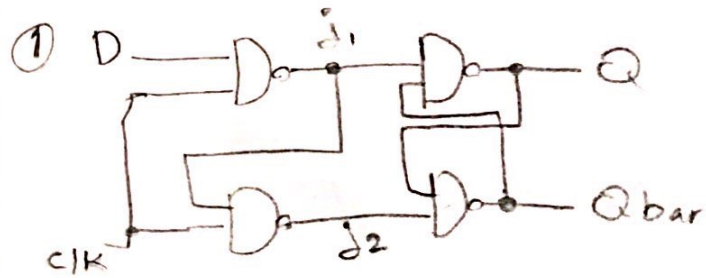


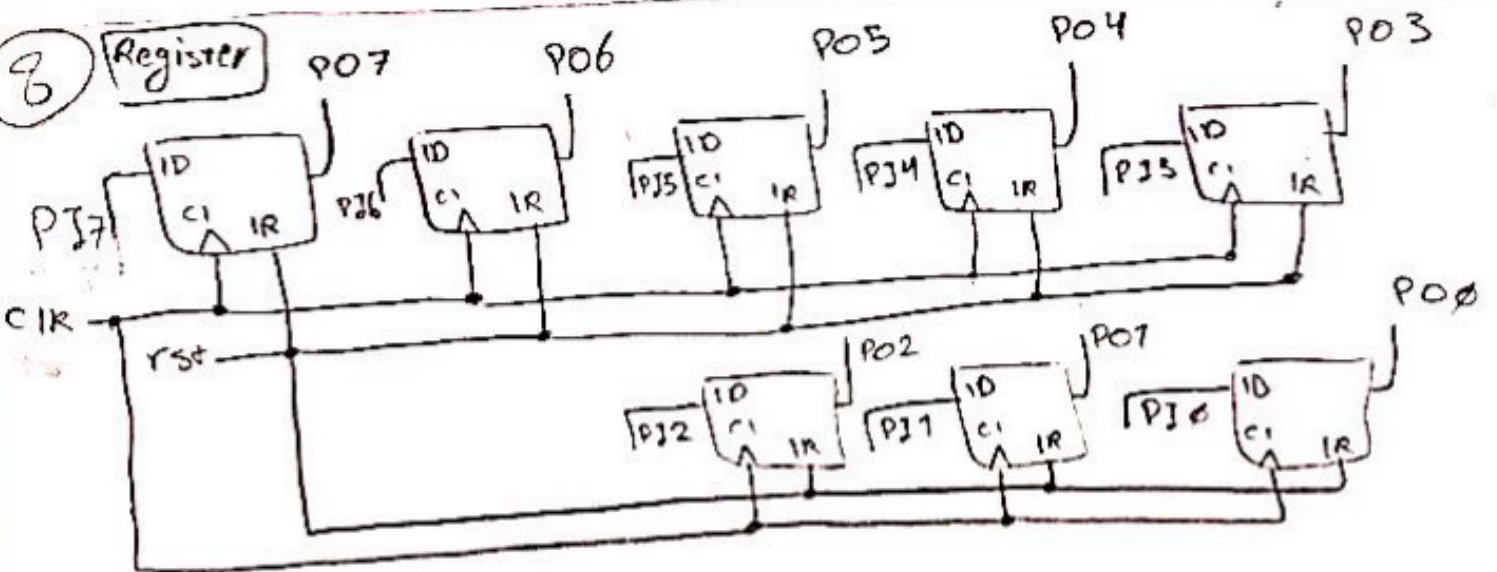
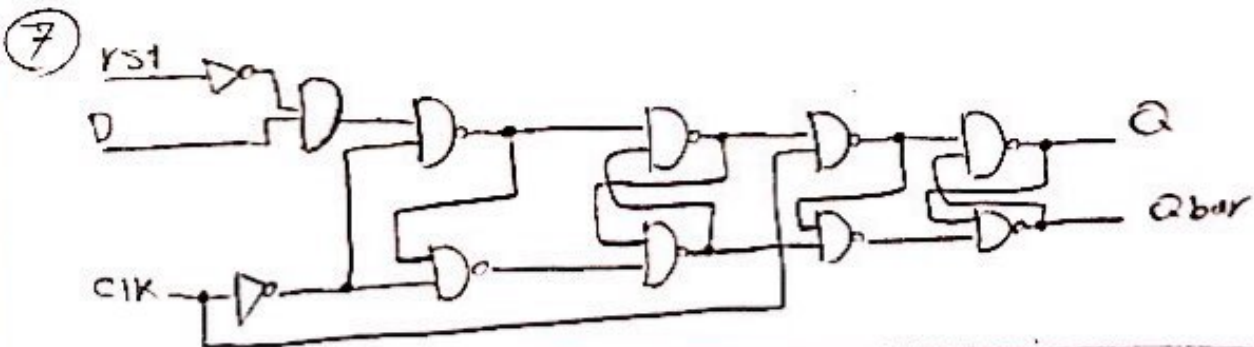
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⑥ As this circuit based on D-Latch, it's not working as we expected, because output doesn't change only on edges and until clk is 1, output can be change and get shifted.



shift register

