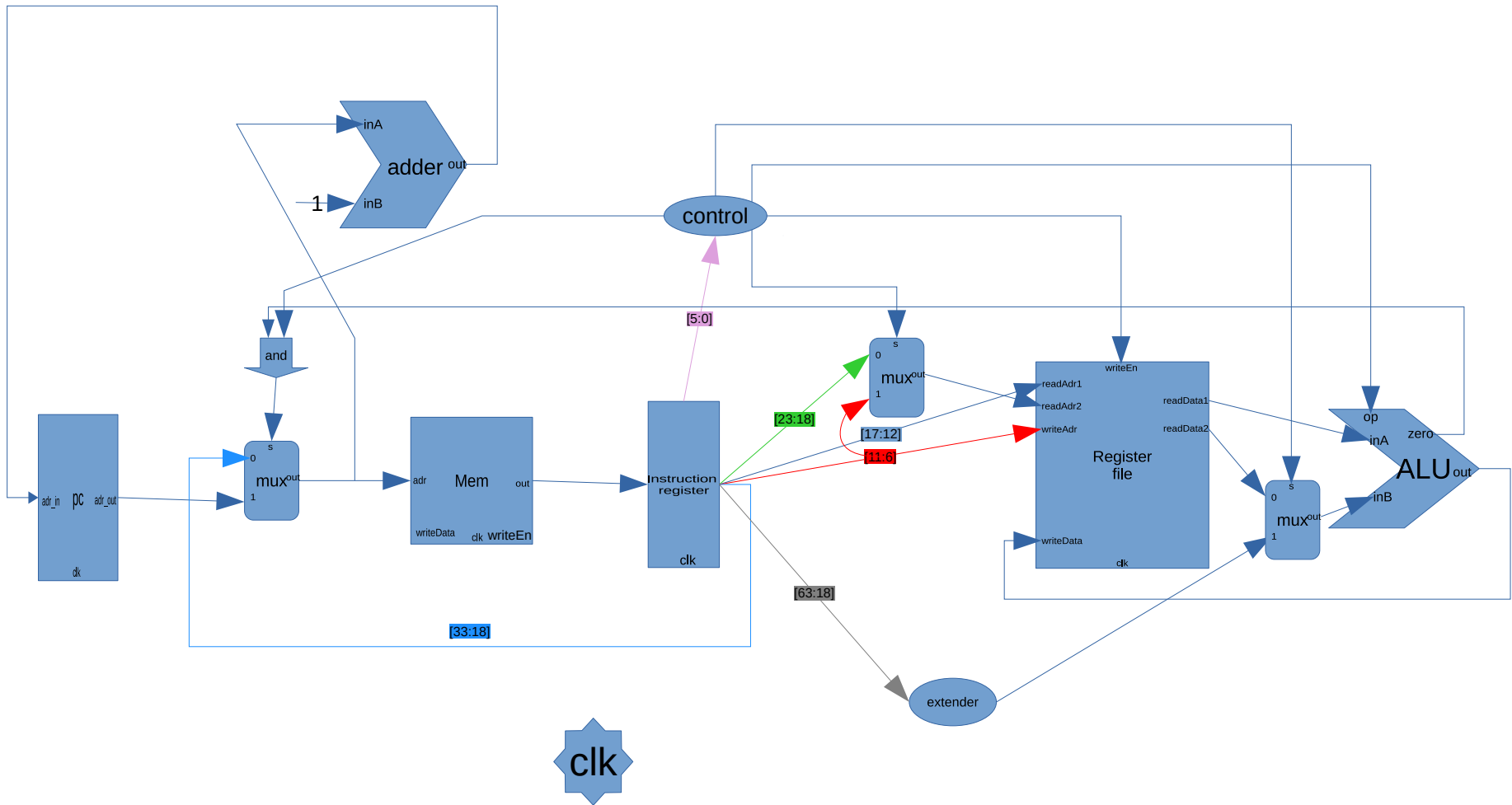
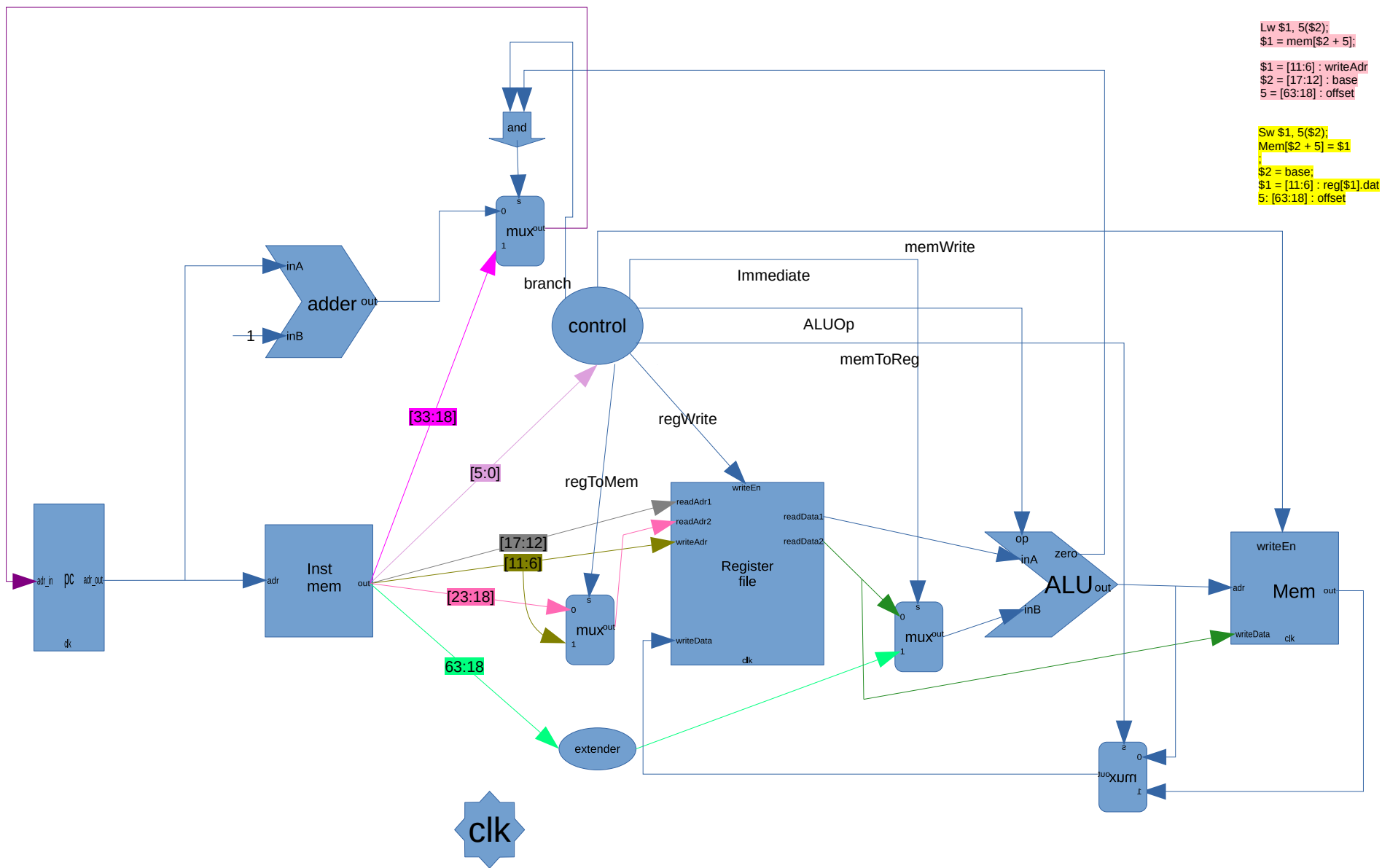


beq





	ALUOp	memWrite	regWrite	regToMem	memToReg	immediate	branch
Load word	10	0	1	0	1	1	0
Store word	11	1	0	1	0	1	0
Branch equal	20	0	0	1	0	0	1
Add immediate	30	0	1	0	0	1	0
add	00	0	1	0	0	0	0