 Proficient in RTL design with Verilog  
. Skilled or willing to learn HLS(High Level Synthesis)

**Preferred:**  
. Knowledge of Video Codec(e.g. HEVC, AV1) Algorithm  
. Knowledge in Deep Learning  
. Experienced in FPGA prototyping

bluedot is a technology venture company founded to deliver cloud-based semiconductor IP. We are developing high-performance video encoder semiconductor IP that is essential for the growing live streaming market including live social video, cloud gaming, and immersive video in the age of 5G network.  
  
(Homepage)  
<https://blue-dot.io/>

 HDL and DSP design experience.

Simulate, test, and verify the proper operation of the FPGA

* Minimum of 3 years of hands-on experience writing and debugging C/C++ in embedded environments

**The junior VHDL designer will:**

* read and complement the hardware description documents (HHDs) he is assigned to
* write VHDL code that meets the intent of the corresponding HDDs
* update the HDDs per the detailed design
* synthesize the code for the FPGA technology used
* support the verification engineer

The successful applicant will interface with DSP algorithm developers, embedded software developers and board designers to define the FPGA logic architecture. This individual will develop logic applications for SED Systems' newest generation of wide-bandwidth and high-speed RF products and solutions that implement cutting edge DSP algorithms and utilize the most advanced FPGAs available in the market today.

Long proven track record of successfully delivering complex RTL designs targeting large FPGAs/ASICs;  
Deep knowledge of VHDL/SystemVerilog/Verilog;  
**Working knowledge of SCM tools:** Git, SVN;

Familiarity with modern FPGA device families and tools;  
Expertise in floor planning, synthesis and place & route optimization, timing closure, and related design constraints;  
Direct experience implementing high speed serial links and protocols using multi-gigabit transceivers;  
Experience in functional simulation using modern verification techniques;  
Background using programming and scripting languages (Python, C/C++, TCL, Make);  
Excellent English verbal and written communication skills; and,  
A highly motivated self-starter, able to work independently, while being a team player.