

Roofline Model for UAVs: A Bottleneck Analysis Tool for Onboard Compute Characterization of Autonomous Unmanned Aerial Vehicles

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Abstract—We introduce an early-phase bottleneck analysis and characterization model called the F-1 for designing computing systems that target autonomous Unmanned Aerial Vehicles (UAVs). The model provides insights by exploiting the fundamental relationships between various components in the autonomous UAV, such as sensor, compute, and body dynamics. To guarantee safe operation while maximizing the performance (e.g., velocity) of the UAV, the compute, sensor, and other mechanical properties must be carefully selected or designed. The F-1 model provides visual insights that can aid a system architect in understanding the optimal compute design or selection for autonomous UAVs. The model is experimentally validated using real UAVs, and the error is between 5.1% to 9.5% compared to real-world flight tests. An interactive web-based tool for the F-1 model called Skyline is available for free of cost use at: <https://bit.ly/skyline-tool>

I. INTRODUCTION

Autonomous machines like Unmanned Aerial Vehicles (UAVs) are on the rise [1]–[6]. Amongst these, quadcopters account for the vast majority of the market [7], [8] due to their ability to vertically take off and land (VTOL) and navigate in confined spaces. These unique capabilities enable them to be deployed in numerous applications, such as search and rescue [3], [9], package delivery [10], surveillance [11], and sports photography [12]. These applications have motivated the industry to build domain-specific hardware [13]–[16].

However, building computing for UAVs is challenging as UAVs differ from traditional systems (embedded systems, servers, etc.). They are severely size, weight, and power (SWaP) constrained. Moreover, UAVs have many components like sensors, compute mechanical frames, and rotors, interacting to work as one coherent system. The selection of these components greatly affects the UAV’s velocity, mission time, energy, etc. Components such as the sensor and autonomy algorithm processing rate determine how fast the UAV reacts in a dynamic environment. Likewise, the payload weight and thrust determine if the physics allows UAVs to accelerate and move faster.

To guide compute design for SWaP and cost constrained systems, we need tools to understand the interactions between the various components and quantify the bottlenecks. UAVs are complex autonomous systems, and the computing platform is just one among many other components. To design the balanced onboard compute, we need to consider the role of computing

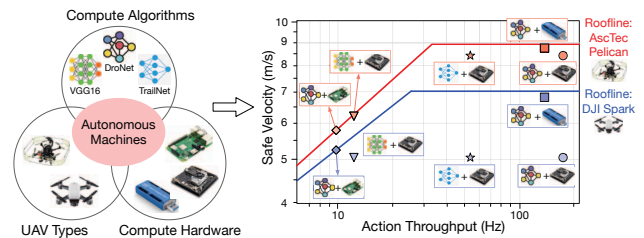


FIG. 1: A bottleneck analysis tool for UAV onboard compute characterization. The tool takes a full-system view of UAVs and identifies the various bottlenecks using the F-1 model. The “roofline” helps us quickly determine whether the UAV is sensor bound, compute bound, or body-dynamics bound.

in the context of the whole UAV system. As we demonstrate later, traditional isolated compute performance metrics can lead to misguided conclusions.

In this paper, we introduce a “Roofline Model for UAVs,” an early-phase bottleneck analysis tool to guide the design of balanced computing systems for UAVs. The tool determines which of the UAV components (compute, sensor, or physics) limits the safe operating velocity; *safe high-speed autonomous navigation remains one of the key challenges in enabling aerial robot applications [17]–[20]*. A high safe velocity ensures that the UAV is reactive to a dynamic environment and ensures that the UAV finishes tasks quickly, thereby lowering mission time and energy [21]. Fig. 1 shows that the tool takes the autonomy algorithm, onboard compute, and the UAV type to perform bottleneck analysis. The coupling between the different choices leads to different “rooflines.” For example, the AscTec Pelican [22] has a different roofline from DJI Spark [23] where the y -axis is the maximum safe velocity, and the x -axis is the decision-making rate (action throughput). The individual design points on the chart reflect the maximum safe velocity for a given UAV system configuration. The output visually resembles that of a traditional computer system roofline model [24]; however, unlike the roofline model, the parameters in our model quantify the UAV as a holistic system as opposed to the computing system in isolation. We can use our tool in two ways. It can be used as a visual performance model to understand various bounds and bottlenecks in the UAV system. It can also guide us toward building a balanced compute system for UAVs.

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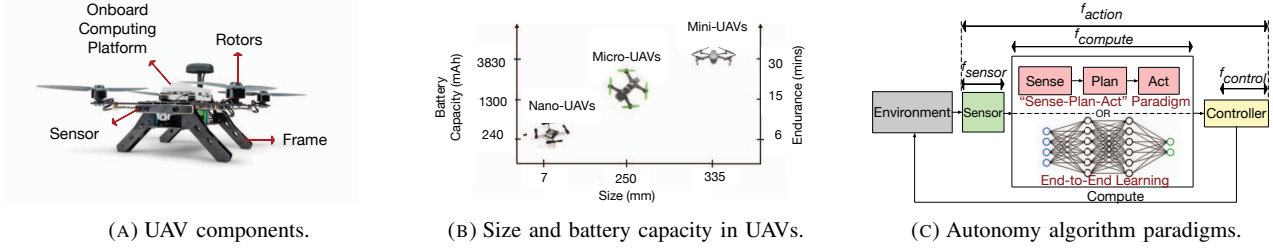


FIG. 2: (A)-(B) UAV components, implications of size on battery and flight time. (C) The sensor-compute-control pipeline which determines the decision making rate, based on bottleneck analysis model [28], [29].

We use the tool to answer a wide array of important characterization questions for designing onboard computing systems for UAVs. First, how does the ad-hoc selection of onboard compute affect the UAV's safe velocity? Second, given a UAV and onboard compute, how do we systematically evaluate different autonomy algorithms and their impact on UAV's safe velocity? Third, given a UAV, autonomy algorithm, and sensor, how do we systematically characterize the impact of redundancy [25] in onboard compute on UAV's safe velocity? Finally, given several onboard compute, autonomy algorithms, and sensor choices, how can we systematically select off-the-shelf components that maximize the UAV's safe velocity, and how does this selection differ as we change the UAV?

Our deep introspection and characterization studies reveal an intertwined relationship between various components in UAVs and their mission performance. The current state-of-the-art techniques rely on selecting or designing onboard compute in an *ad-hoc* fashion or based on isolated compute metrics such as peak compute throughput and low-power without considering its actual impact on the UAV's performance. For instance, our model shows that selecting onboard compute in this fashion results in $2.3\times$ degradation in safe velocity.

Our study also reveals that ad-hoc design choices solely based on isolated compute metrics such as throughput, power, or energy efficiency can be misleading and shows architects how much optimization effort is needed to maximize the UAVs' performance. For instance, hardware accelerators optimized solely for low-power [26] can degrade the safe velocity by $4.3\times$ for a nano-UAV [27] due to their inability to make fast-enough decisions. To this end, we see a clear need for a systematic methodology to characterize onboard computers in these complex UAV systems. Our work introduces a modeling tool that gives insights into how component selections impact the UAV's safe velocity and offers optimization targets for designing balanced onboard compute for autonomous UAVs.

In summary, we make the following contributions:

- 1) A roofline-like performance model called the F-1 model for UAV system characterization. The F-1 model is experimentally validated with real-world UAV flight tests.
- 2) An interactive web-based tool for the F-1 model called Skyline provides various insights into different bounds and bottlenecks as UAV components (parameters) change.
- 3) A detailed characterization study using the Skyline tool by varying each UAV component and quantifying its effect

on the overall, end-to-end UAV's mission performance.

- 4) A comprehensive system-level characterization of UAVs with commercial onboard compute choices and hardware accelerators explicitly built for autonomous UAVs.

II. AUTONOMOUS UAVS

Our work primarily targets quadcopters; henceforth, we will refer to these systems as UAVs or drones interchangeably. This section provides a background on the UAV system components.

A. UAV Components

Autonomous UAVs typically have three key components (Fig. 2a), namely rotors, sensors, and an onboard computing platform. Rotors determine the thrust a UAV can generate. The sensor allows the UAV to sense the environment. The computer executes the autonomy algorithm based on sensor data. The physical size of a UAV plays an important role in selecting the components. Also, the size of the UAV imposes constraints on the maximum weight of each component. A bigger UAV will have the capability to have multiple sensors and a powerful computing platform. In contrast, a smaller UAV will have limited sensor and computing capabilities.

B. Size, Weight, and Power (SWaP) Constraints

The endurance and energy available to carry out missions vary drastically based on a UAV's size (Fig. 2b). The battery capacity and endurance are commensurate with the size of the UAV. For instance, a mini-UAV (e.g., Asctec Pelican) frame size is typically 350 mm and higher and has a battery with 3830 mAh. In contrast, nano-UAVs (e.g., CrazyFlie) have a frame size of around 7mm or less and have a battery of 240 mAh. As the battery capacity decreases, so does its endurance.

C. Onboard Compute

The SWaP (and cost) constraints have strong implications on the onboard compute capabilities. On one extreme, we have the nano-UAVs that, due to their size and weight, typically use microcontrollers such as ARM Cortex-M4 [30] as the onboard computing platform. On the other end, mini-UAVs, which are bigger and have a higher payload carrying capacity, use a general-purpose onboard computer such as Intel NUC [22].

D. Flight Controller

While the primary onboard computer is responsible for making high-level decisions, the low-level task of stable flight and control is delegated to a dedicated flight controller, which is realized using PID controllers. The flight controller firmware stack is computationally light and is typically run on the microcontrollers [31], [32] such as Arm Cortex M4 [31]. The flight controller uses the onboard sensors, such as the Inertial Measurement Unit (IMU) [33] and GPS, to stabilize and control the UAV. To stabilize the UAV from unpredictable errors (sudden winds or damaged rotors), the inner-loop typically runs at closed-loop frequencies of up to 1 kHz [34], [35].

E. Autonomy Algorithms

Autonomy is achieved using different algorithms. The autonomy algorithms process the sensor data on the onboard computer to determine the high-level actions later translated to low-level controls by the flight controller. These algorithms can be classified into two broad and main categories, i.e., the “Sense-Plan-Act” (SPA) and “End-to-End Learning” (E2E).

In SPA, the algorithm is broken into three or more distinct stages, namely “sensing”, “planning”, and “control”. In the sensing stage, the sensor data is used to create a map [36]–[39] of the environment. The planning stage [40], [41] then uses the map to determine the best trajectory. Finally, the control stage uses the trajectory information, which actuates the rotor.

In E2E, the machine learning algorithm processes the input sensor information and they often use a neural network model to produce output actions directly. Unlike the SPA paradigm, the end-to-end learning methods do not require maps or separate planning stages. The model can be trained using supervised learning [42]–[45] or reinforcement learning [46]–[50].

III. F-1: AN INSIGHTFUL VISUAL PERFORMANCE MODEL

This section introduces a visual performance model, which we refer to as *F-1*, that helps us understand whether a UAV’s performance is bottlenecked by onboard compute or by other components such as sensors or UAV physics.¹ We start with the F-1 model overview and explain how it can be useful. We then describe how we construct the F-1 model.

A. A Visual Performance Model

The F-1 model establishes a roofline-like relationship between the safe velocity of the UAV and its decision-making rate. To intuitively understand safe velocity, let us consider a simple scenario depicted in Fig. 3a where a UAV has a sensor with a sensing distance of ‘d’ meters and a field of view (FoV) [51]. The UAV can sense any obstacle within the FoV, and the autonomy algorithm running on an onboard computer can make appropriate decisions at the rate of f_{action} . Considering all the payload (sensor, compute, battery, etc.), the UAV’s physics can allow it to accelerate by up to a_{max} . In such cases, the UAV can safely fly up to a velocity of V_{safe}

¹The F-1 name is inspired by the high-performance Formula-1 (F-1) racing cars, where components are added, removed, engineered and sometimes even fine-tuned based on the vehicle’s aerodynamic regulations and specifications.

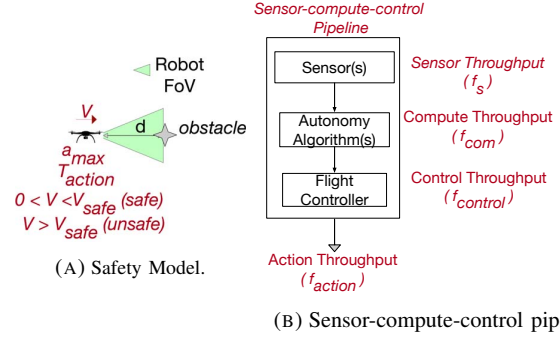


FIG. 3: (A) The safety model for UAV. (B) Action throughput in Sensor-compute-control pipeline.

such that even if an obstacle is within its FoV, it can safely stop without colliding. Any velocity greater than V_{safe} means the UAV cannot stop safely and will collide with the obstacle. Achieving high safe velocity is crucial as it lowers the mission time and overall mission energy [21].

The decision-making rate (‘Action Throughput’) determines how fast the UAV can sense, process, and take appropriate actions. It can be calculated as the throughput of the sensor-compute-control pipeline shown in Fig. 3b. As the stages in the sensor-compute-control pipeline can be run concurrently, the minimum latency of the pipeline can never be smaller than the maximum latency of each component in the subsystem:

$$\max(T_{sensor}, T_{compute}, T_{control}) \leq T_{action} \quad (1)$$

If the stages of the pipeline are not fully overlapped, the total pipeline latency can never exceed:

$$T_{action} \leq T_{sensor} + T_{compute} + T_{control} \quad (2)$$

Thus, between Eq. 1 and Eq. 2 we have the lower bound and upper bound in the pipeline latency. From these relationship we can estimate the upper-bound on the Action Throughput (f_{action}) using the bottleneck analysis [28], [29]:

$$f_{action} = \min\left(\frac{1}{T_{sensor}}, \frac{1}{T_{compute}}, \frac{1}{T_{control}}\right), \quad (3)$$

$T_{sensor} = 1/f_{sensor}$ is the latency to sample data from the sensor. If the UAV has 60 FPS camera, the sensor data can be sampled at 16.67 ms interval, i.e., the sensor latency.

$T_{compute} = 1/f_{compute}$ is the latency of the algorithm to estimate the high-level action commands. We can estimate the compute throughput ($f_{compute}$) by characterizing the autonomy algorithm on a given onboard compute.

$T_{control} = 1/f_{control}$ is the latency to generate the low-level actuation commands. Typical $f_{control}$ values are ~ 1 kHz [35].

The relationship between the safe velocity (V_{safe}) and Action Throughput (f_{action}), results in a roofline-like model as shown in Fig. 4a. The model can be expanded to provide meaningful abstractions such as bounds and bottleneck analysis for computer architects designing onboard computers.

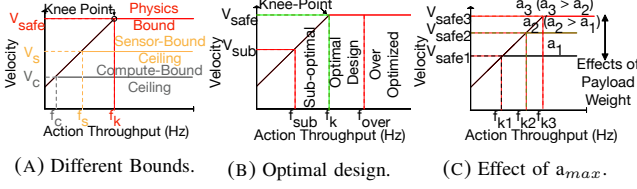


FIG. 4: (A) Using the F-1 model to understand different bounds in compute, sensor, and physics, respectively. (B) Determining optimal design using the F-1 model. (C) Weight affects a_{max} .
B. Sensor, Compute and Physics Bounds

The F-1 model can be used to perform a bound-and-bottleneck analysis to determine if the safe velocity is affected by the sensor/compute or the UAV's physics. Any point to the left of the knee-point in F-1 (Fig. 4a) denotes that the safe velocity is bounded by the compute or sensor, and any point to the right of the knee-point denotes the velocity is bounded by the physics of the UAV. To achieve a balanced pipeline design, its action throughput should be equal to that of the knee-point.

Physics Bound. A UAV's physical properties, such as weight and thrust produced by its rotors, determine how fast it can move. Hence, the ultimate bound on the safe velocity (V_{safe}) will be determined by its physics (i.e., body dynamics). We call the region to the right of the knee-point (i.e., when sense-to-act throughput is greater than or equal to f_k) as *Physics bound*. Unless the physical components are improved, the velocity cannot exceed the current peak safe velocity no matter how fast a decision is made (i.e., faster compute/sensor).

Sensor Bound. The choice of onboard sensors may also limit the decision-making rate (f_{action}), which in turn can limit the safe velocity (V_{safe}). As shown in Fig. 4a, a robot's velocity is sensor-bound if its action throughput is equal to the sensor's frame rate (f_{sensor}) but less than the knee-point throughput (f_k). The sensor-bound case occurs when the compute throughput ($f_{compute}$) is greater than the sensor throughput (f_{sensor}), and $f_{sensor} < f_k$. In this scenario, the sensor adds a new ceiling, thus, bounding the velocity under V_s . In this region, unless the sensor throughput is improved, velocity cannot exceed the sensor-bound ceiling (V_s) no matter how fast the onboard computer can process the sensor input.

Compute Bound. The choice of onboard compute (or autonomy algorithm) also affects the decision-making rate (f_{action}). Fig. 4a shows that a UAV's velocity is compute-bound if its compute throughput ($f_{compute}$) is less than the sensor's frame rate (f_s) and the knee-point throughput (f_k). The computing platform adds a new ceiling to the model, bounding the velocity under this limit (V_c). Unless the compute throughput is improved, the velocity cannot exceed V_c .

C. F-1 Model for Quantifying Optimal Compute Designs

Every UAV configuration has a unique F-1 model, thus resulting in a unique knee-point. Recall that the knee-point is the minimum action throughput required to maximize the safe velocity. Therefore, we can use this information to determine an ideal or balanced onboard compute (or autonomy algorithm) for a given UAV. Furthermore, if the compute system design

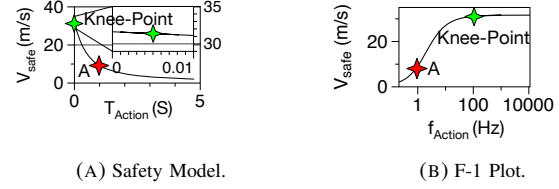


FIG. 5: The safety model and the F-1 roofline model.

is sub-optimal, it helps us understand the performance gap between the current compute design and the optimal design.

Optimal Design. For a given UAV with fixed mechanical properties, changing the sensor, onboard compute, or autonomy algorithm affects the f_{action} . Consequently, the optimal design point is when the action throughput is equal to the knee-point throughput (f_k) as shown in Fig. 4b.

Over-Optimal Design. If the action throughput $f_{over} > f_k$, then either the sensor/computer is over-optimized since any value greater than f_k yields no improvement in the velocity of the UAV. Such an over-designed computing/sensor involves extra optimization effort.

Sub-Optimal Design. If the action throughput is f_{sub} , such that $f_{sub} < f_k$, then the sensor/computer is under-optimized, which signifies that the system is off by $(f_{sub} - f_k)$ and there is scope for improvement through a better algorithm or selection (or design) of the computing system as shown in Fig. 4b.

D. Constructing the F-1 Visual Performance Model

In this section, we describe how we construct the F-1 model starting from prior work [22] that has established and validated the relationship between the UAV component parameters and the safe velocity of a UAV as described by the equation below:

$$v_{safe} = a_{max} \left(\sqrt{T_{action}^2 + 2 \frac{d}{a_{max}}} - T_{action} \right) \quad (4)$$

Eq. 4 states that if the UAV's physics permits it to accelerate by a_{max} , its compute and sensors permit it to sense and act at T_{action} ($1/f_{action}$), and its sensor(s) can sense the environment as far as 'd' meters, then robot can travel as fast as V_{safe} .

To construct the model, we sweep the T_{action} from 0 to 5 s along with typical accelerations values ($a_{max} = 50 \text{ m/s}^2$) and the sensor range ($d = 10 \text{ m}$), as shown in Fig. 5a. We observe an asymptotic relation between velocity and T_{action} such that as $T_{action} \rightarrow 0$, the velocity $\rightarrow 32$ (Fig. 5b). Likewise, as the $T_{action} \rightarrow \infty$, the velocity $\rightarrow 0$. To derive F-1 from Figure 5a, we plot the f_{action} (inverse of T_{action}) and velocity in Fig. 5b. There is a point beyond which increasing f_{action} does not increase the velocity, showing a *roofline*. We annotate the plots with two sample points denoted as point 'A' and 'knee-point.' The point A has an f_{action} of 1 Hz while the knee-point has an f_{action} of 100 Hz. From point A to knee-point denotes a 100 \times improvement in action throughput and translates to an increase in velocity from 10 m/s to 30 m/s. However, after the knee-point, even 100 \times improvement in f_{action} results in only 1.0004 \times improvement in velocity. Hence, increasing the action throughput (e.g., faster sensor or compute, etc.) beyond the knee-point yields no improvement in V_{safe} .

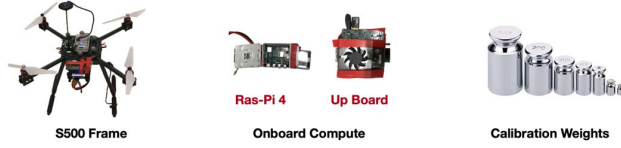


FIG. 6: We build four custom drones by changing the onboard compute (Ras-Pi4 and UpBoard) and calibration weights (50 g and 150 g). The mechanical frame is an S500 quadcopter frame for all four UAVs. Note that the figure is not to scale.
E. F-1 Model Captures the Effects of Payload Weight

The F-1 model can visualize the effect of varying payloads such as onboard computers, sensors, and batteries. Usually, an onboard computing platform with higher TDP (thermal design power) weighs heavier due to the larger heatsink and board-level components. Payload weight such as onboard computer and larger heatsink affects UAV's acceleration (a_{max}). On the one hand, a larger payload weight lowers a_{max} , which lowers the safe velocity. The ceilings in the F-1 model capture this effect (See a_1 in Fig. 4c). On the other hand, a lighter payload weight can affect the a_{max} less and can allow the UAV to have a higher safe velocity. This effect is captured by the roofline or increased ceilings (a_2 or a_3 in Fig. 4c). Hence, the F-1 model helps computer architects understand how the onboard compute power, its TDP (or heatsink) impacts a_{max} and safe velocity.

IV. EXPERIMENTAL VALIDATION OF THE F-1 MODEL

This section discusses the F-1 model validation based on the real-world UAV flight. We demonstrate that the knee-point for different drones, which determines the roof, matches the values predicted by the F-1 model within acceptable error bounds.

We built four drones by changing two onboard compute platforms (which change the payload weight) and calibration weights (to simulate different heatsink weights) while keeping the software stack and mechanical frame the same (Fig. 6). Our choice of the four UAV configurations was primarily based on the limitations of current UAV technology. We needed the ability to customize the UAV and have programmable control. We also needed a minimum onboard computing capability to run autonomy algorithms and several ROS packages (e.g., ground truth localization). In addition, we needed support for customization in terms of payload. Hence, we built the custom UAV from scratch using off-the-shelf components to validate Eq 4, as well as expand it as the F-1 roofline model to allow characterization and bottleneck analysis for other types of autonomous UAVs. In contrast, commercial off-the-shelf UAVs use proprietary components, have limited payload choice, and have almost no programmable access to the control software.

UAV(s) Specification. All component specifications used in four UAVs are tabulated in Table I. UAV-A and UAV-B use the same motor with a pull (thrust) of 435 g per motor. Likewise, UAV-C and UAV-D use additional calibration weights of 50 g and 150 g, which will change the thrust-to-weight ratio of the UAV. These weights simulate additional payload components. The onboard compute on UAV-A, UAV-C, and UAV-D is Ras-Pi4 (ARM-based system), whereas, on UAV-B,

TABLE I: Specification of four custom UAVs we build for F-1 model validation. Each of the four UAVs will have different real-world flight characteristics due to different takeoff weights.

UAV Components	UAV A	UAV B	UAV C	UAV D
Flight Controller	NXP FMUK66			
Base Weight (Motors + ESC + Frame)	1030 g	1030 g	1030 g	1030 g
Battery	3S 5000 mAh, 11.1 V			
Autonomy Algorithm	Custom Controller based on MAVROS			
Onboard Compute	Ras-Pi4	UpBoard	Ras-Pi4	Ras-Pi4
Motor Propulsion	ReadytoSky 2210 920 KV			
Motor Pull (single motor)	≈ 435 g	≈ 435 g	≈ 435 g	≈ 435 g
Payload Weight (Batteries + Onboard Compute)	590 g	800 g	640 g	690 g

it is Up Squared (x86 based system). Considering the entire payload system (i.e., onboard compute + battery) has a weight difference of 210 g, these differences result in UAVs having completely different flight characteristics. Also, due to these differences, the four drones will have unique rooflines (since each has different a_{max}) captured by our F-1 model. Later on, we describe how our validation methodology generalizes to other UAV types, such as nano- vs. micro-UAV, and so forth.

Experimental Validation Methodology. To experimentally validate that the safe velocity (knee-point) predicted by the F-1 model matches the actual drone safe velocity, we write our custom autonomy algorithm to precisely control the drone's position, velocity, and acceleration. It is developed using the MAVROS UAV drivers running on Robot Operating System (ROS). The entire algorithm is executed on Ras-Pi4 or Up Squared. The ground truth of the UAVs' positions is captured from a well-calibrated Vicon motion capture system to verify the infractions in the stopping position. The drone's velocity and acceleration are obtained from the flight controller's IMU.

In our experiments, we start with an obstacle placed at 3 m from the drone's current position, and the goal of the autonomy algorithm is to move and safely stop before the obstacle. We also assume that the sensing distance is at least 3m (i.e., $d = 3$ m) to detect the obstacle and make the decision to stop. This is a reasonable setup since we can assume that the 3 m stop is an intermediate goal in a larger trajectory [52] which could be generated using either SPA or E2E learning (see Section II-E) algorithm. If infractions exist beyond the 3 m, it signifies that the drone has collided with the obstacle. Likewise, if the drone stops before the 3 m, it suggests that it has not collided.

For each UAV (UAV-A to UAV-D) and its specification (see Table I), we first obtain the safe velocity predicted by the F-1 model. Using these values as the seed, we vary the drone's velocity in the seed value neighborhood. For example, the safe velocity predicted by the F-1 model for UAV-A is 2.13 m/s. In flight tests, we control UAV-A's velocity from 1.5 m/s, 1.9 m/s, 2 m/s, 2.1 m/s, 2.2 m/s, and 2.5 m/s respectively.

Fig. 7a shows the trajectories obtained from UAV-A flight by setting the velocity from 1.5 m/s to 2.5 m/s. For the 1.5 m/s the UAV-A will always stop safely. For 2.5 m/s, the UAV-A will always have infractions. We vary the UAV-A velocity to the point where we see no infractions ($v=1.9$ m/s), corresponding

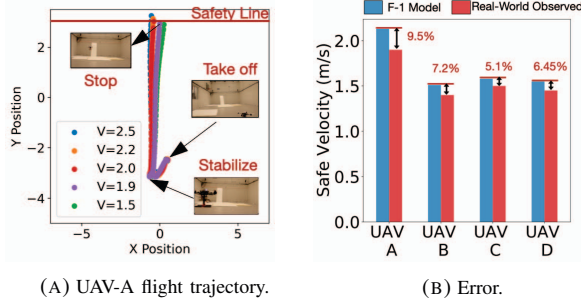


FIG. 7: (A) Real-world flight trajectory for UAV-A. The F-1 model predicted safe velocity is 2.2 m/s. Based on the real-world flight test, the safe velocity for UAV-A is 1.9 m/s. (B) Error % between F-1 predicted and observed safe velocity.

to the safe velocity of UAV-A in the real world.

We run five different trails for each UAV and each velocity point to rule out the statistical noise. For instance, with 2 m/s, the UAV-A had infractions twice out of five trials. But we still consider this velocity to be unsafe. Upon finding the safe velocity, we calculate the error between the F-1 model's safe velocity and the real-world UAV's safe velocity for flying.

After we validate the safe velocity, we also need to validate the knee-point. Recall that the y -value of the knee-point is safe velocity, and the x -value is the action throughput that achieves this safe velocity. Since our autonomy algorithm is programmed in MAVROS, the ROS loop rate parameter sets the action throughput. Our experiments set the loop rate to 10 Hz, which matches the knee-point determined by the F-1 model for these drones. Fig. 7b shows the error between the F-1 model predicted safe velocity and the real-world flight. The end-to-end modeling error is 9.5%, 7.2%, 5.1%, and 6.45% for each drone type, i.e., UAV-A through UAV-D, respectively.

Note that even though the payload weight difference (as shown in Table I) between UAV-A and UAV-C versus UAV-C to UAV-D is the same (i.e., 50 g), the drop in safe velocity (V_{safe}) is not proportional in Fig. 7b. This is due to the non-linear relationship between the safe velocity, acceleration (a_{max}), and the payload weight. To determine the relationship between the velocity and payload weight, we must start from Eq 4 and calculate the acceleration as a function of the payload weight. We estimate the upper bound in acceleration for each payload weight by using Eq 5 in Fig. 8, where T is the total thrust, α is the angle pitch angle, and m is the mass of the payload, and F_D is the drag force, which depends on the aerodynamic properties of the UAV. Since we aim to provide an early characterization and bottleneck analysis tool to guide onboard computing system design or selection, we do not model drag in the F-1 model. The F-1 model calculates the a_{max} based on the payload weight. Fig. 9 shows V_{safe} velocity as a function of the payload weight. We map the four UAV configurations in Table I onto the velocity versus payload weight curve in Fig. 9. A 50 g payload weight increase in UAV-A to UAV-C causes a $\sim 35\%$ decrease in velocity (from 2.13 m/s to 1.58 m/s), whereas the same 50 g weight increase from UAV-C to UAV-D results in $<3\%$ reduction in velocity (1.58

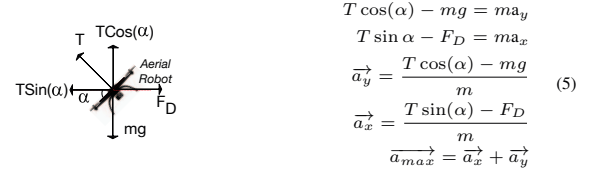


FIG. 8: Estimation of acceleration.

m/s to 1.53 m/s). Likewise UAV-B (Intel UpBoard), which is 210 g heavier than UAV-A (Ras-Pi4), causes a $\sim 41\%$ decrease in safe velocity from 2.13 m/s to 1.51 m/s as shown in zoomed-in sub-plot in Fig. 9. The y -axis in the zoomed-in subplot is in the log scale. Hence, it is important to understand which region the UAV is operating in and how the design/selection of various payloads (e.g., onboard compute) impacts UAV's performance. Generally speaking, as the UAVs get smaller in form factor, their payload carrying capability decreases, and as such, their safe velocity is more dramatically affected due to the non-linear relationship shown in Fig. 9.

Generalization to Other UAVs Beyond Table I. We discussed earlier that our UAV selection(s) was largely determined by the minimum capabilities that we required for the experimental validation of the F-1 model. The lowest-end onboard compute platform that can run MAVROS and other software packages is the Ras-Pi4. However, this computing platform requires a separate onboard battery (due to the limitations in UAV power delivery), weighing 590 g. We had also selected another Intel UpBoard capable of running MAVROS and autonomy algorithm as another onboard compute platform. Similar to Ras-Pi4, the UpBoard computer board also requires a separate battery. The Intel UpBoard onboard computer and battery for its power supply weigh around 800g. Hence, between these two extremes (UAV-A and UAV-B), we added calibration weights to create two additional UAV configurations (UAV-C and UAV-D) to validate the F-1 model.

But in general, one can pick any point in the curve (signifying a different UAV configuration) from Fig. 9. In the future, it would be feasible to integrate specific board-level components within the SoC with future technological advancements to reduce the payload weight further. Also, a different UAV (e.g., DJI Spark) would likely have a different payload weight sensitivity based on its thrust and other UAV characteristics. Hence, it is essential to consider these effects when selecting or designing the onboard computer for UAVs. Existing efforts in designing custom hardware/miniaturization (Section VII) are based on isolated compute metrics without understanding how these design choices impact the UAV's flight performance. Hence, there is a need for a model like F-1 that provides abstractions similar to the Roofline model [24] to allow computer architects to characterize onboard computers and determine various bottlenecks affecting UAV's performance.

Understanding the Potential Source(s) of Error(s). Although we envision the F-1 model to be used as an early-phase design tool to characterize the computing system for UAVs, it

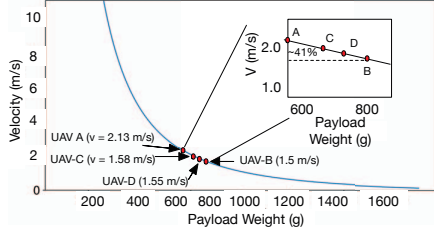


FIG. 9: Non-linear relationship between the safe velocity (V_{safe}) and the maximum payload weight.

TABLE II: Summary of knobs available in the Skyline tool.

Parameter	Unit	Description
Sensor Framerate	Hz	Throughput of the sensor.
Compute TDP	W	Maximum TDP of the onboard compute. Used to design the heatsink.
Autonomy Algorithm	N/A	Select a pre-configured autonomy algorithm.
Compute Runtime	s	Measures the latency of the autonomy algorithm. Used to calculate compute throughput.
Sensor Range	m	Maximum range of the sensor.
Drone Weight	g	Maximum weight of the UAV without any extra payload.
Rotor Pull	g	Measures the thrust produced by the rotor propulsion.
Payload Weight	g	Total weight of the payload including onboard compute, sensors, battery etc.

is crucial to understand the source of errors to compensate for it during the later part of the design cycle. We believe there are three reasons for this error. First, the F-1 model introduces a linearization error when converting the curve to the left of the knee-point with a straight line (Fig. 5b). Second, the F-1 model calculates the impact of computing weight on a drone's acceleration. However, in real-world flight, drag (Fig. 8) can also impact the drone's acceleration. Third, we build the drone from the ground up (sourcing various components) and attach payload weights directly to the frame. The sudden movements (e.g., jerk) of the payload components can affect the drone's dynamics and center of mass. We believe that with precise mechanical design, one can minimize these errors.

Implications for Onboard Computer Design. The F-1 model predicts a higher safe velocity than the real-world observation. For computer architects who want to use the F-1 model to understand different bottlenecks, the optimistic estimation of the F-1 model is beneficial. Any onboard compute designed at the v_{safe} predicted by F-1 will also ensure that the design will not affect the a_{max} at a lower safe velocity. Hence, overestimation by the F-1 model will always ensure that the onboard compute will never be the bottleneck.

V. SKYLINE: AN INTERACTIVE VISUAL TOOL FOR F-1

We provide an interactive web-based tool (Figure 10) to plot the F-1 model for a given UAV. It gives the end-user the ability to do exploratory studies on the impact of various parameters of the UAVs, such as algorithms, onboard computers, and sensors.

A. Overview

The tool has three major components. The first component includes the interactive knobs for various UAV components and their parameters. The second component is the visualization area that dynamically plots the F-1 model. The third component is the analysis and guidance area, where the tool shows the characterization results and optimization tips for the architects.

B. UAV System Parameter Knobs

We divide the parameters into three categories: sensor, compute, and physics. Recall that the F-1 model (Section III) ties the various UAV component interactions together to determine the safe velocity. The maximum safe velocity has implications on the overall mission performance [21]. Table II summarizes the available knobs in the Skyline tool.

The tool, as shown in Figure 10, also provides user-defined knobs. These knobs all the end-user to specify unique parameter values for sensor range, sensor framerate, compute platform TDP, compute latency, UAV weight, and rotor thrust. These parameters can be used for future design characterization studies to understand how these parameters impact the UAV's performance. Later on, in the results section, we demonstrate how to use these knobs via a variety of different case studies.

C. Visualization Area

The visualization area plots the F-1 model based on the UAV configurations. The interactive UAV parameter knobs result in an interactive plot that gives the user an understanding of how these parameters affect the UAV's safe velocity. For example, increasing the TDP of the compute platform increases the heatsink and payload weight. As payload weight increases, the maximum acceleration decreases, lowering the safe velocity. Likewise, changing the sensor framerate affects the decision-making rate, determining how fast the UAV can travel. Thus, using the interactive visualization of the F-1 model enables the user to intuitively understand the effect of UAV parameters.

D. Automatic Analysis

The Skyline tool has an analysis section that outputs information, such as knee-point throughput and safe velocity achievable, for a given UAV. In addition to the performance data, the analysis section also provides information about the fundamental limits in safe velocity (i.e., sensor bound, compute-bound, or physics bound) and several optimization tips.

VI. EVALUATION

We present several case studies (Table III) on how to use the F-1 model for the characterization of various components in a UAV. The analysis gathers architectural insights that can build efficient onboard compute platforms. From Section VI-A to Section VI-C, we first show how to use the model for characterizing individual components in the UAV system. Then, in Section VI-D, we demonstrate how the model can be used for complete end-to-end UAV system characterization.

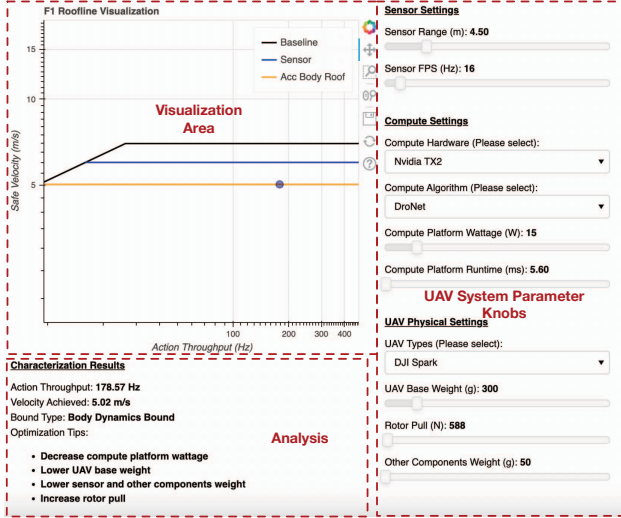


FIG. 10: Skyline is an interactive tool to visualize the F-1 model in action. It allow users to change various UAV and compute system parameters and observe the resulting end-to-end effects. The tool also provides recommendations for easy data analysis.

A. Onboard Compute Characterization

In this case study, we answer the following research questions: *Given two or more onboard compute choices, how to select a suitable computer system for a given UAV?* The onboard computer is a vital component for achieving autonomy in UAVs. However, despite its importance, the selection (and even design, as we show in Section VII) of onboard compute remains ad-hoc [22], [53], [54]. We characterize two commercially available off-the-shelf onboard compute systems, Nvidia AGX and Intel NCS (shown in Fig. 11a), and examine their impact.

UAV Configurations. For the autonomy algorithm, we select the DroNet [43]. Our interactive tool, Skyline, provides a few autonomy algorithms for out-of-the-box usage. We then select the DJI Spark UAV as the form factor. For the onboard compute engine, we toggle between Intel NCS and Nvidia AGX. Intel NCS (USB-like form factor) is a sub-1 W compute system that weighs around 47 g. The Nvidia AGX module without a heatsink weighs 280 g. The tool internally calculates the heatsink weight [55], which for a 30 W TDP is 162 g. We keep the sensor FPS at 60 Hz to ensure we are not in the sensor-bound region for both these UAVs.

Analysis. The F-1 plot from Skyline is shown in Fig. 11b. Based on the F-1 plots for the DJI Spark UAV, the Nvidia AGX (denoted as Nvidia AGX-30W) has a lower roofline than Intel NCS. The higher roofline for Intel NCS is because the NCS onboard compute weighs less than the Nvidia AGX, which means the DJI Spark with Intel NCS can achieve higher acceleration (a_{max}) compared to DJI Spark with Nvidia AGX. Hence, the UAV's physics restricts it from achieving a higher safe velocity between these onboard compute choices even though Nvidia AGX (230 FPS) can achieve $1.5\times$ more compute throughput than Intel NCS (150 FPS) running the DroNet [43]

TABLE III: Overview of the evaluation case study detailed in Section VI. The highlighted parameter cell is varied while the rest of the UAV's parameters are kept constant.

Case Studies	Comments	UAV System Configurations			
		Onboard Compute	Autonomy Algorithm	Payload Redundancy	UAV Type
VI-A	Onboard compute	Intel NCS & Nvidia AGX	DroNet [43]	None	DJI Spark
VI-B	Autonomy algorithms	Nvidia TX2	Sense-Plan-Act [21] & TrailNet [53]	None	AscTec Pelican
VI-C	Payload Redundancies	Two Nvidia TX2	DroNet [43]	Dual Modular Redundancy	AscTec Pelican
VI-D	Full UAV System	Nvidia TX2 Nvidia AGX Intel NCS Ras-Pi	CAD2RL [46] DroNet [43] TrailNet [53]	None	AscTec Pelican & DJI Spark

algorithm. This suggests that the high compute performance cannot always translate to a higher safe velocity as the UAV's body dynamics/physics becomes the limiting factor.

To improve the respective UAV's performance, a future optimization for the next iteration of Nvidia AGX could lower the power of Xavier AGX at the cost of performance (since AGX is over-provisioned by $33\times$). This power reduction will reduce the TDP, resulting in a smaller heatsink weight. To demonstrate the reduction of heatsink weight, we consider a scenario where we reduce the TDP of AGX from 30 W to 15 W using any architectural optimization. For simplicity, we assume this is achieved without impacting the compute throughput. Based on the heatsink calculator [55] used in the F-1 model, we observe that the reduction in TDP also reduces the heatsink weight by half (from 162 g to 81 g as shown in Fig. 12). Furthermore, the reduction of the compute payload weight increases the DJI Spark's safe velocity by 75% as shown by the ceiling (denoted as Nvidia AGX (15W)) in Fig. 11b.

We can employ any means to optimize for performance improvement or power reduction, such as software optimizations, runtime scheduling, microarchitecture design, device-level optimization, etc. Regardless of the applied method, once we have the final performance and power (TDP) for a given autonomy algorithm, we can enter it in the Skyline tool. The tool calculates the heatsink weight based on the heatsink calculator [55] while taking into account the other UAV parameters to plot the F-1 roofline as shown in Fig. 11b.

Takeaway. Selection of onboard cannot be ad-hoc or chosen based on compute performance in isolation, and one must consider the effects of all the UAV parameters and understand what implications the compute has on these parameters. A high-performance computer does not necessarily translate into a high-performing UAV. Skyline can help architects understand the fundamental role of computing in autonomous UAVs.

B. Autonomy Algorithm Characterization

In this case study, we answer the following question: *Given a fixed UAV and onboard computer, what is the effect of different autonomy algorithms on V_{safe} ?* The primary function of an

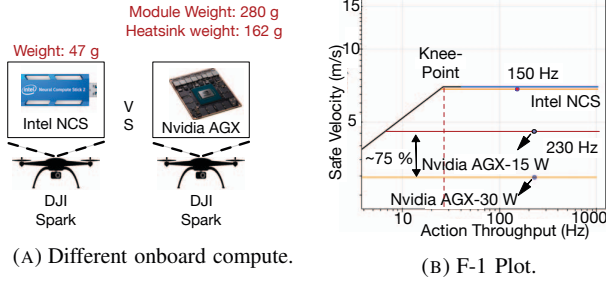


FIG. 11: Case study of choosing between Intel NCS and Nvidia AGX for DJI Spark running DroNet autonomy algorithm.

autonomy algorithm is to make intelligent decisions to achieve the mission goals while guaranteeing safety. However, despite its importance, the selection or design of the algorithm is often made in isolation without considering the impact of onboard computer or UAV components. So we characterize how changing autonomy algorithms impact the UAV's performance.

UAV Configurations. We consider two autonomy algorithm paradigms, namely SPA and E2E (Section II), and evaluate them on an AscTec Pelican UAV with TX2. For SPA, we characterize the package-delivery application from MAVBench [21]. For the E2E algorithm, we choose TrailNet [45] and DroNet [43]. In both of these cases, we keep the UAV, onboard computer fixed, and evaluate the different autonomy algorithms to understand their impact on the UAV's maximum safe flying velocity.

Analysis. The resulting plots are shown in Fig. 13b. On the one hand, the SPA algorithm achieves a compute throughput of 1.1 Hz on Nvidia TX2. However, due to its low decision-making rate, the maximum achievable safe velocity is limited to 2.3 m/s. On the other hand, TrailNet and DroNet (E2E algorithms) achieve a compute throughput of 55 Hz and 178 Hz on Nvidia TX2, which achieves a higher safe velocity.

For an AscTec Pelican UAV with the TX2 onboard compute, the knee-point throughput is 43 Hz, suggesting that TrailNet and DroNet are over-provisioned by $1.27 \times$ and $4.13 \times$. In the E2E paradigm, the high compute throughput of the autonomy algorithm does not translate to a higher safe velocity unless the UAV's physics changes (i.e., a UAV with a higher thrust-to-weight ratio). However, for the SPA paradigm, the UAV's safe velocity is compute-bound. Therefore, the compute throughput needs to be improved by $39 \times$ to achieve the optimal safe-

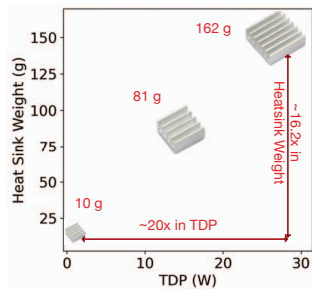


FIG. 12: Reduction in heat-sink weight with TDP.

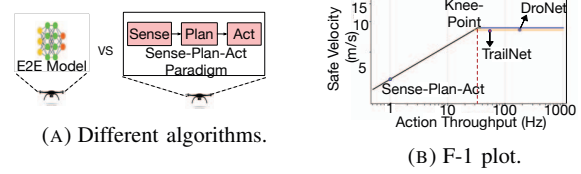


FIG. 13: Changing autonomy algorithms on the UAV. For SPA, we characterize the 'package delivery' application from MAVBench [21]. For E2E, we consider TrailNet [43], [53].

velocity permitted by the UAV's physics.

Takeaway. When designing autonomy algorithms for UAVs, metrics like high compute throughput or energy efficiency can be misleading and can result in over-optimization (increases the design cost) or under-optimization (affects robot performance). In an over-optimized scenario, the higher compute performance might not result in higher UAV performance. In an under-optimized scenario, the F-1 model can give us the performance targets to achieve in their optimization efforts, thereby helping us design balanced onboard computers for UAVs.

C. Modular Redundancy Characterization

In this case study, we answer the following research questions: *Given a UAV with fixed autonomy algorithm and sensor, what is the impact of redundancy in onboard compute on the UAV's performance?* UAVs need to operate robustly in dynamic environments [56], [57], and redundancy in compute or sensor ensures safety in the event of a failure. Sometimes dual redundancy [58] (or triple redundancy [59]) increases reliability where a majority vote determines the final decision. However, though redundancy can increase reliability, it can also increase the cost and negatively affects the UAV's performance. So we characterize the effects of adding redundancy in onboard compute and evaluate its effects on the UAV's safe velocity.

UAV Configurations. We evaluate how adding dual compute redundancy affects the UAV's safe velocity. Using Skyline, we first select UAV [45] from the pre-configured suite of autonomy algorithms. We select the AscTec Pelican UAV and an RGB-D camera with a frame rate of 60 FPS and a sensing distance of 4.5 m. In this UAV configuration, we evaluate the effects of having a single Nvidia-TX2 and dual Nvidia-TX2 SoC sharing the same board and evaluate the effects of dual compute redundancy. We select the Nvidia-TX2 platform to estimate the baseline safe velocity. To evaluate the effects of having dual redundancy in onboard compute (as shown in Fig. 14a), we assume that another Nvidia-TX2 is added to the UAV platform and has the sensor input. The output from these two platforms is validated and then sent to the controller (similar to Tesla's FSD stack). To model this scenario using Skyline, we account for the payload weight for the additional TX2, including the computing platform and the heatsink weight.

Analysis. The resulting two plots are combined and shown in Fig. 14b. Since the same autonomy algorithm (and onboard compute) is used in both these UAVs, it achieves a throughput of 178 Hz running on Nvidia TX2. The baseline scenario (single onboard compute) is annotated as "Roofline-TX2" in

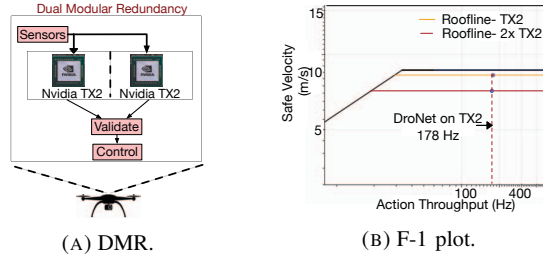


FIG. 14: Characterizing the effect of modular redundancy. the Fig. 14b. For the dual compute redundancy, the increase in payload weight lowers the maximum acceleration capability of the UAV, which lowers the roofline (annotated as “Roofline- 2 \times TX2 in Fig. 14b), thereby reducing the safe velocity by 33% compared to the baseline. Thus, there is a trade-off between enabling redundancy and robot operational performance.

To overcome the drop in operational efficiency of this UAV due to dual compute redundancy, architects can replace the over-provisioned TX2 with an onboard computer with the $\frac{1}{5}$ th of throughput for DroNet. This will lower the TDP, which will help accommodate two onboard computers within the same power envelope and reduce the payload weight.

Takeaway. Dual modular redundancies improve UAVs’ safety with the downside of increasing costs and lower performance. To fully understand the consequences of redundancies, there is a need to characterize the effects of additional payload and its impact on a UAV’s decision-making rate and physics. F-1 gives us the intuition into various bottlenecks and performance targets to build safe yet efficient redundancy systems.

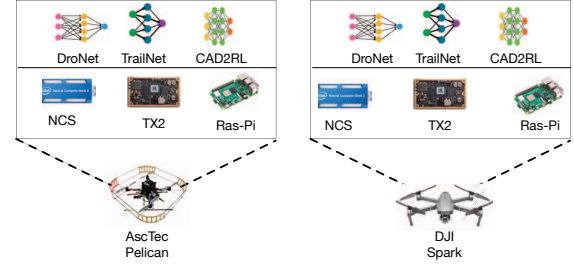
D. Putting it All Together: Full System Characterization

In this case study, we answer the following question: *Given the choice of several onboard computers, autonomy algorithms, and sensors, how do we systematically characterize and select components to maximize the UAV’s safe velocity? How does this selection differ as we change the UAV types?*

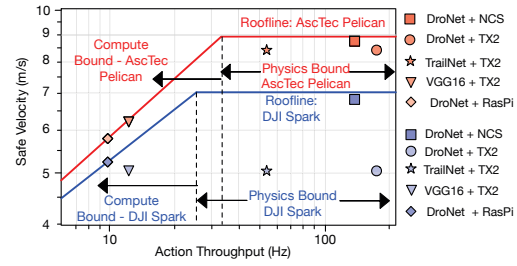
UAV Configurations. We consider several different choices for each UAV. For onboard computers, we consider Nvidia TX2, Nvidia AGX, Ras-Pi, and Intel NCS. Similarly, for different UAVs, we consider AscTec Pelican and DJI Spark. Finally for autonomy algorithms, we consider DroNet [43], TrailNet [53], and CAD2RL [46]. Using Skyline, we characterize each combination and we share insights from the F-1 model on how we can optimize each of these combinations.

Analysis. The annotated version of the results are combined and shown in Fig. 15b. Based on the F-1 plots, we can classify these designs as compute-bound or physics-bound. Below, we discuss what computer architects can do if the design point falls into the compute-bound/physics-bound category.

Compute-Bound Designs. In the compute-bound scenarios, the safe velocity is bounded by the compute throughput of the autonomy algorithm. For designs in this region, computer architects can apply the traditional optimization techniques ranging from micro-architectural to algorithmic optimizations to improve the compute throughput. For instance, in the AscTec Pelican UAV, Ras-Pi4 does not have sufficient computing



(A) Full UAV system characterization.



(B) F-1 plot.

FIG. 15: F-1 model to characterize the full UAV system.

capability to run autonomy algorithms such as DroNet, TrailNet, and CAD2RL. Therefore, any new architectural optimization for Ras-Pi4 (e.g., building a custom accelerator within the Ras-Pi4 system) would need to improve the compute throughput by 3.3 \times for DroNet, 110 \times for TrailNet, and 660 \times for CAD2RL.

Physics-Bound Designs. In physics-bound design, the safe velocity is bounded by the physical dynamics of the UAV rather than the autonomy algorithm or the onboard compute system’s performance. If the designs in this region lower the roofline, then computer architects can help alleviate this problem by optimizing for lower TDP. For instance, in the case of DJI Spark with TX2, as an onboard compute running DroNet, it achieves a throughput of 178 Hz. However, the knee-point for this UAV is only 30 Hz suggesting that it is over-provisioned by a factor of 6 \times . We can trade off this excess performance for a lower TDP (e.g., at a lower clock frequency) in order to reduce its heatsink weight and other board-level components, thus lowering the overall payload weight effect on the UAV.

Takeaway. Ad-hoc selection of UAV components or designing them in isolation impacts UAV’s performance. Hence, a systematic methodology is needed to characterize these UAV systems holistically. Skyline tool can give fundamental insights into how various component selections impact the UAV’s performance. Moreover, they also provide optimization goals for designing onboard compute for workload targeting UAVs.

VII. PITFALLS IN DESIGNING HARDWARE ACCELERATORS

Thus far, we have presented various case studies to demonstrate the general usefulness of the F-1 model. In this section, we pivot to using the F-1 model to discuss the common pitfalls in designing domain-specific hardware accelerators for UAVs based purely on isolated compute metrics such as ‘compute throughput’ and ‘low-power.’ We consider two popular accelerators, namely Navion [60] and PULP-DroNet [26] that

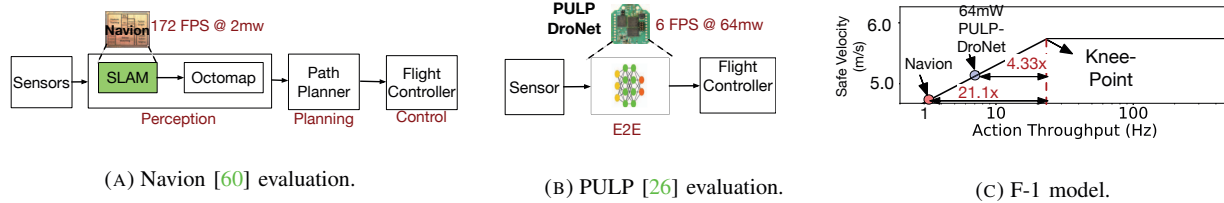


FIG. 16: Evaluation of custom hardware accelerators built specifically for autonomous UAV applications.

were specifically built for UAVs. We characterize them for a nano-UAV [27]. Our characterization demonstrates the need for a systematic approach to understanding the performance requirement needed for a particular UAV rather than designing a high-performance/low-power computer in isolation.

Navion. Navion is a hardware accelerator for visual-inertial odometry in the SPA paradigm, which achieves a throughput of 172 FPS in 2mW. Using Navion in a UAV to achieve full autonomy still requires other algorithms like integration of maps, motion planning, and control. Fig. 16a shows how the Navion chip will be used in an end-to-end UAV application. For the other stages in the SPA, we characterize the package-delivery application in MAVBench [21] and replace the SLAM with performance numbers reported in Navion [60]. This estimate is still optimistic for Navion, considering it does not perform the loop closure [61] in SLAM, whereas in our characterization in MAVBench, the SLAM kernel accounts for loop closure. Briefly, loop closure is the recognition of when a UAV has returned to a previous region that it has mapped. It helps reduce the uncertainty in the estimation of the map.

PULP-DroNet. PULP [26] is designed for accelerating DroNet [43] for a nano-UAV. It achieves 6 Hz at 64 mW power [26]. Unlike Navion, PULP can enable end-to-end autonomous operations (full autonomy) in UAVs since DroNet directly operates on raw sensor information to control the UAV.

Analysis. We characterize the Navion and PULP as onboard compute along with other components of the nano-UAV [27]. We also assume the sensor framerate is 60 FPS to ensure we don't fall into sensor-bound scenarios. The F-1 plot for these two configurations are shown in Fig. 16c. PULP achieves a throughput of 6 FPS @ 64 mW, which results in a compute-bound scenario since it is to the left of the knee-point in the F-1 roofline plot (see Fig. 16c). Therefore, the performance of the PULP hardware accelerator has to be increased by $4.33\times$ to achieve a peak velocity achievable by this nano-UAV.

In the case of the Navion chip, even though the SLAM stage achieves an impressive performance of 172 FPS @ 2 mW, integrating into the complete SPA pipeline (Fig. 16a) increases the overall latency to 810 ms or action throughput of 1.23 Hz. This accelerator also results in a compute-bound scenario since the UAV knee-point throughput is at 26 Hz. Therefore, the end-to-end throughput of the SPA pipeline must be improved by $21.1\times$ to achieve the peak safe-velocity. Improvements should target building accelerators for mapping [62] and path-planning stages in the SPA pipeline within a similar power envelope.

Takeaway. Optimizing or designing a specialized compute engine for UAVs based on isolated compute metrics like throughput, low power, or energy efficiency can be misleading,

and one needs to consider the compute performance along with the entire autonomy pipeline and other UAV components. Instead, we recommend using the F-1 model to guide architects' intuitions and perform bottleneck analysis to guide optimization efforts more systematically that translate to UAVs capabilities.

VIII. RELATED WORK

In addition to the related work already discussed in the previous sections, other work specifically in the context of models can be broadly bucketed into compute system models and models that are specifically related to autonomy.

Roofline Models. Roofline models [24] and Gables [29] (which is a roofline model for mobile SoCs with multiple IPs) provide insights into the optimization effort required to maximize the compute throughput for a given workload. These roofline models look at isolated compute throughput and do not include UAVs physics. In contrast, our earlier work [63] introduces the existence of a roofline-like model for UAVs. In this work, we extend the scope of the F-1 model with many case studies and build an interactive web-based tool that shows insights into how much optimization is required (similar to the goals of the roofline model) to maximize the drone's performance by considering compute, sensor, and UAV physics. As we build autonomous vehicles of all sorts and forms, our work shows the importance of considering systems holistically, rather than focusing on isolated components such as hardware acceleration in a vacuum as that can lead to suboptimal designs.

Models for Nano-Satellites. Cote [64] considers orbital mechanics for computer design in nano-satellites. They operate at a different regime than quadcopters and thus are not readily comparable or usable for drones. Nonetheless, they are relevant to our work since they have the same fundamental aspects of considering the systems holistically.

IX. CONCLUSION

The F-1 model is an early-stage performance model to systematically characterize and identify bottlenecks when designing onboard compute for autonomous UAVs while considering the full view of the system (i.e., sensor, compute, and physics). Using the model, we can understand how much a custom domain-specific hardware widget improves a given UAV's safe velocity. We believe that the model can be used for automated design space exploration and aid with generating an optimal domain-specific architecture best suited for a UAV.

X. ACKNOWLEDGEMENTS

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