**MIDDLE EAST TECHNICAL UNIVERSITY**

**ELECTRICAL AND ELECTRONICS ENGINEERING DEPARTMENT**



**EE463 STATIC POWER CONVERSION-II**

**PROJECT #1 REPORT**

**Due Date: 07.03.2019**

**Team Members**

**Ali AYDIN 2093326**

**Akın Şavklı 2095115**

**Question 1-)**

**Part a-)**

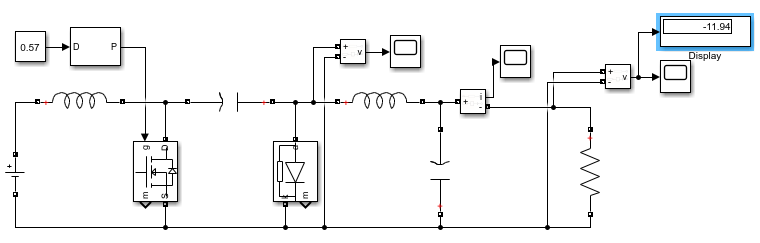


Figure : Cuk Converter Topology

In this part of the question, we are asked to design a Cuk converter but we are not given any values for component. Hence we are going to utilize a number of specifications that are supplied beforehand. First relation to be used is output to input voltage ratio(equation-1) which is 4/3 from which D can be found as follows:

.....................................................equation-1.

* By using this relation, we can find the duty ratio(D) as %57.

Another unknown value is the resistor itself and we can find it by using the following (equation-2):

.........................................................equation-2.

* By using this relation, we can find the resistor value as 4 Ohms.

As a specification, maximum output voltage ripple is desired to be %2. For this purpose, we can utilize the ON state relations with a bunch of assumptions which are the following:

* VL2 is constant during ON state
* Output current is equal to IL2 so there is no current passing thru filter capacitor.
* Hence we can assume that ripple in the o/p voltage is approximately equal to ripple in VC1. So, the limiting element here is C1 itself.

In accordance with the above assumptions, following figure illustrates the computations to find C1 value:

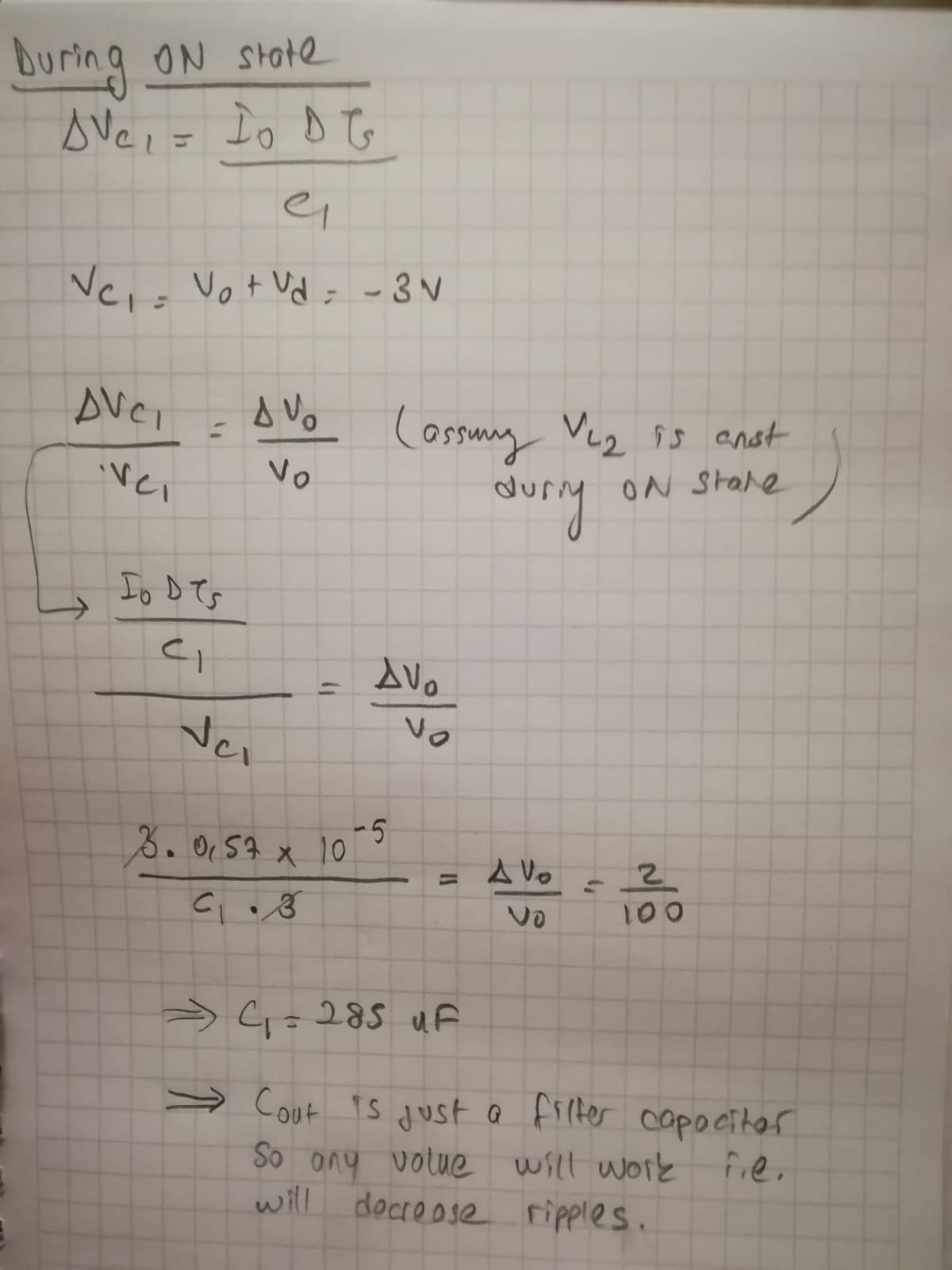


Figure - Computations to find the value of isolating capacitor (C1)

* The closest value available in the market for C1 is 270 uF so we are going to use it.
* Cout can be chosen as 900 nF in order not be bulky and expensive. Note that any arbitrary value for Cout will decrease output voltage ripples further since it is a filtering capacitor whose duty is exactly this.

Also note that we have no idea about inductor current ripples and hence we cannot find the exact numerical value for inductors. However, we know that output current is expected to be constant. Thus we can choose a large and reasonable value for inductor as 5mH. But we should make sure that the converter is not going into DCM, for this purpose we are going to look at simulation results given in the following figures:

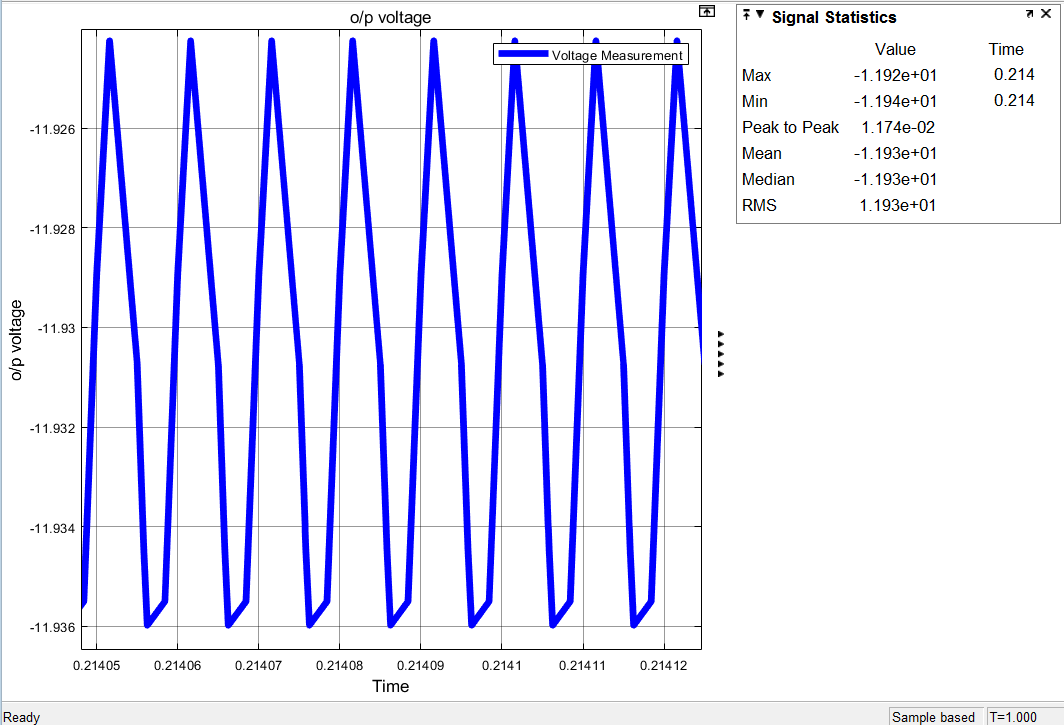


Figure - Output voltage characteristcs

* If we look at figure-3, we can see that our voltage ripple is found to be 0.098% that is even smaller than 2% which is expected due to the existence of an additional capacitor which is filtering capacitor (900nF).

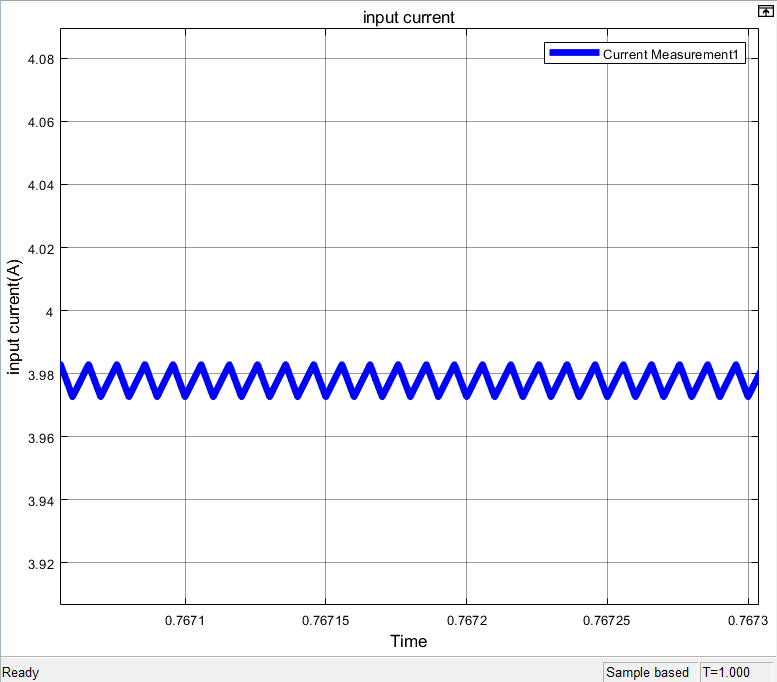
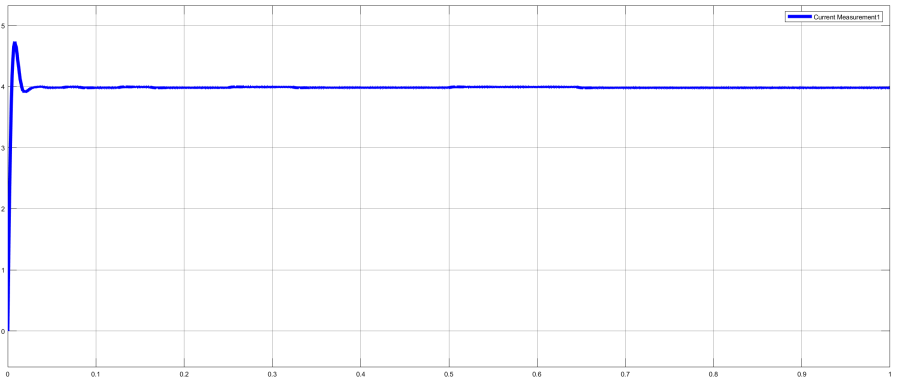
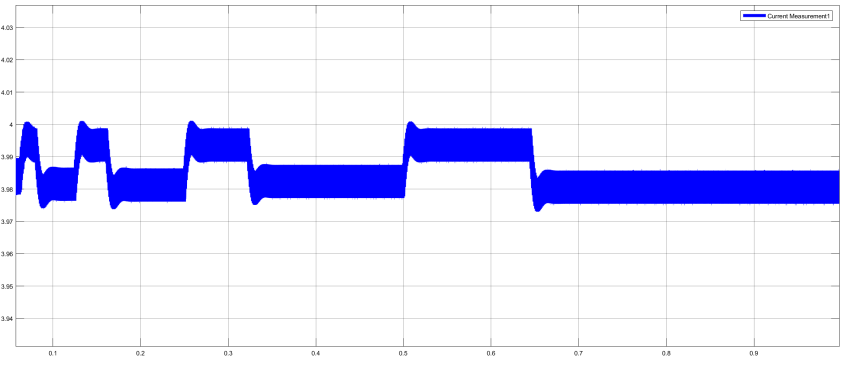


Figure - Inductor (L2) current characteristics

* If we look at figure 4, it is clear that the constructed converter topology is far from going into DCM, so there is no problem with the operation of it.

**Part b-)**





**Part c-)**

**Part d-)**

**Part e-)**