

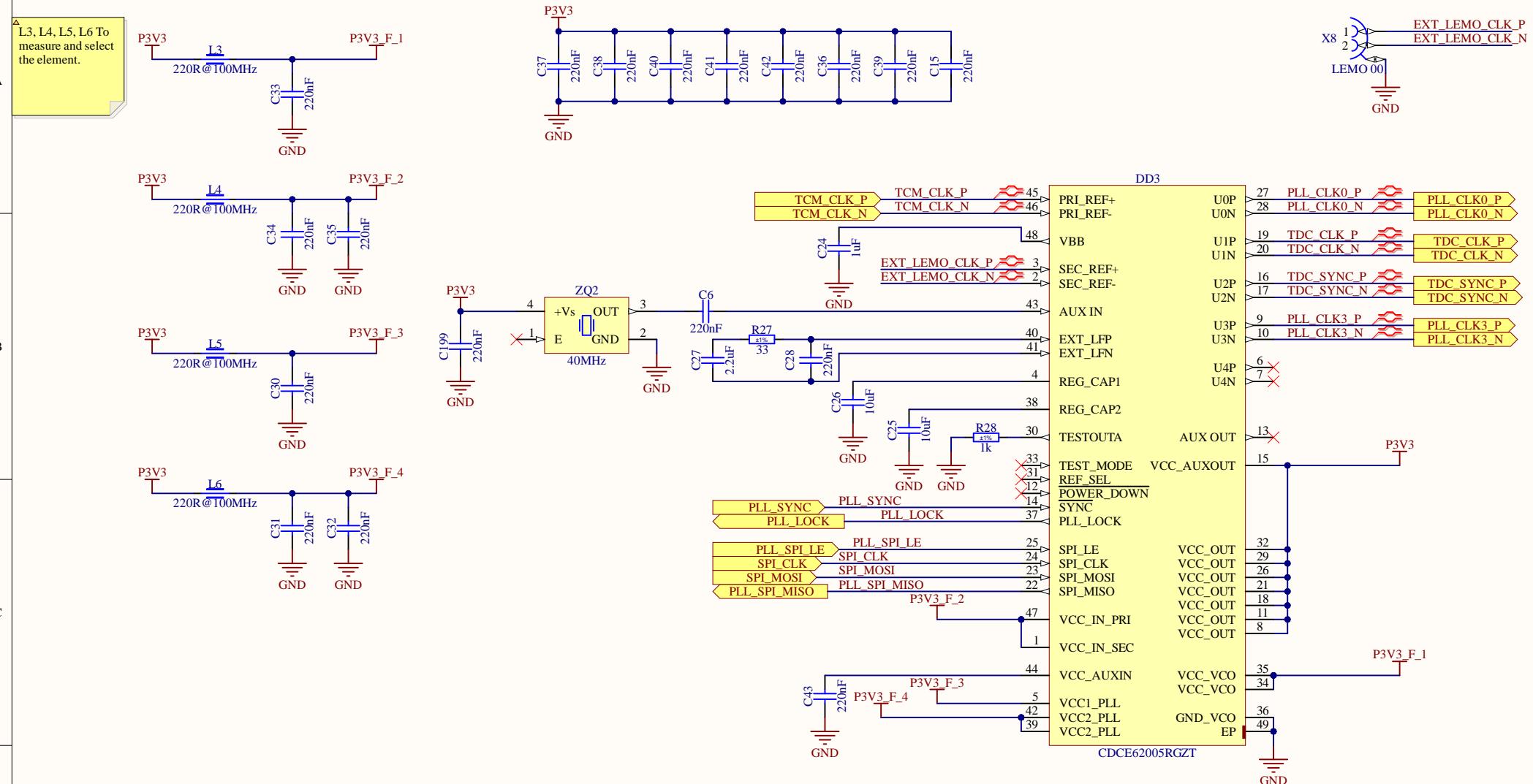
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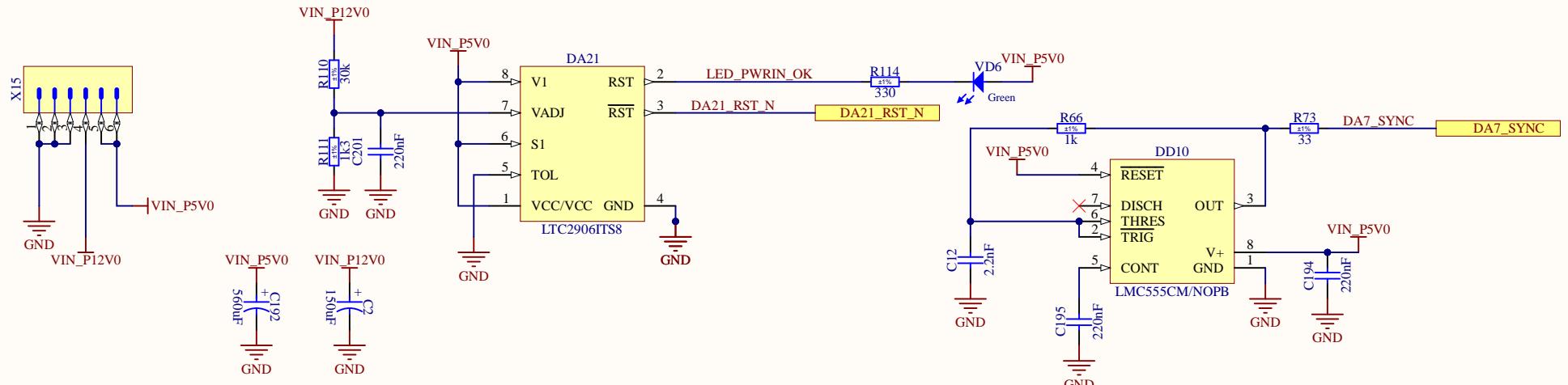
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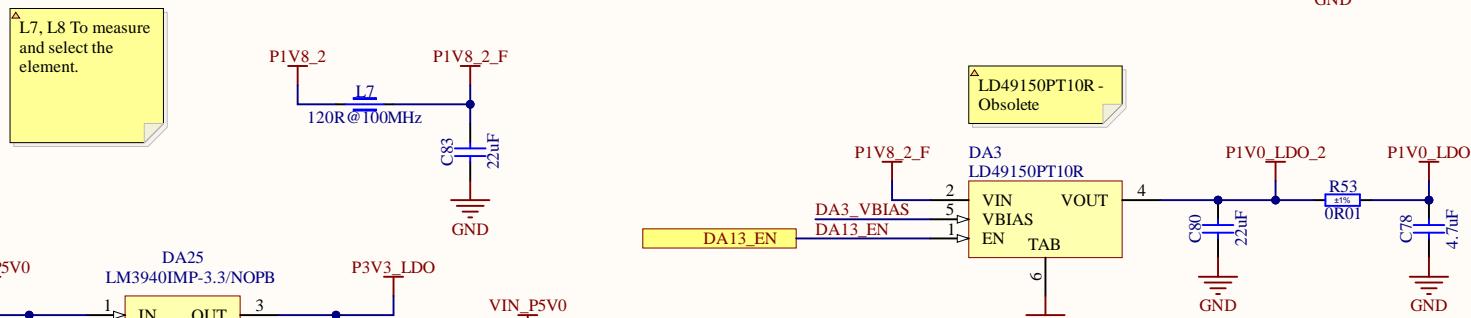
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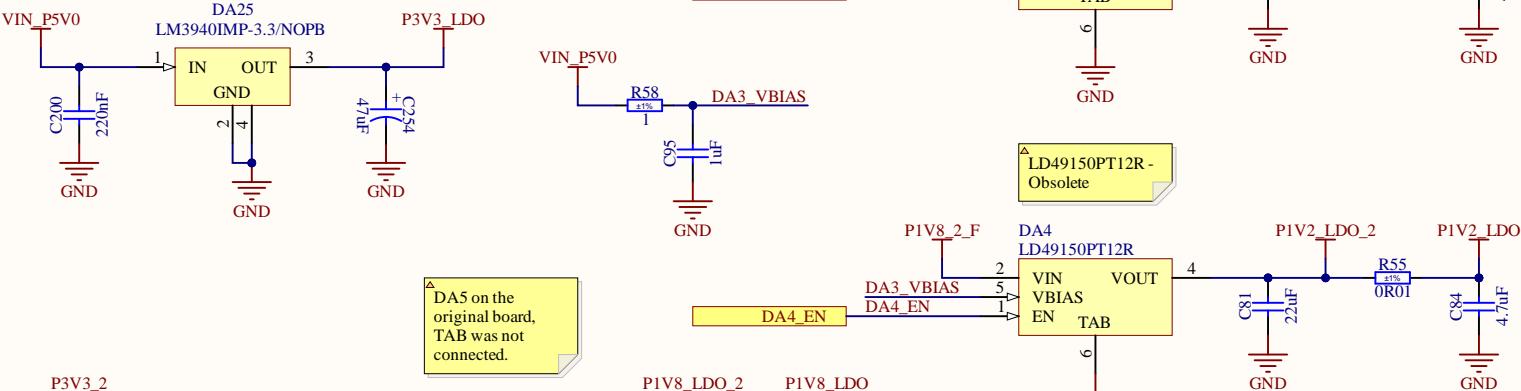
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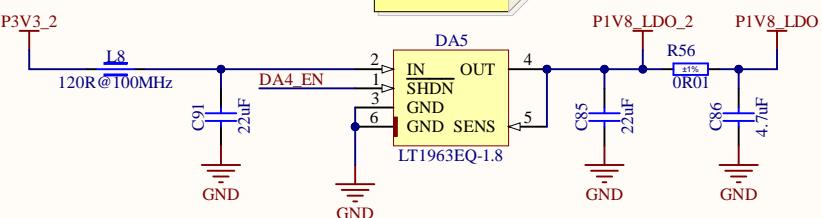
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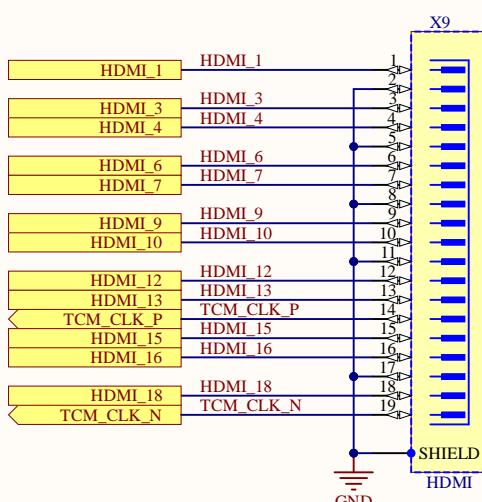
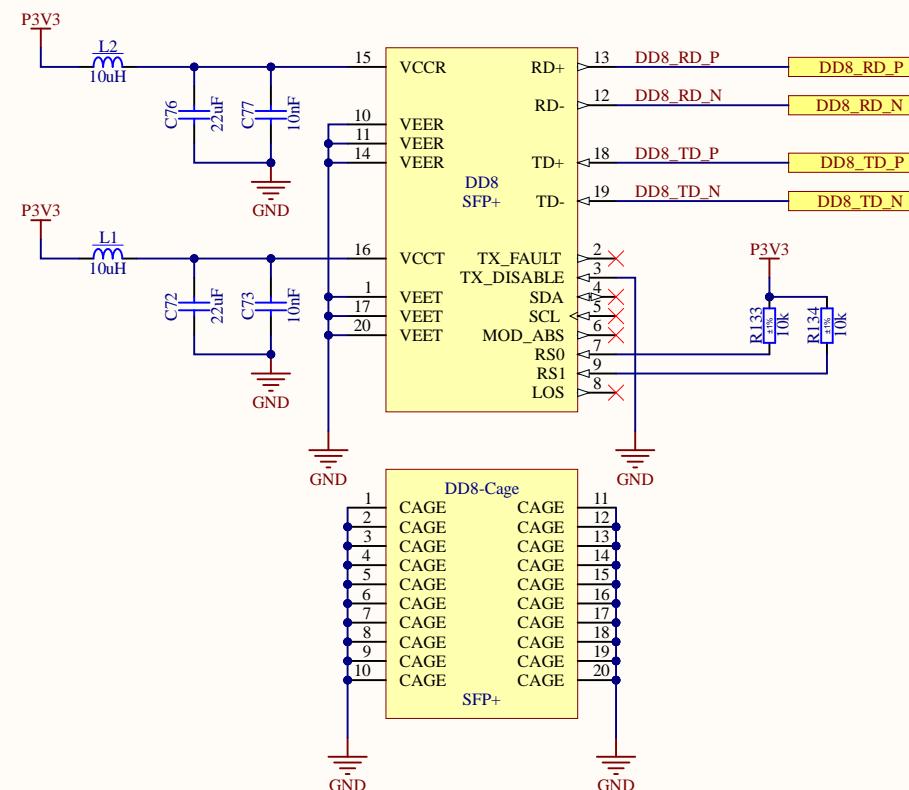
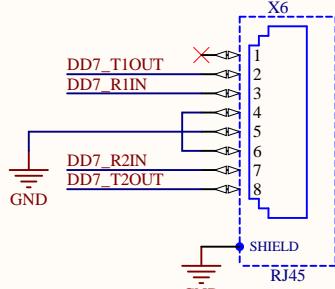
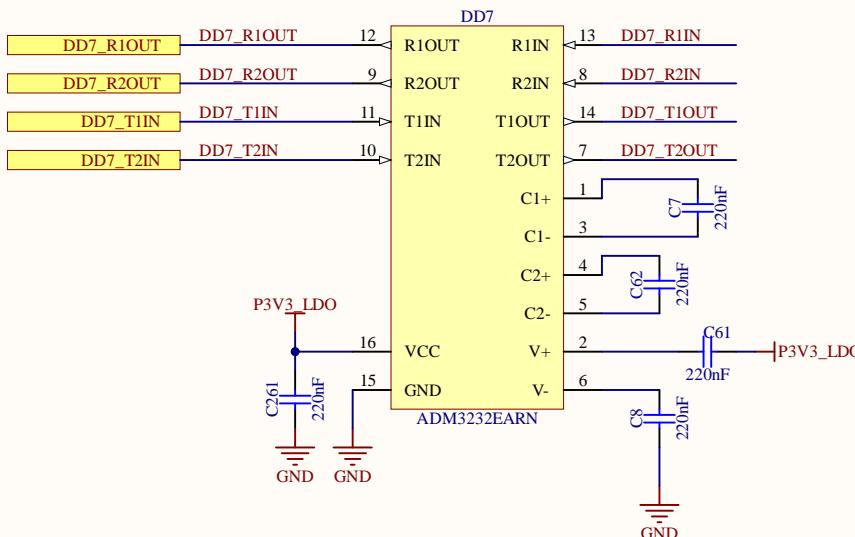
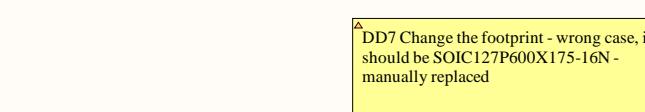
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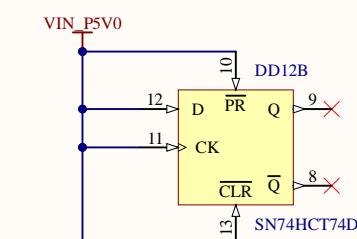
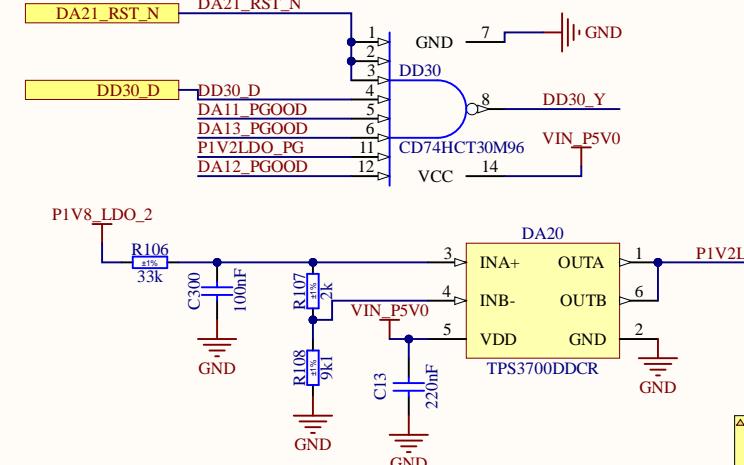
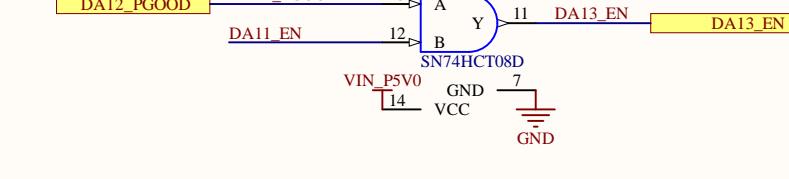
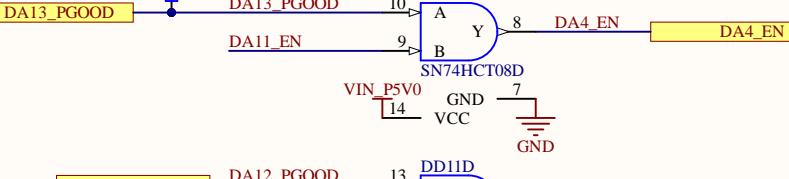
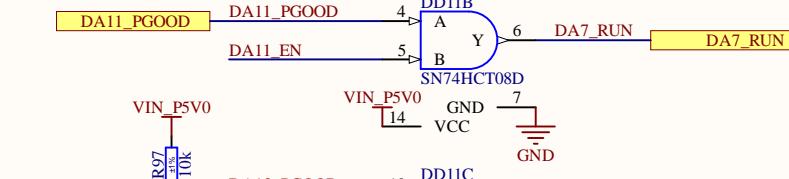
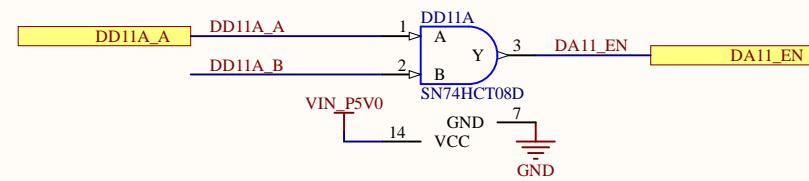


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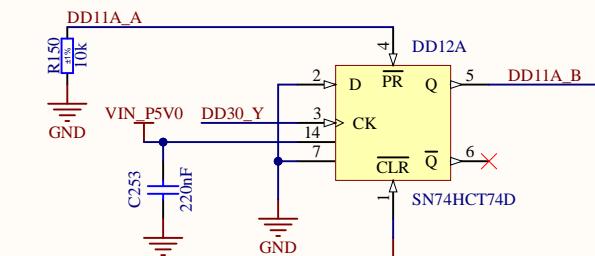
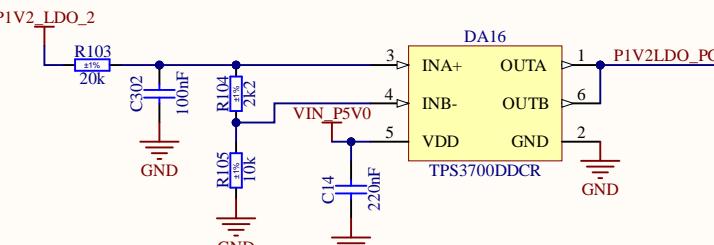
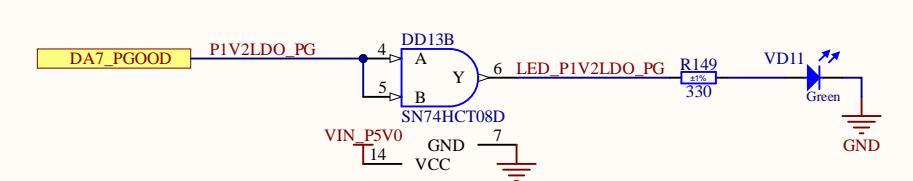
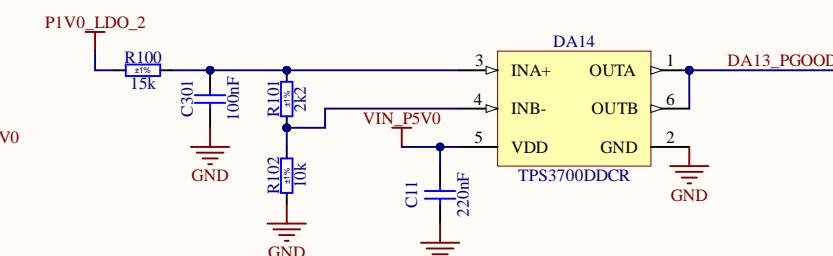
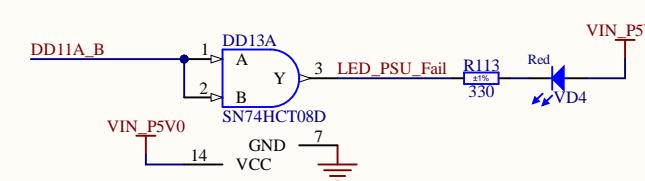
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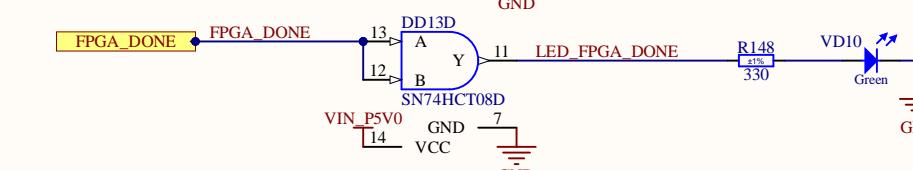
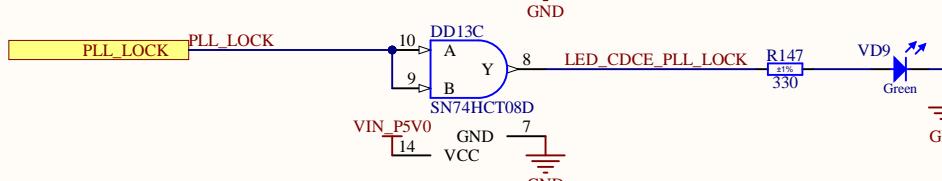
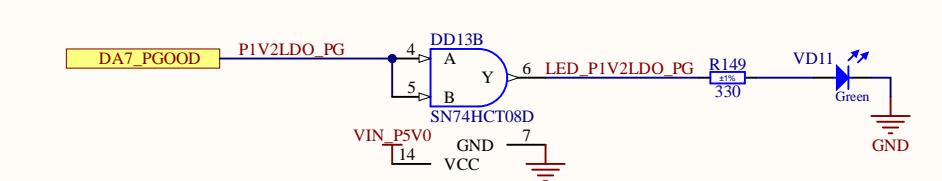


DA14, DA16, DA20
Component to be measured and selected from the CERN library. It is added on a physical board.

B



C

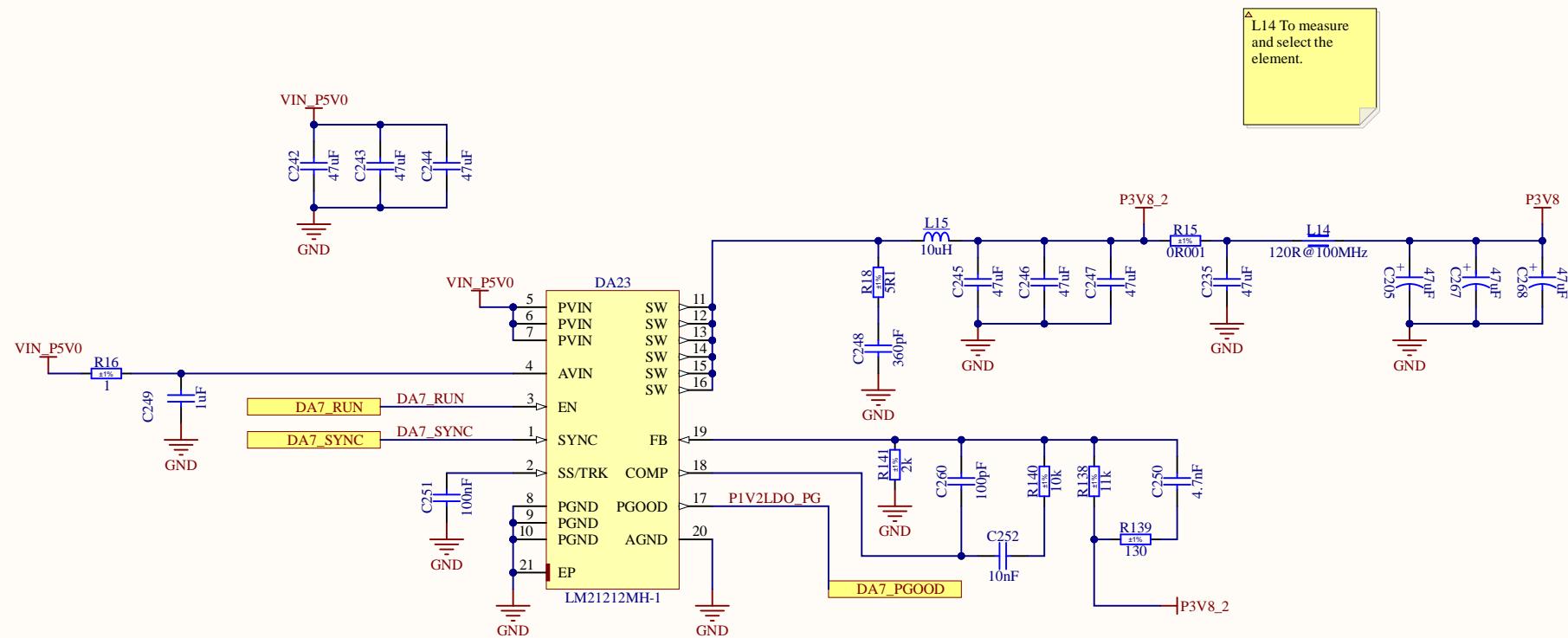


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A

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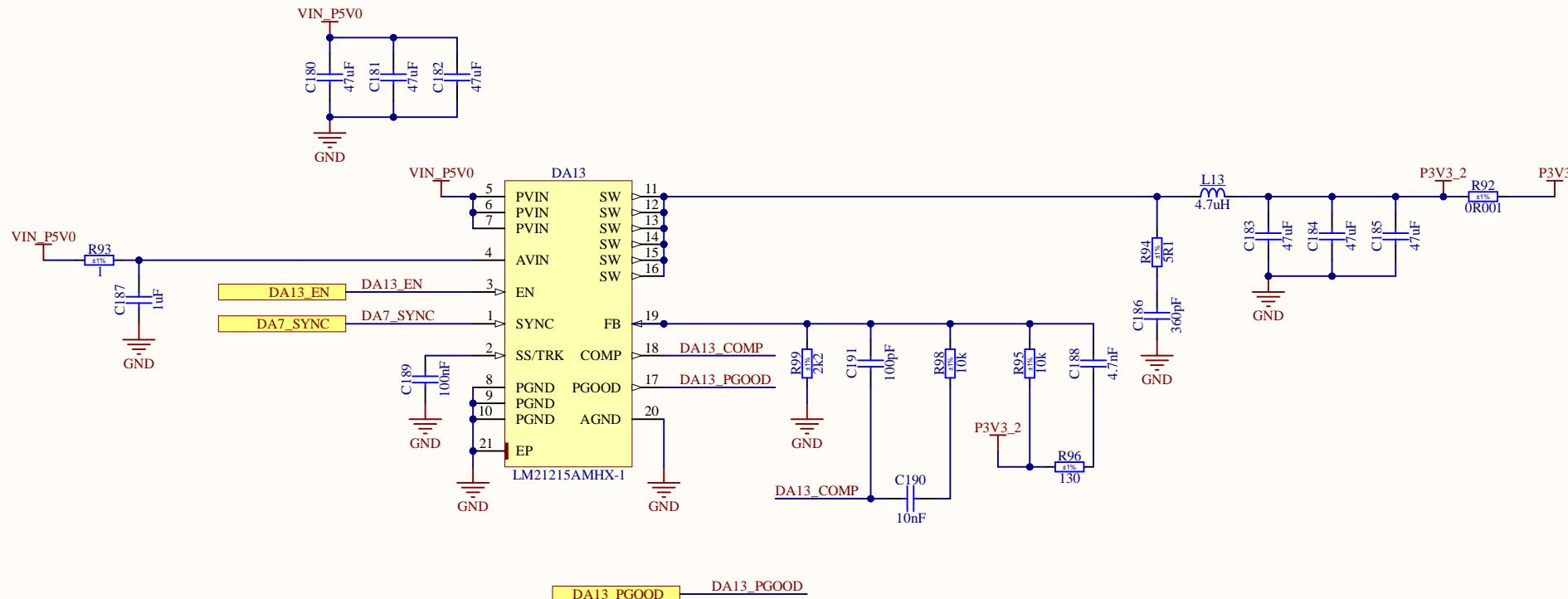
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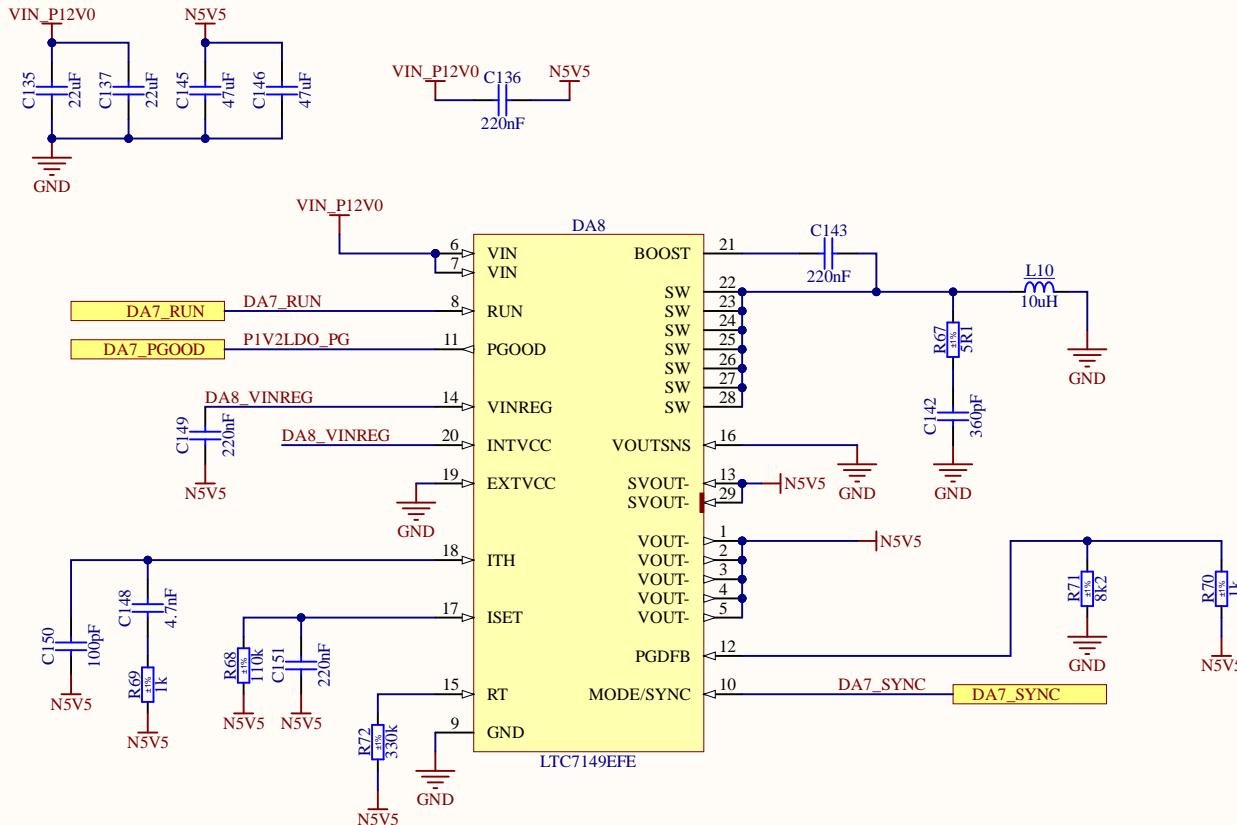
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A



B

B

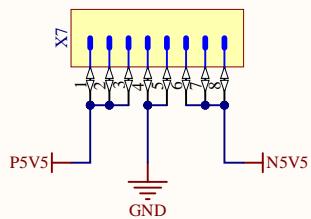
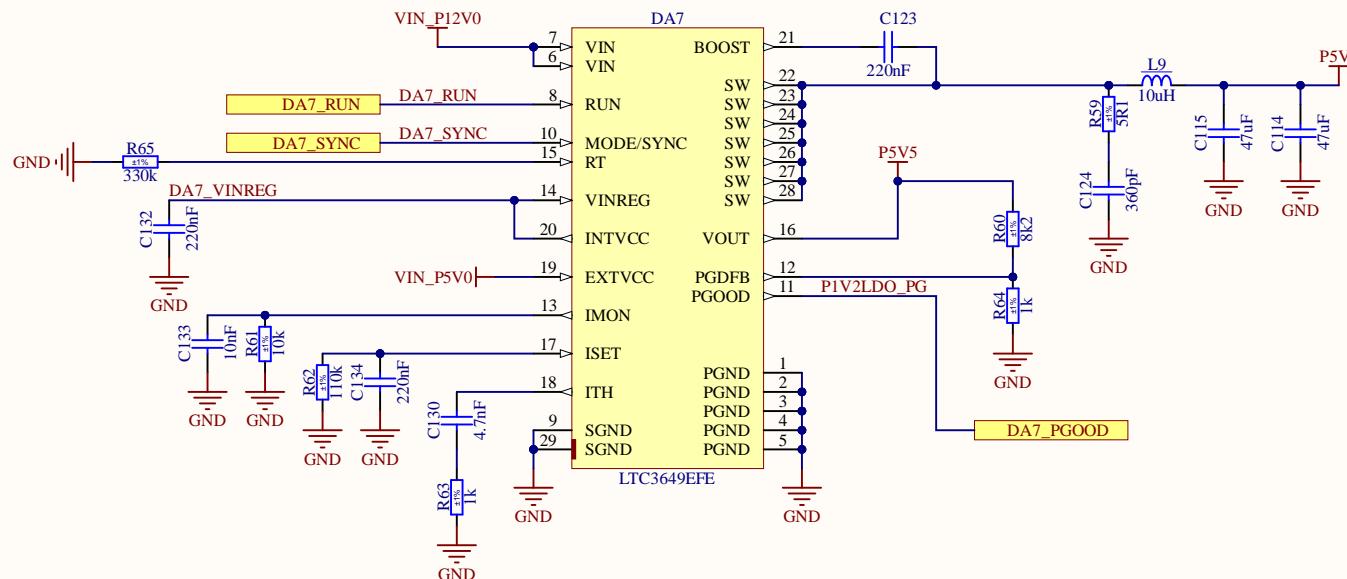
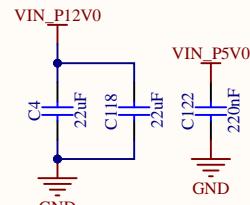
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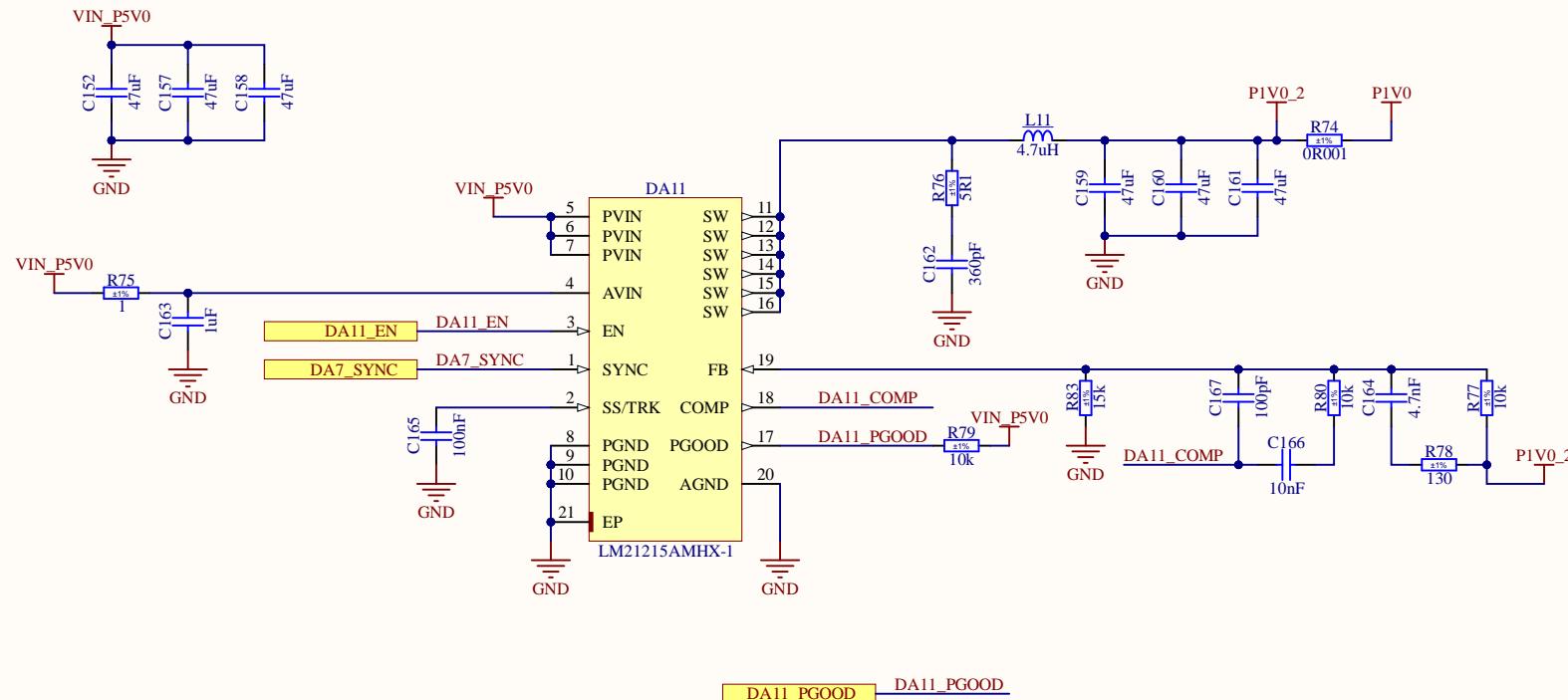
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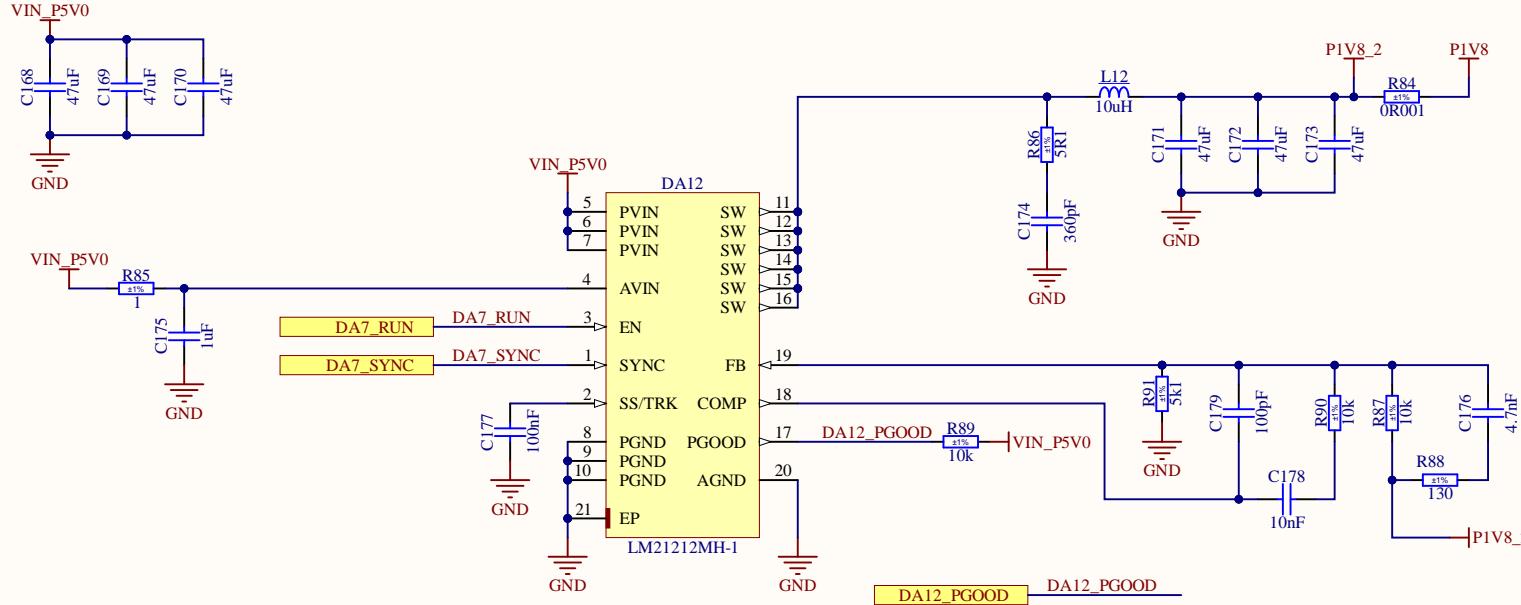
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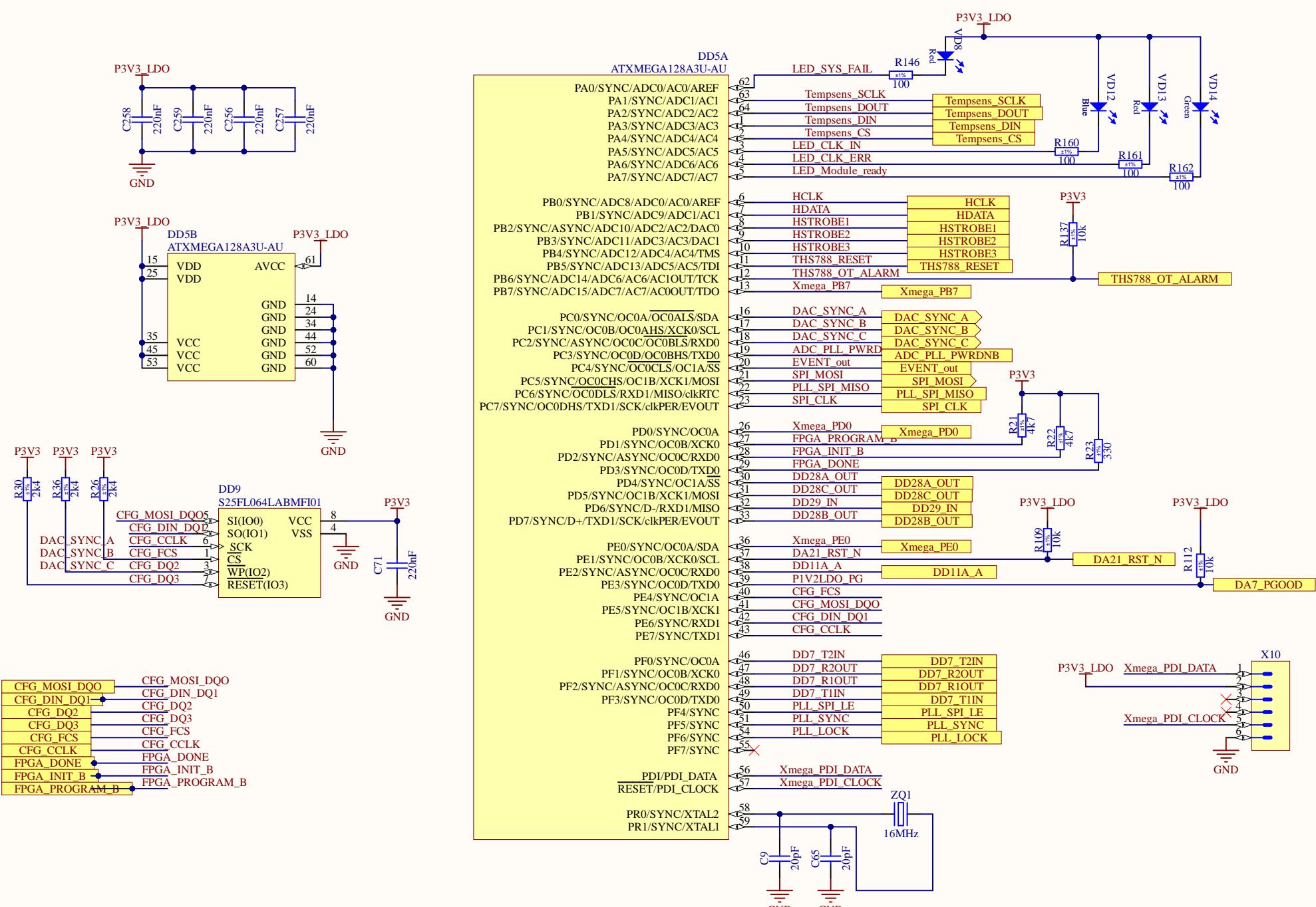
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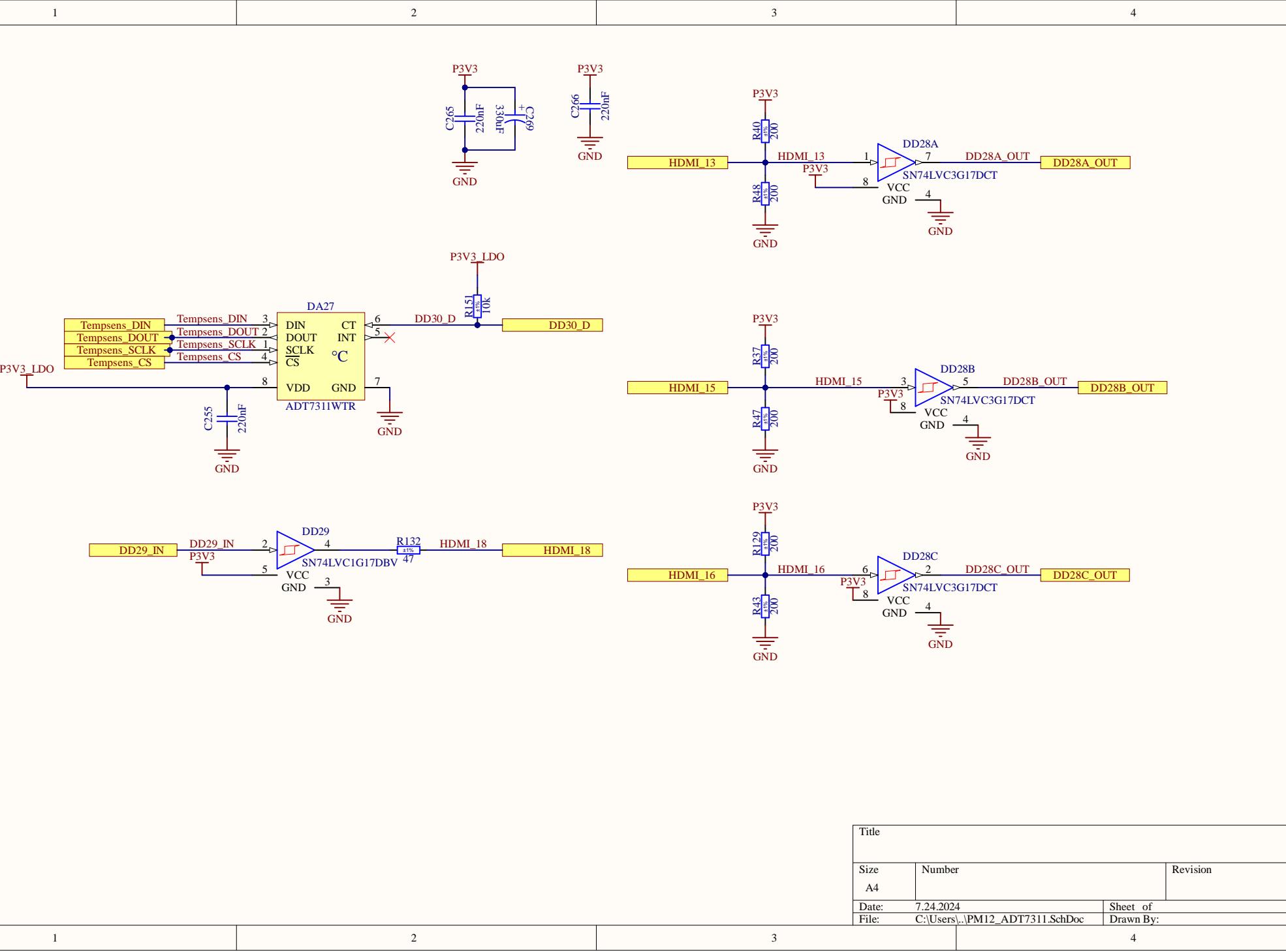
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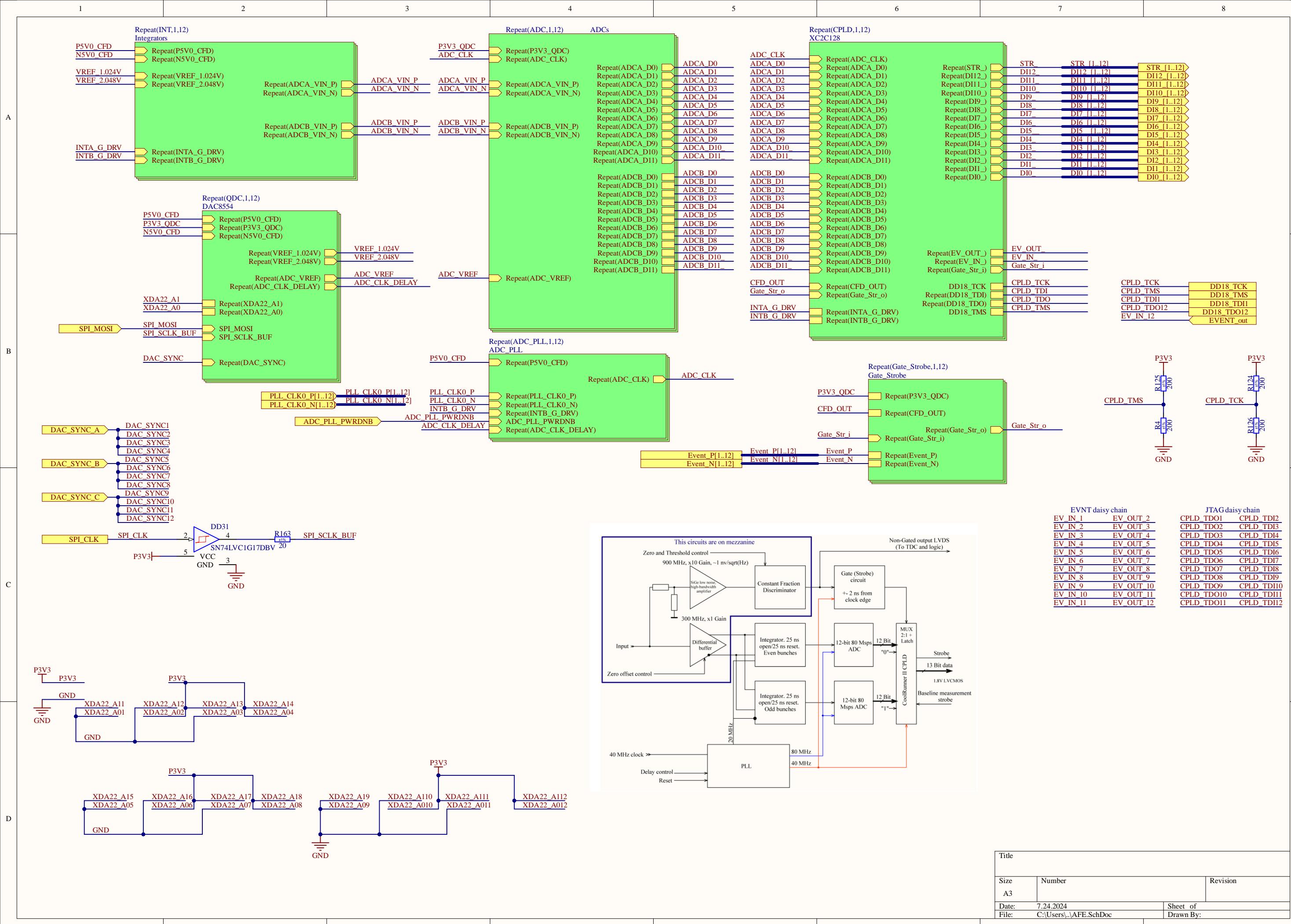
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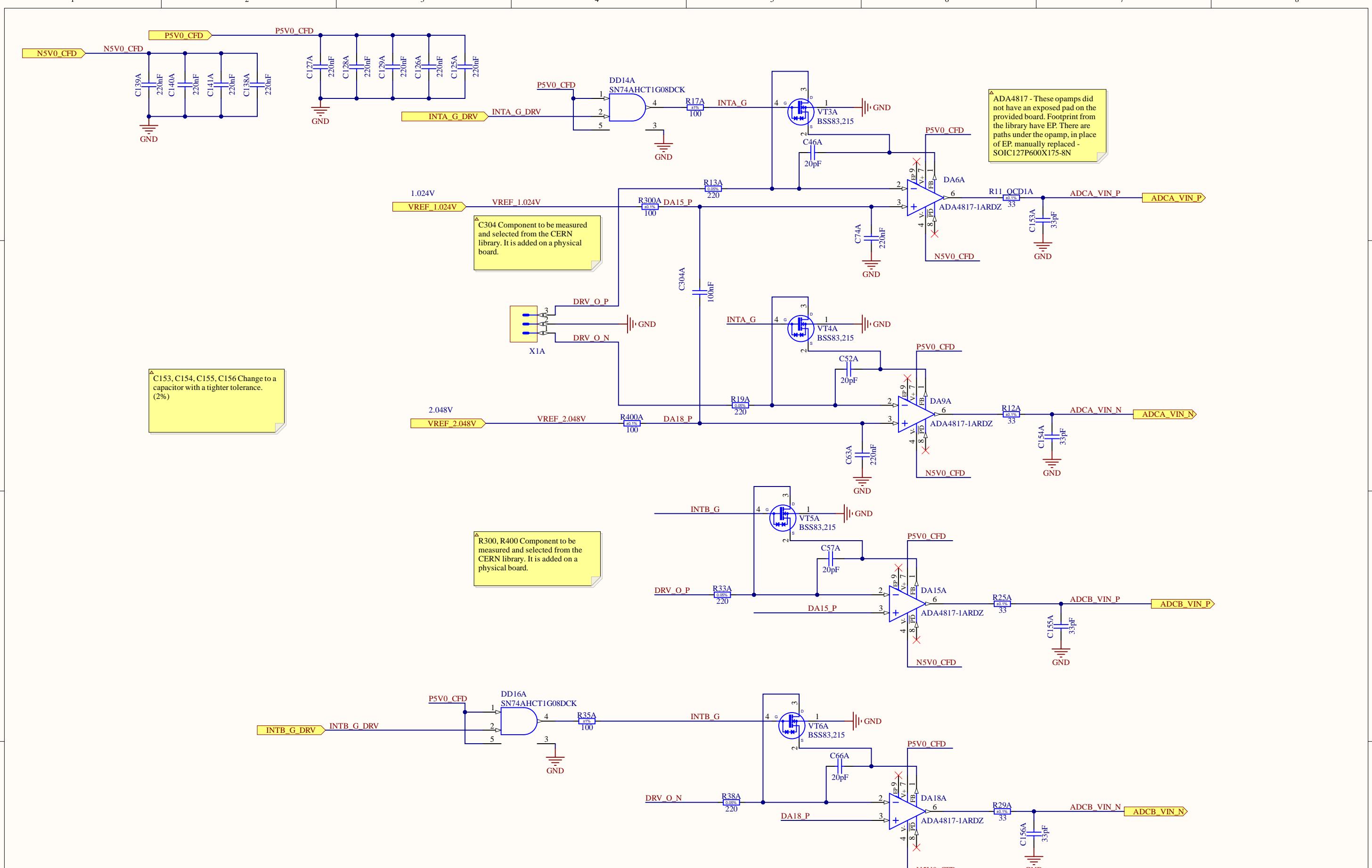
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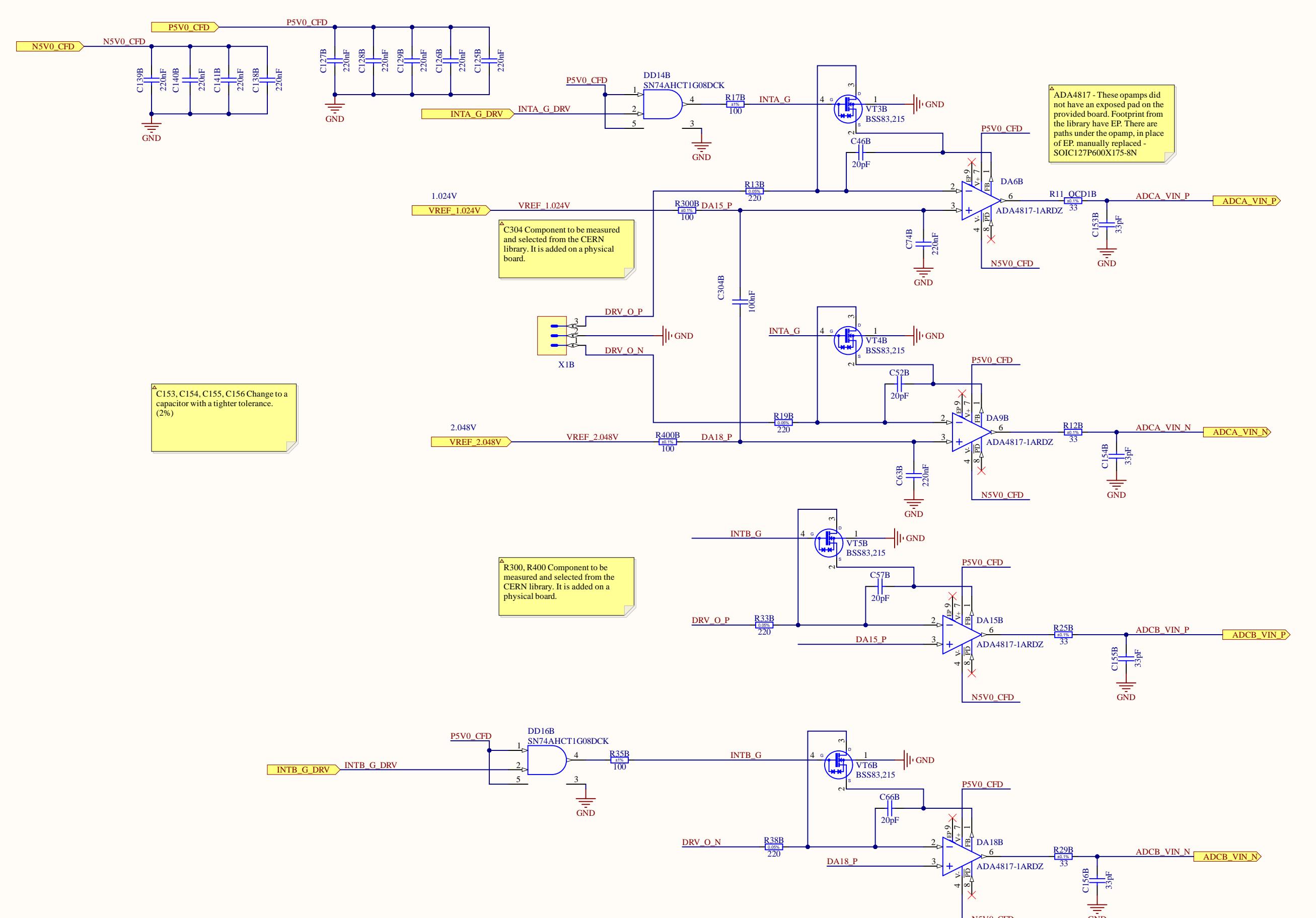




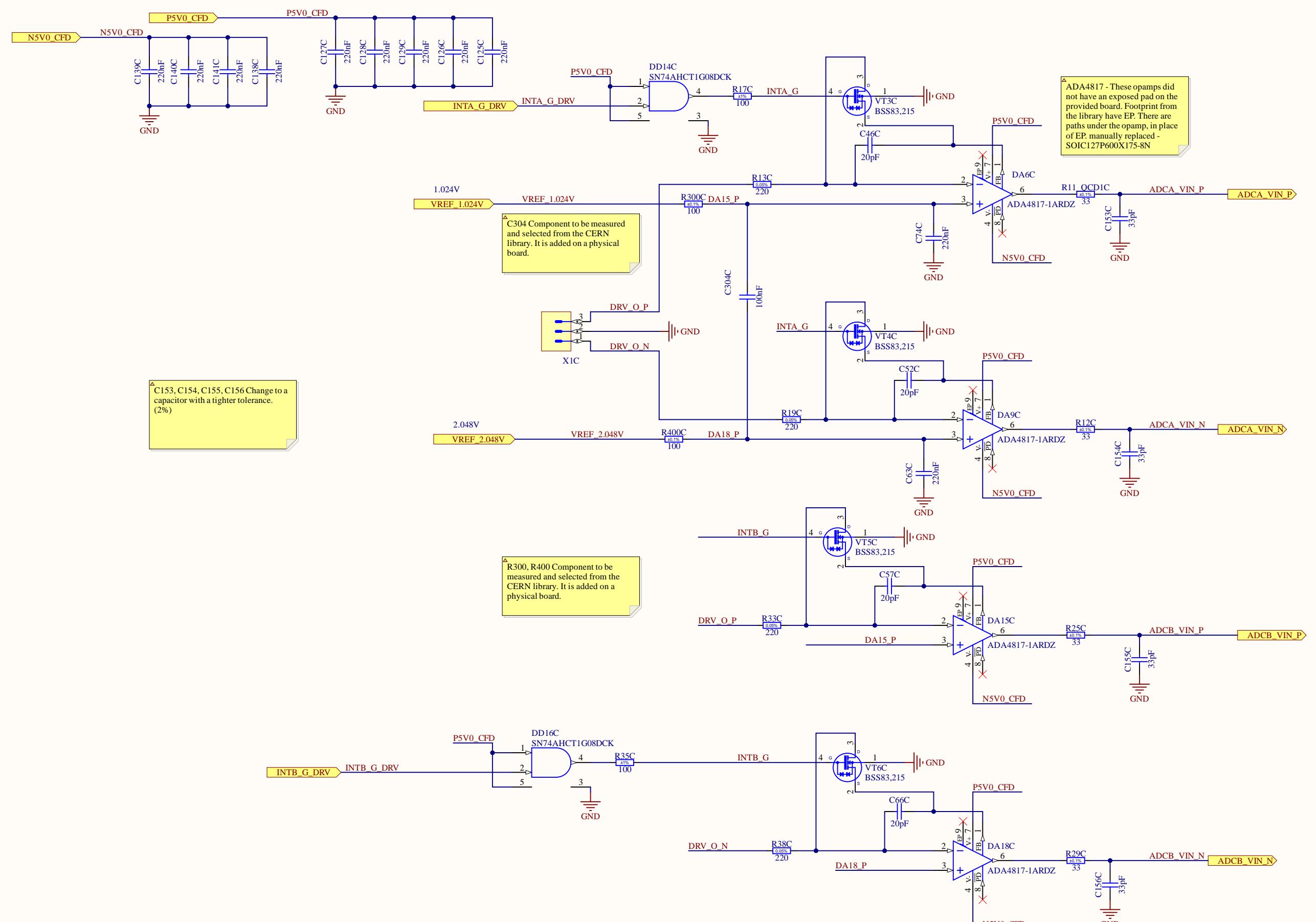


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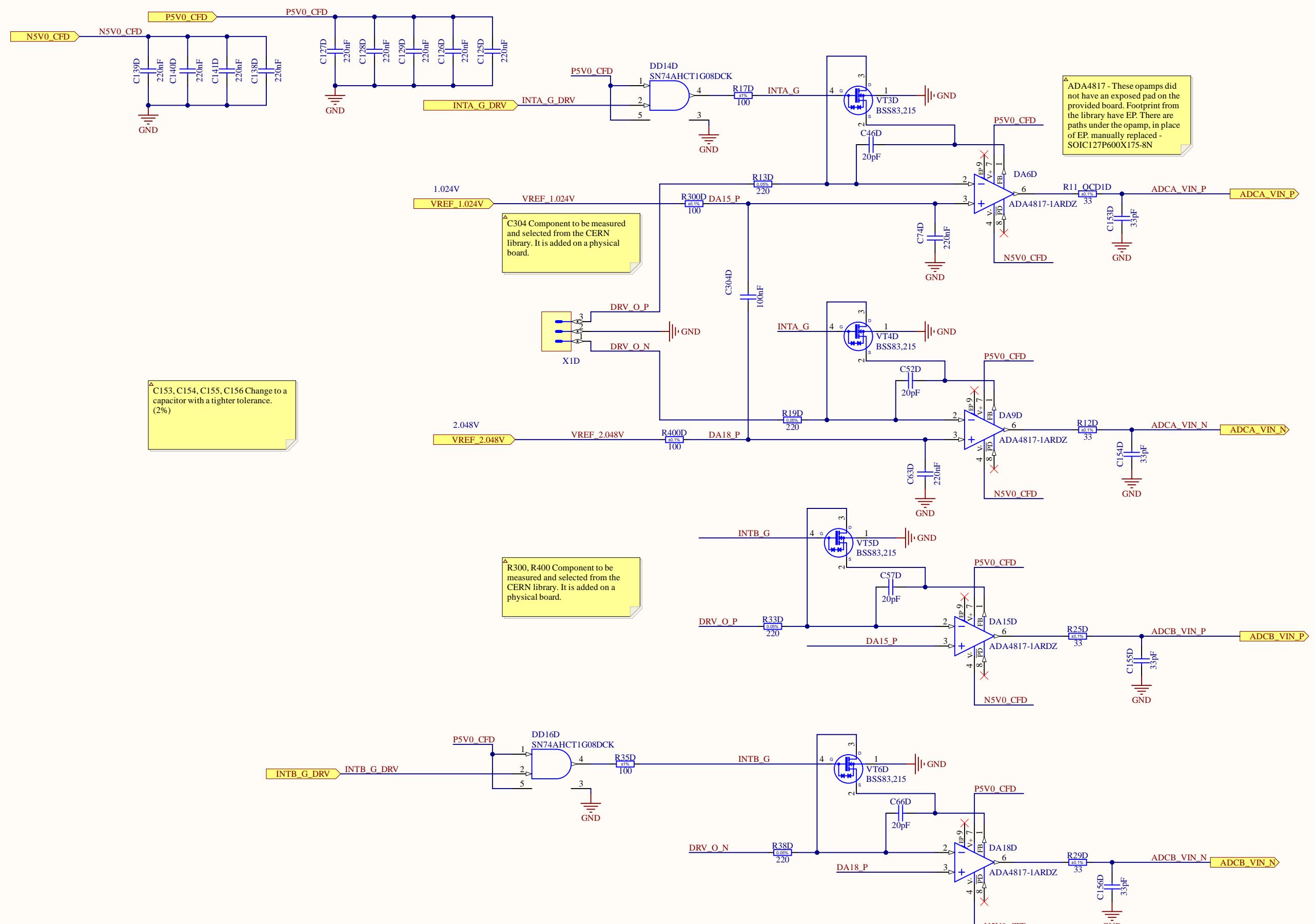
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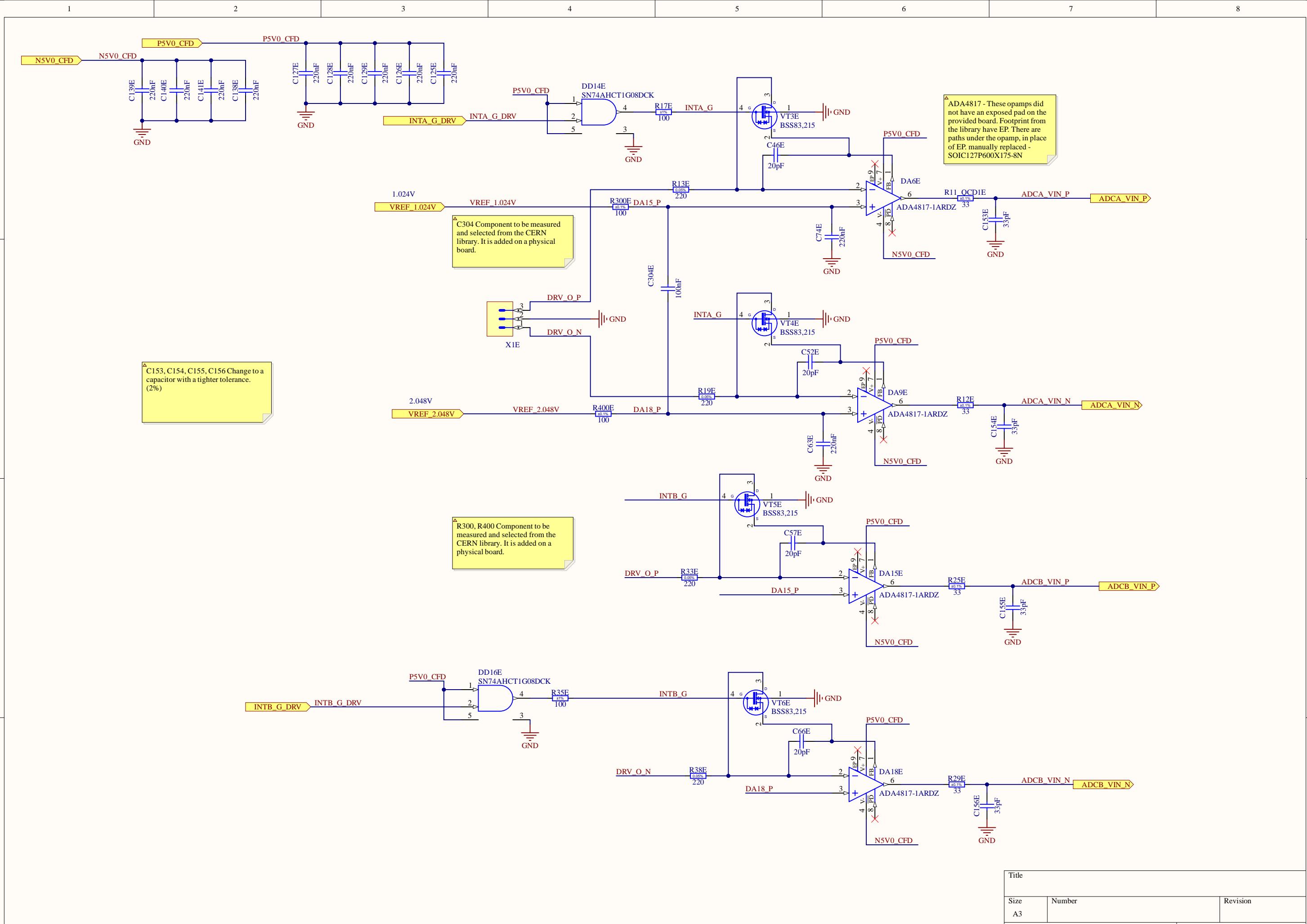
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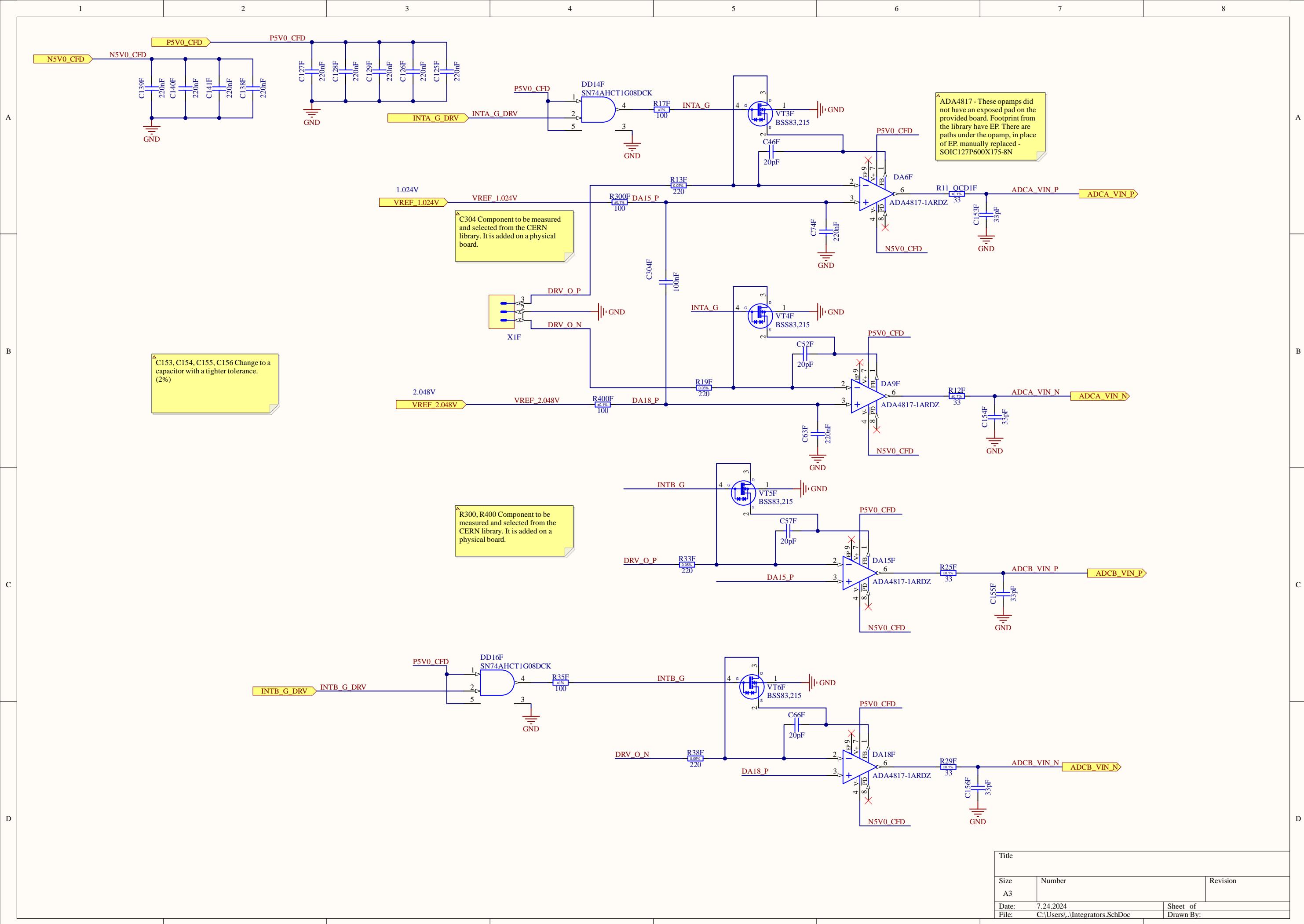


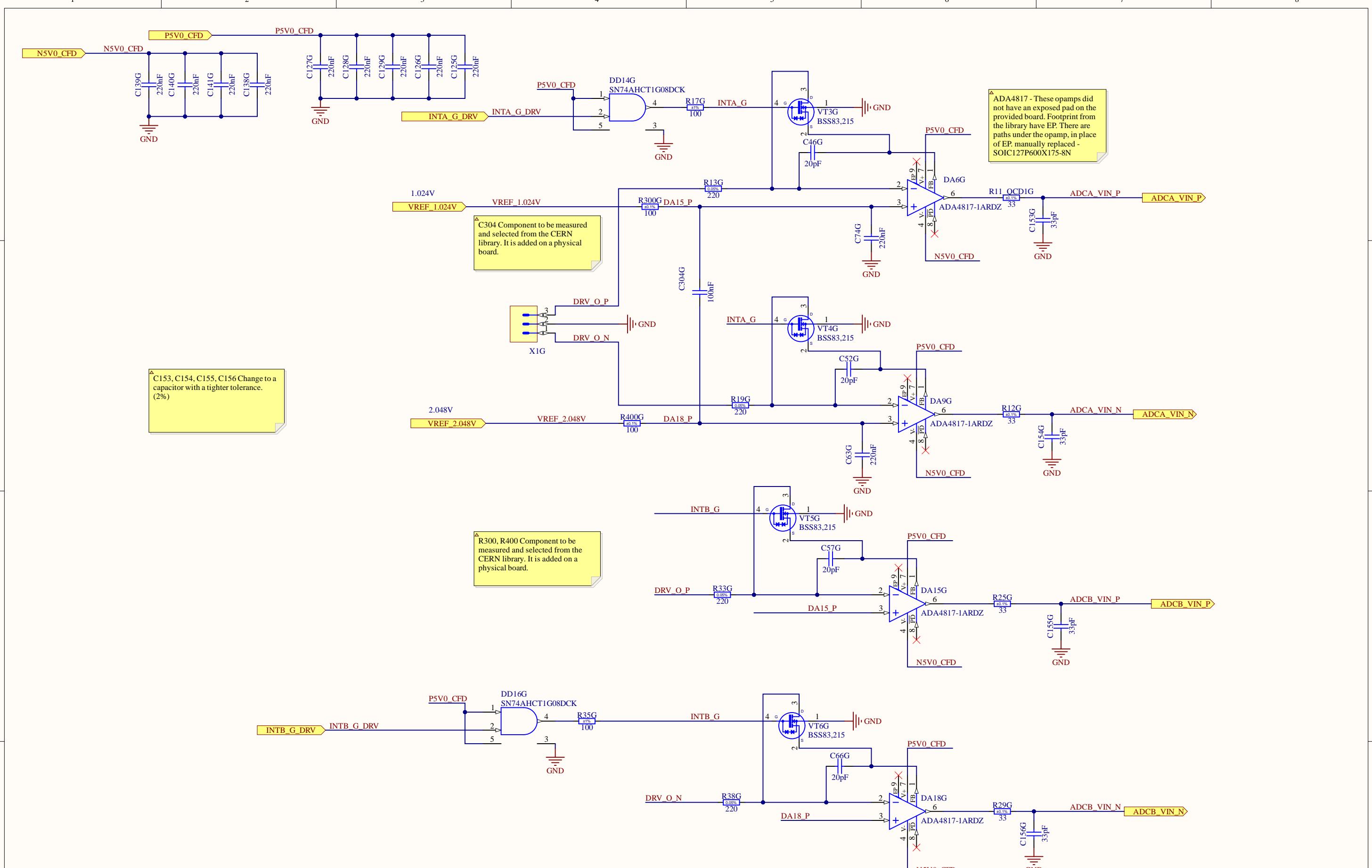
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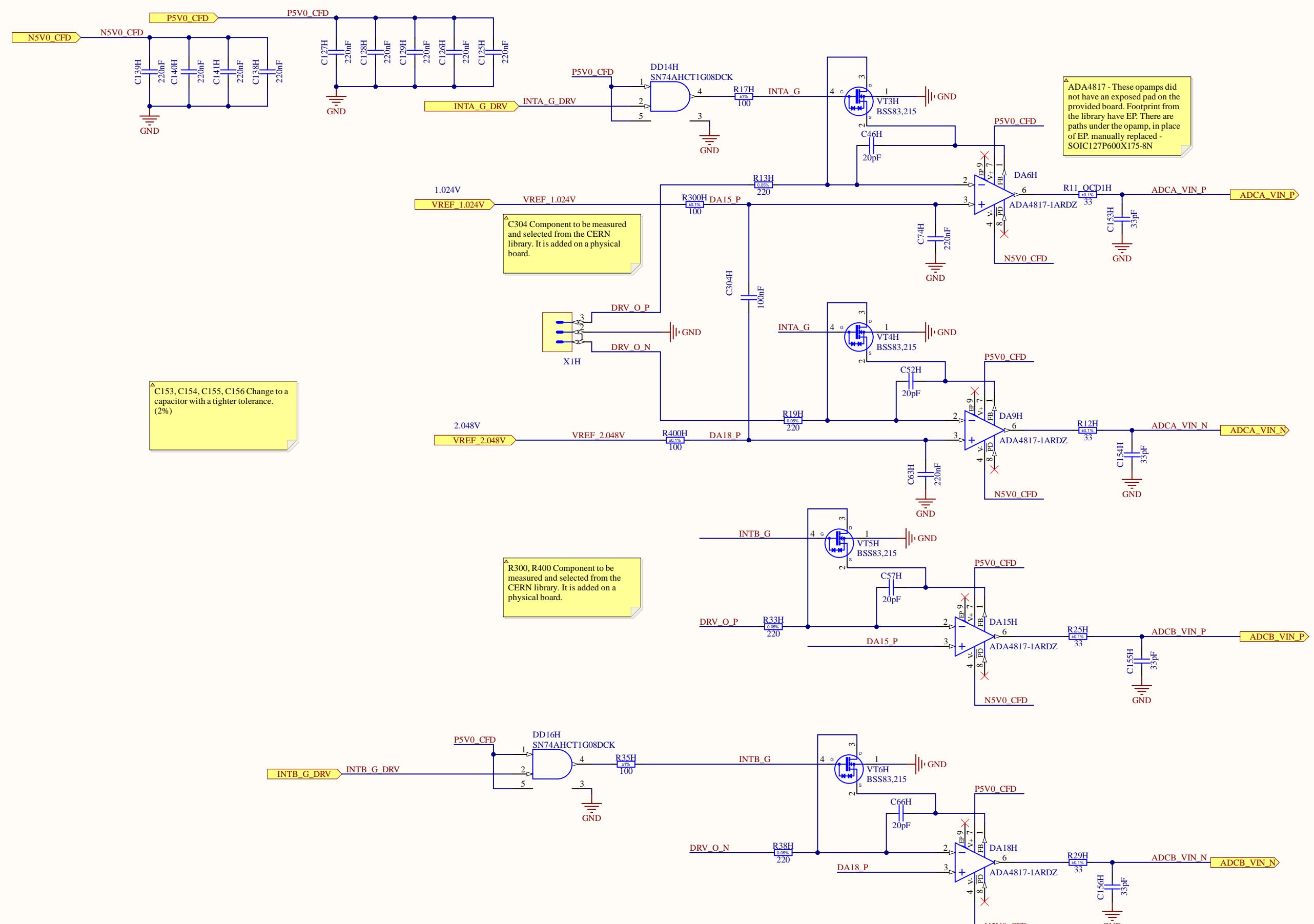
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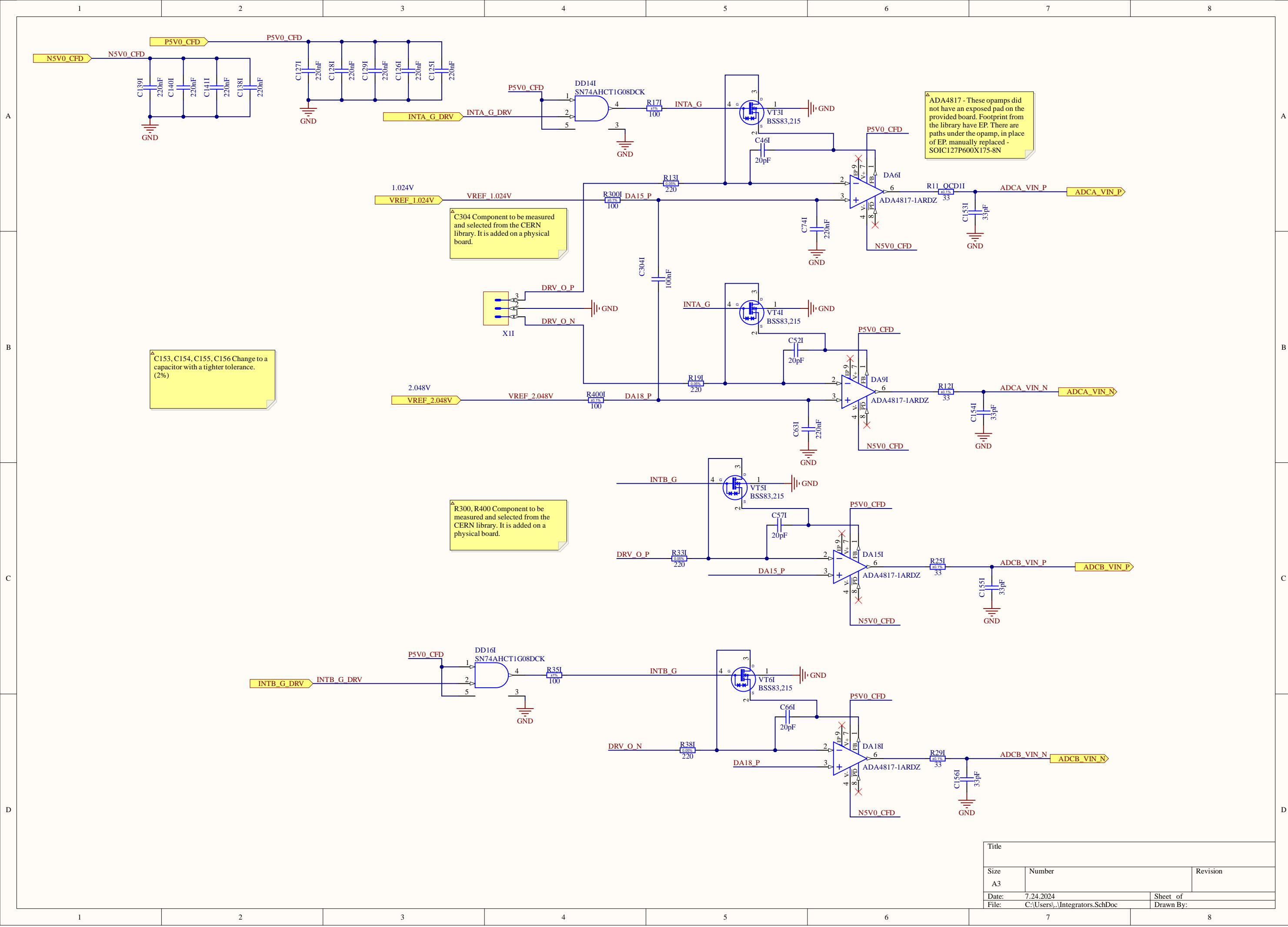
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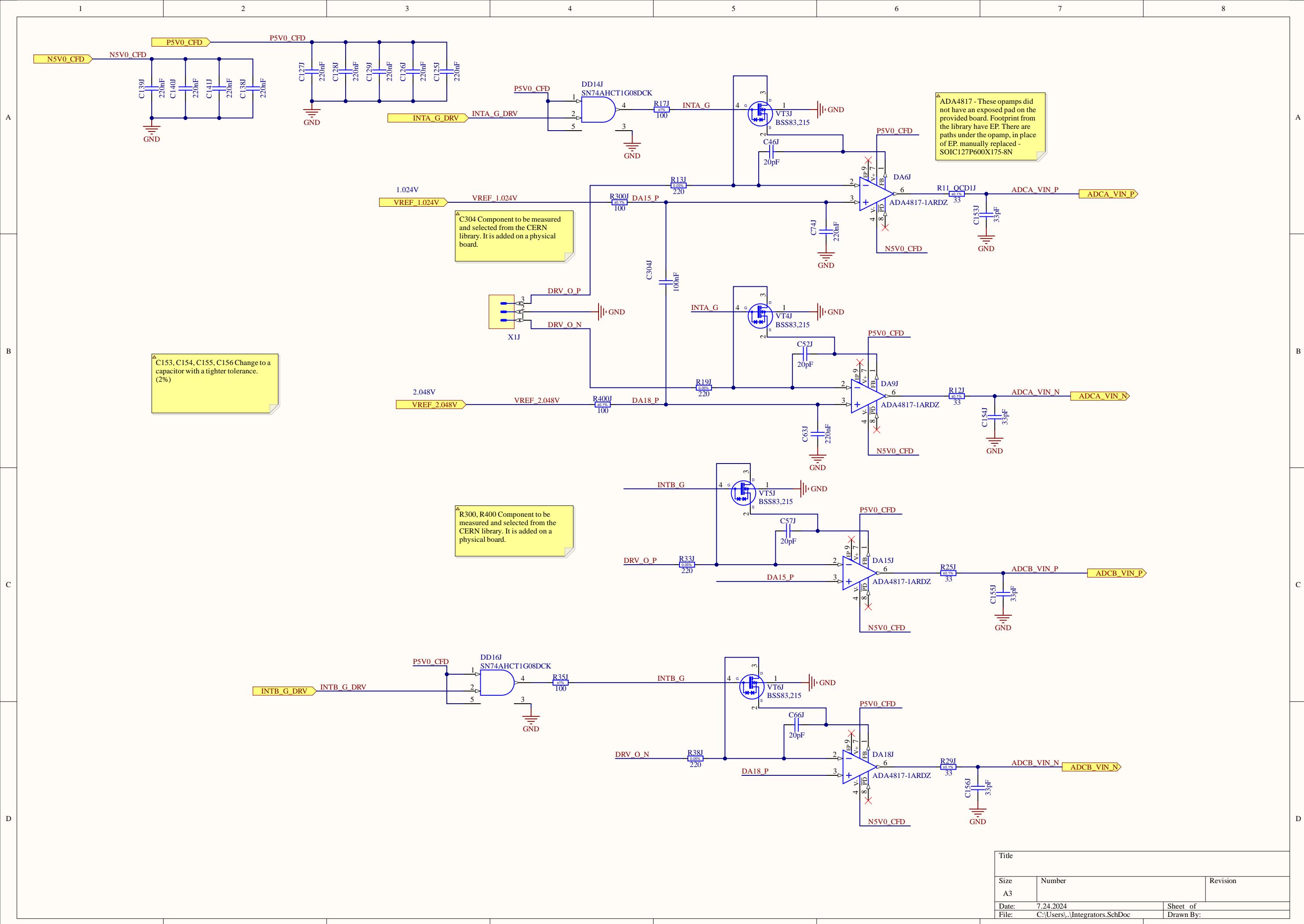
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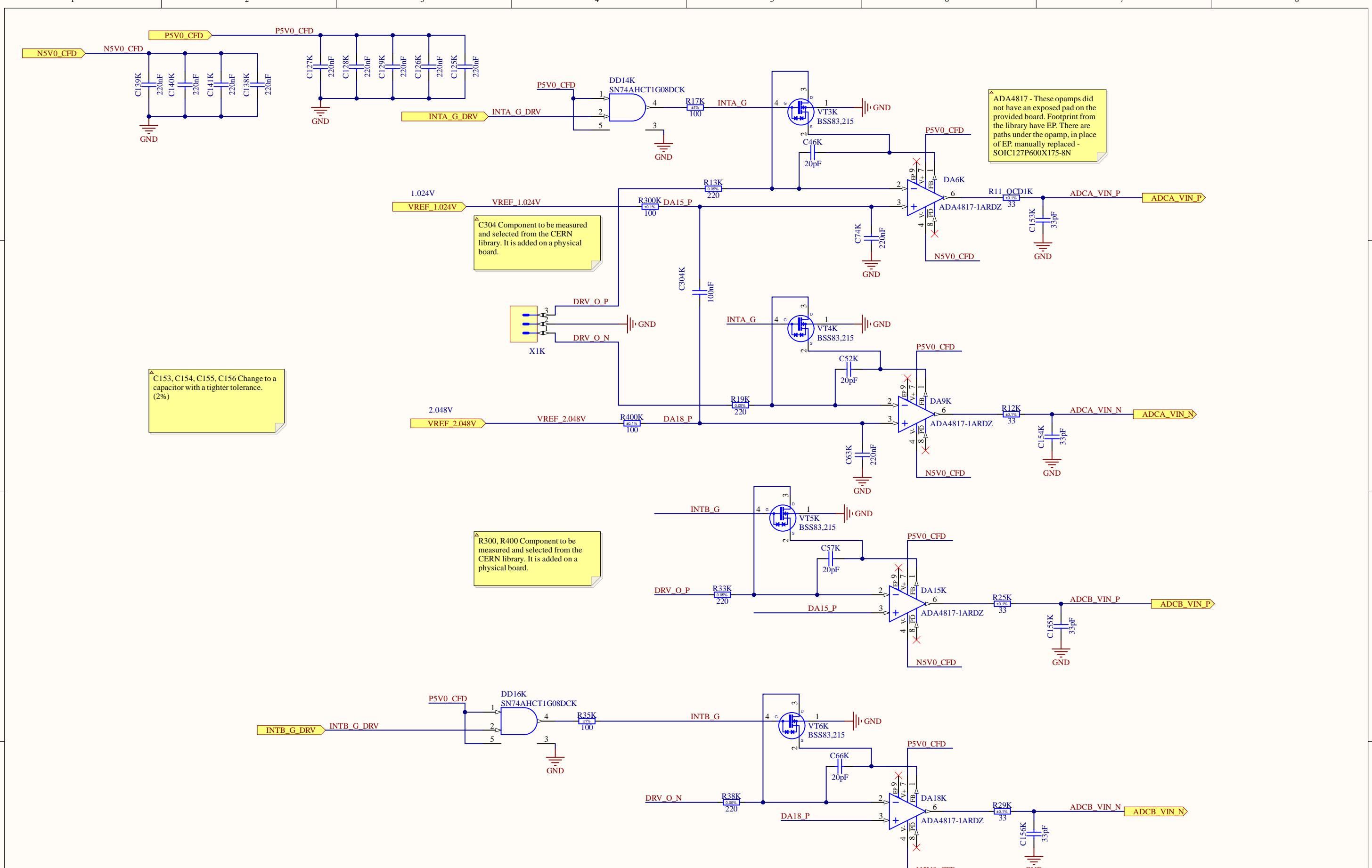
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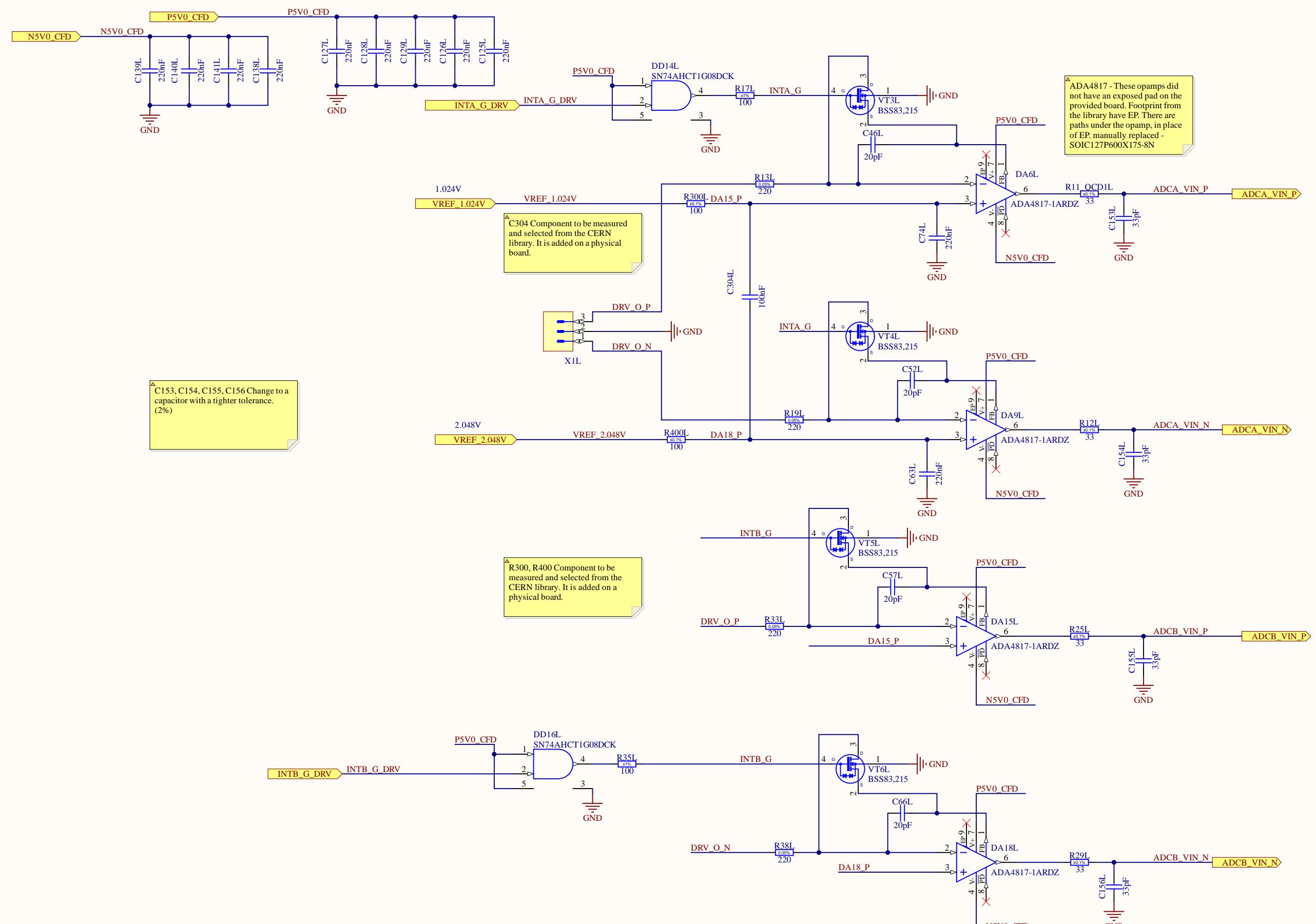




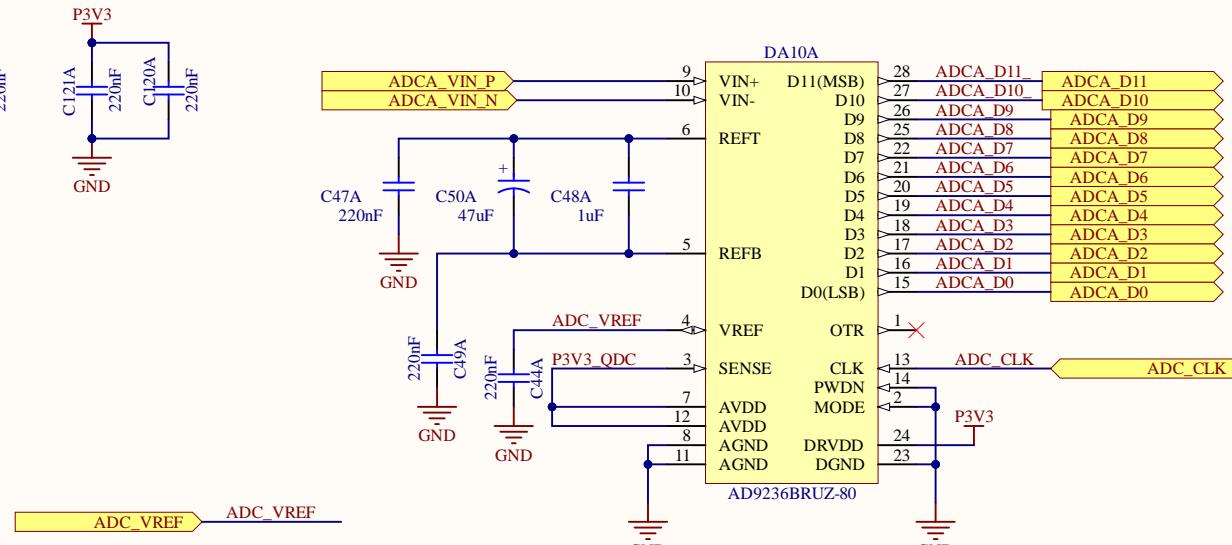
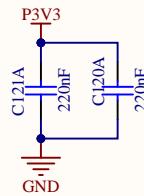
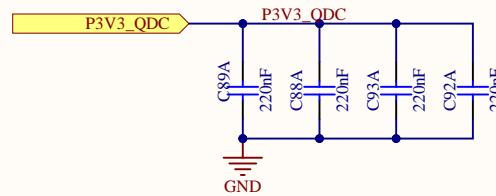


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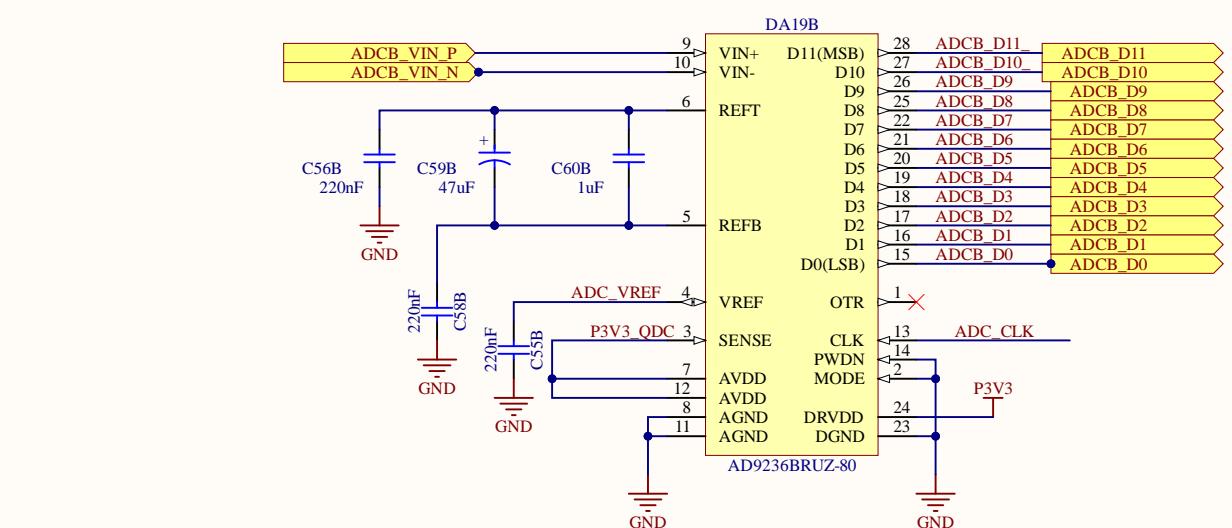
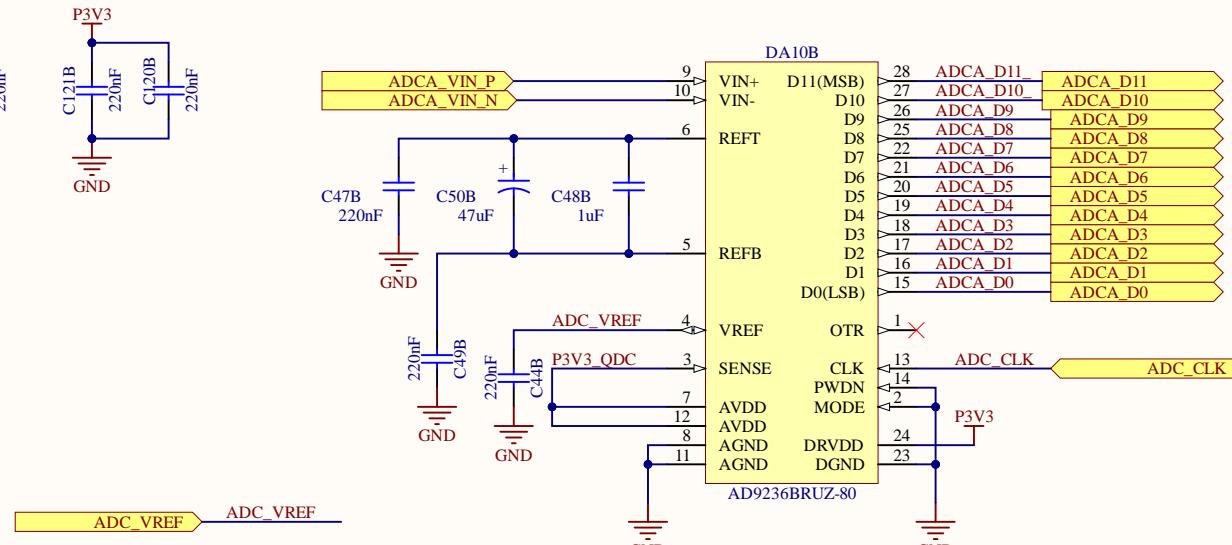
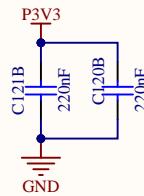
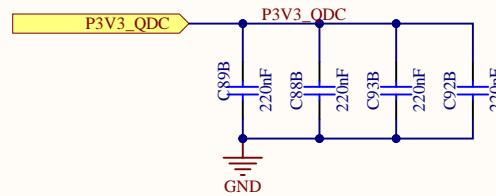
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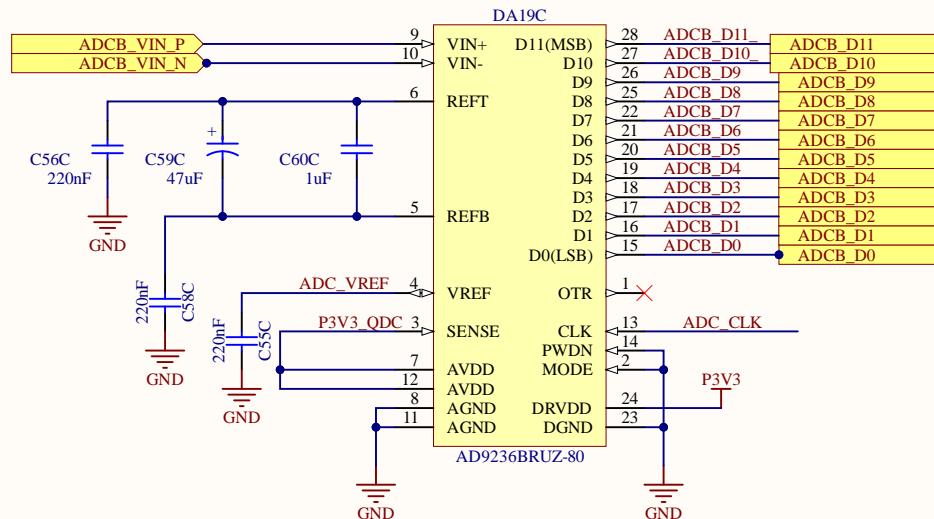
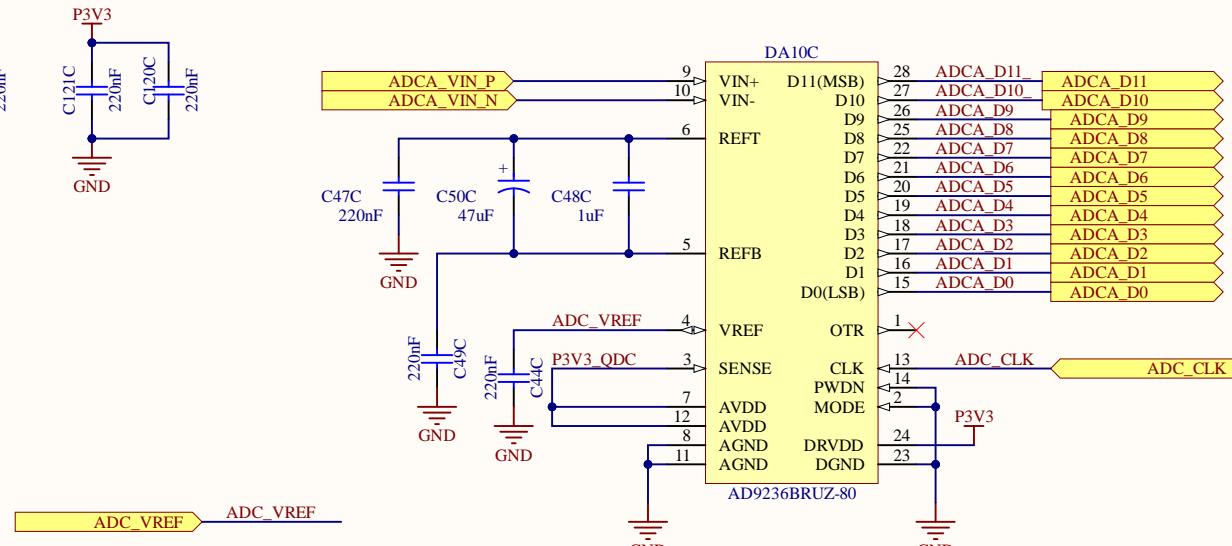
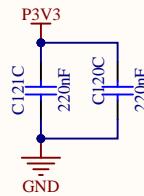
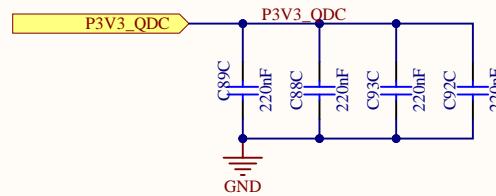
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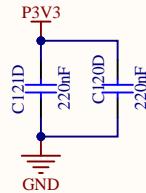
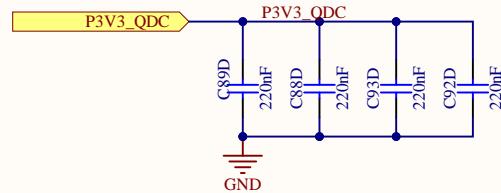
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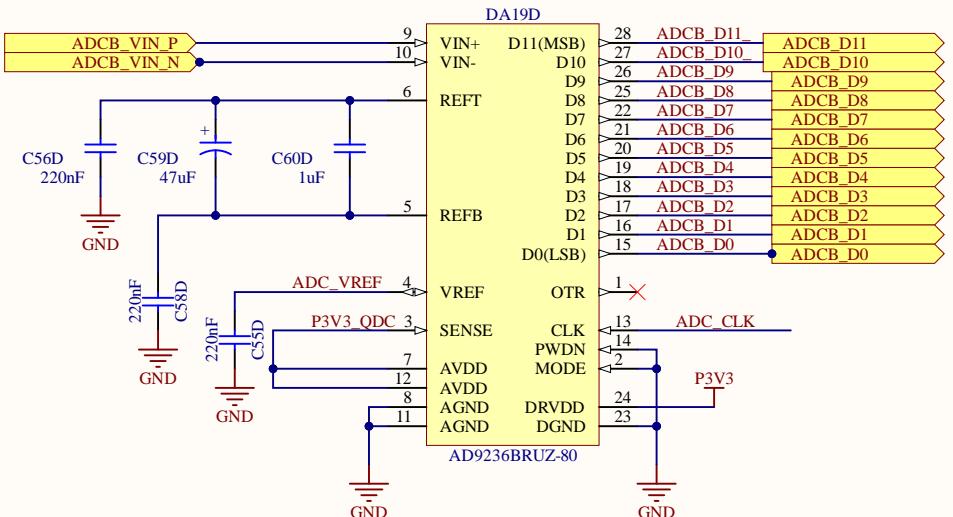
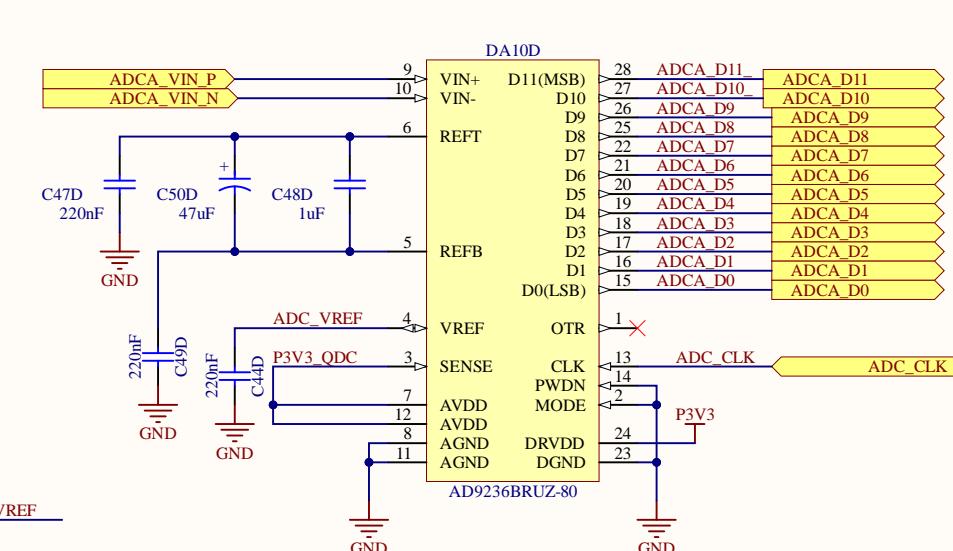
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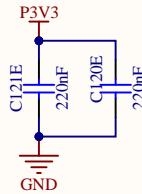
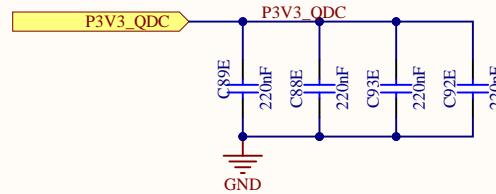
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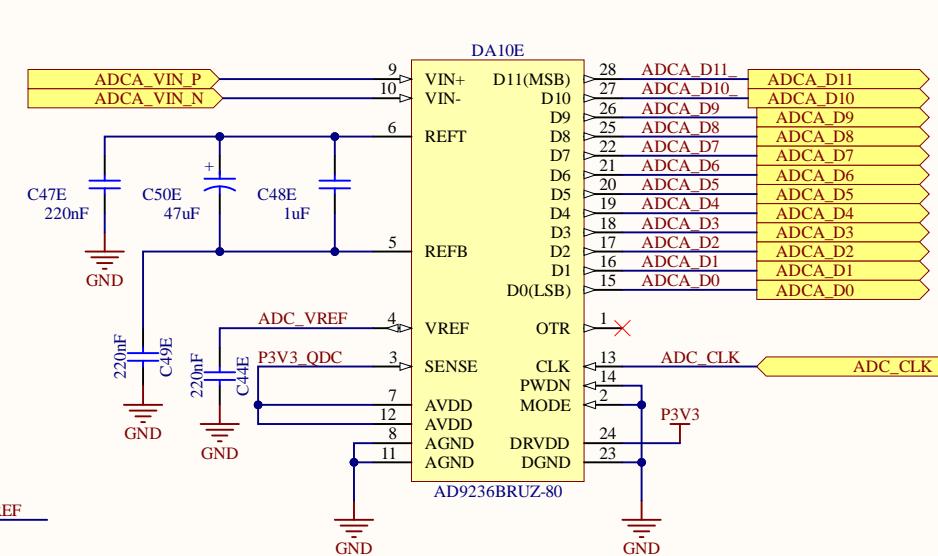
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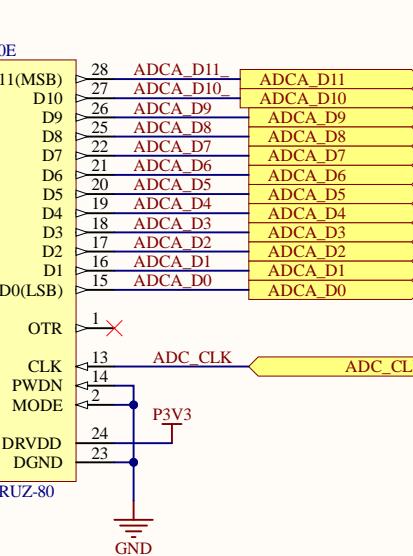


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DA10E

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REFBD	D8	25	ADCA_D8
VREF	D7	22	ADCA_D7
SENSE	D6	21	ADCA_D6
AVDD	D5	19	ADCA_D5
AVDD	D4	18	ADCA_D4
AGND	D3	17	ADCA_D3
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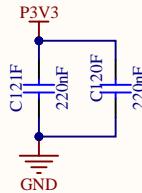
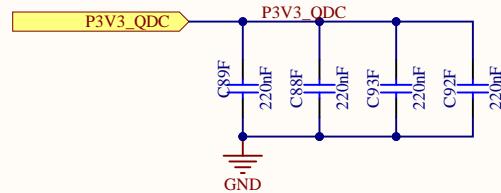


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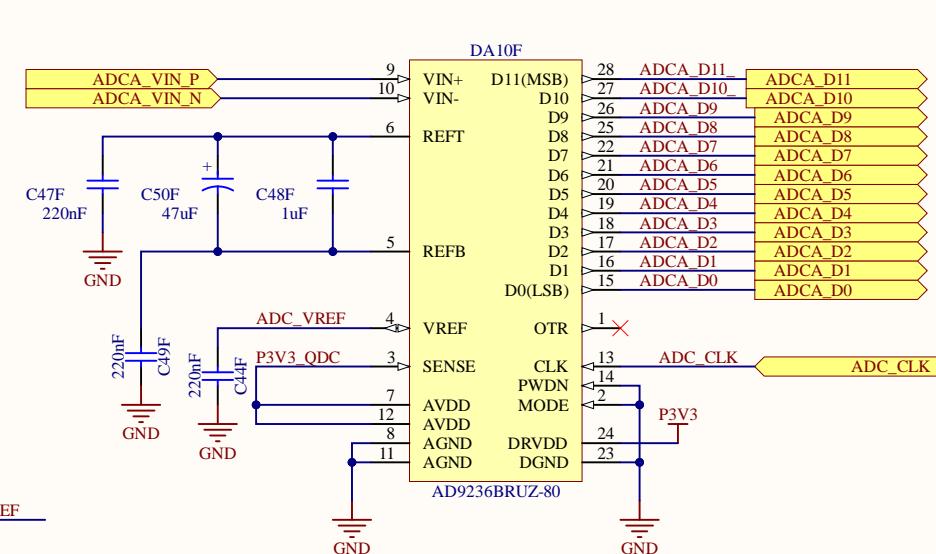
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AVDD	D4	19	ADCB_D4
AGND	D3	18	ADCB_D3
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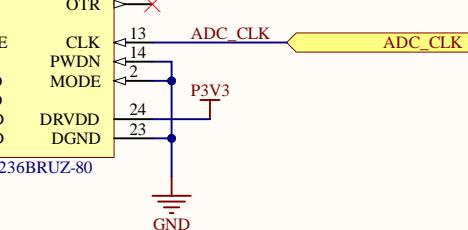
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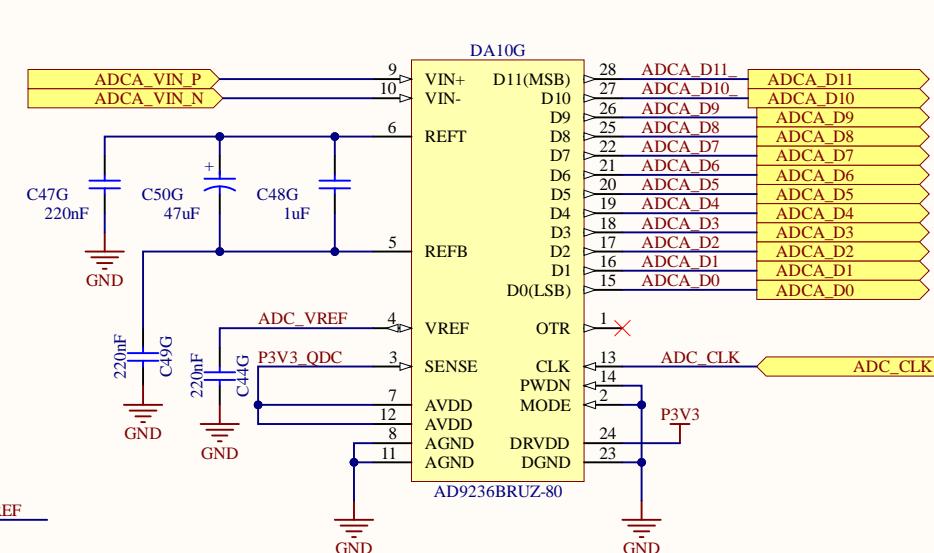
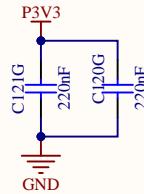
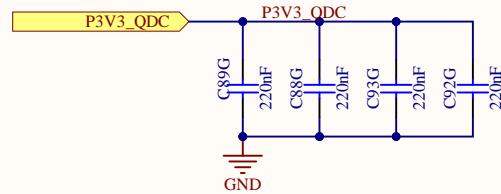


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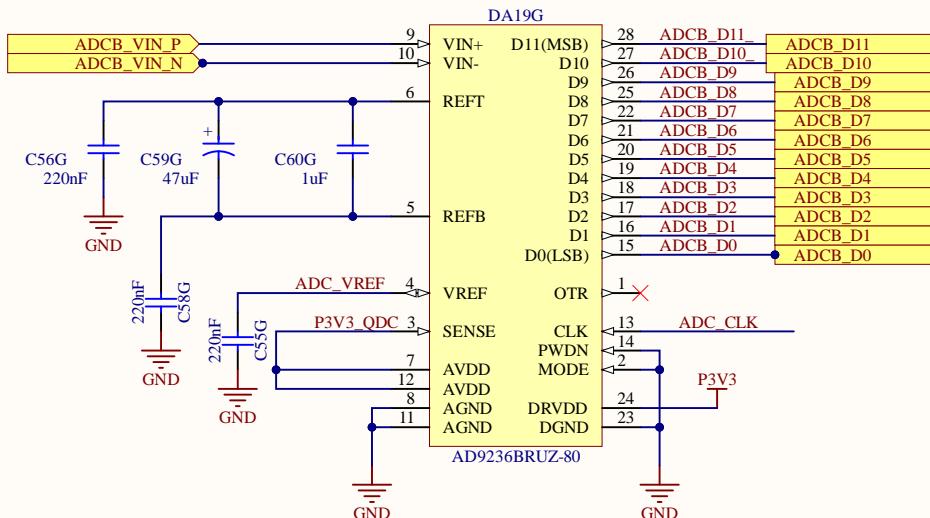


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19	ADCB_D4	ADCB_D4
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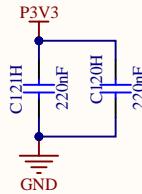
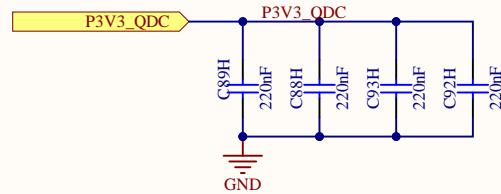
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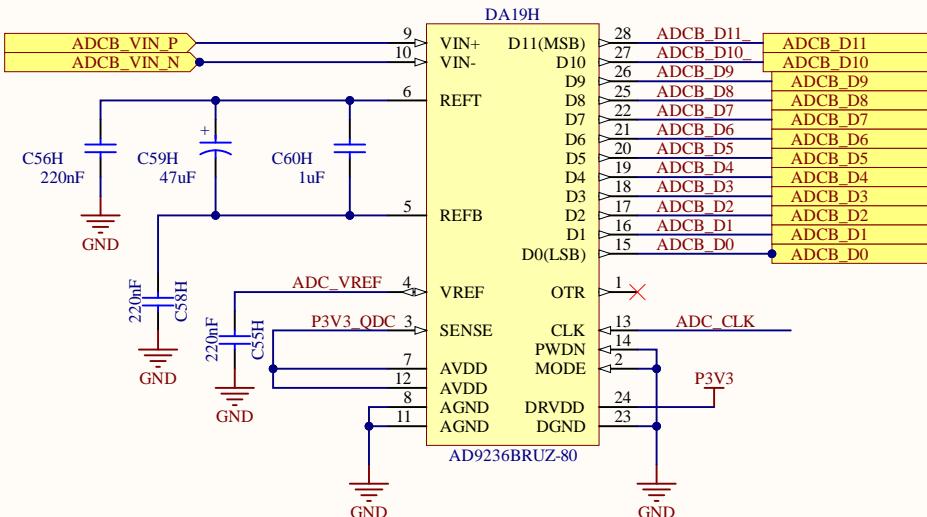
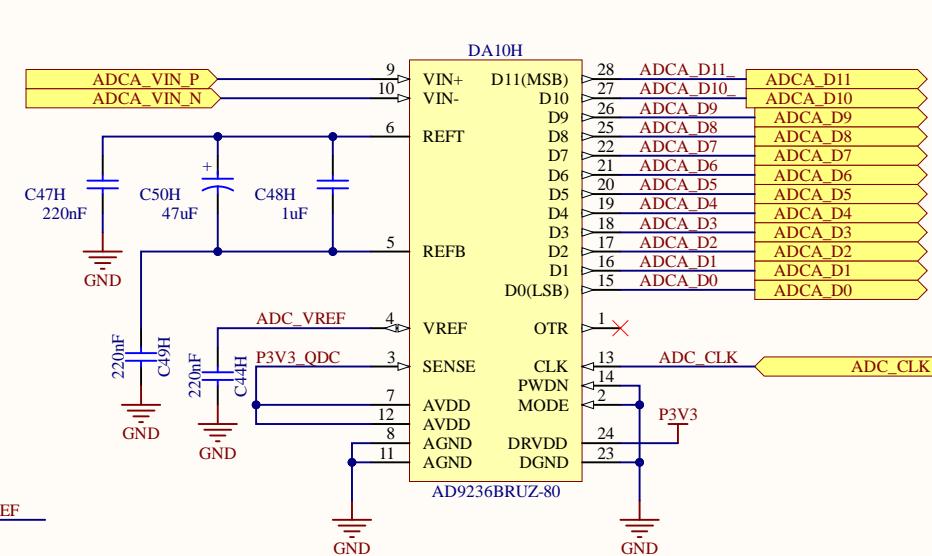
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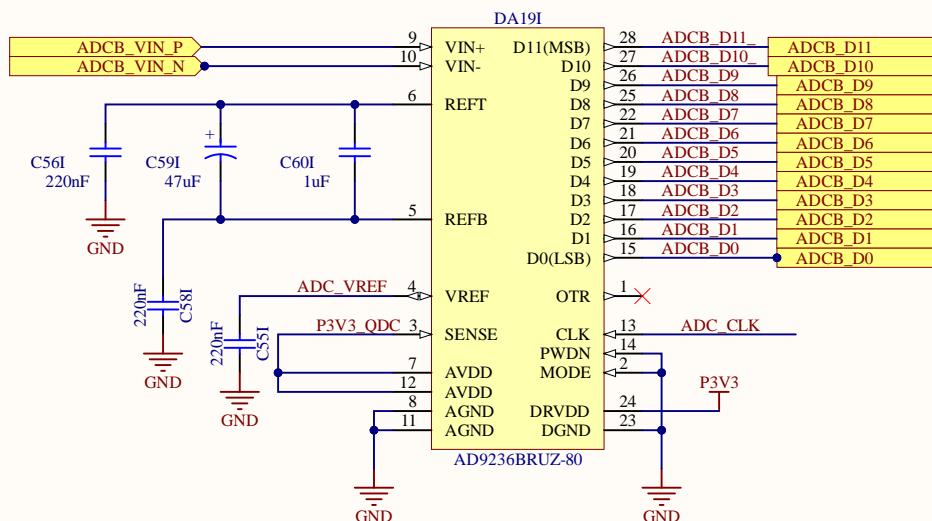
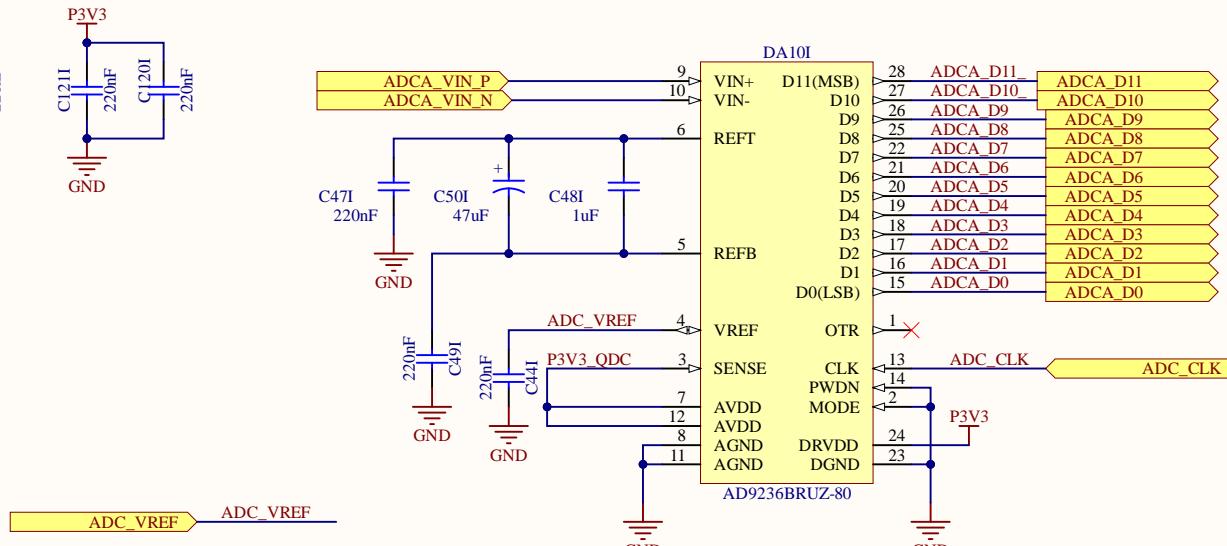
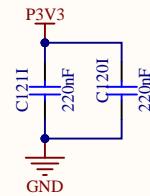
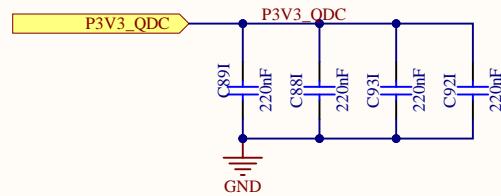
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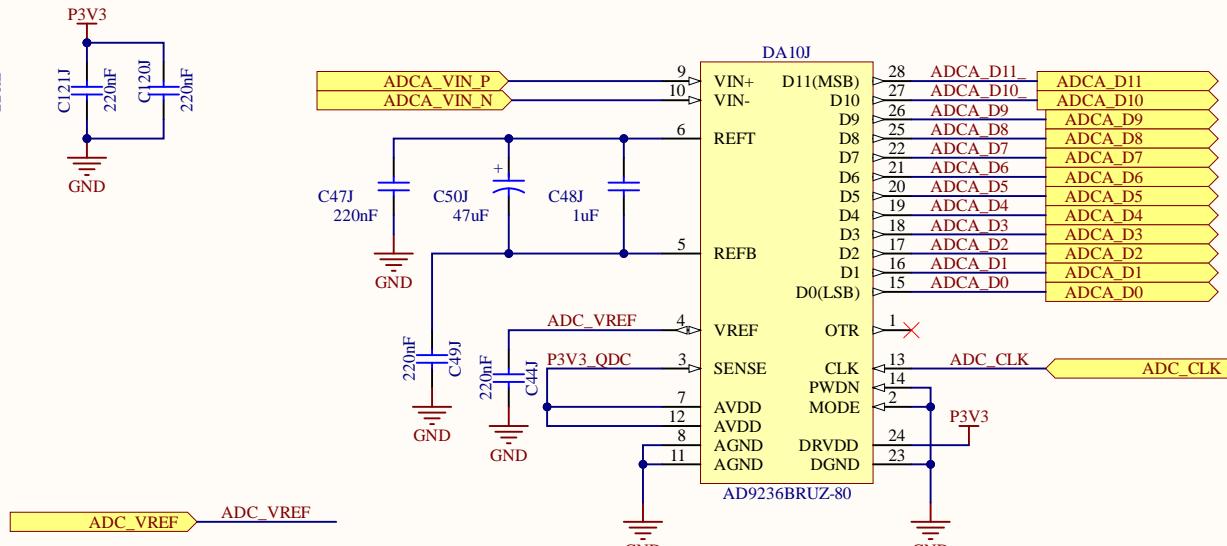
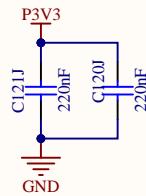
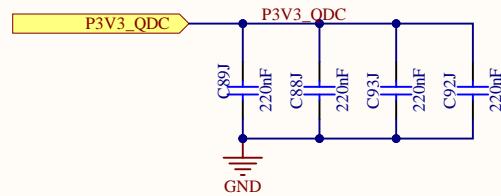
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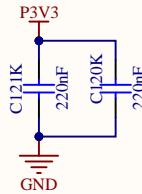
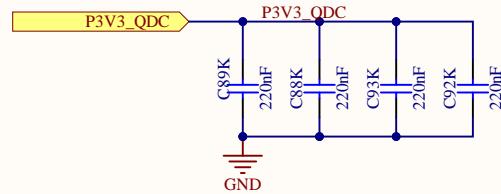
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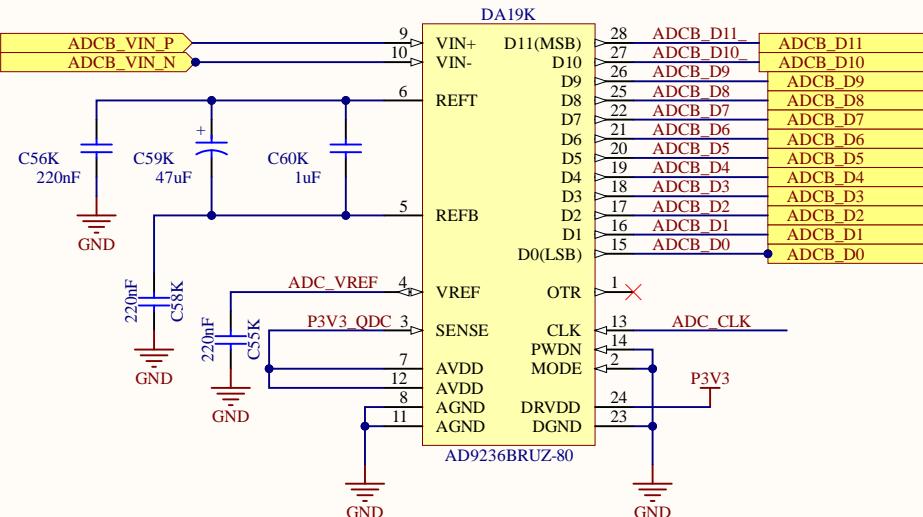
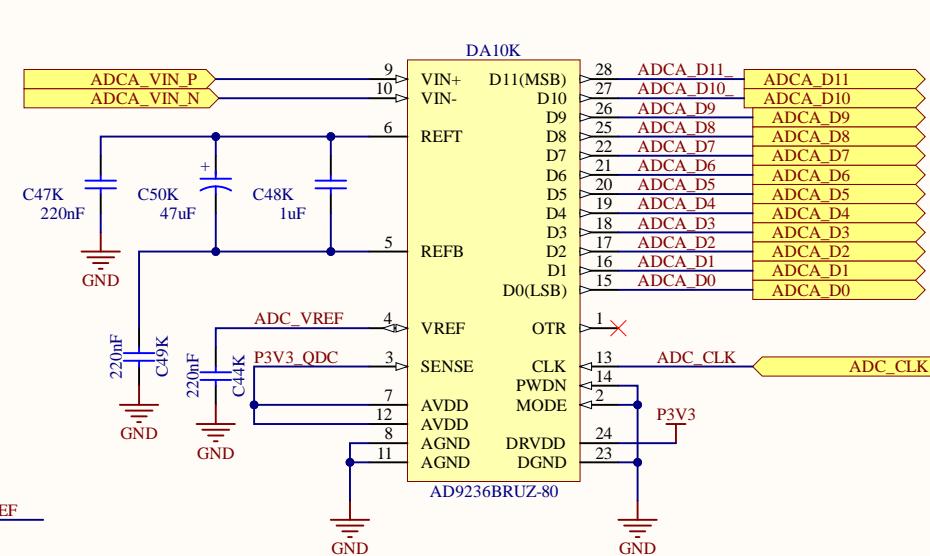
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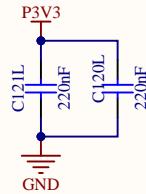
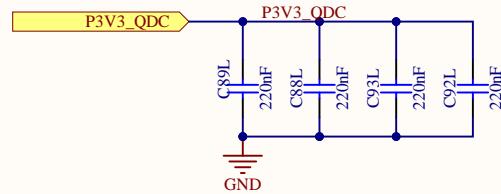
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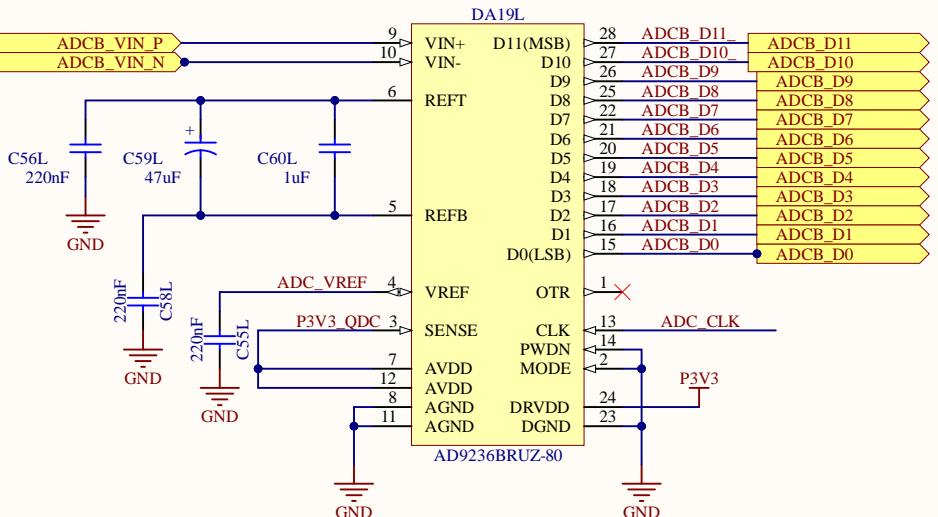
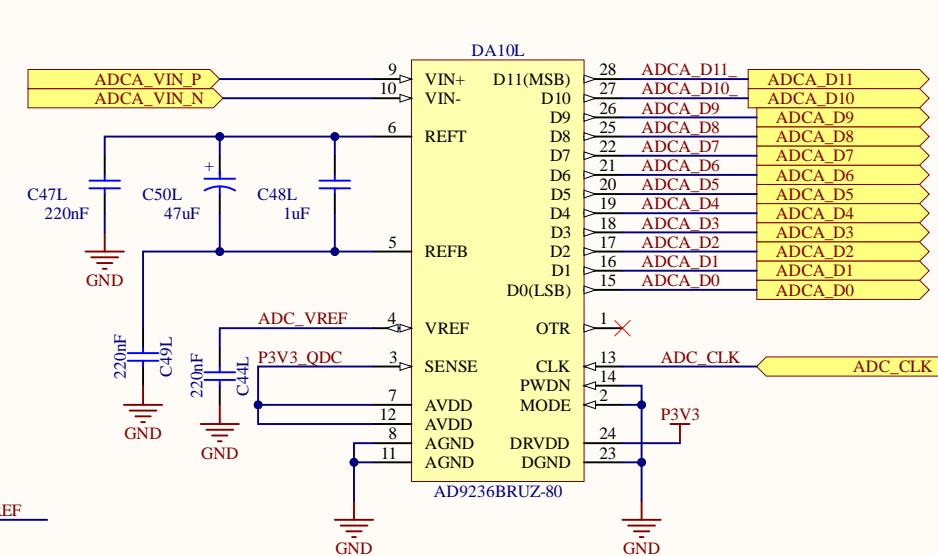
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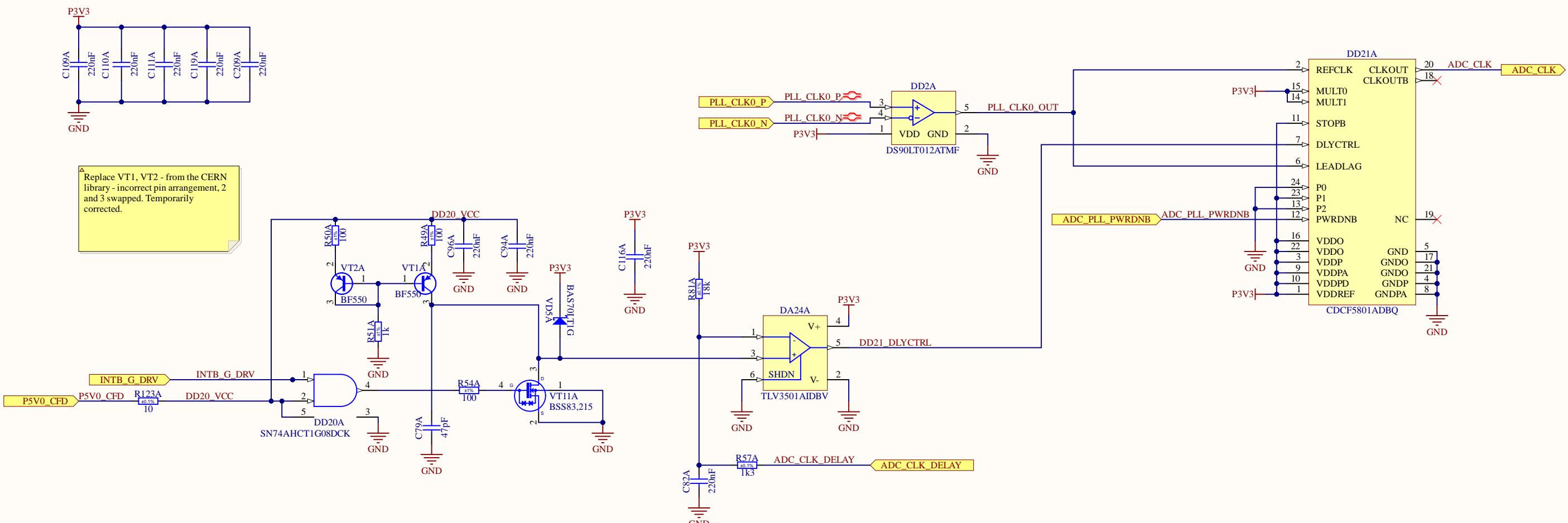
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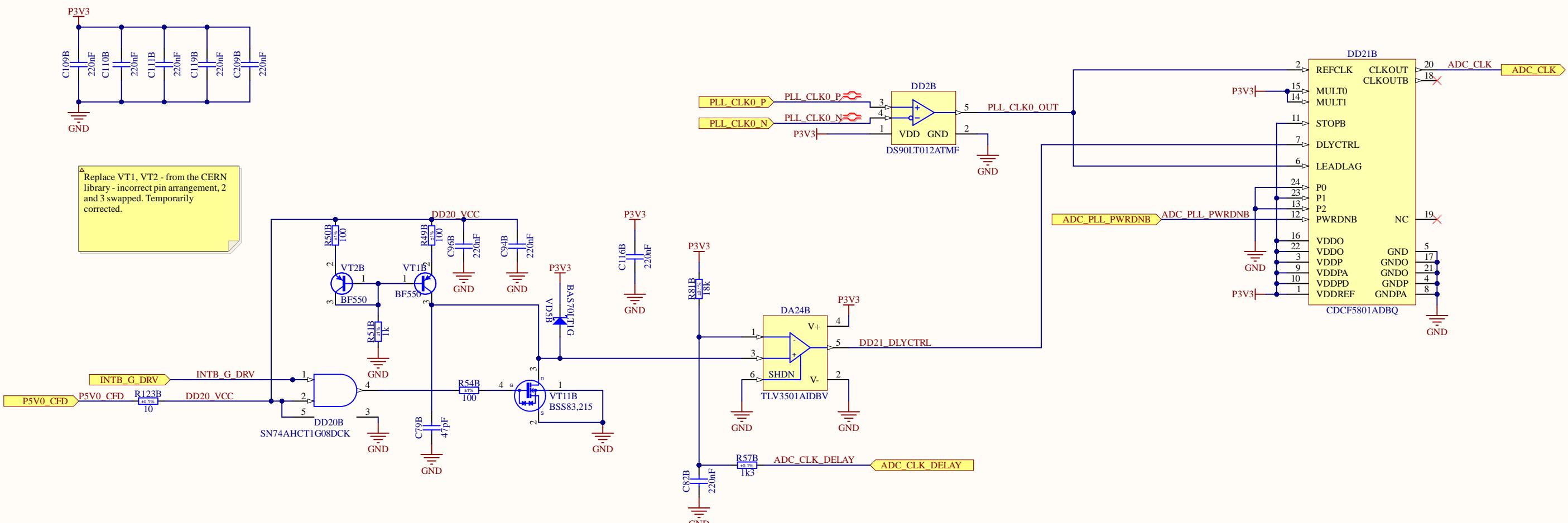
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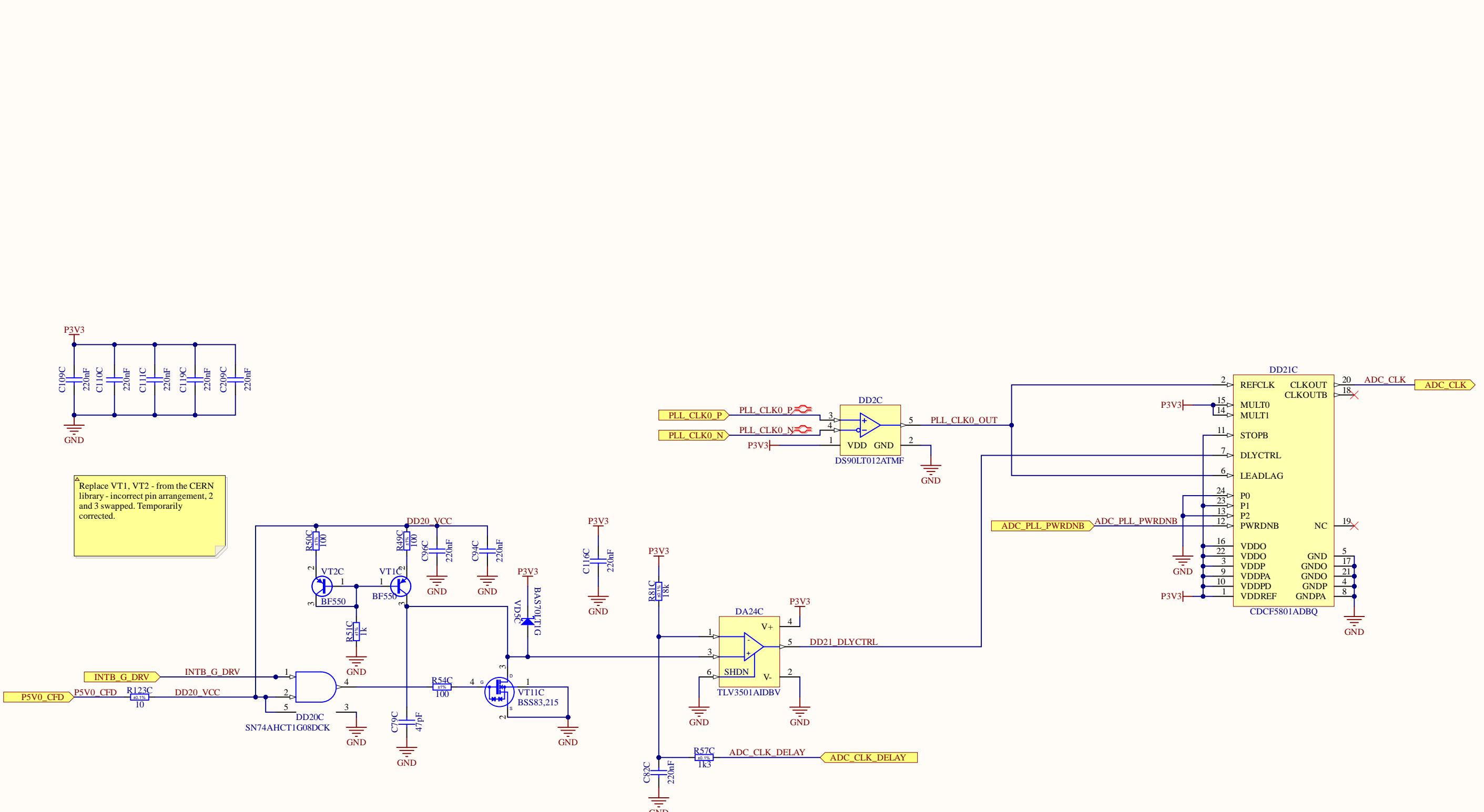
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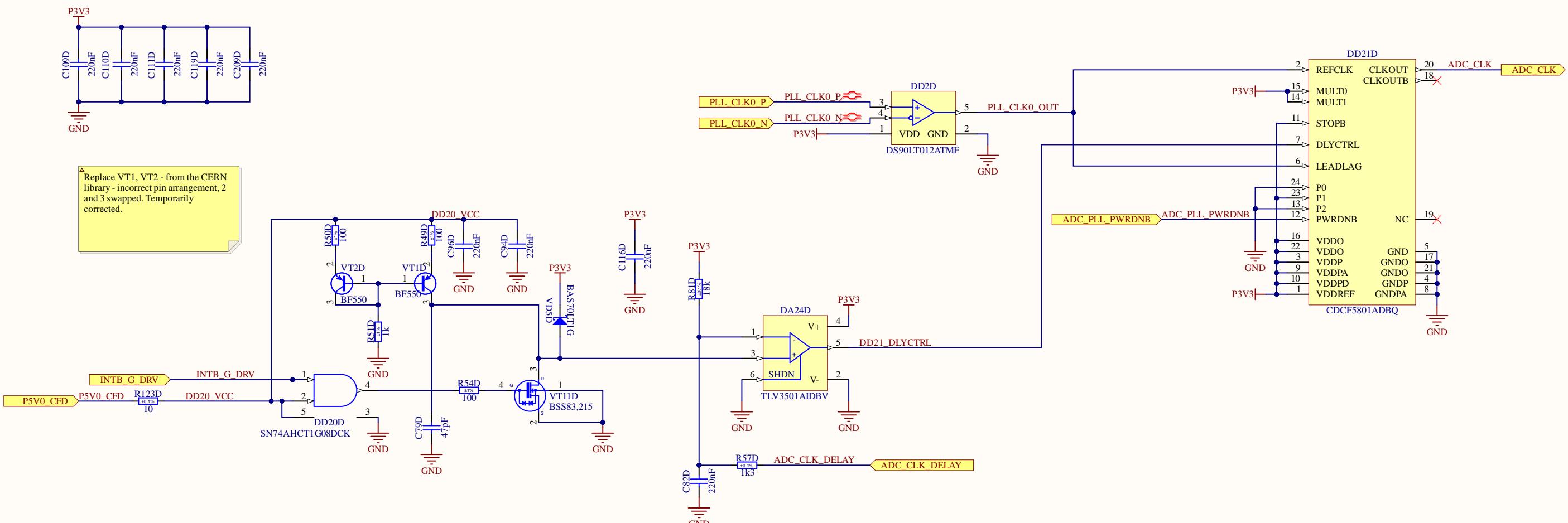
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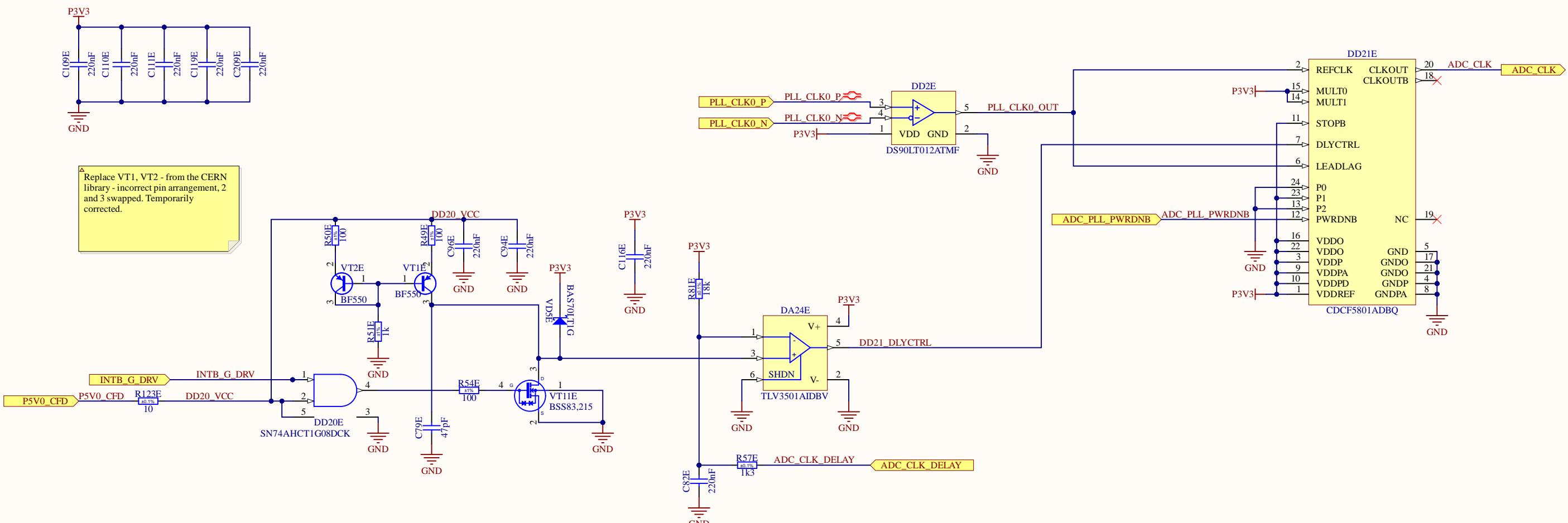
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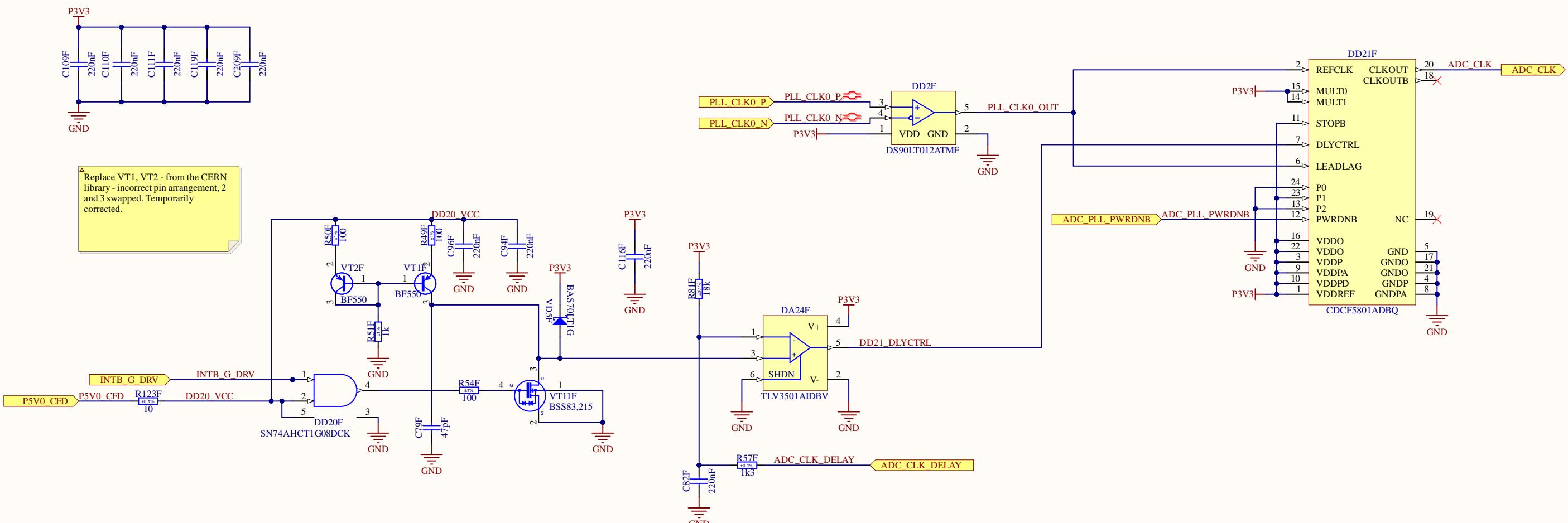
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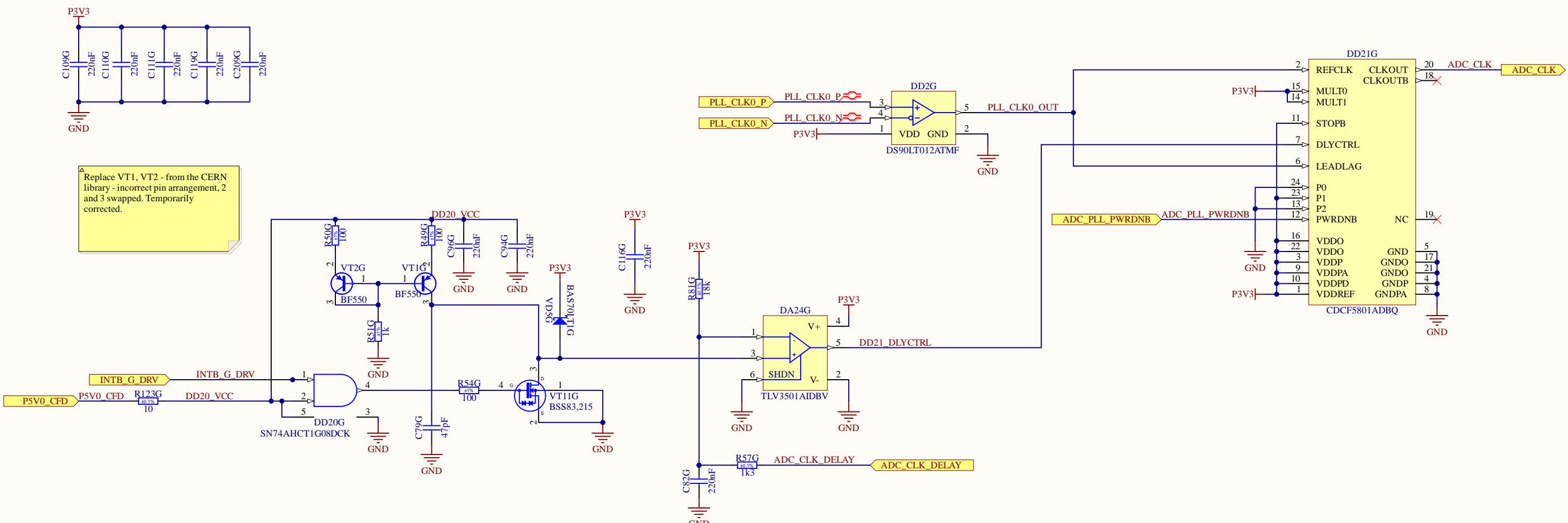
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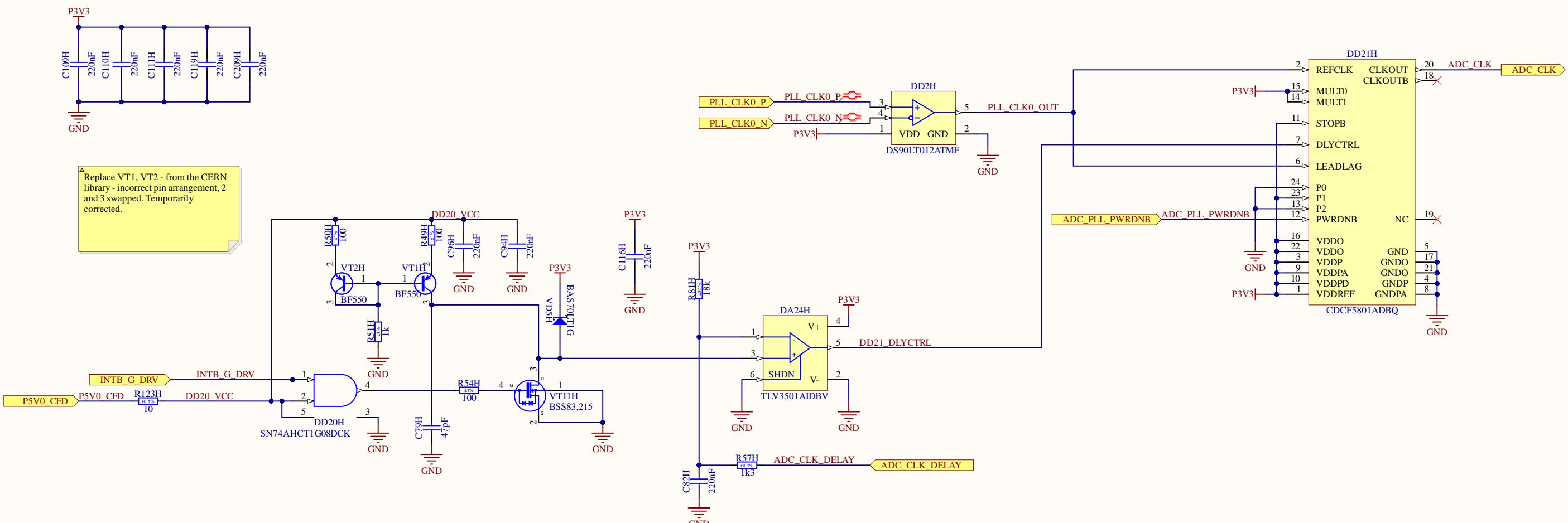
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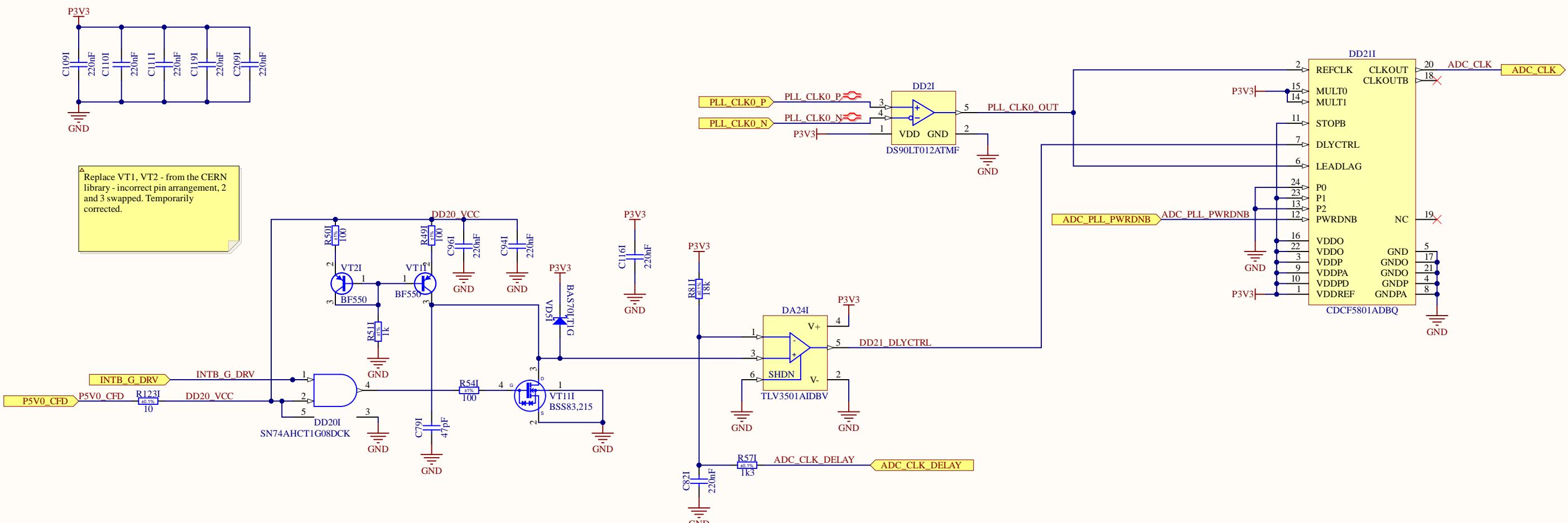
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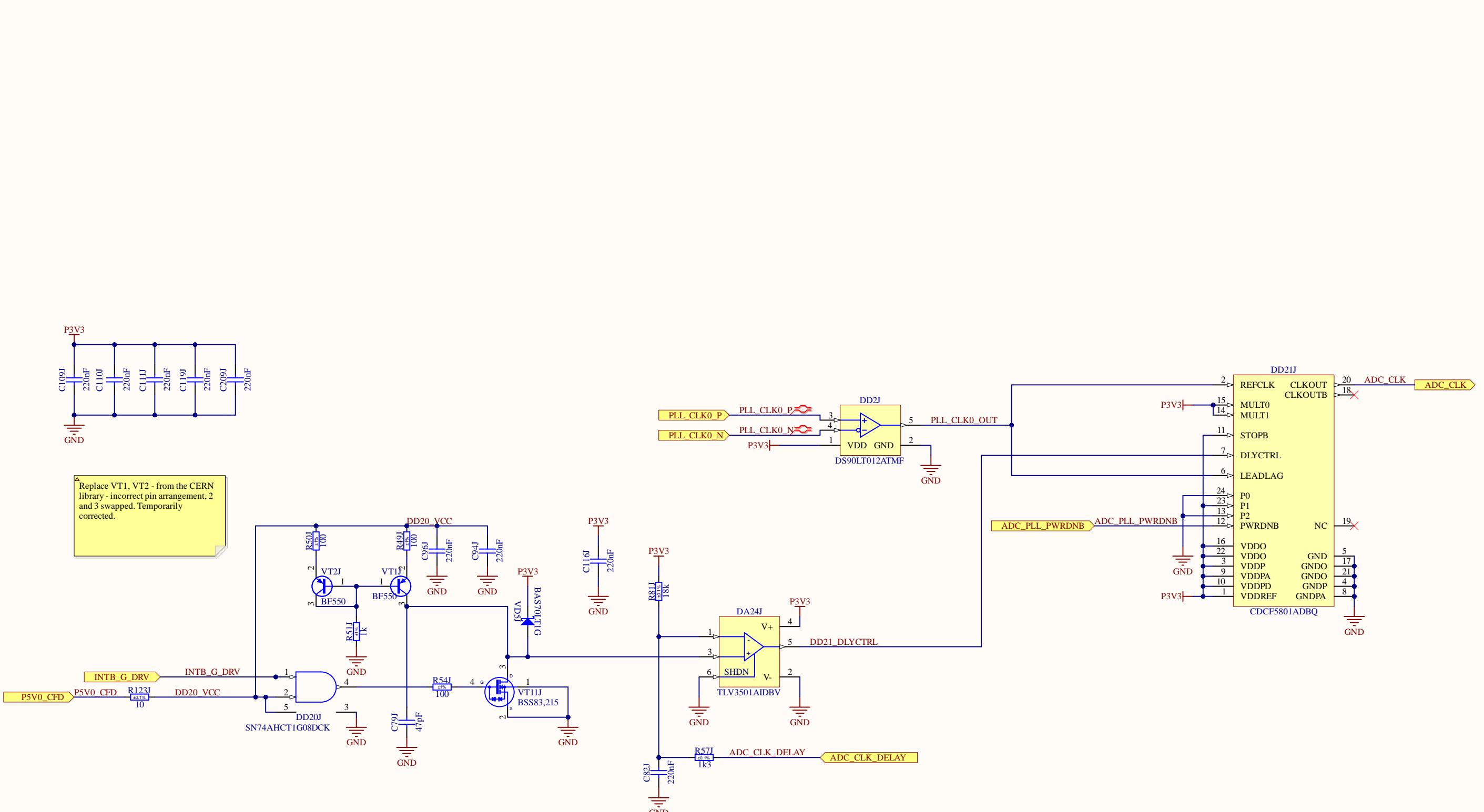
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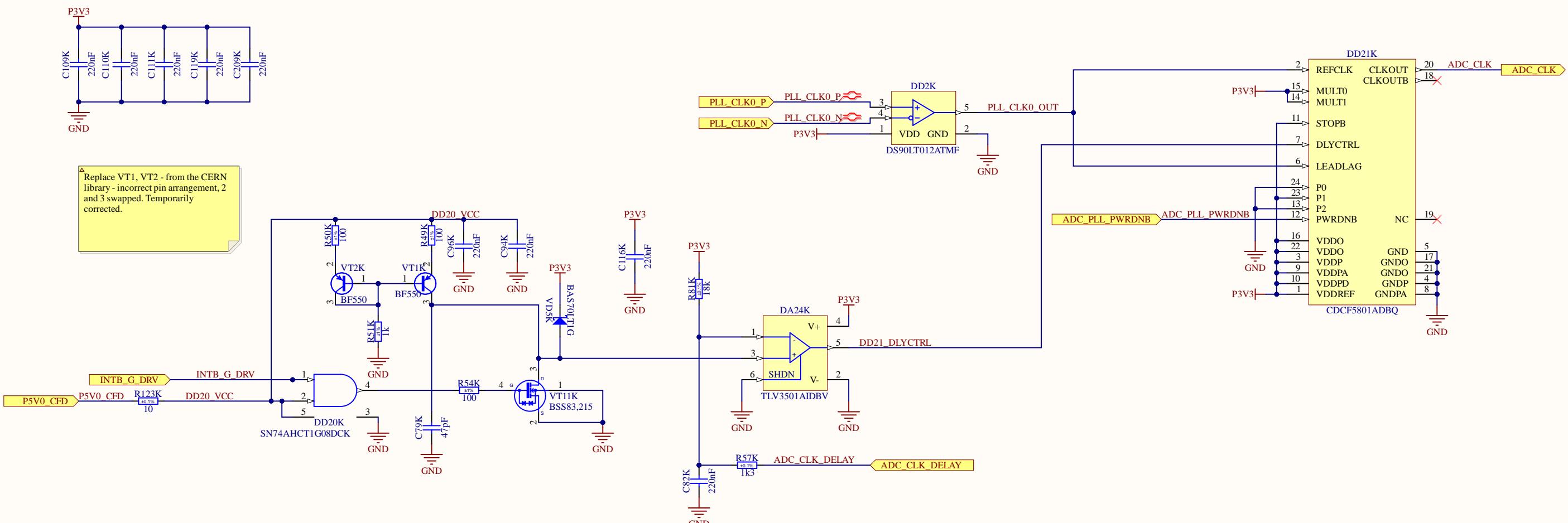
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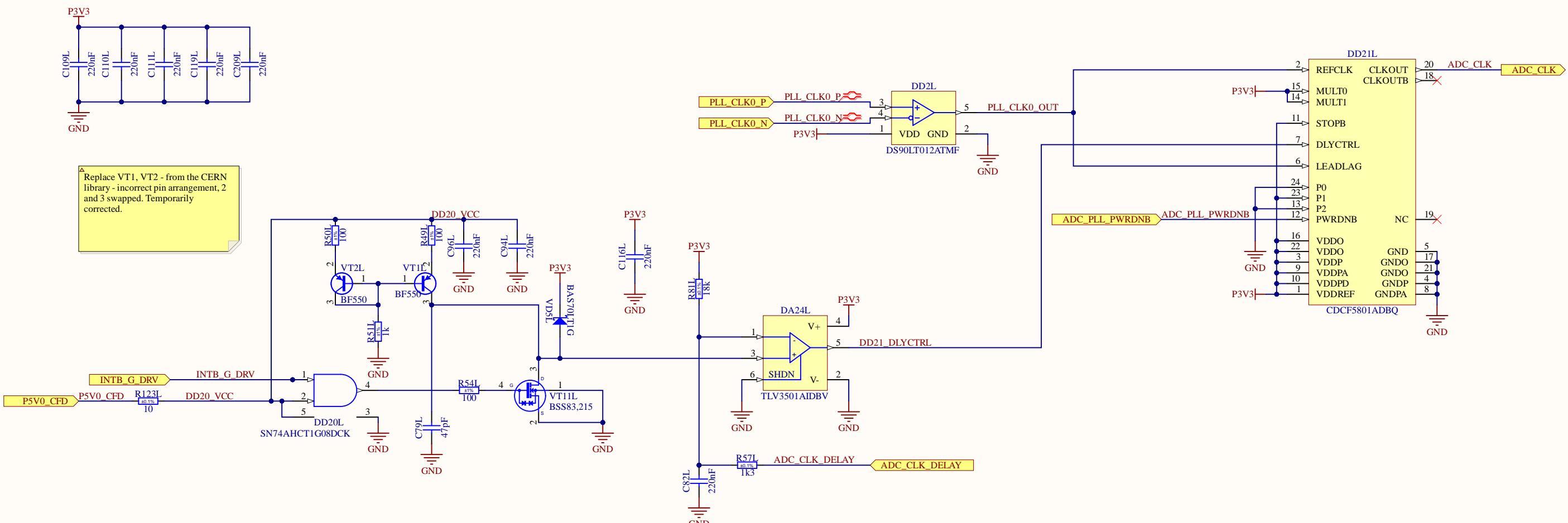
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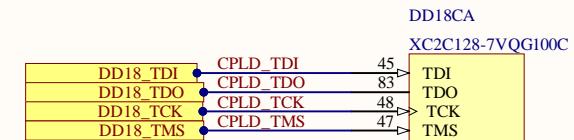
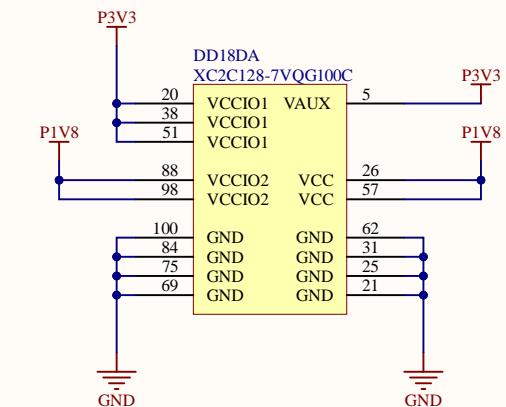
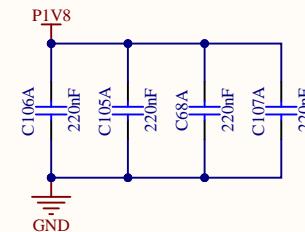
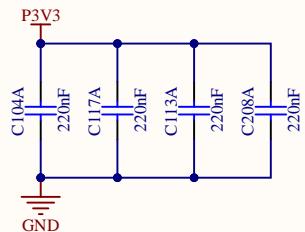
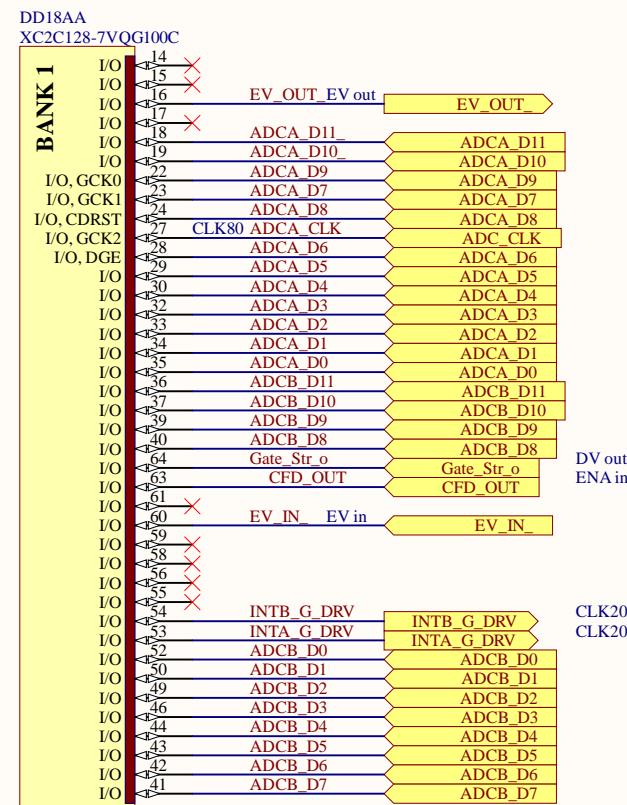
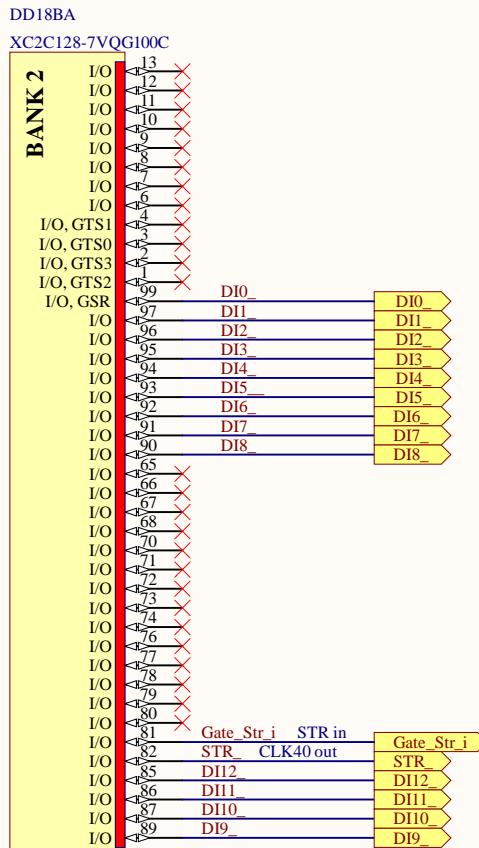
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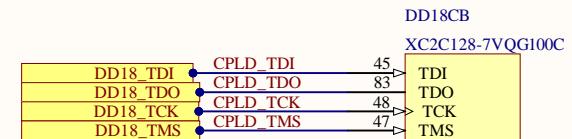
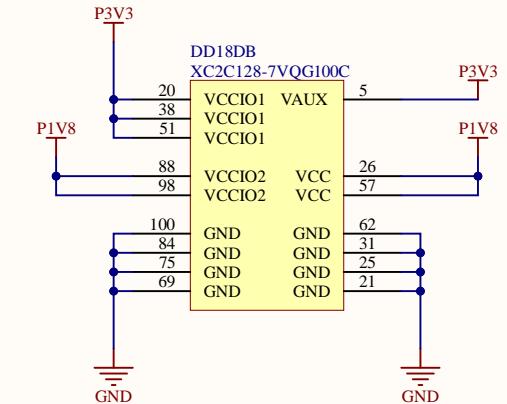
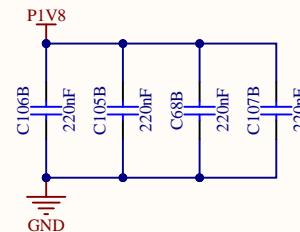
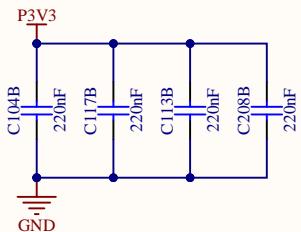
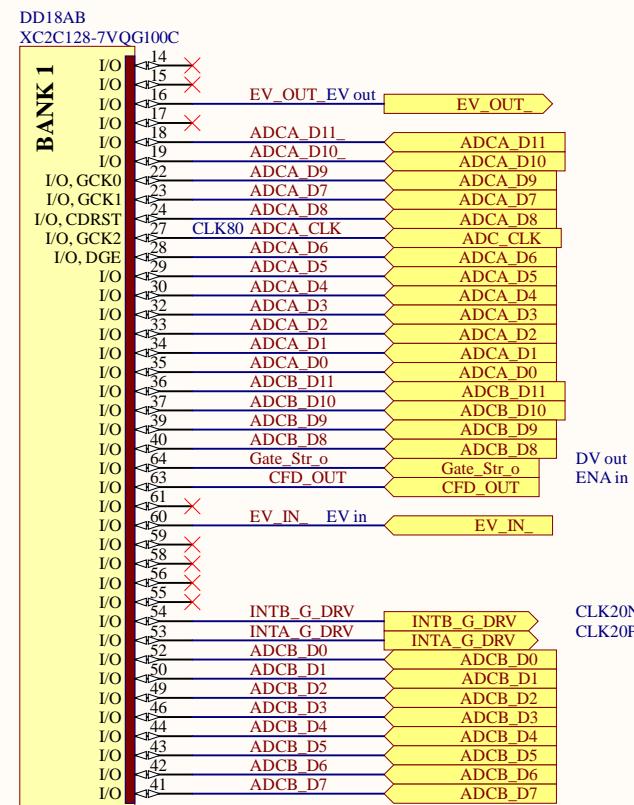
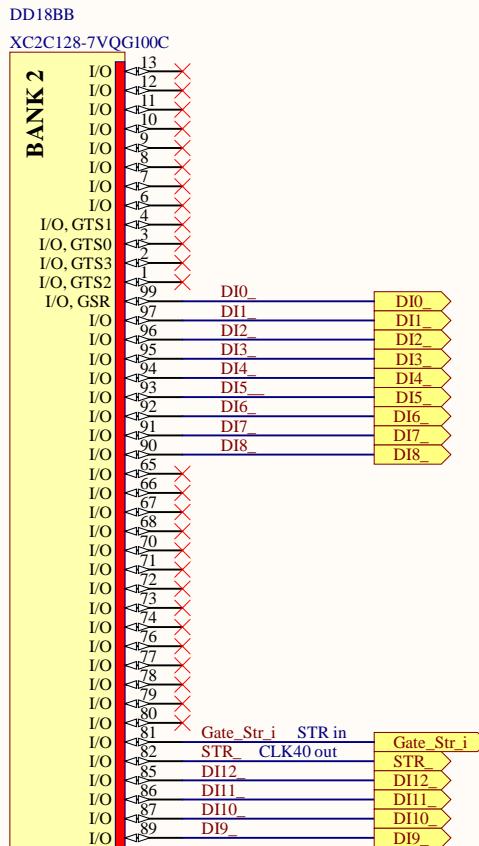
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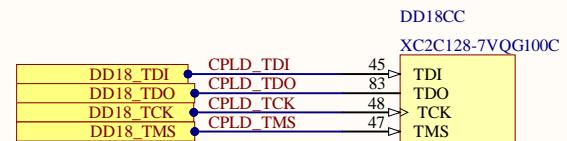
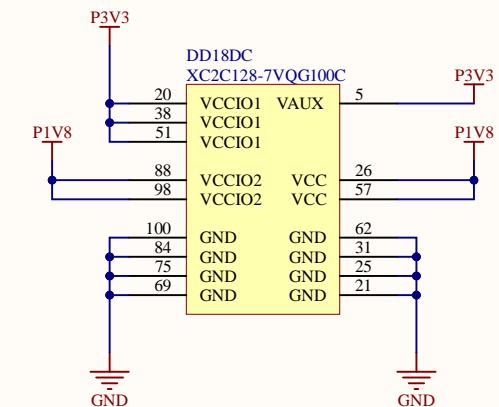
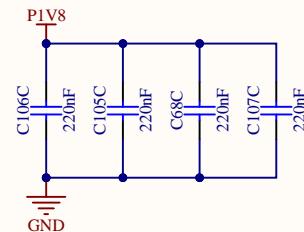
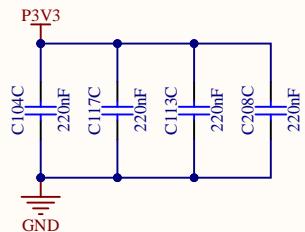
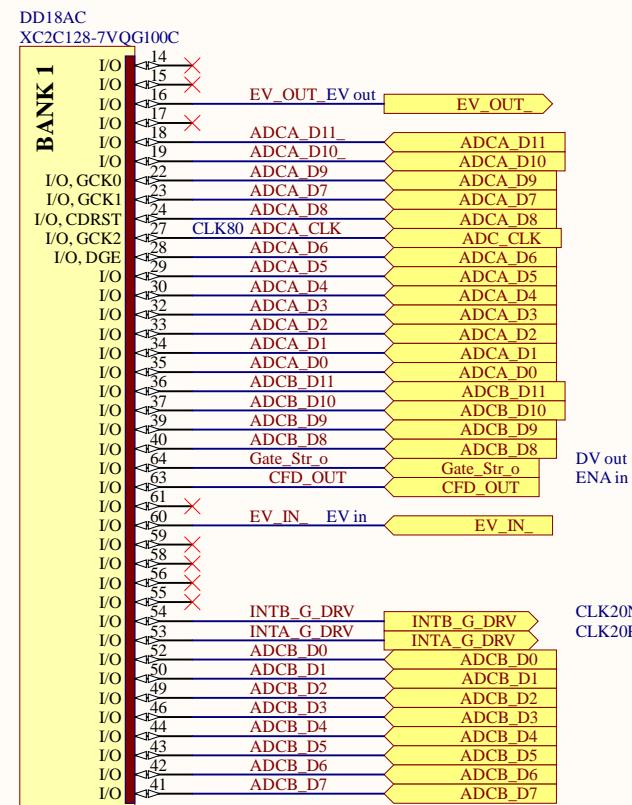
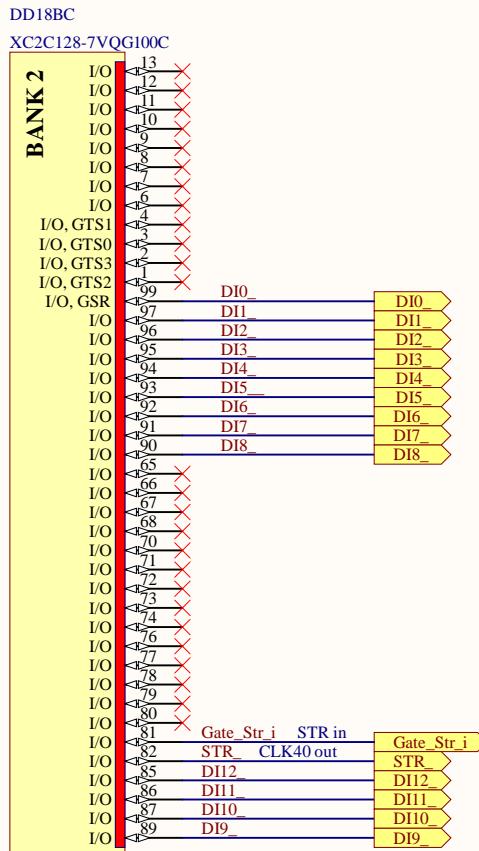
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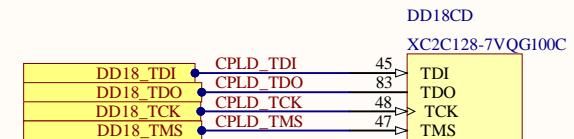
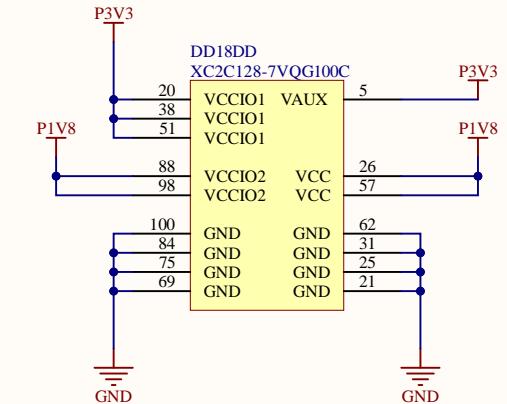
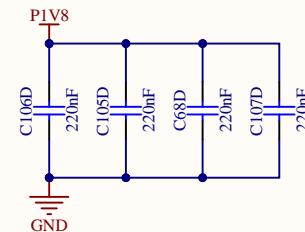
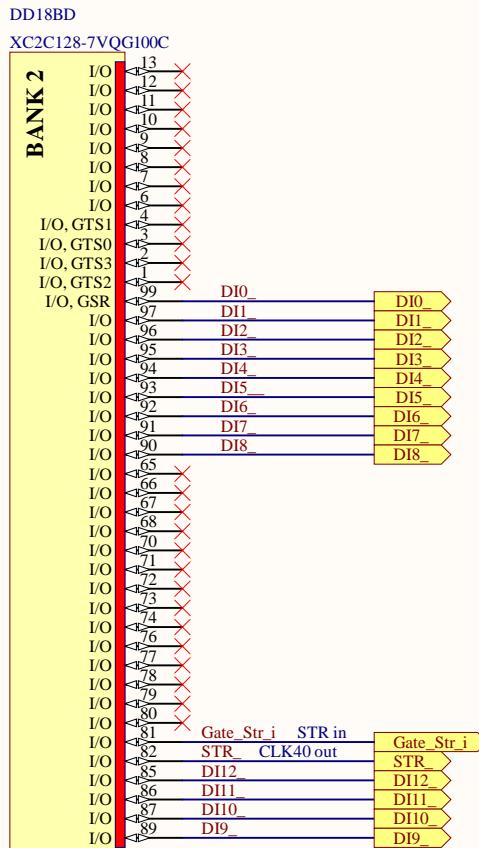
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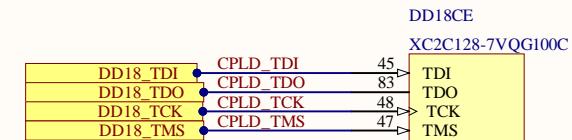
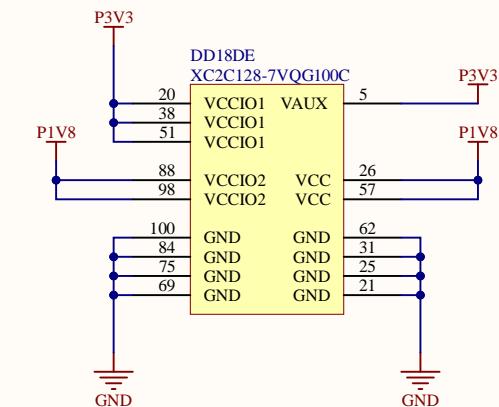
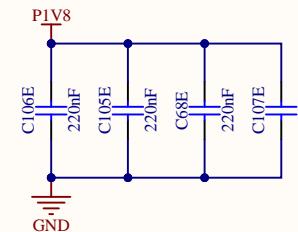
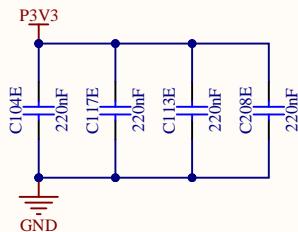
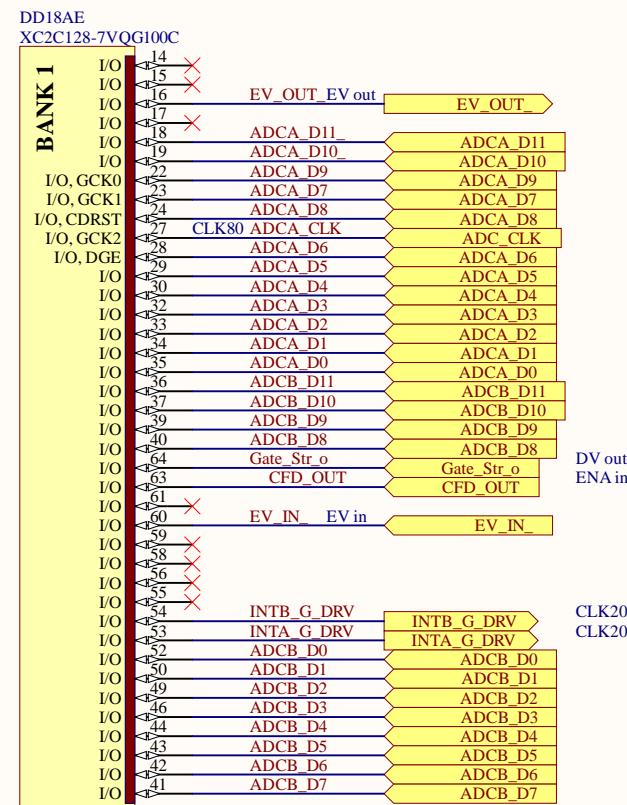
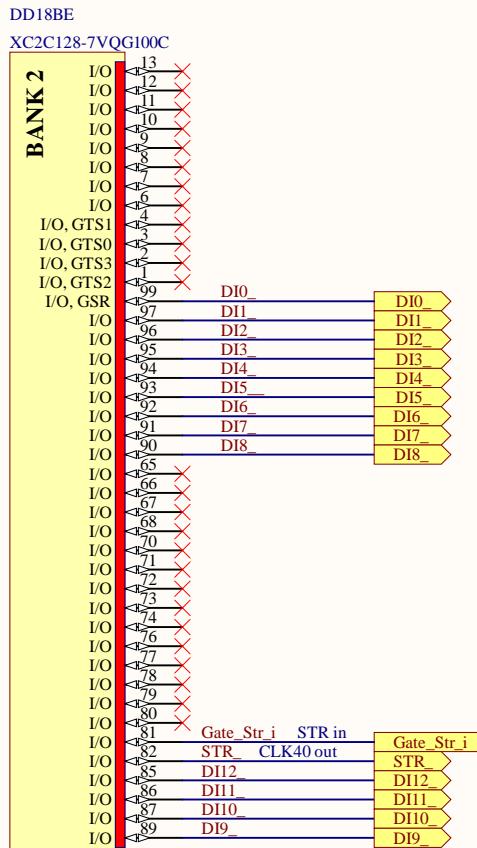
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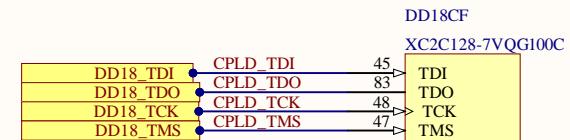
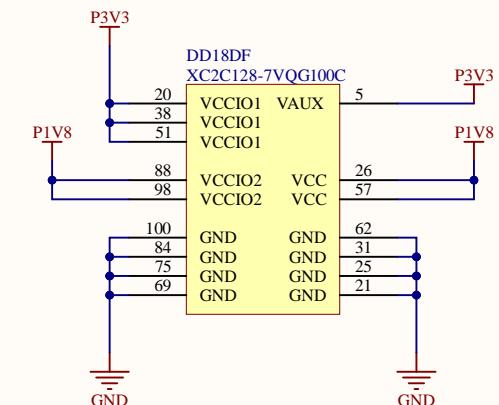
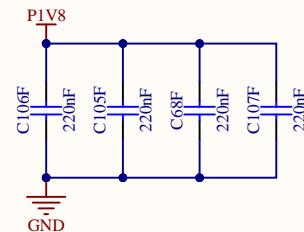
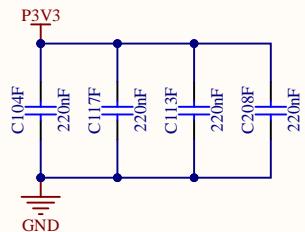
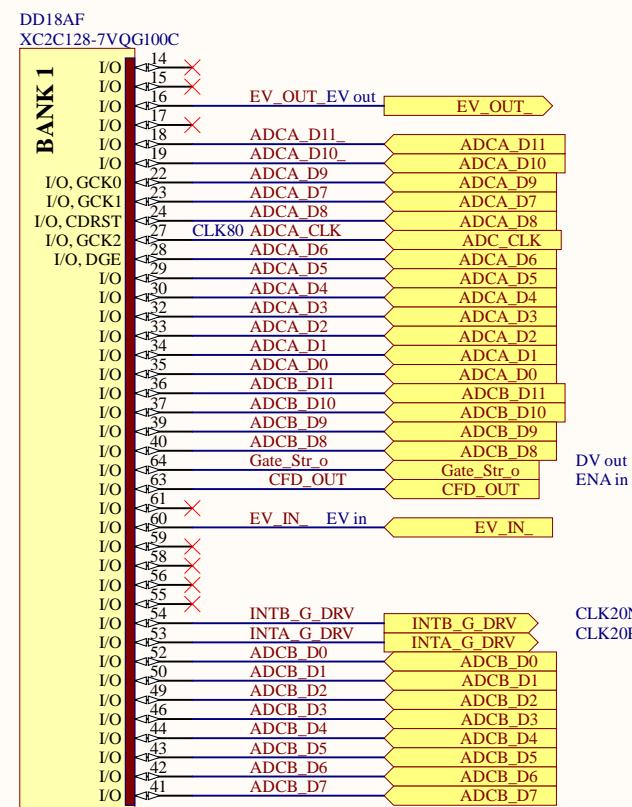
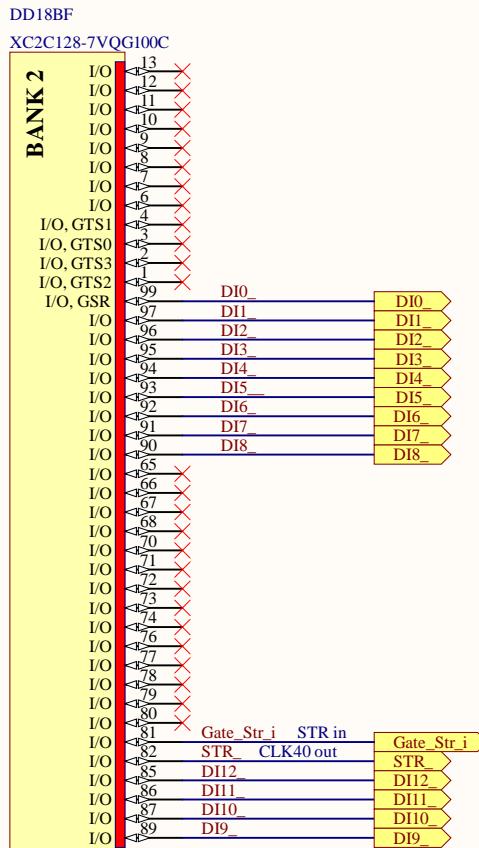
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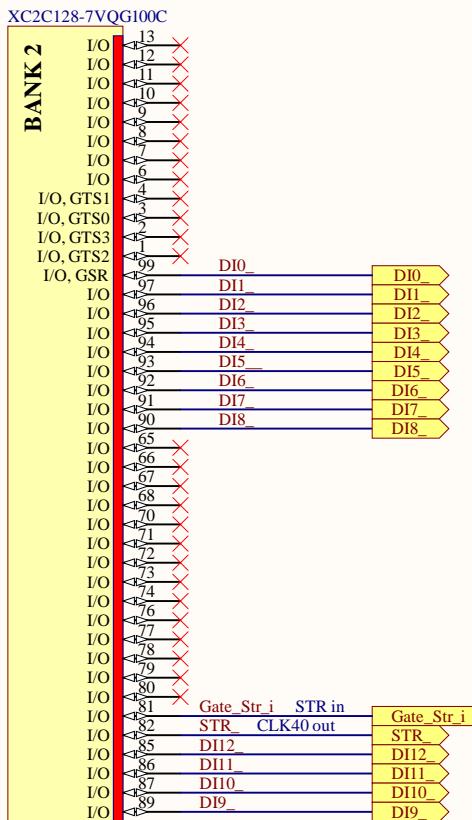
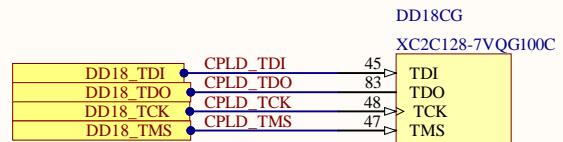
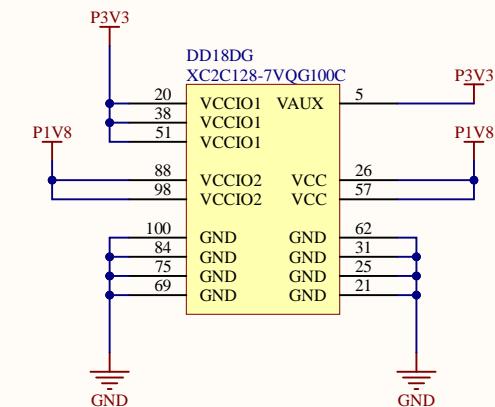
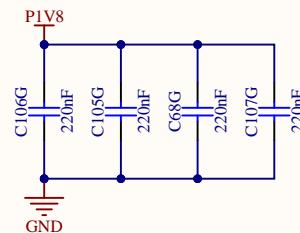
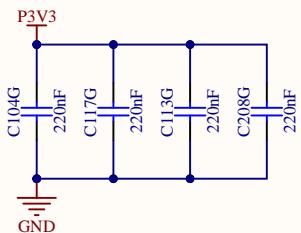
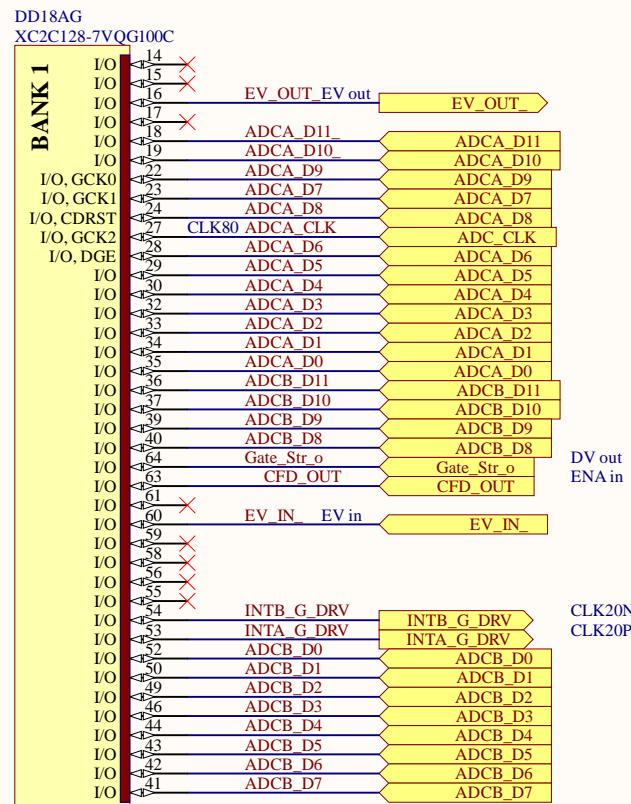
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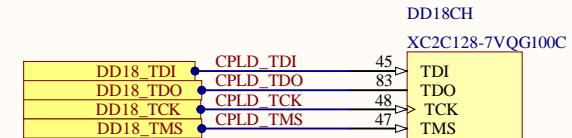
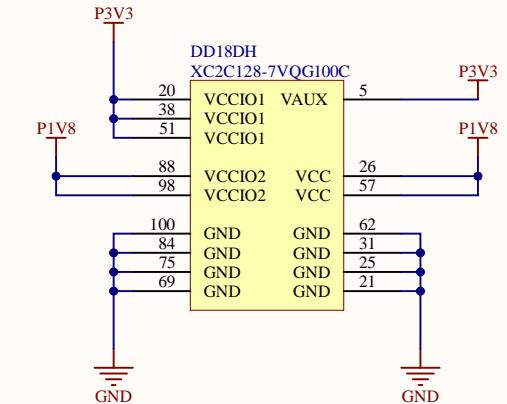
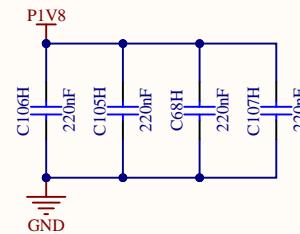
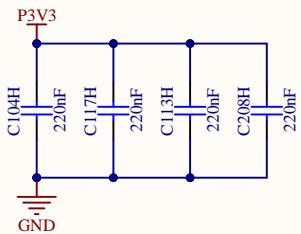
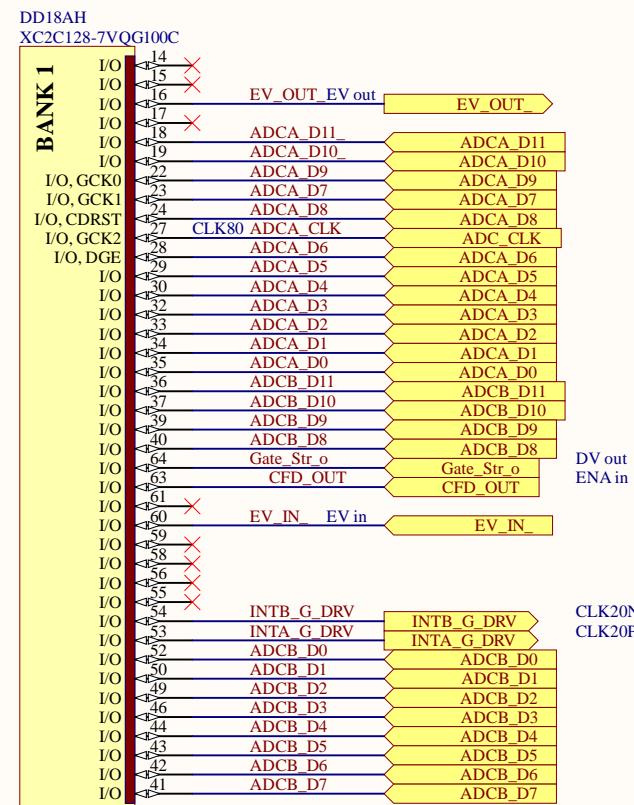
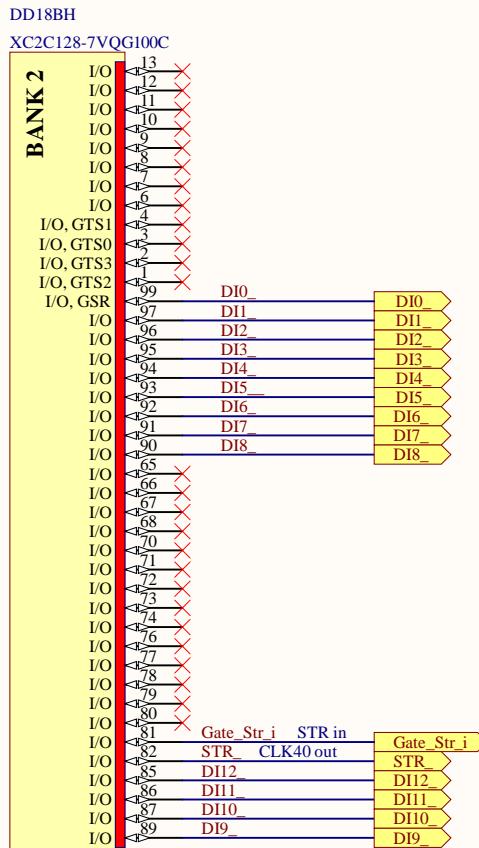
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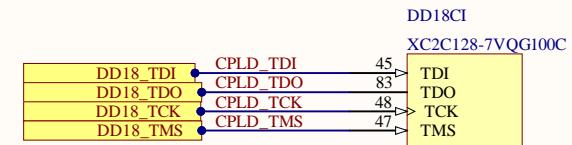
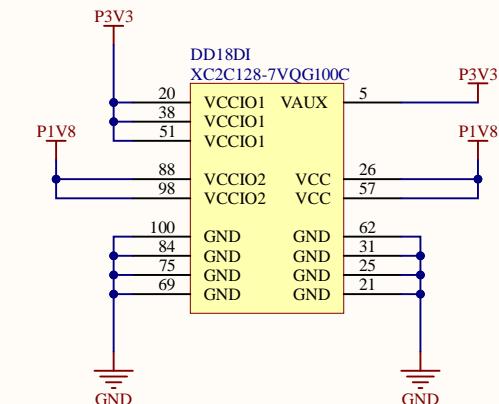
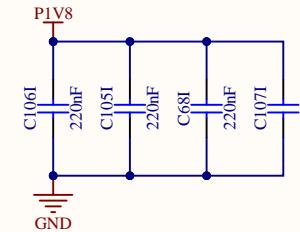
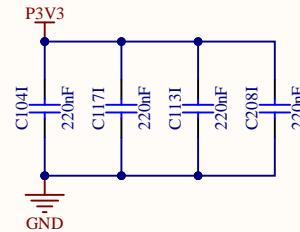
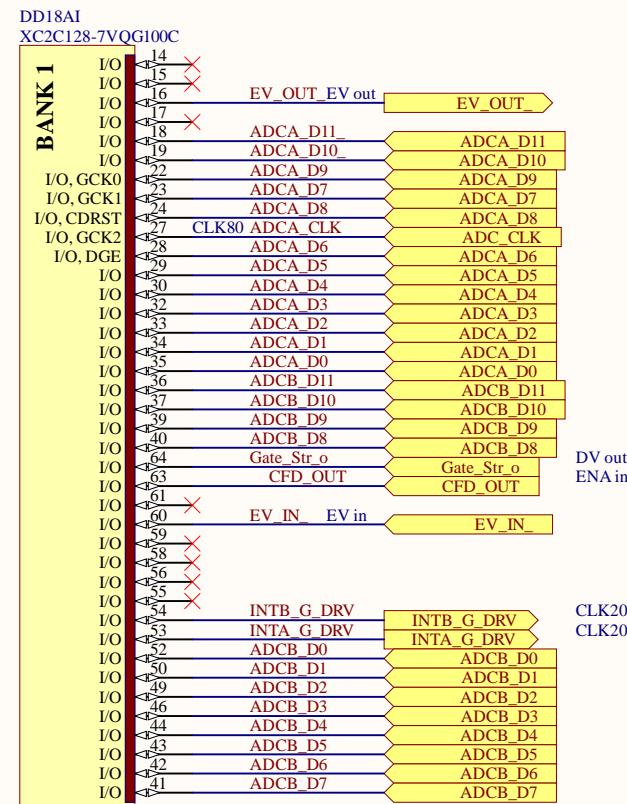
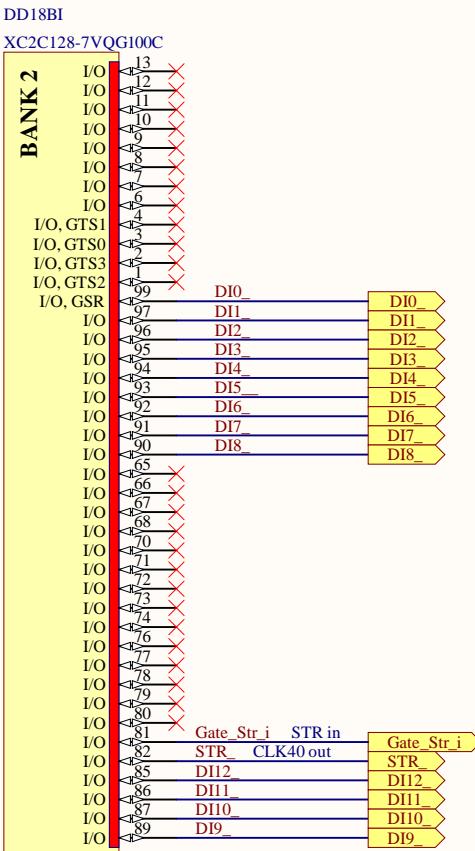
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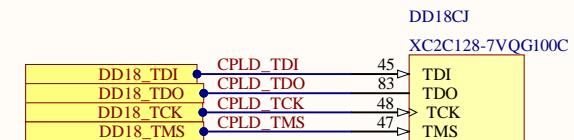
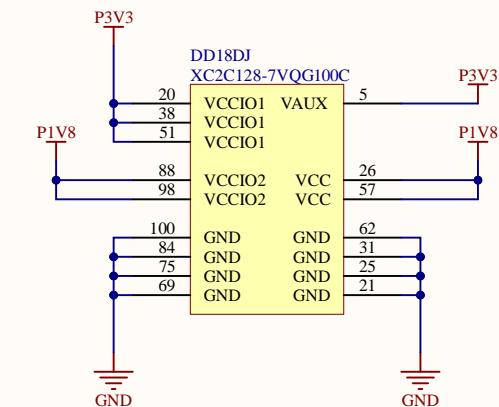
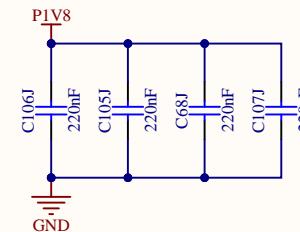
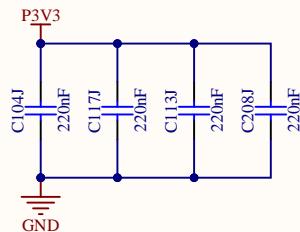
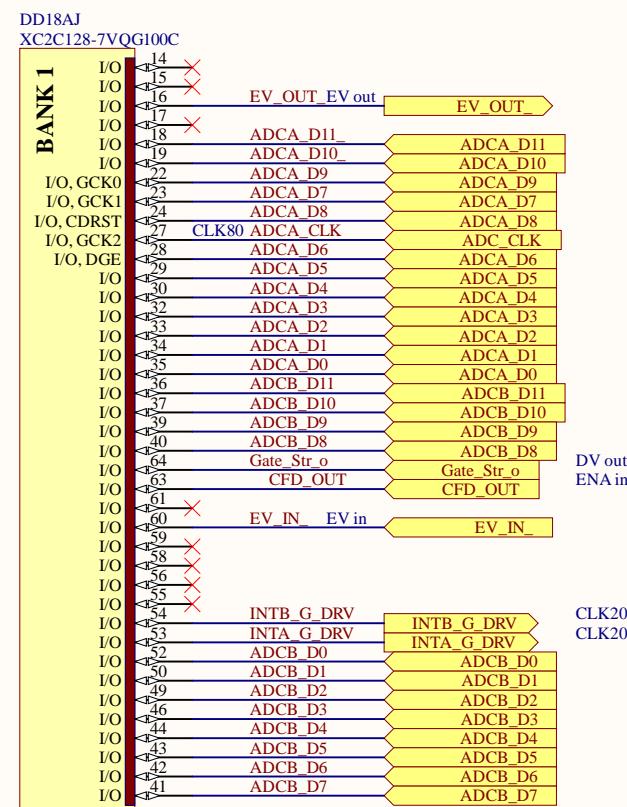
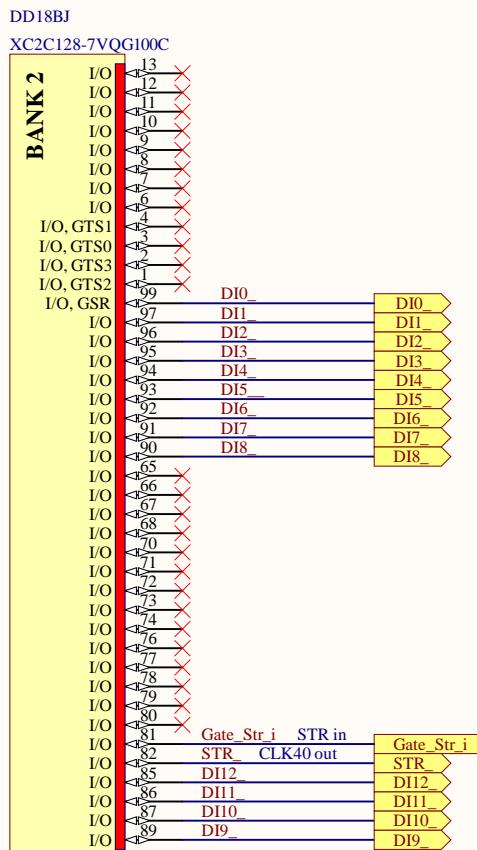
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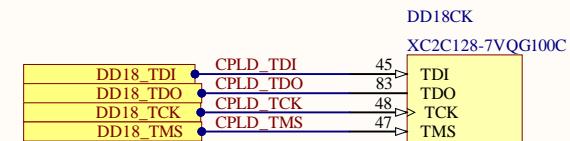
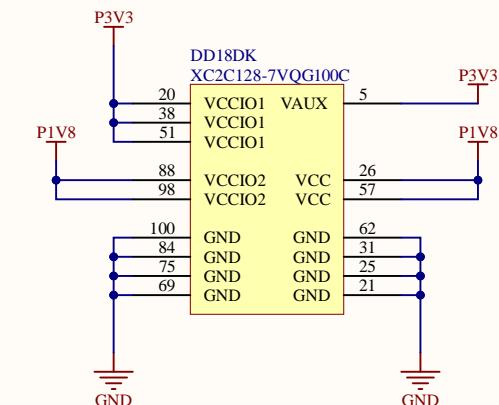
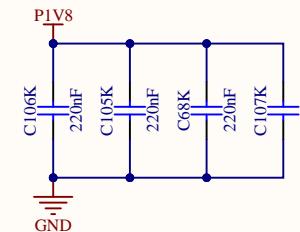
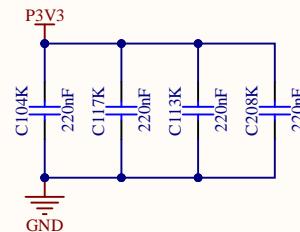
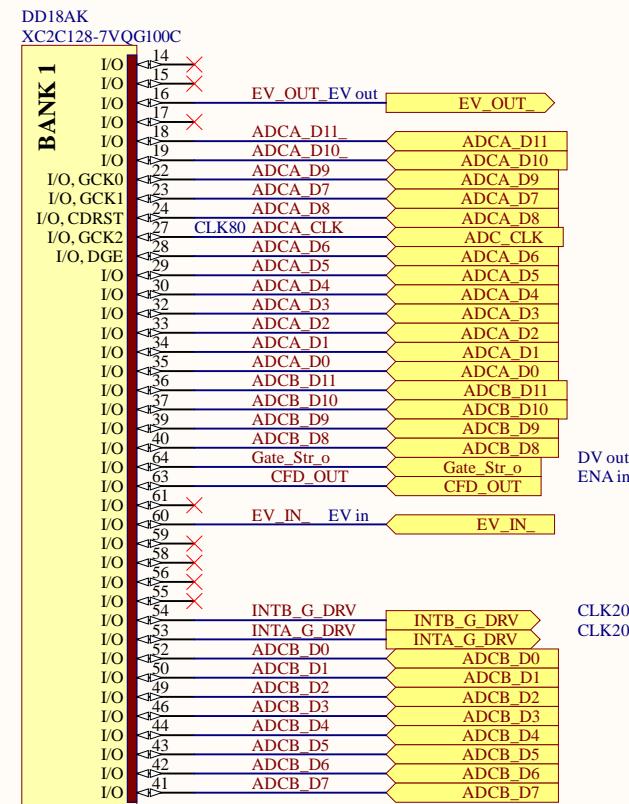
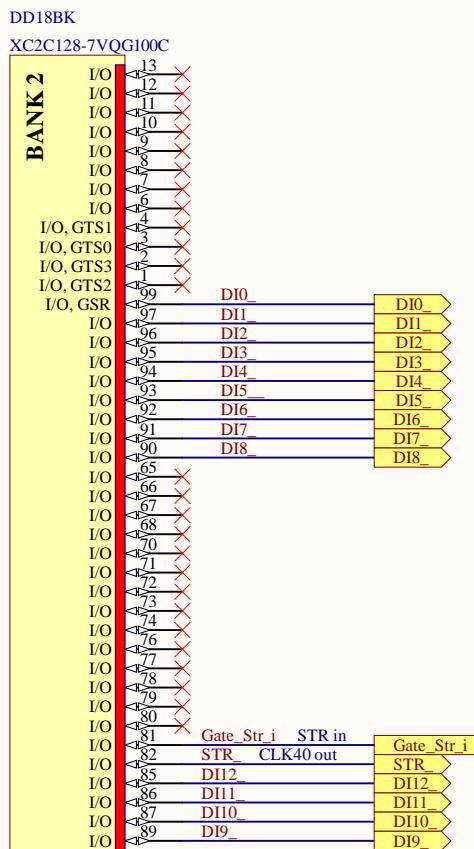
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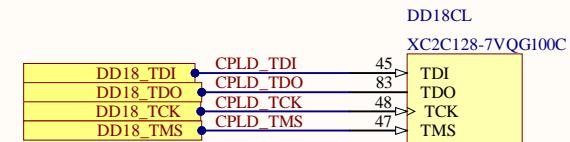
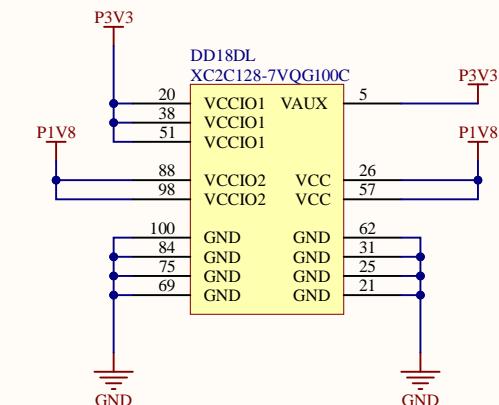
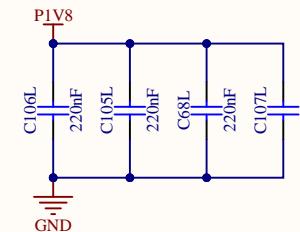
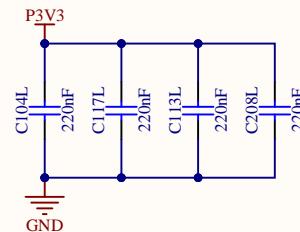
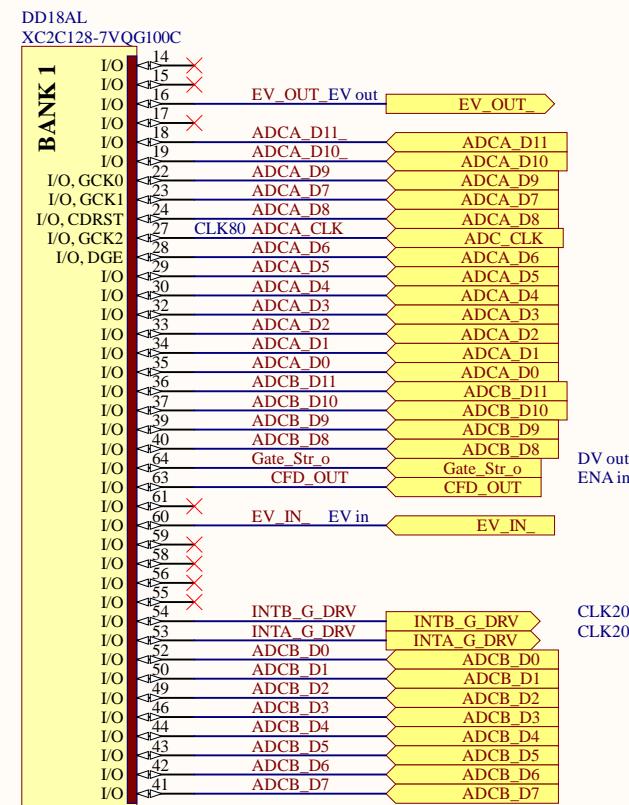
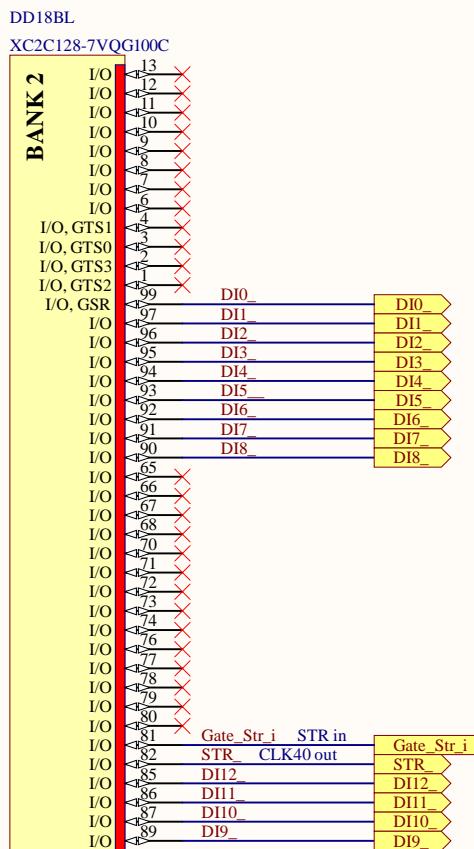
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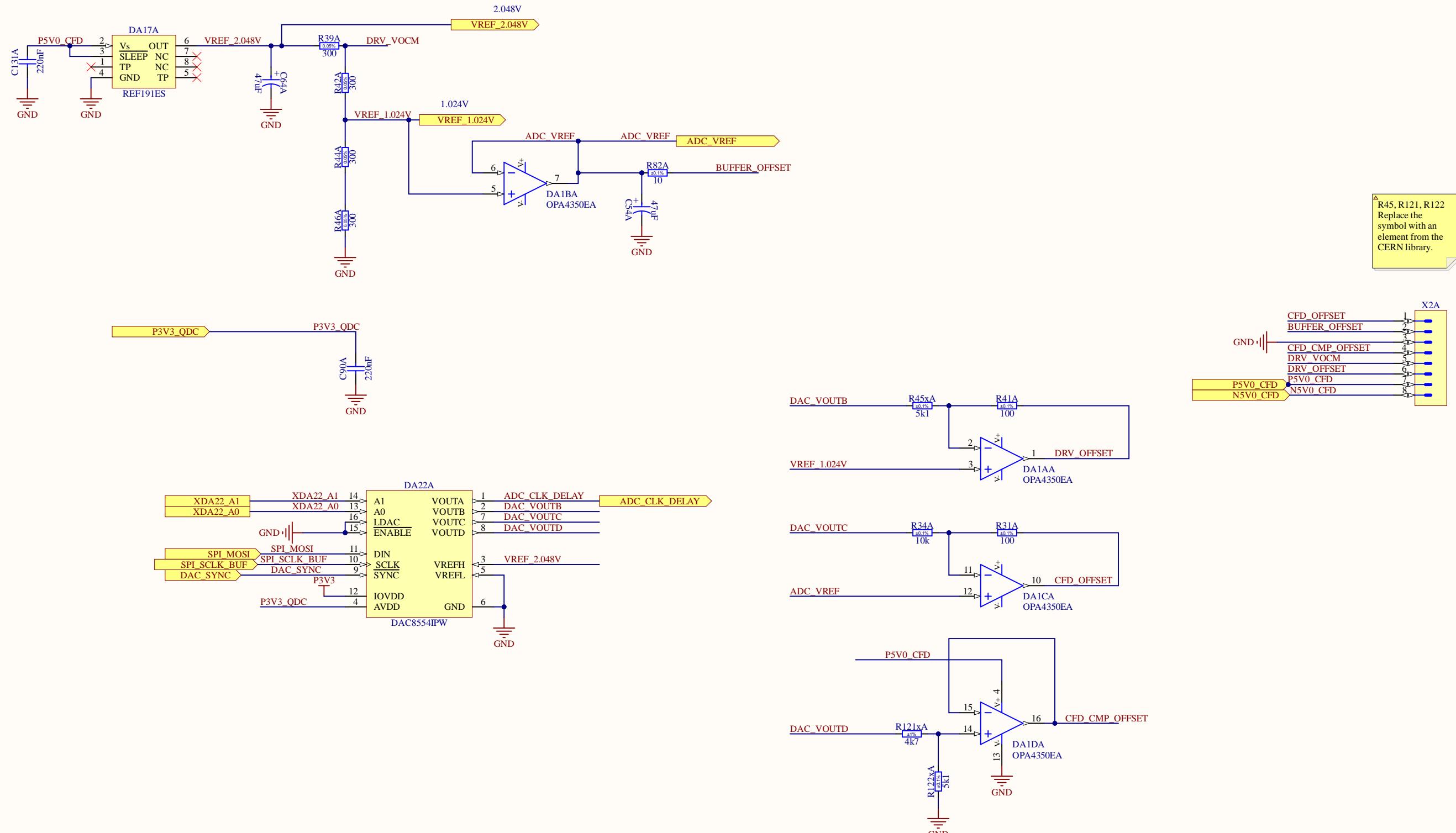
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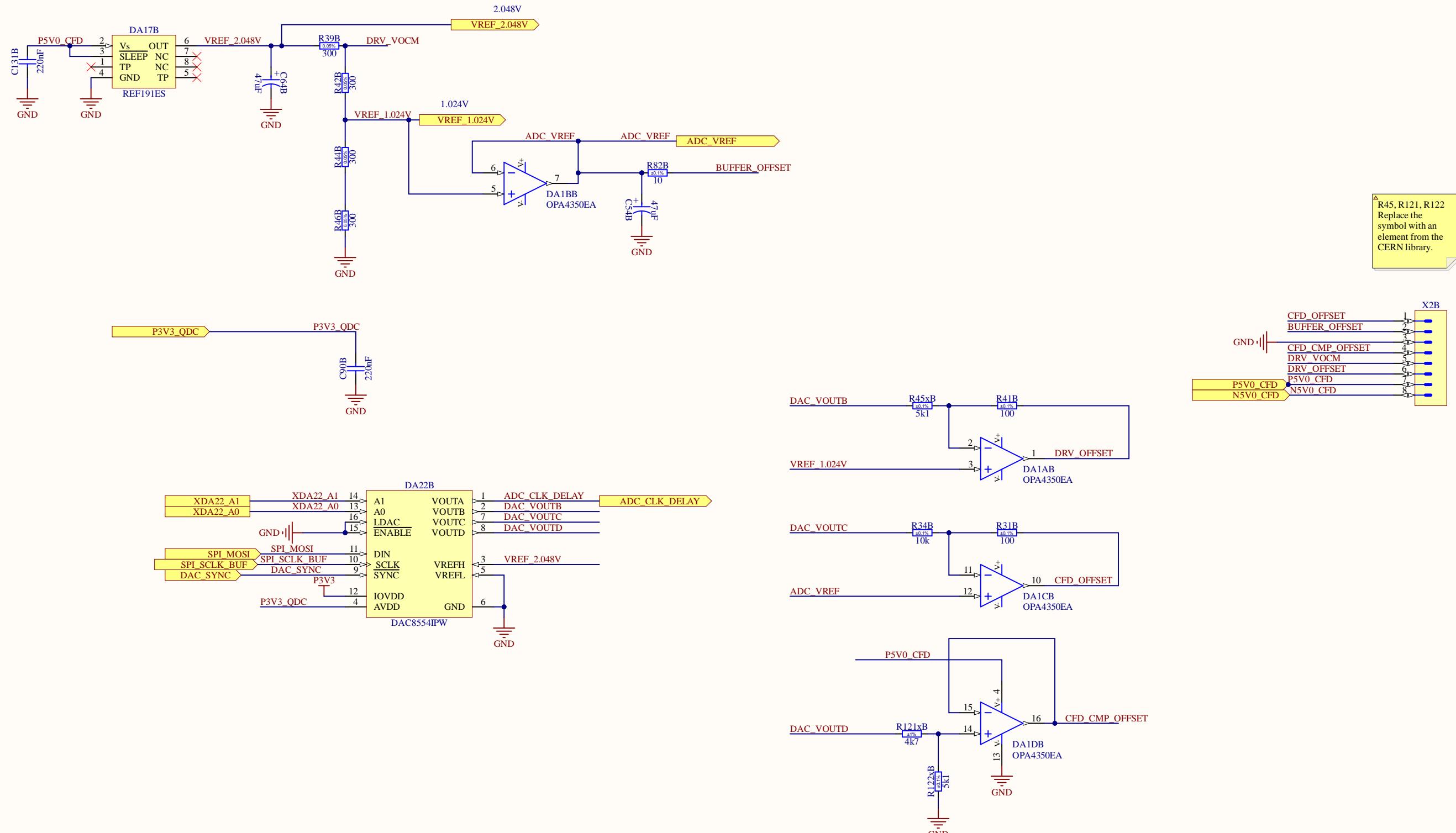
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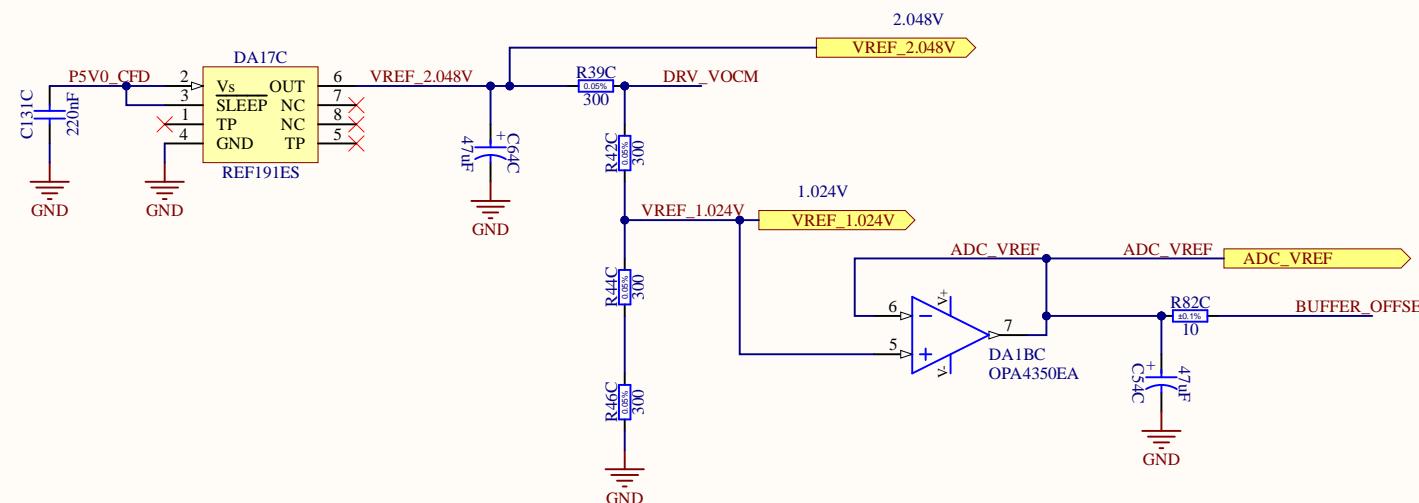
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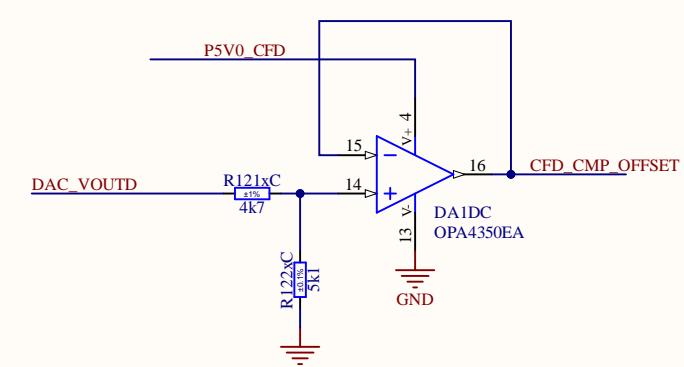
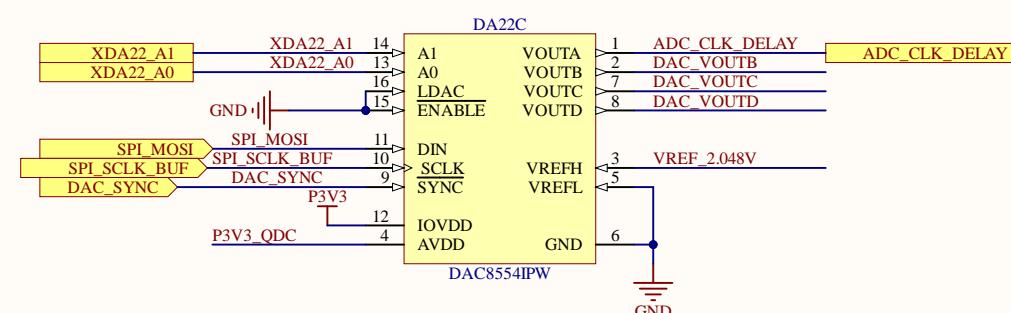
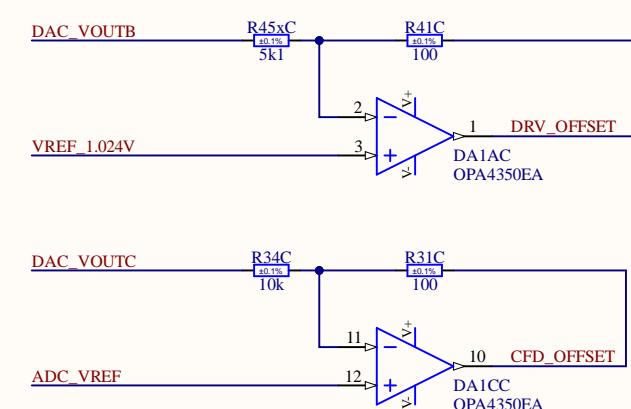
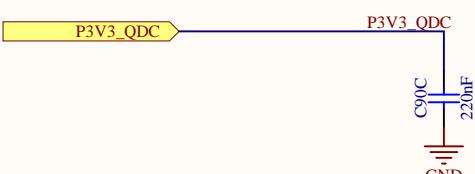
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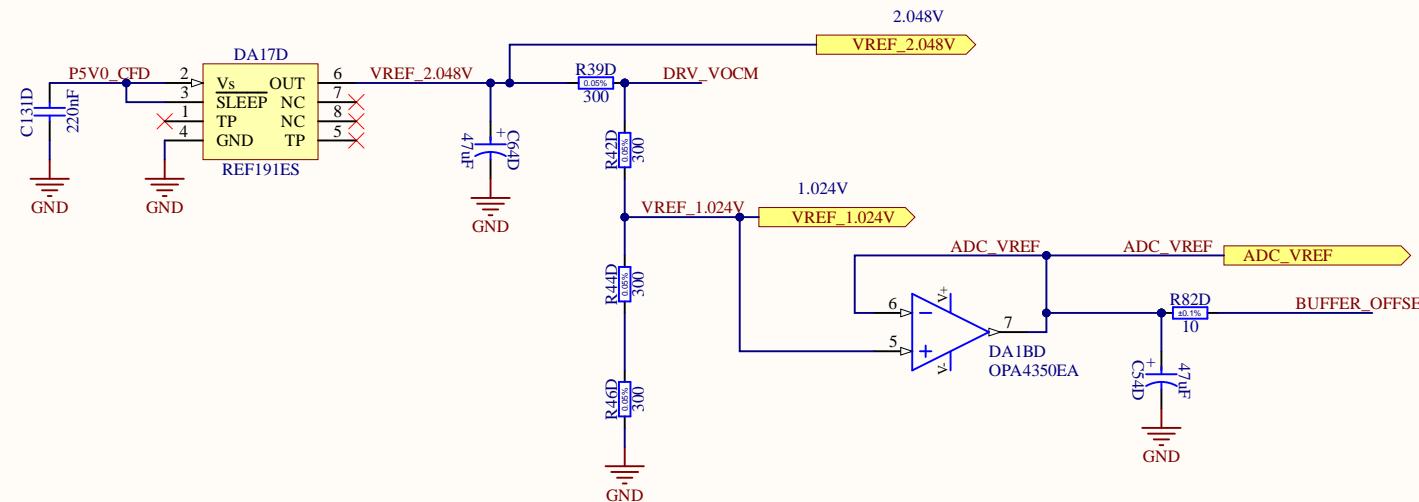
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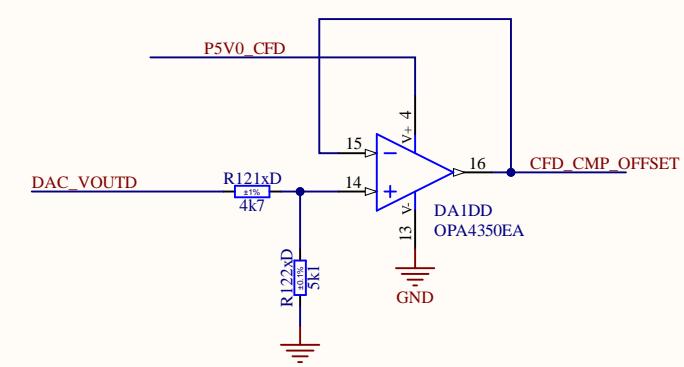
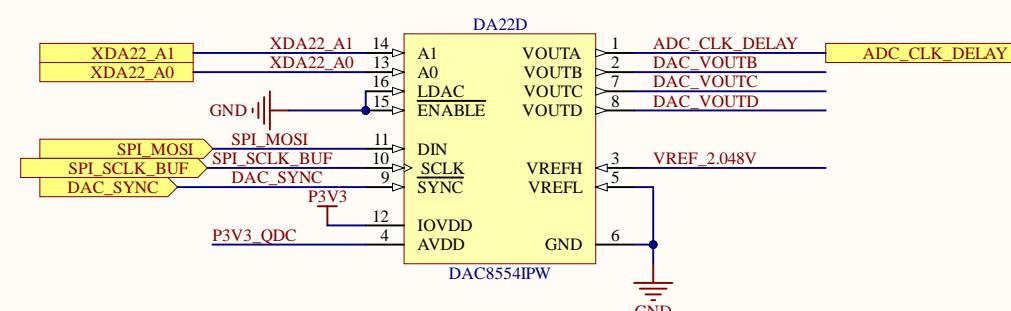
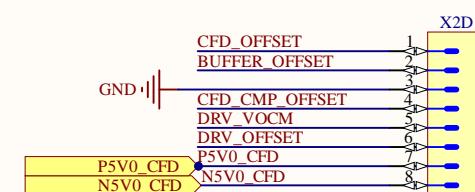
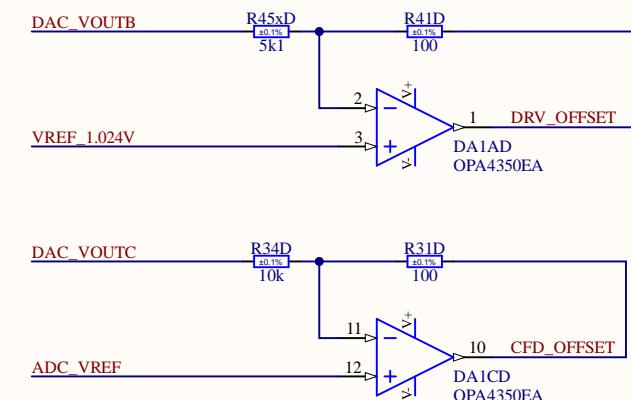
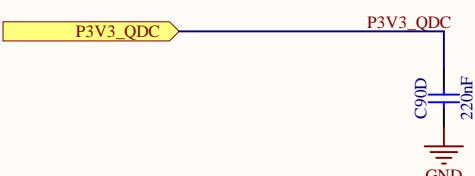
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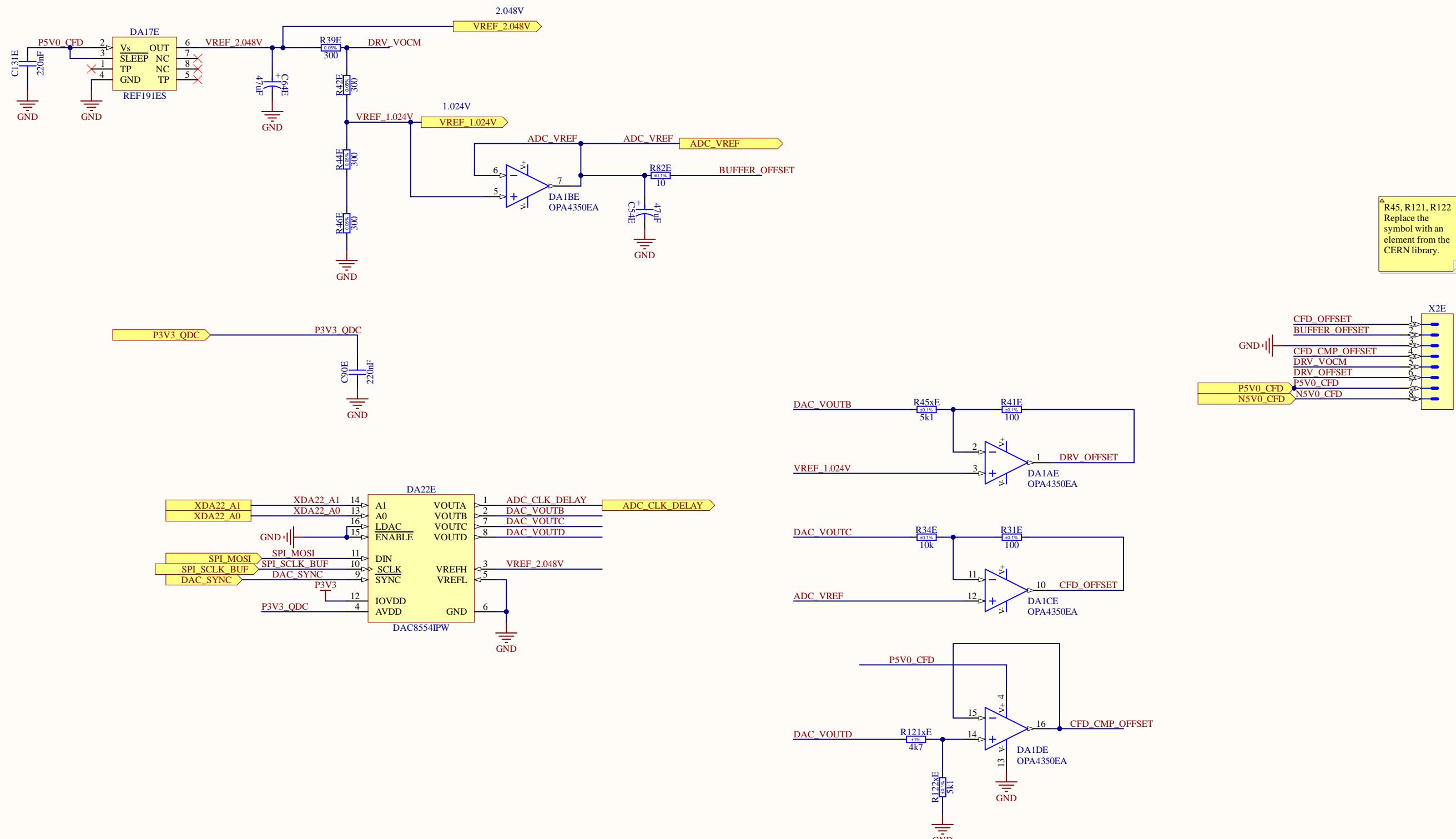
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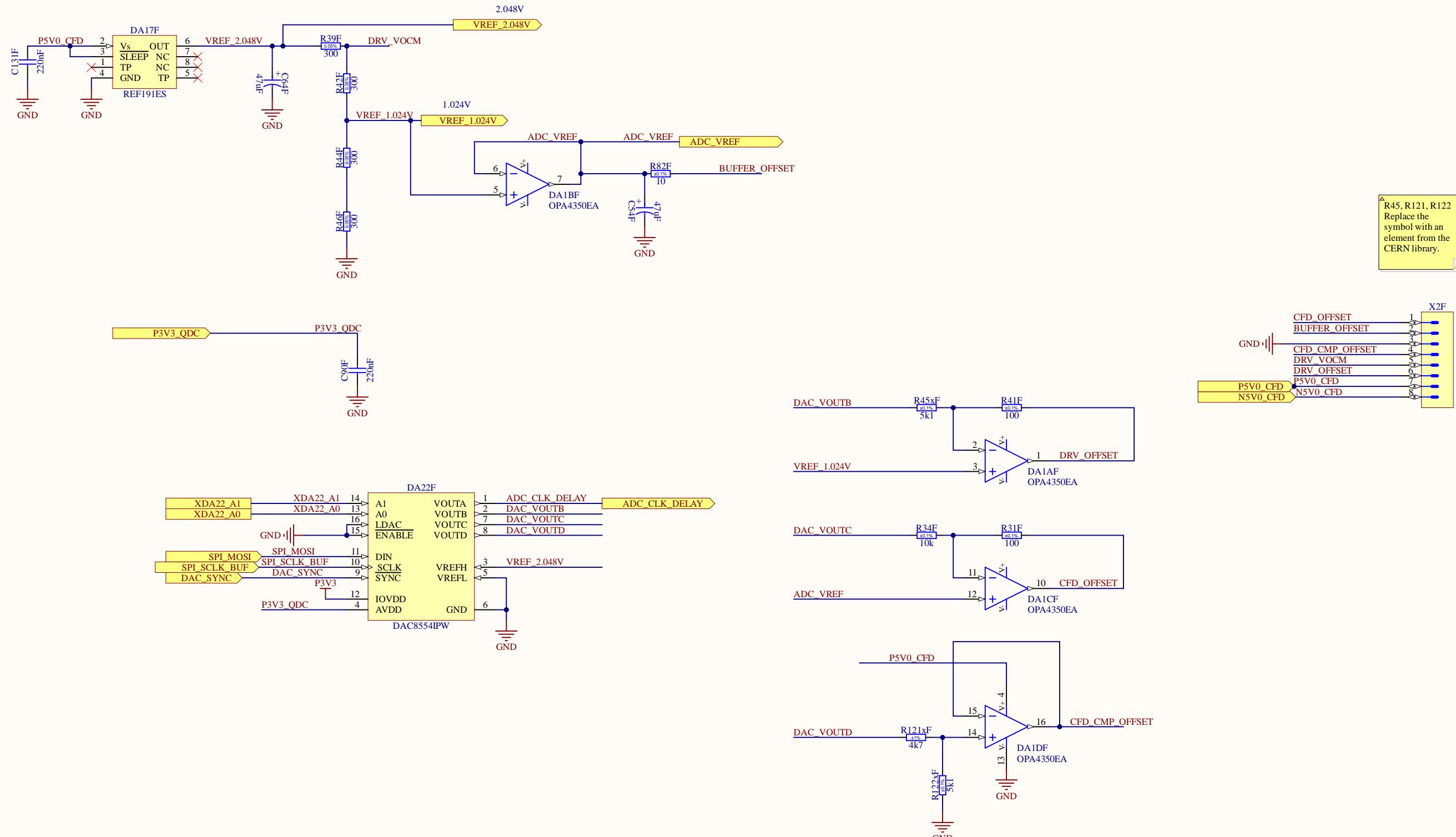
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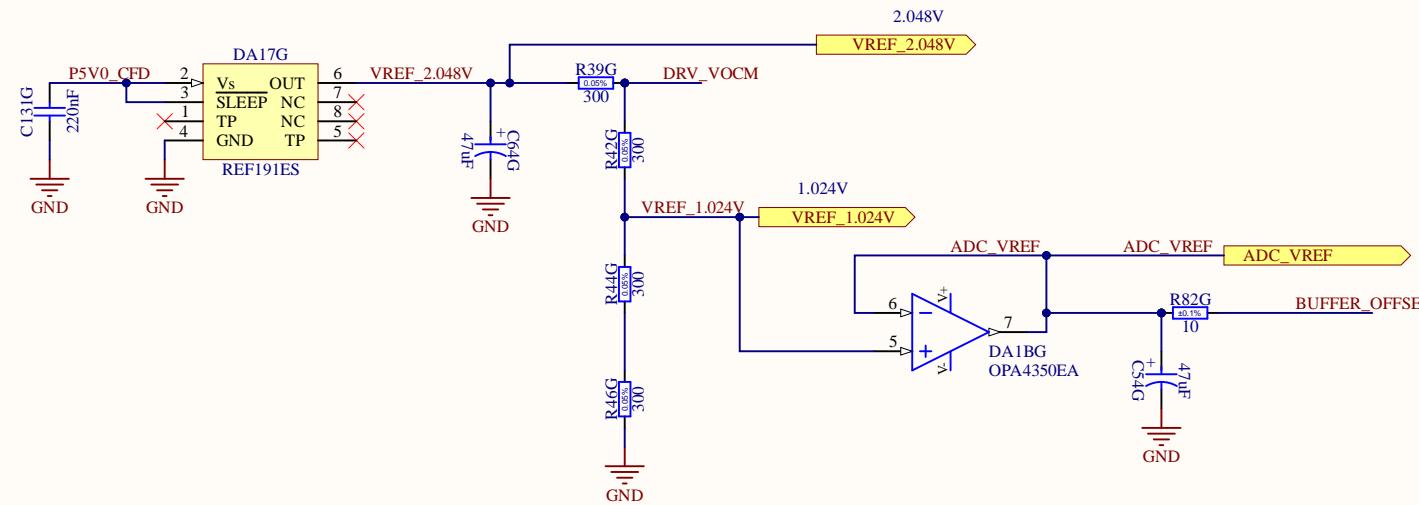
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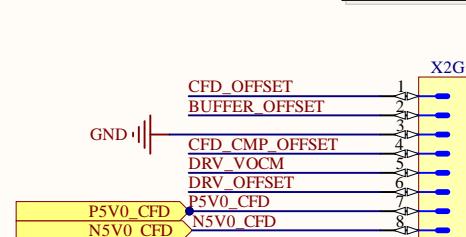
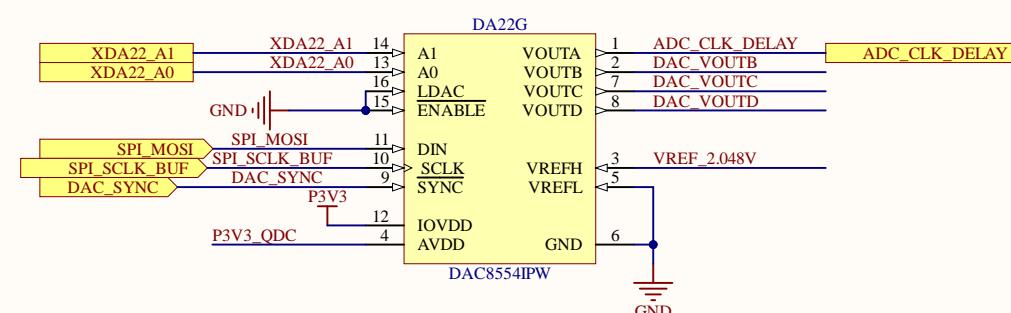
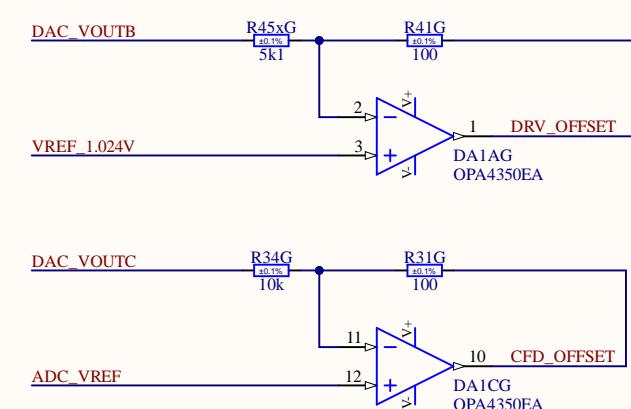
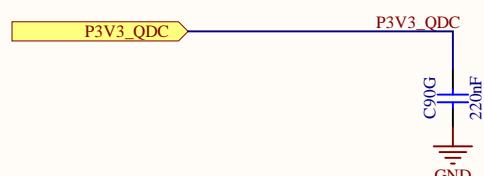
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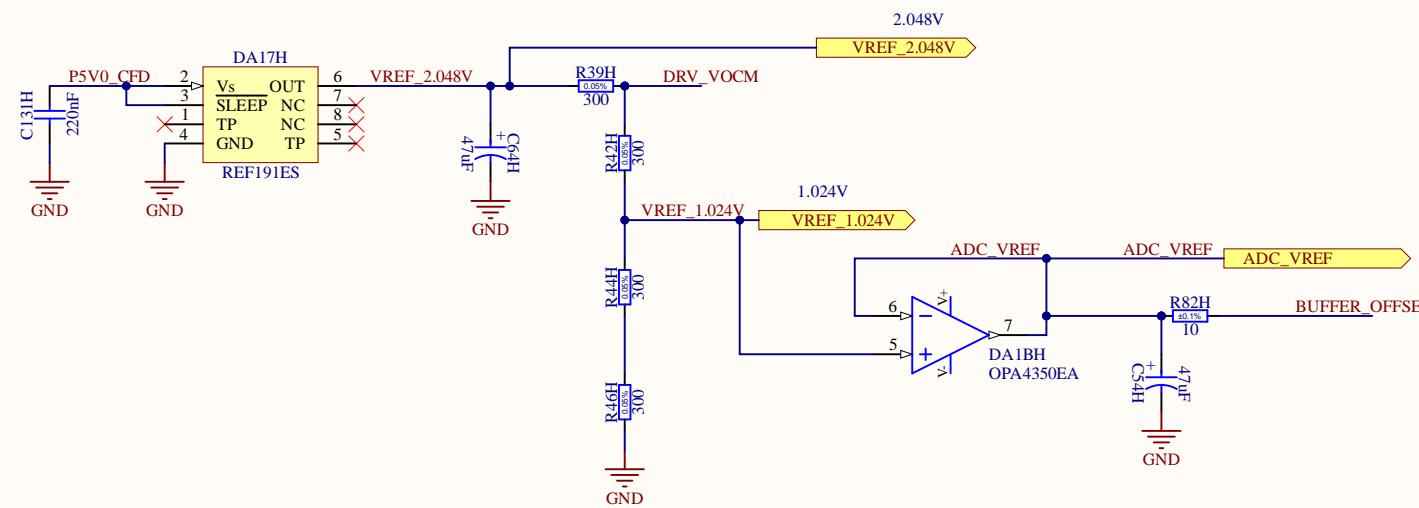
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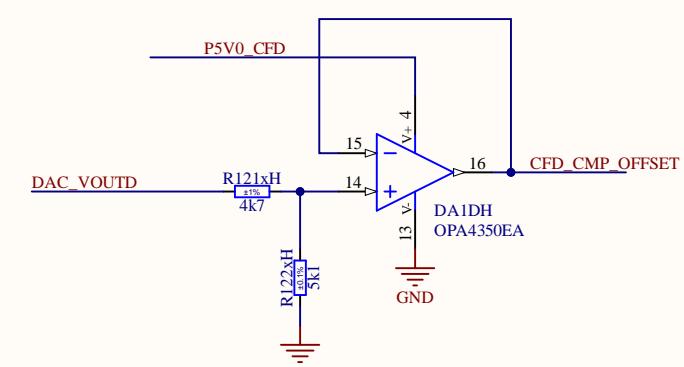
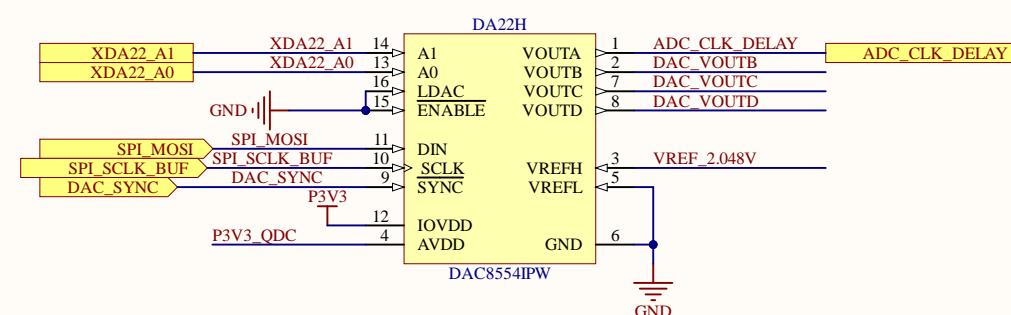
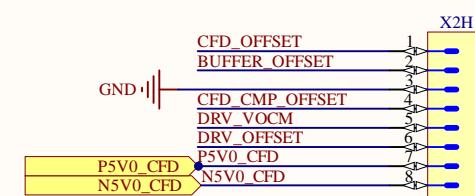
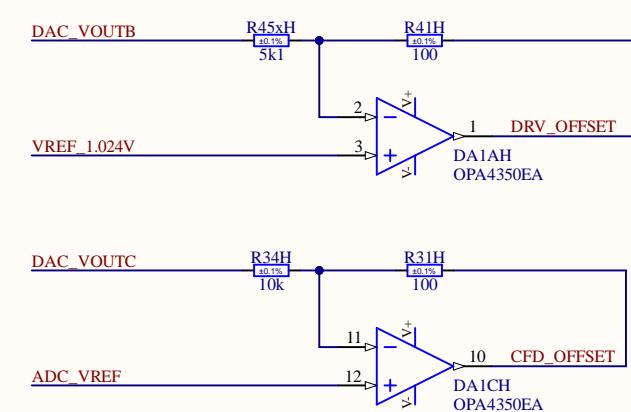
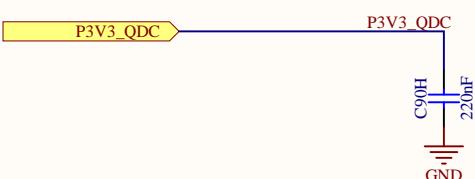
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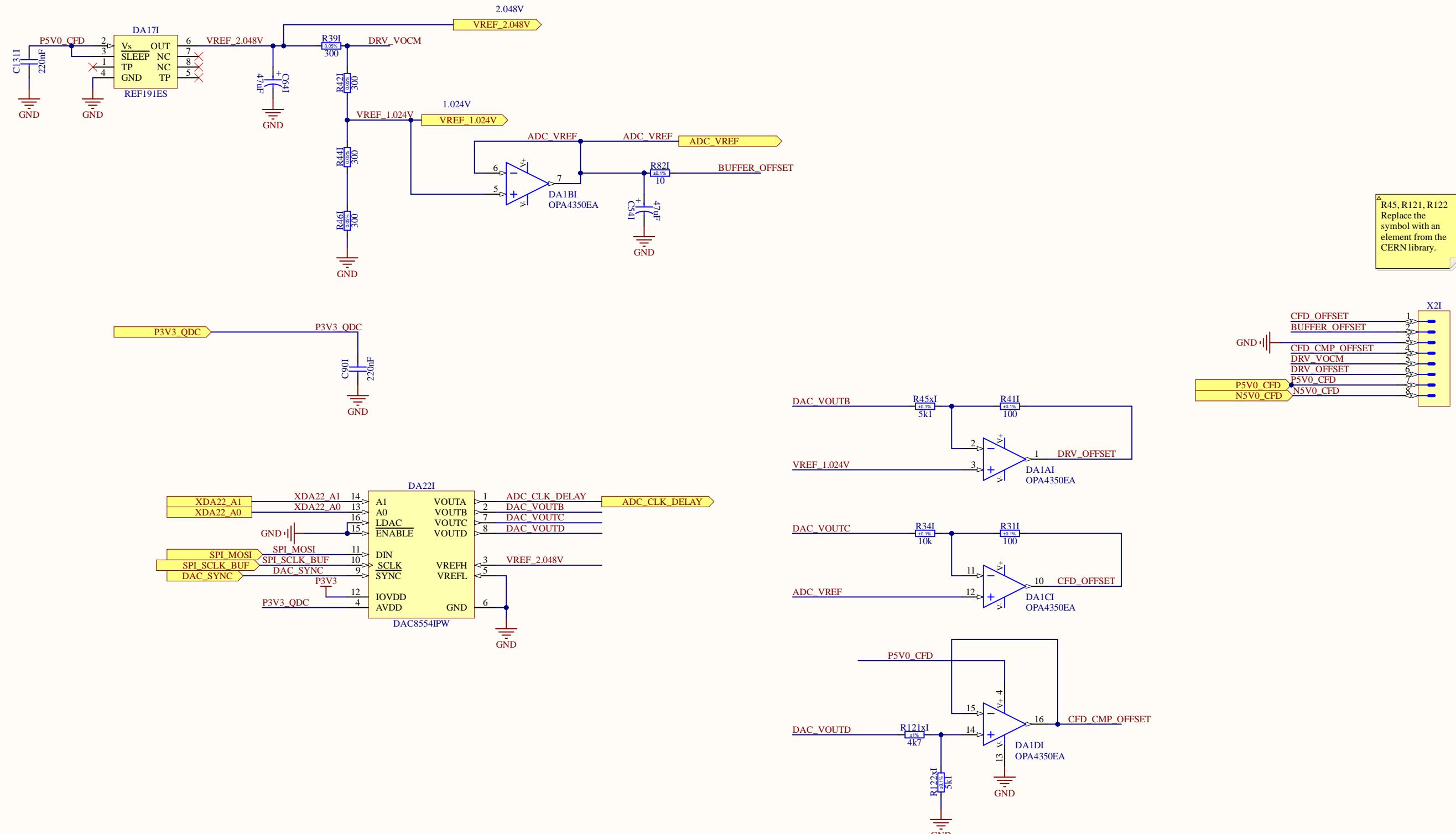
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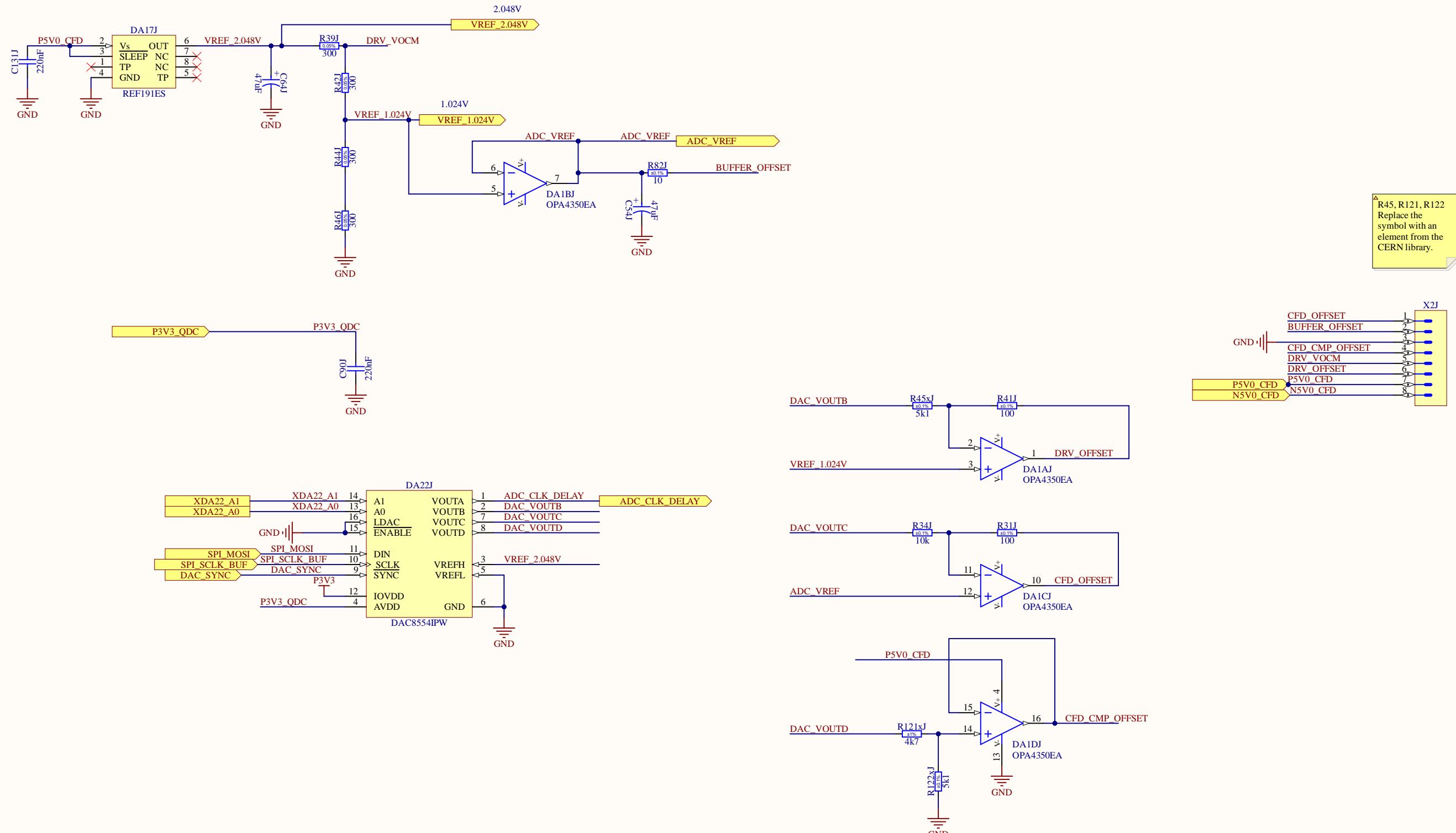
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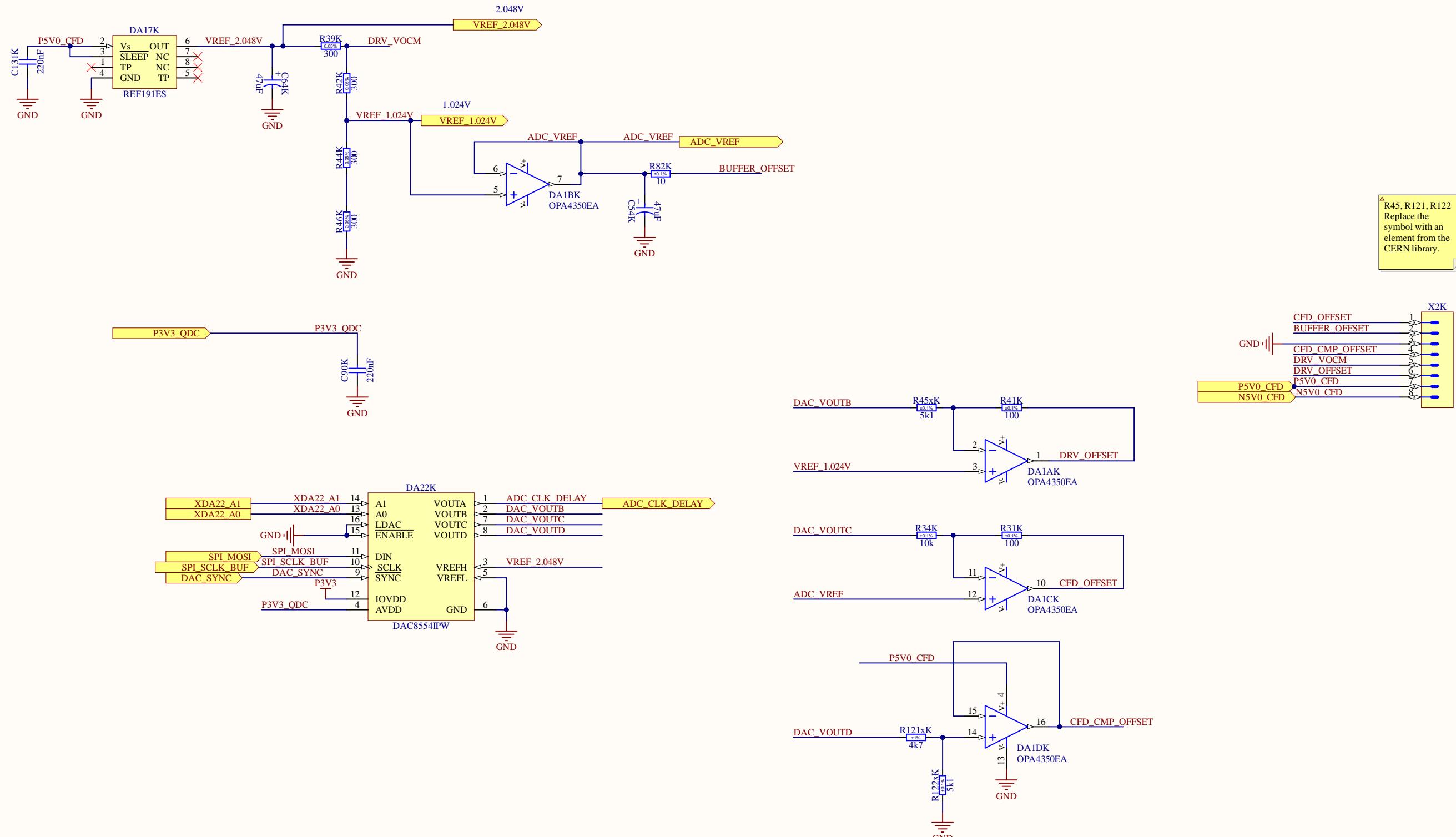
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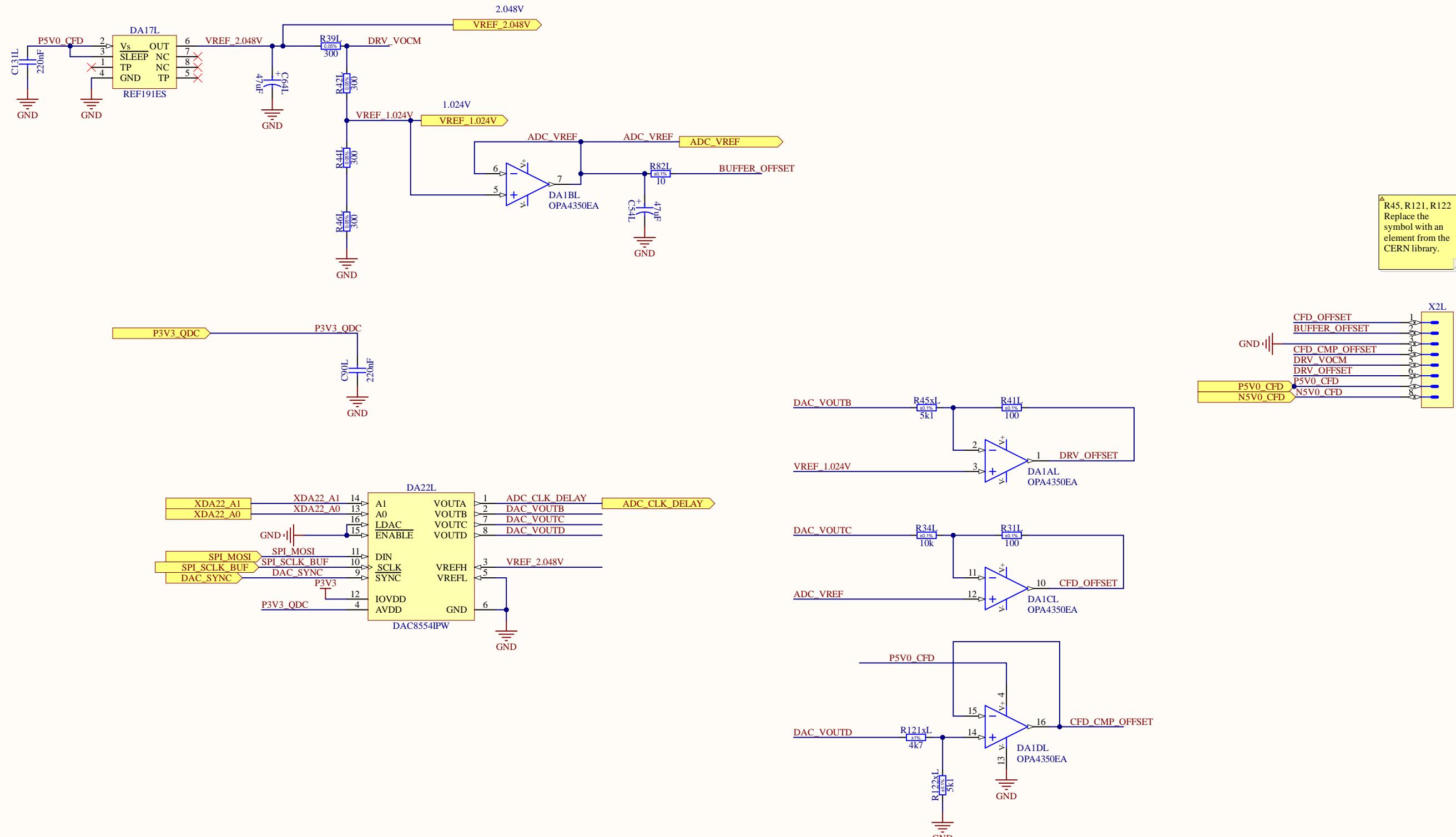
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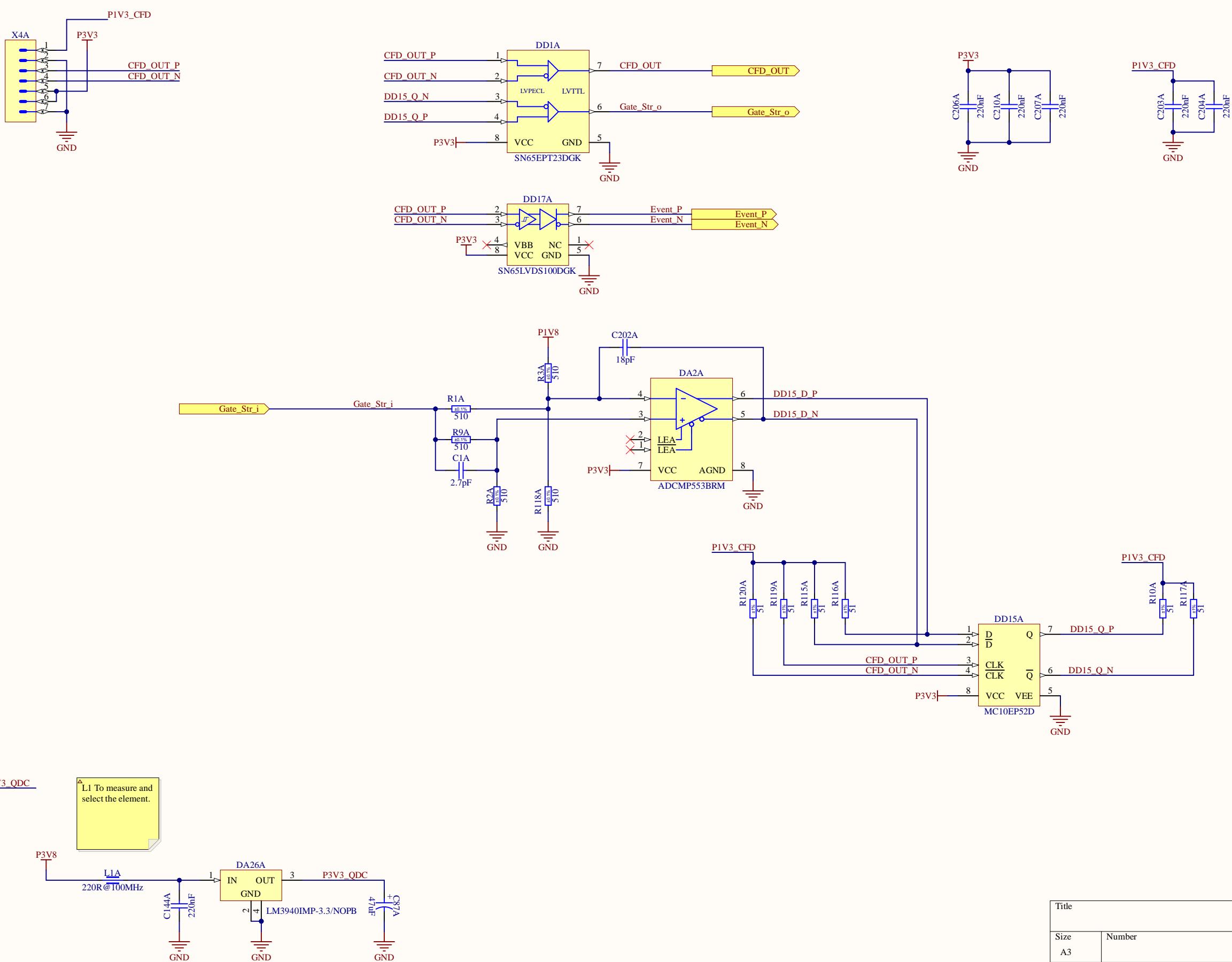
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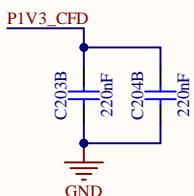
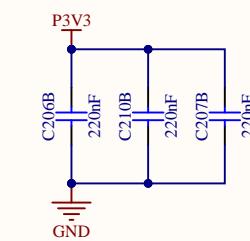
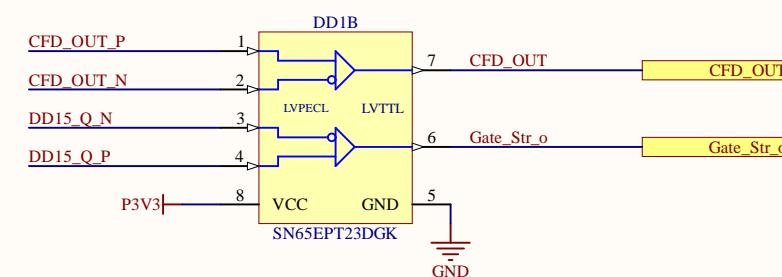
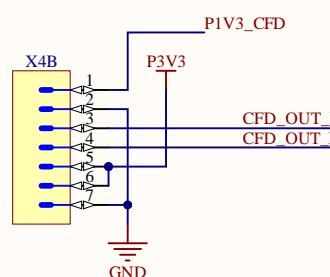


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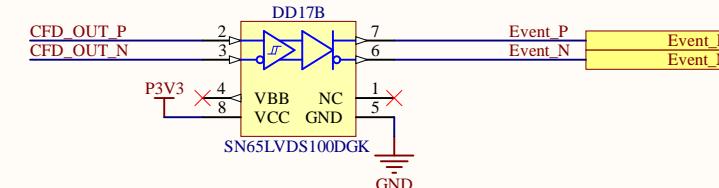


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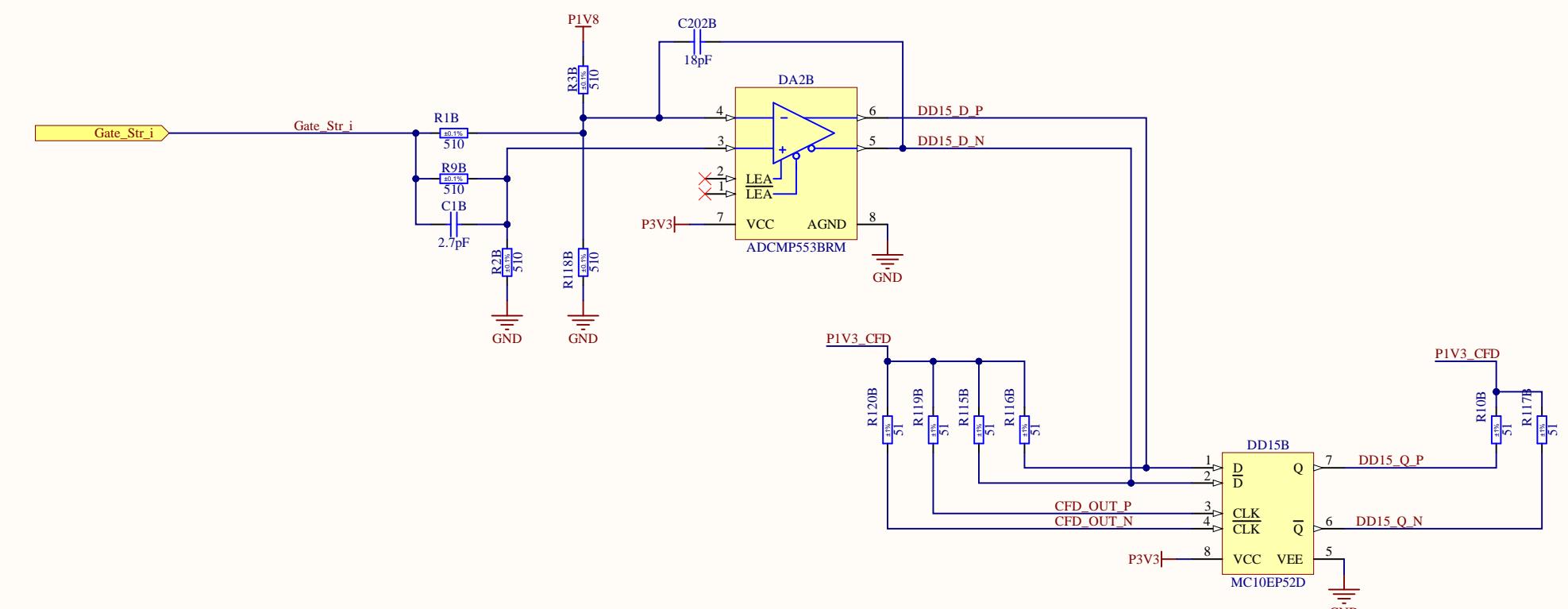
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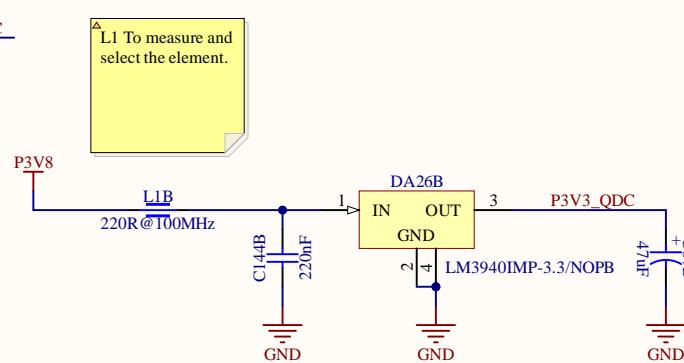
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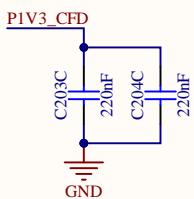
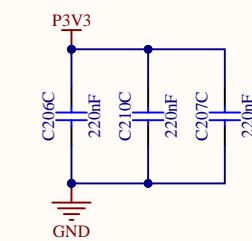
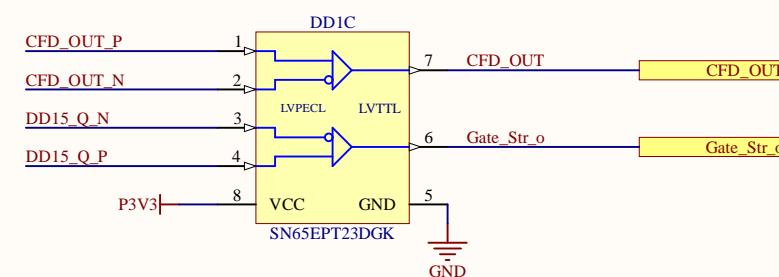
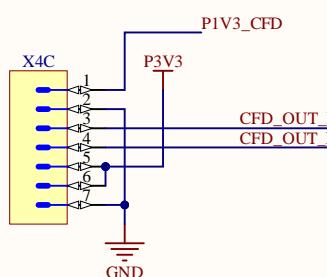


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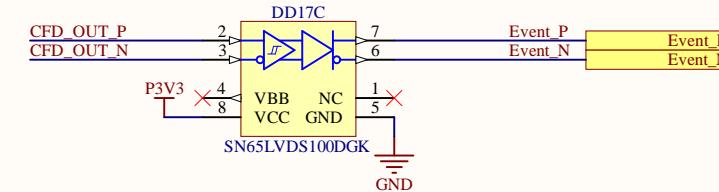


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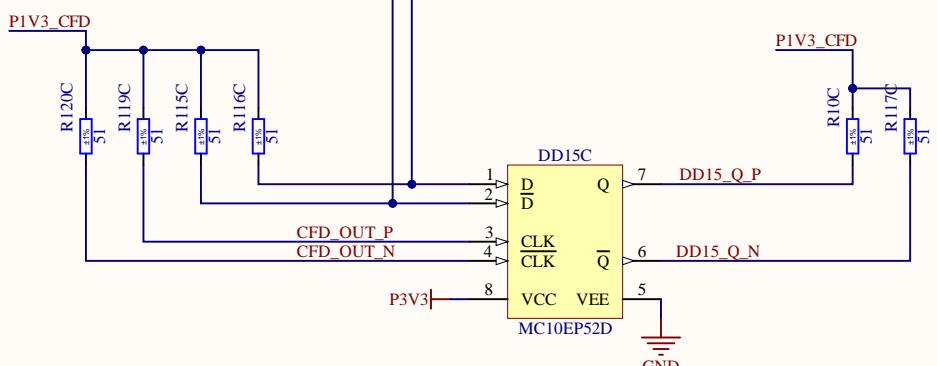
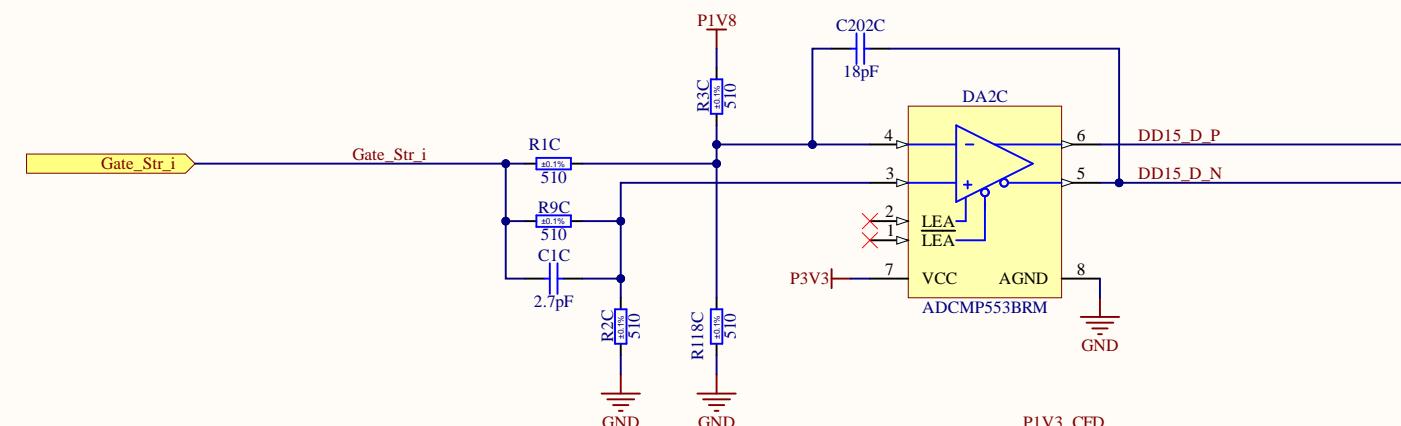
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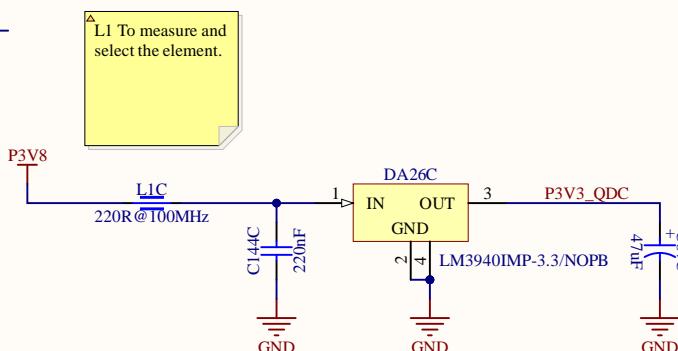
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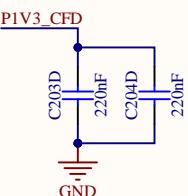
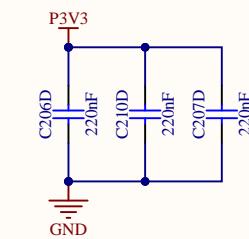
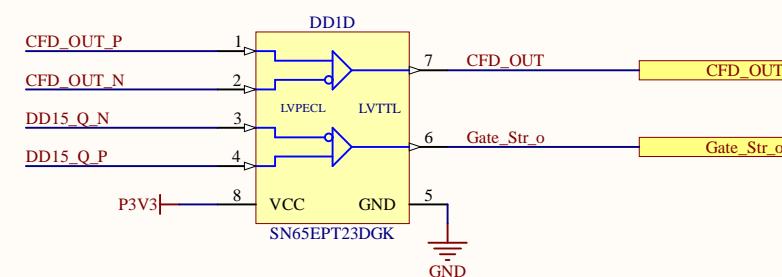
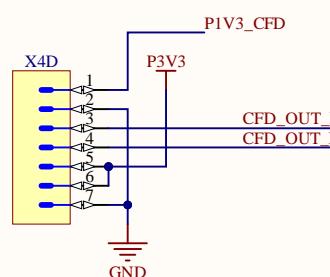


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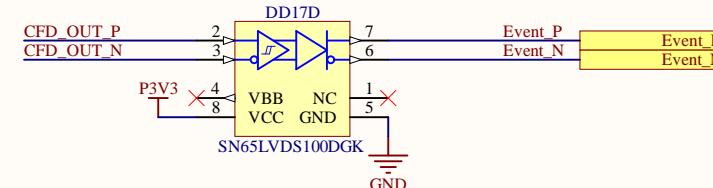


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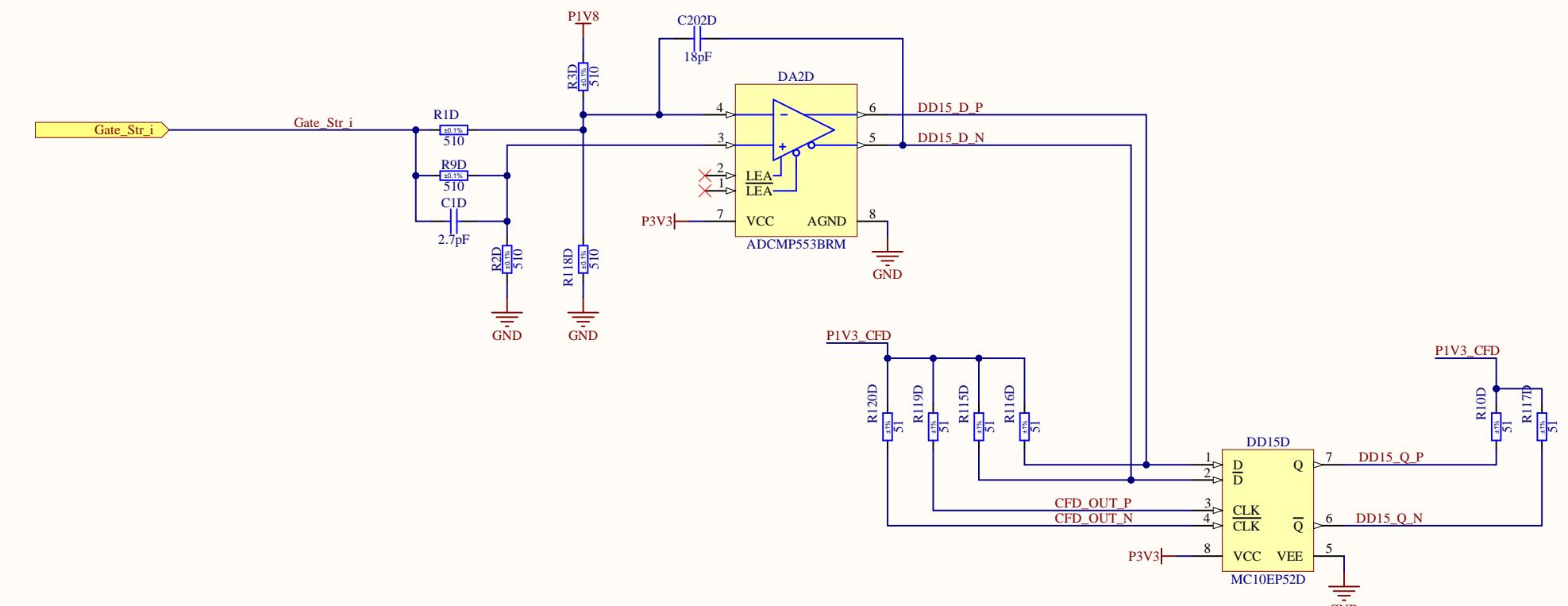
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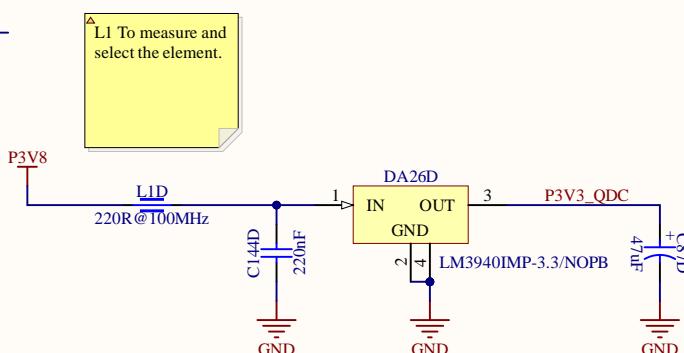
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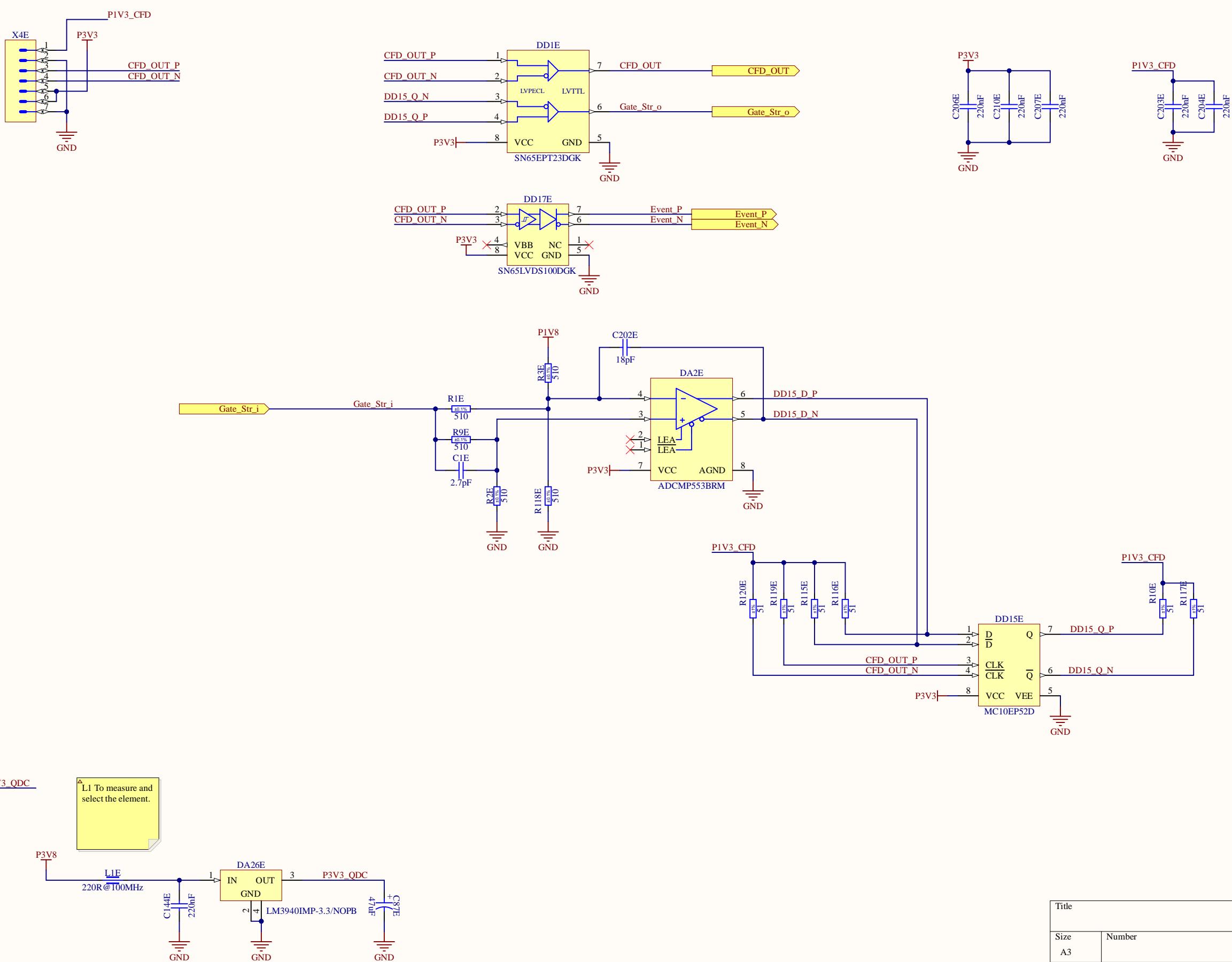
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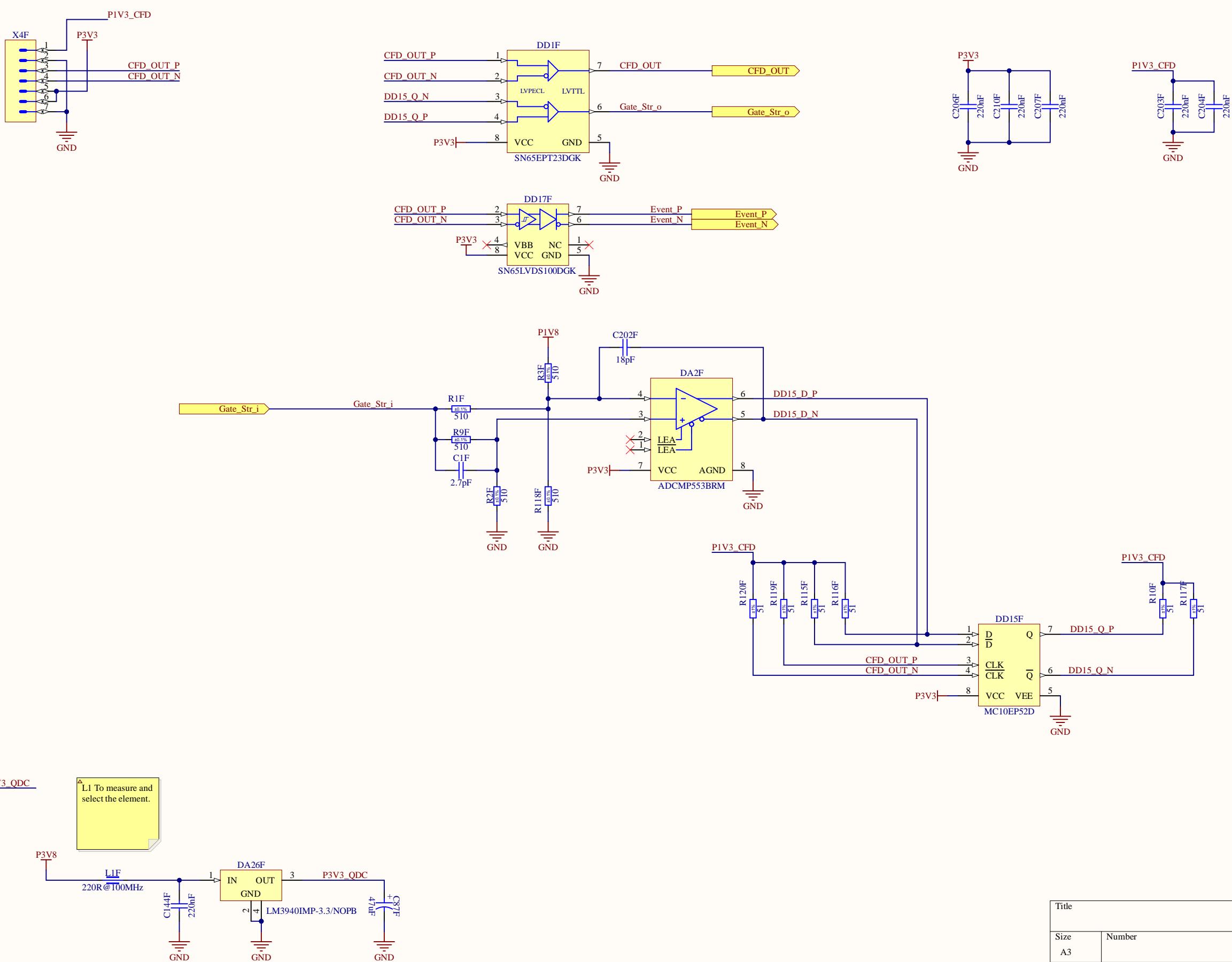
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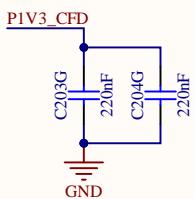
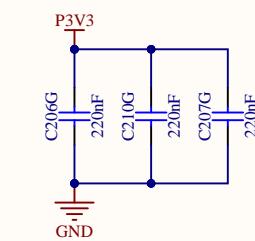
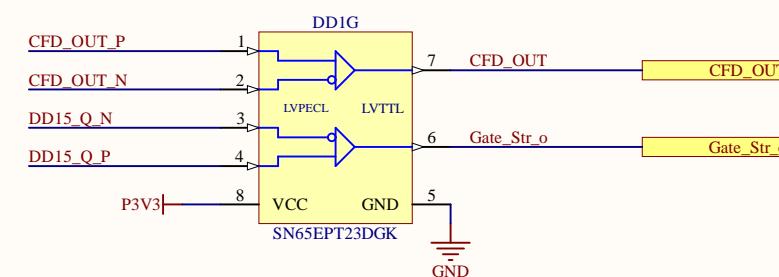
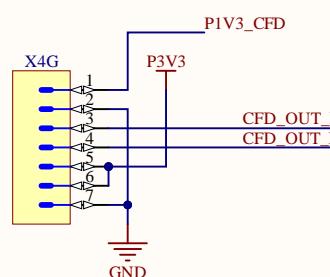


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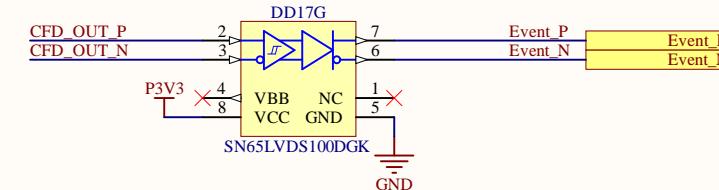


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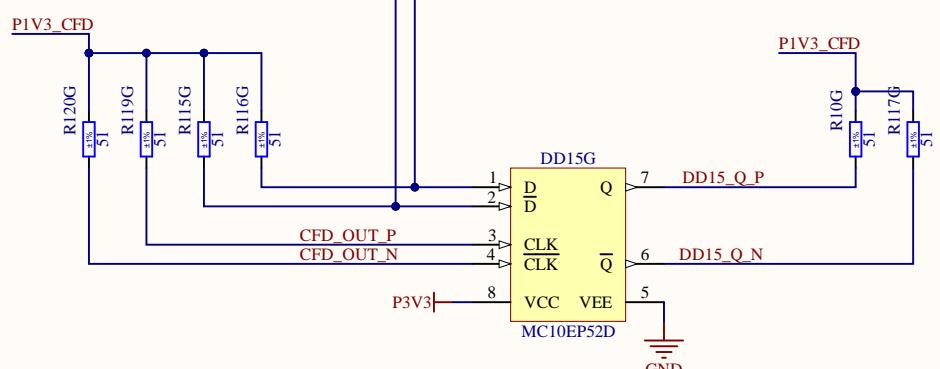
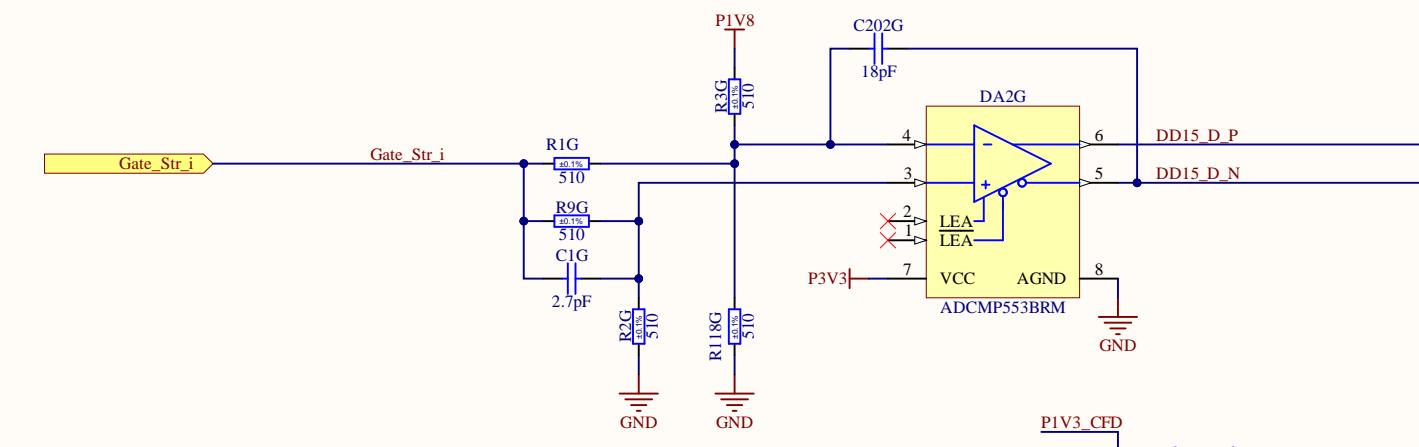
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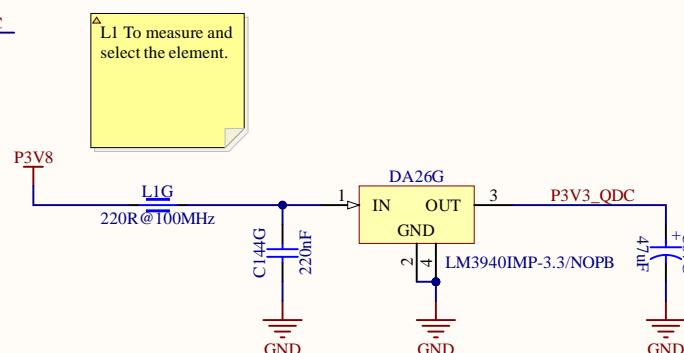
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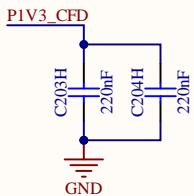
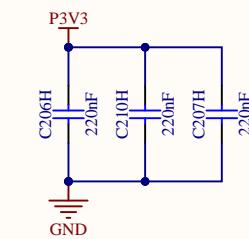
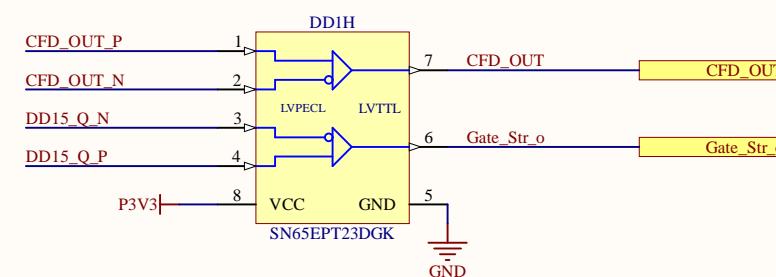
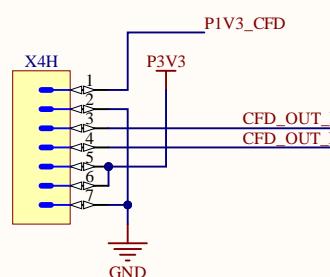


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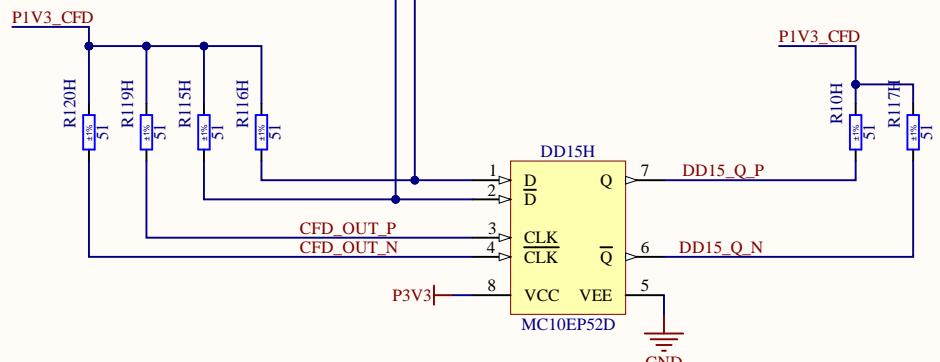
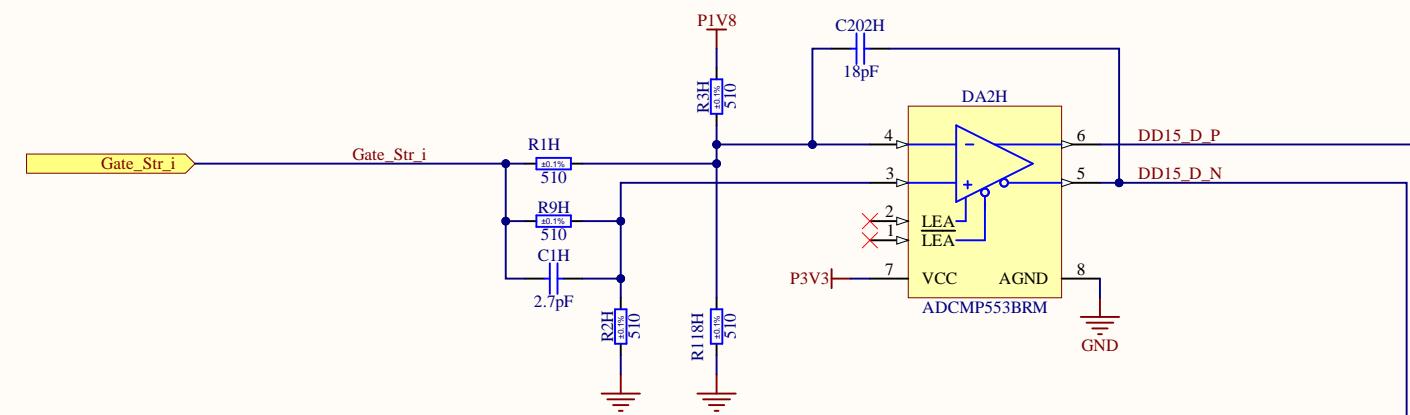
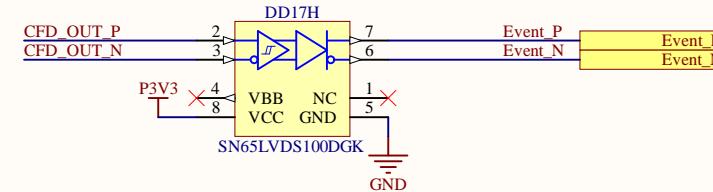


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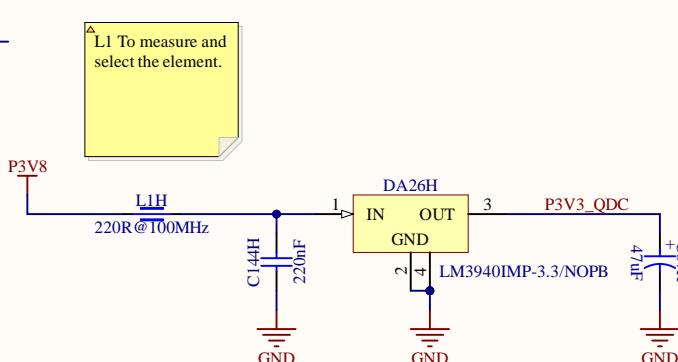
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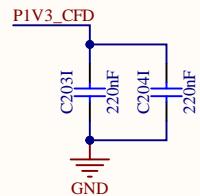
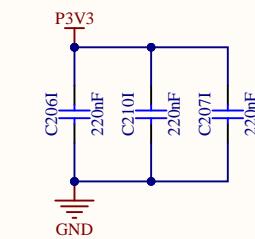
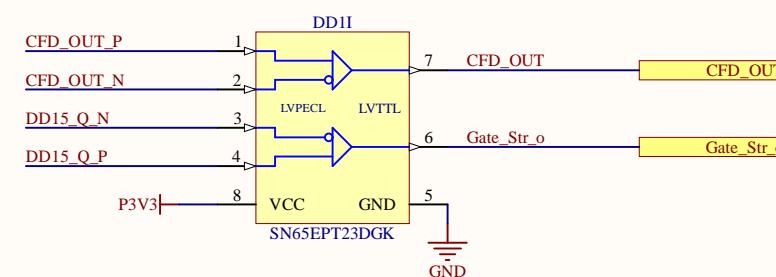
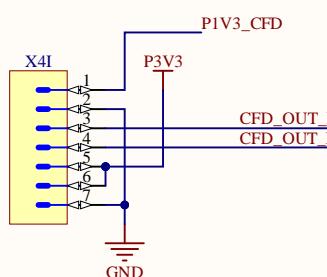
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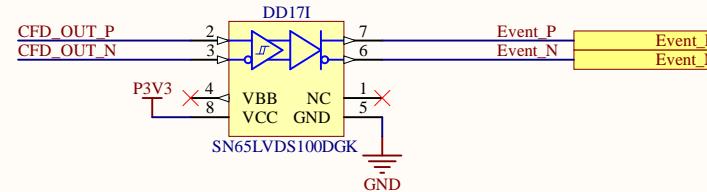
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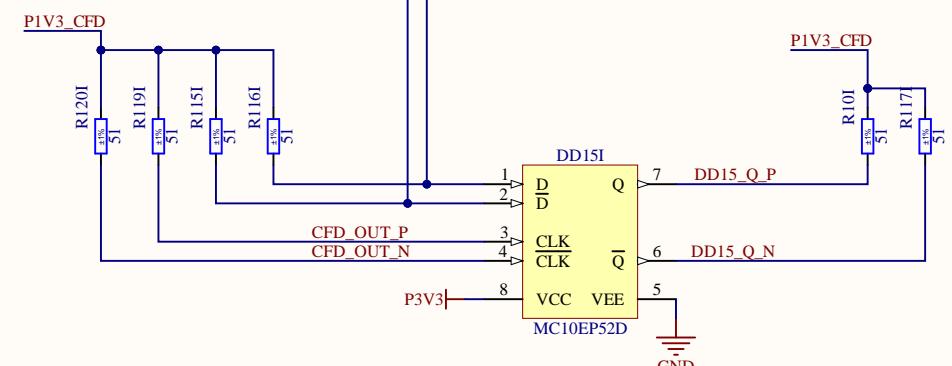
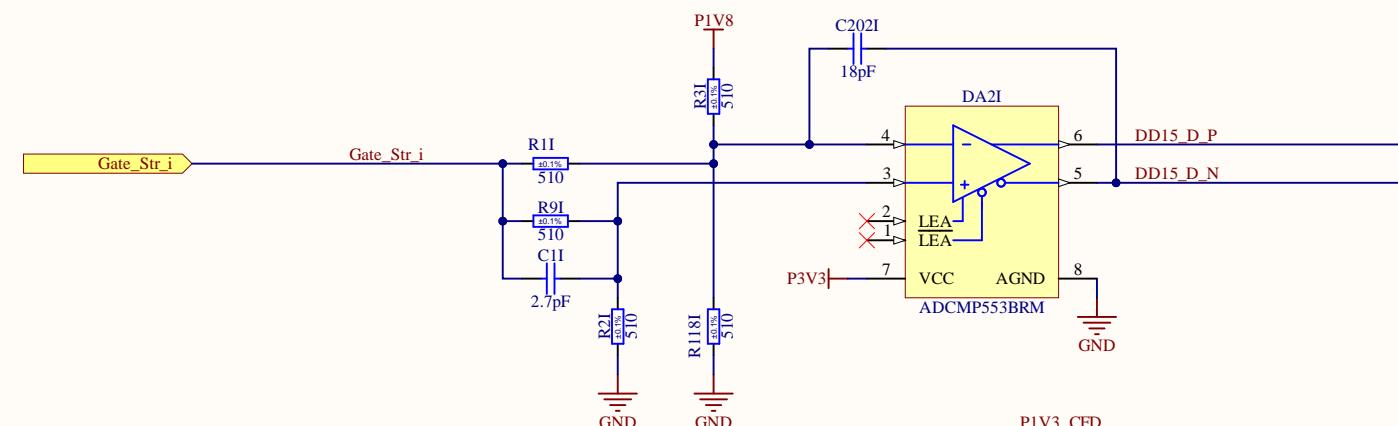
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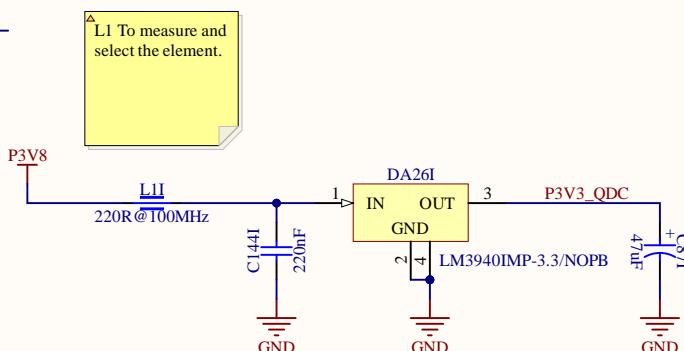
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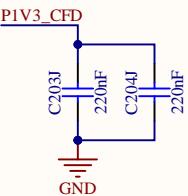
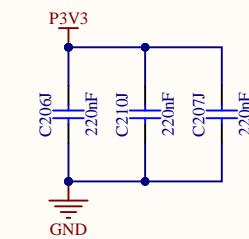
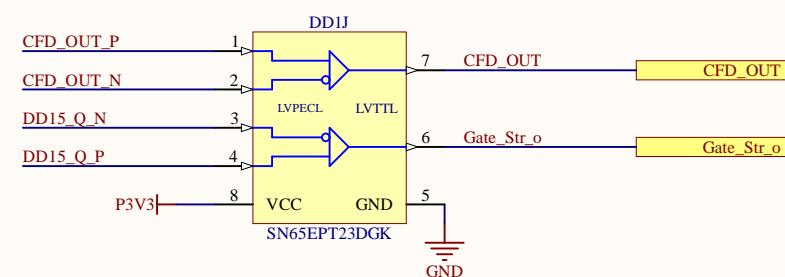
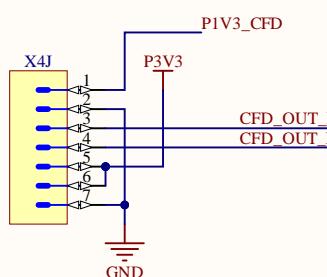


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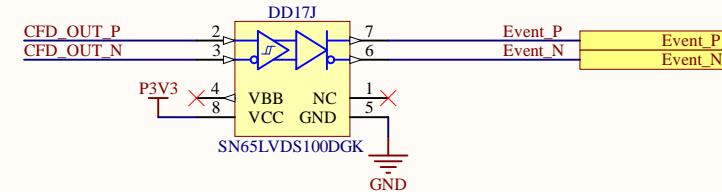


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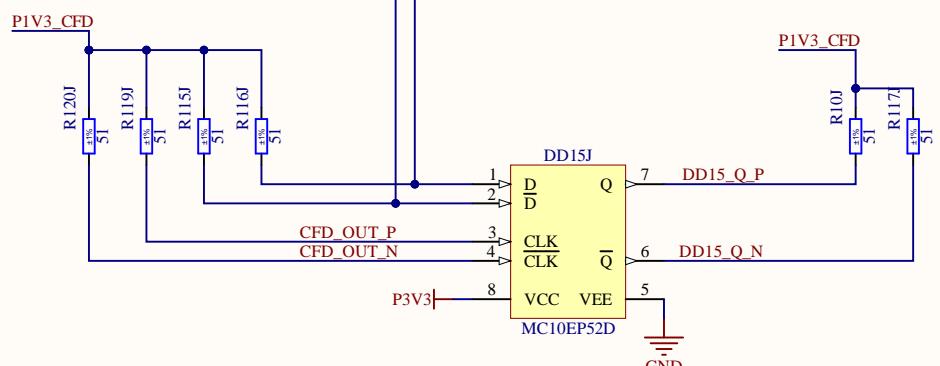
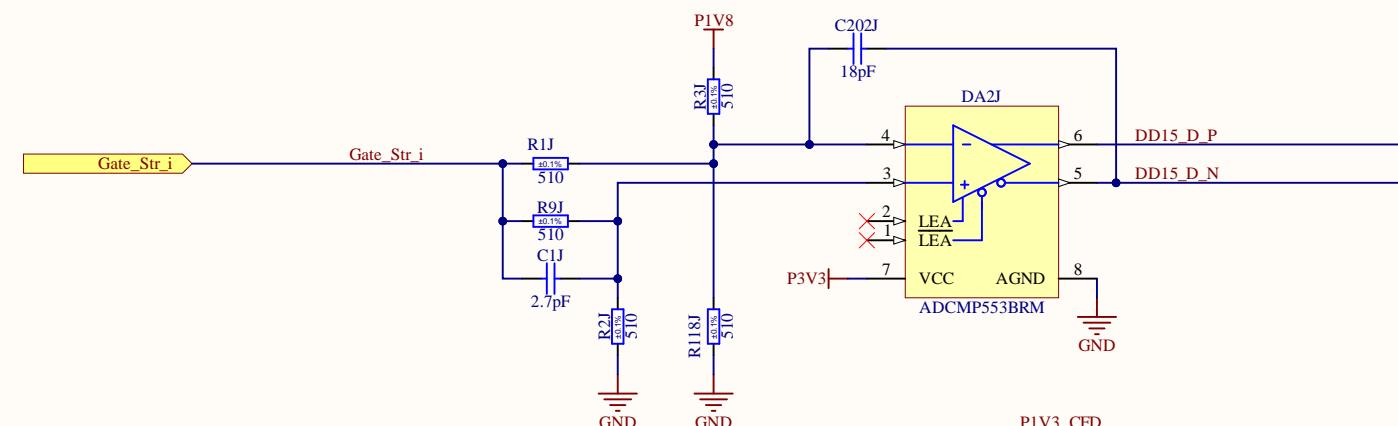
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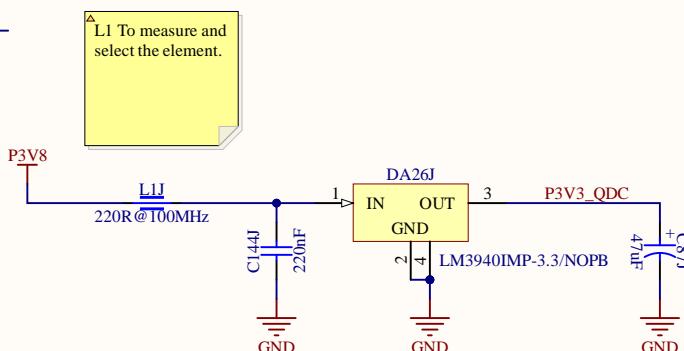
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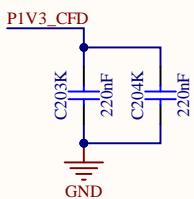
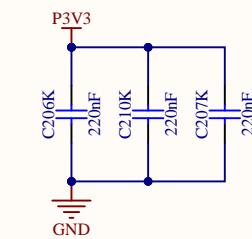
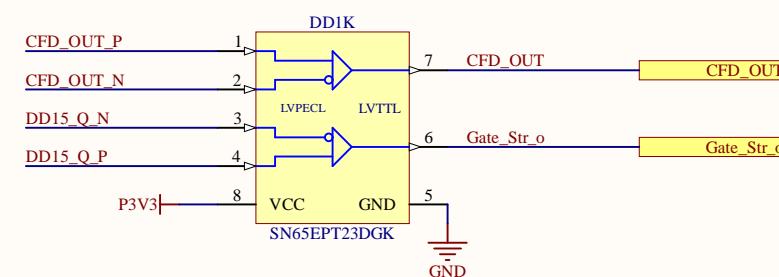
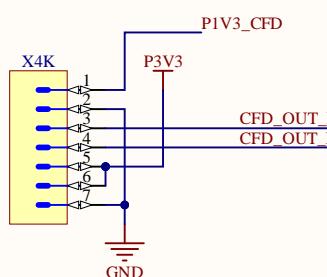


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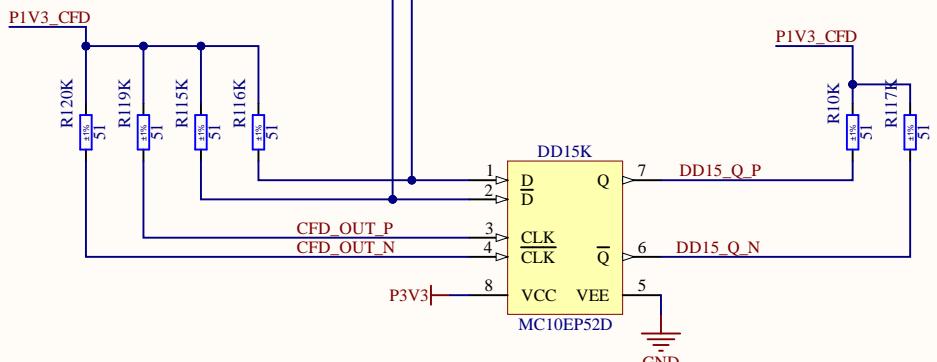
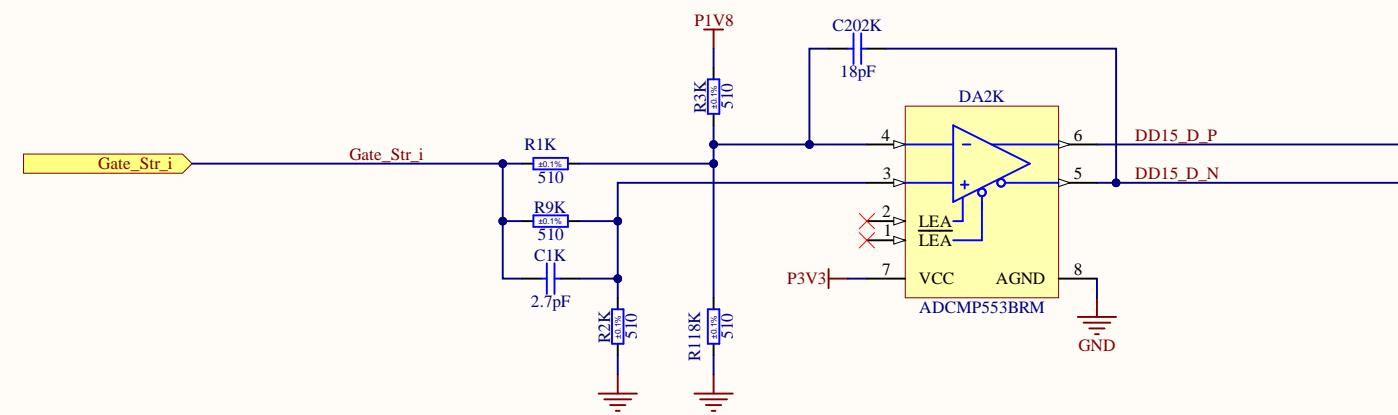
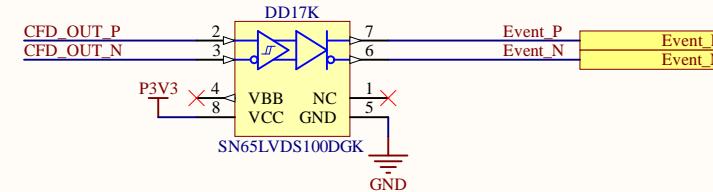


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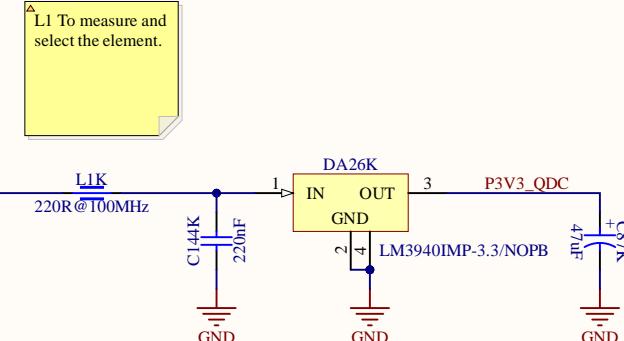
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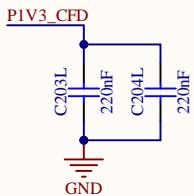
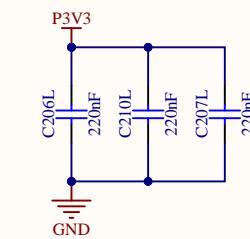
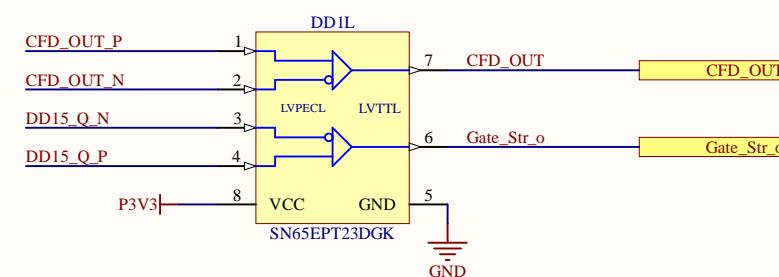
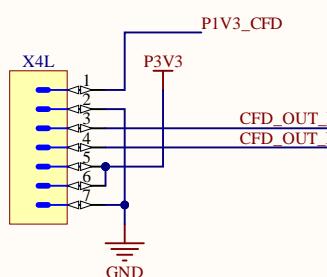
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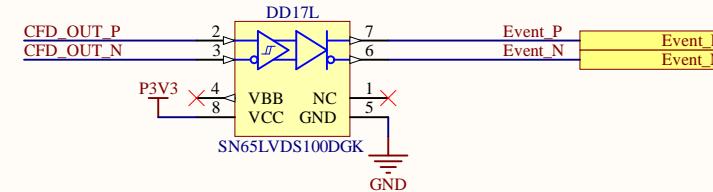
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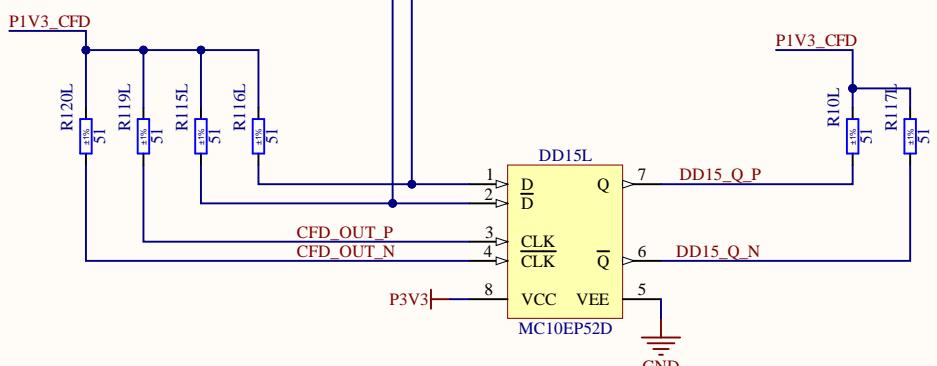
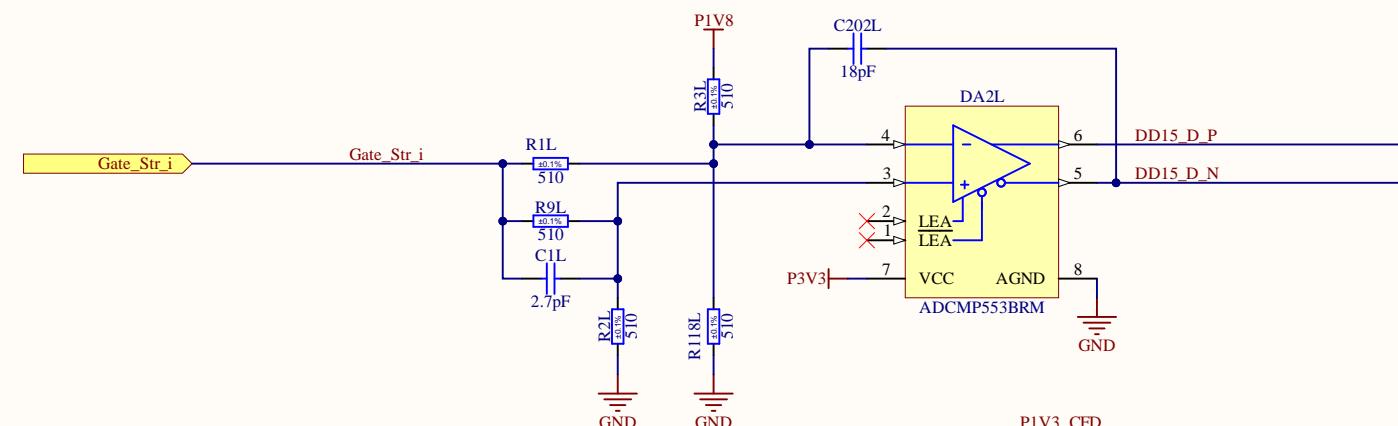
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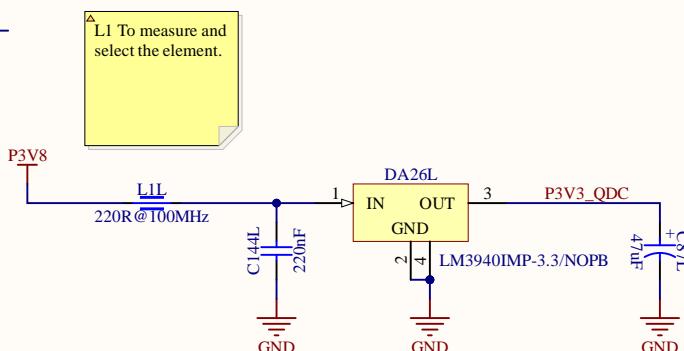
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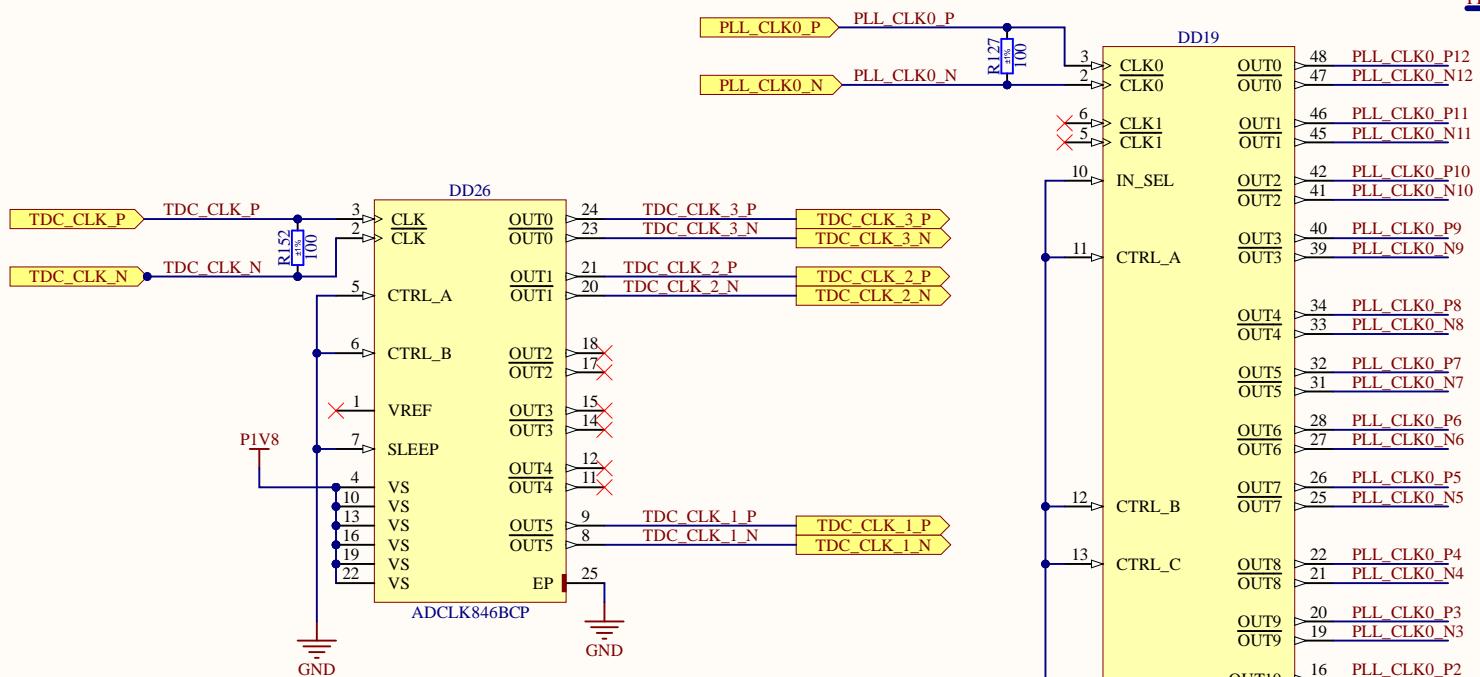


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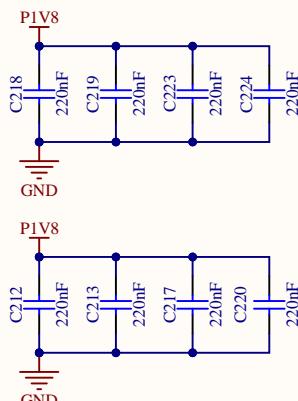


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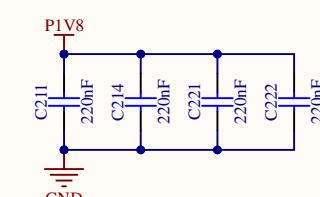
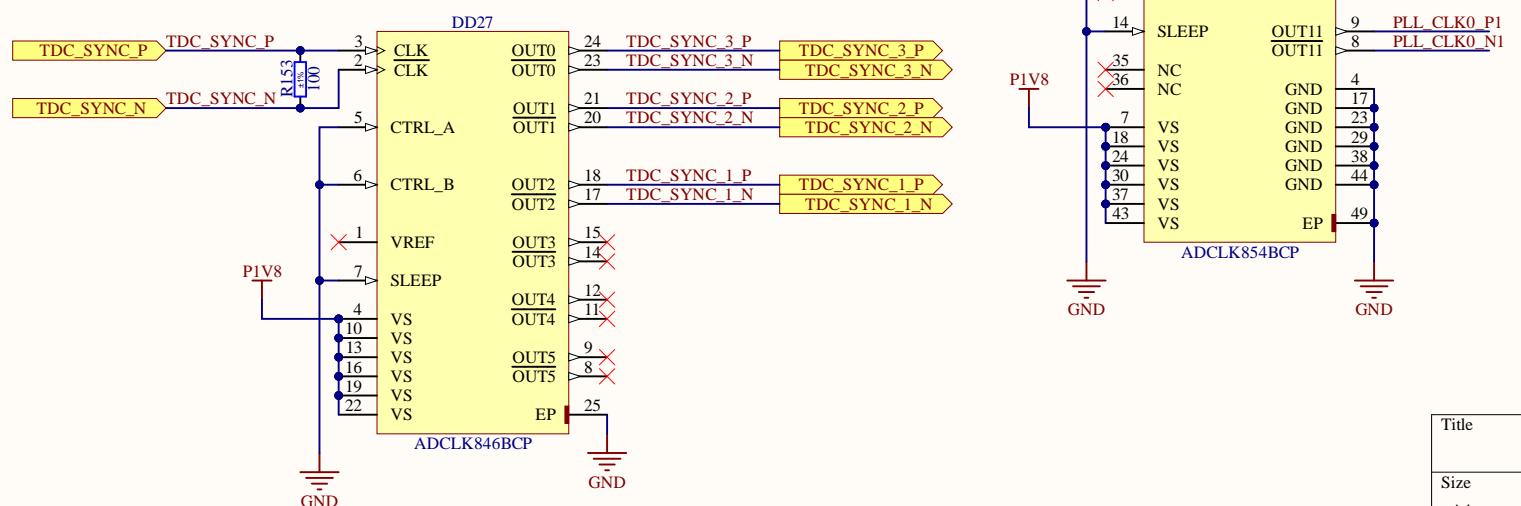
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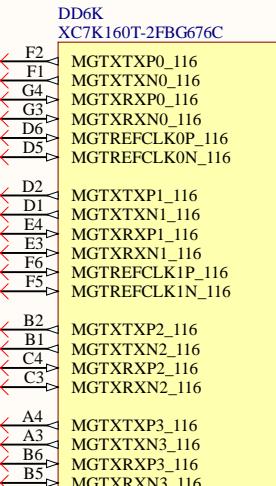
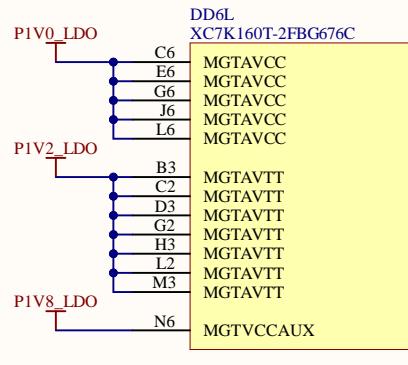
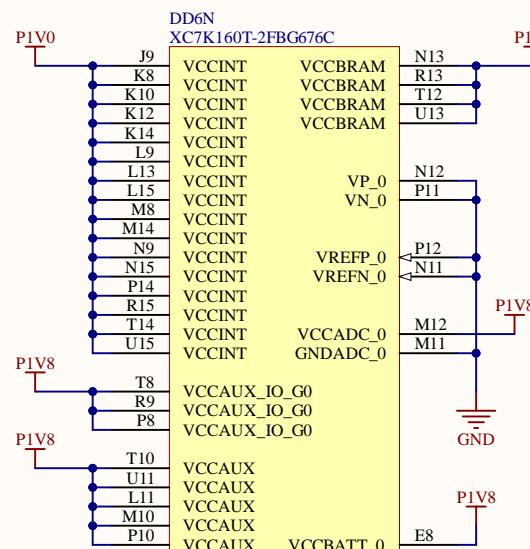
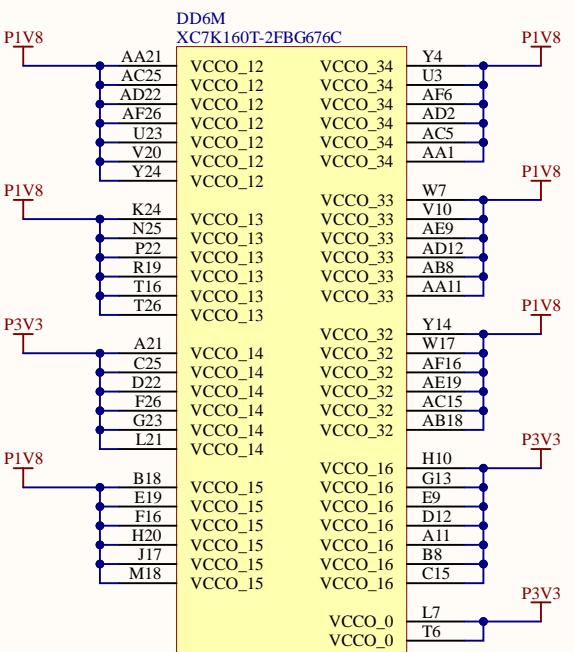
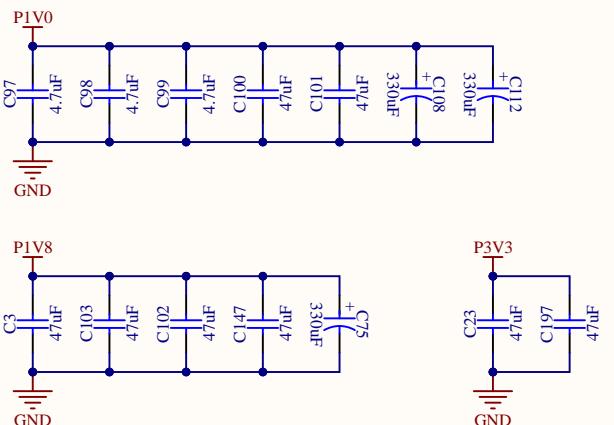
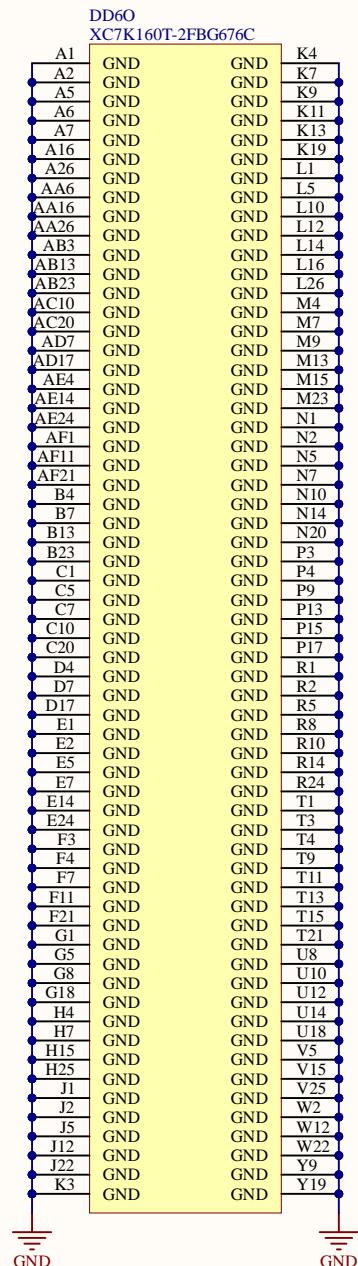


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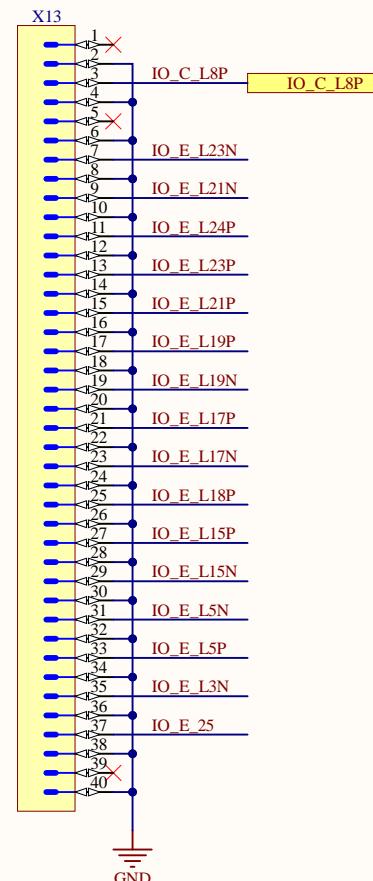
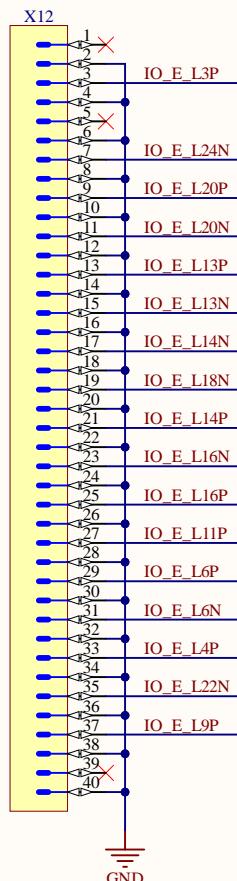
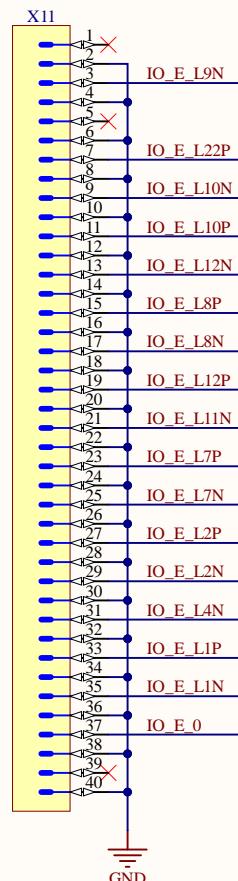
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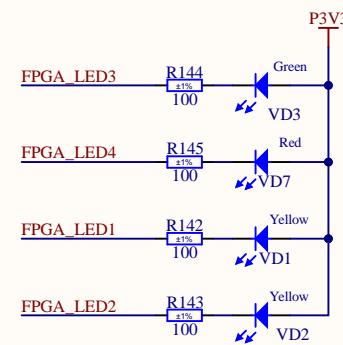
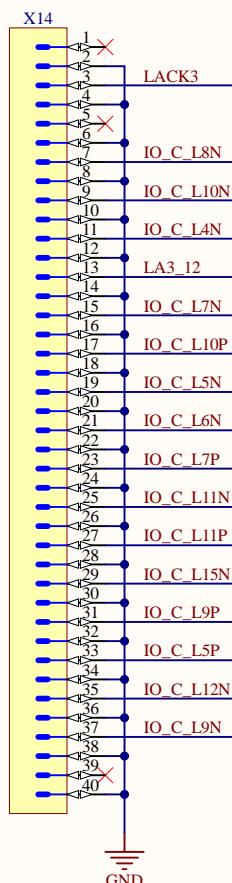
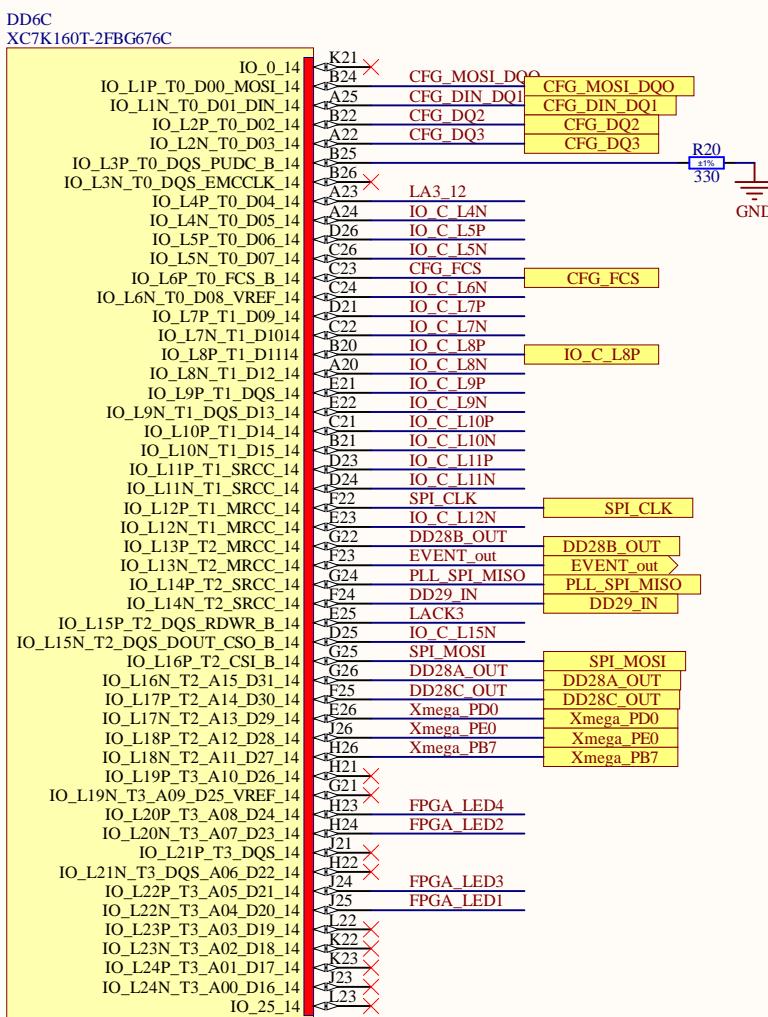
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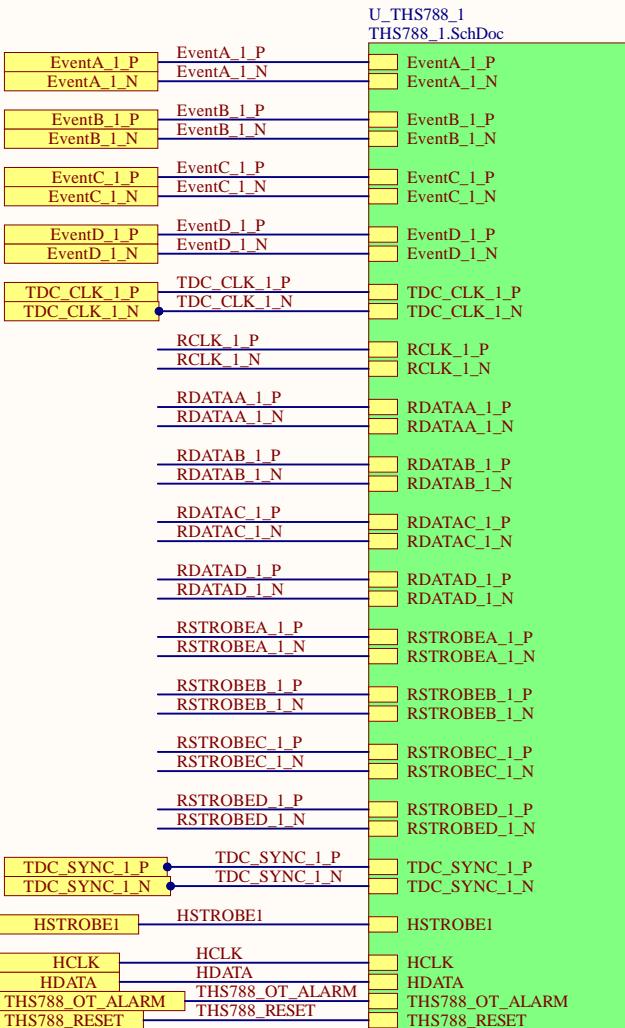
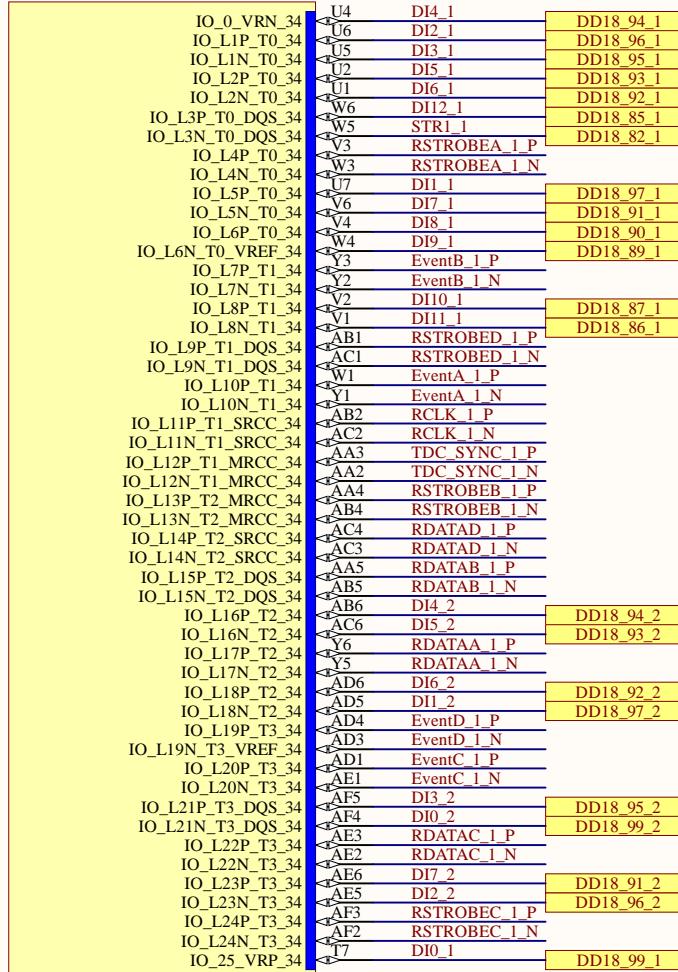
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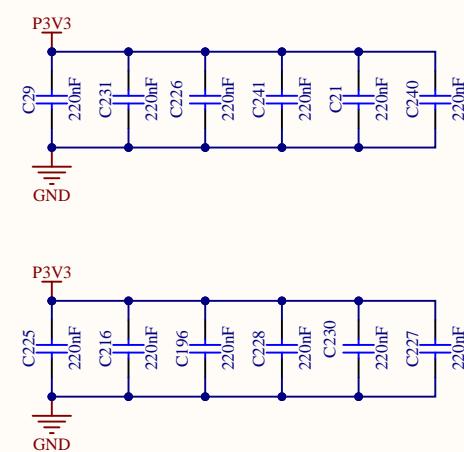
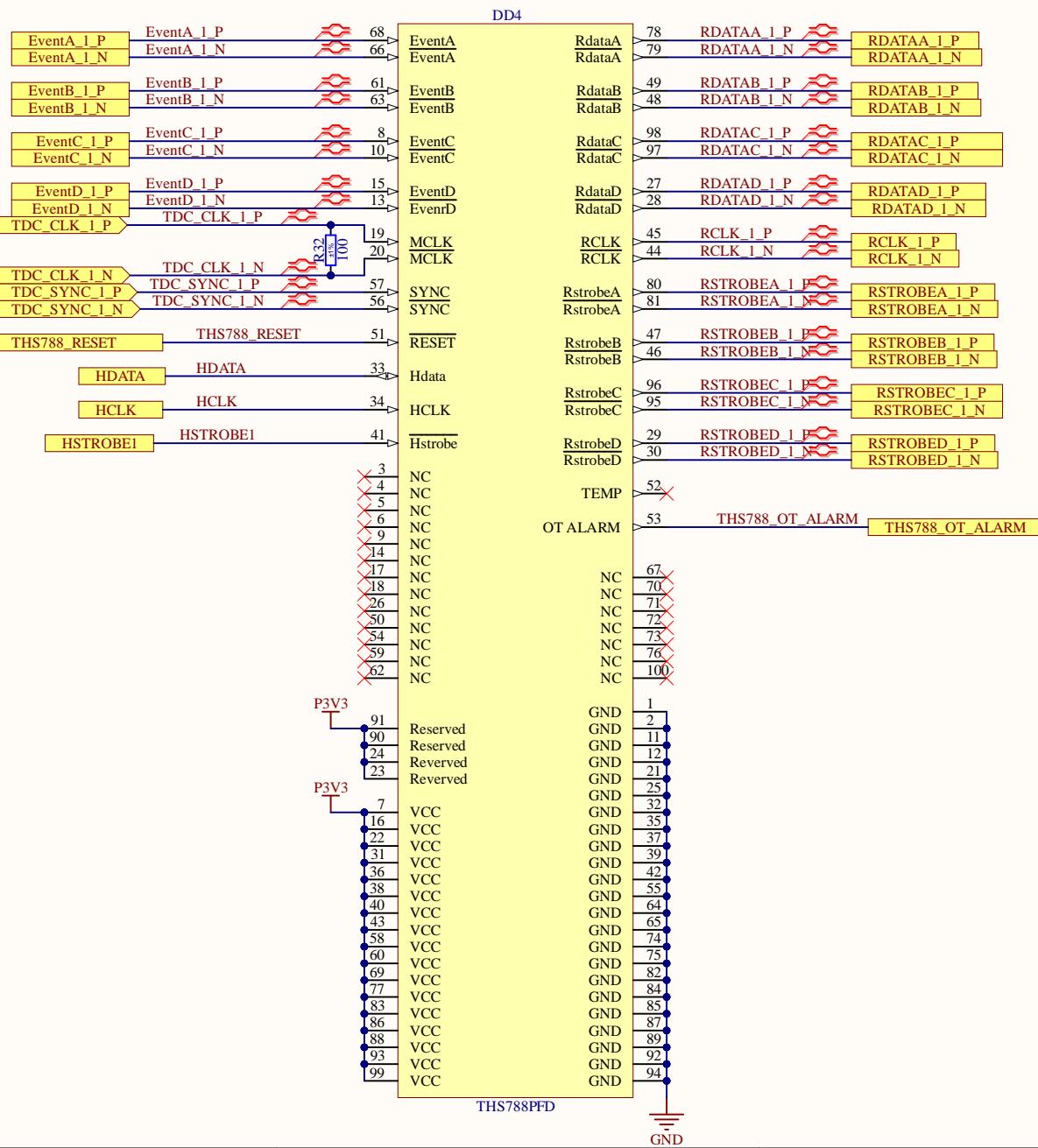
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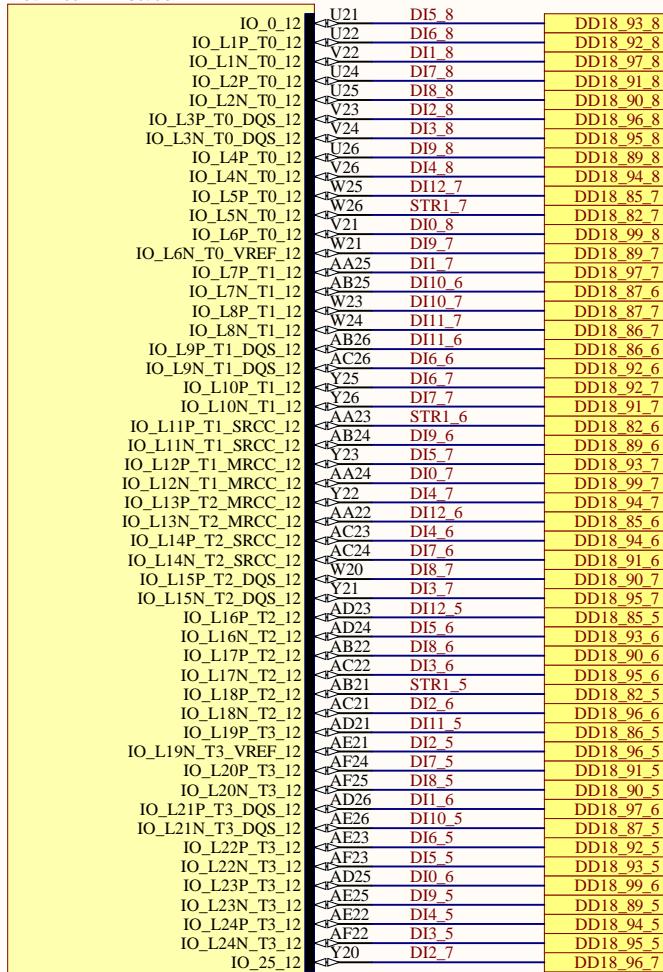
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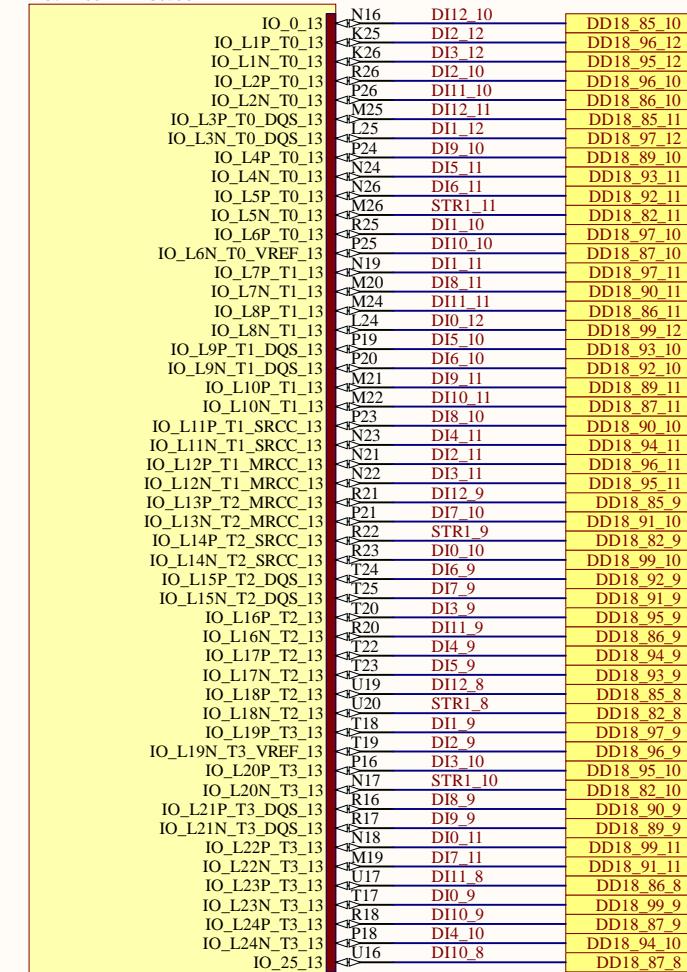
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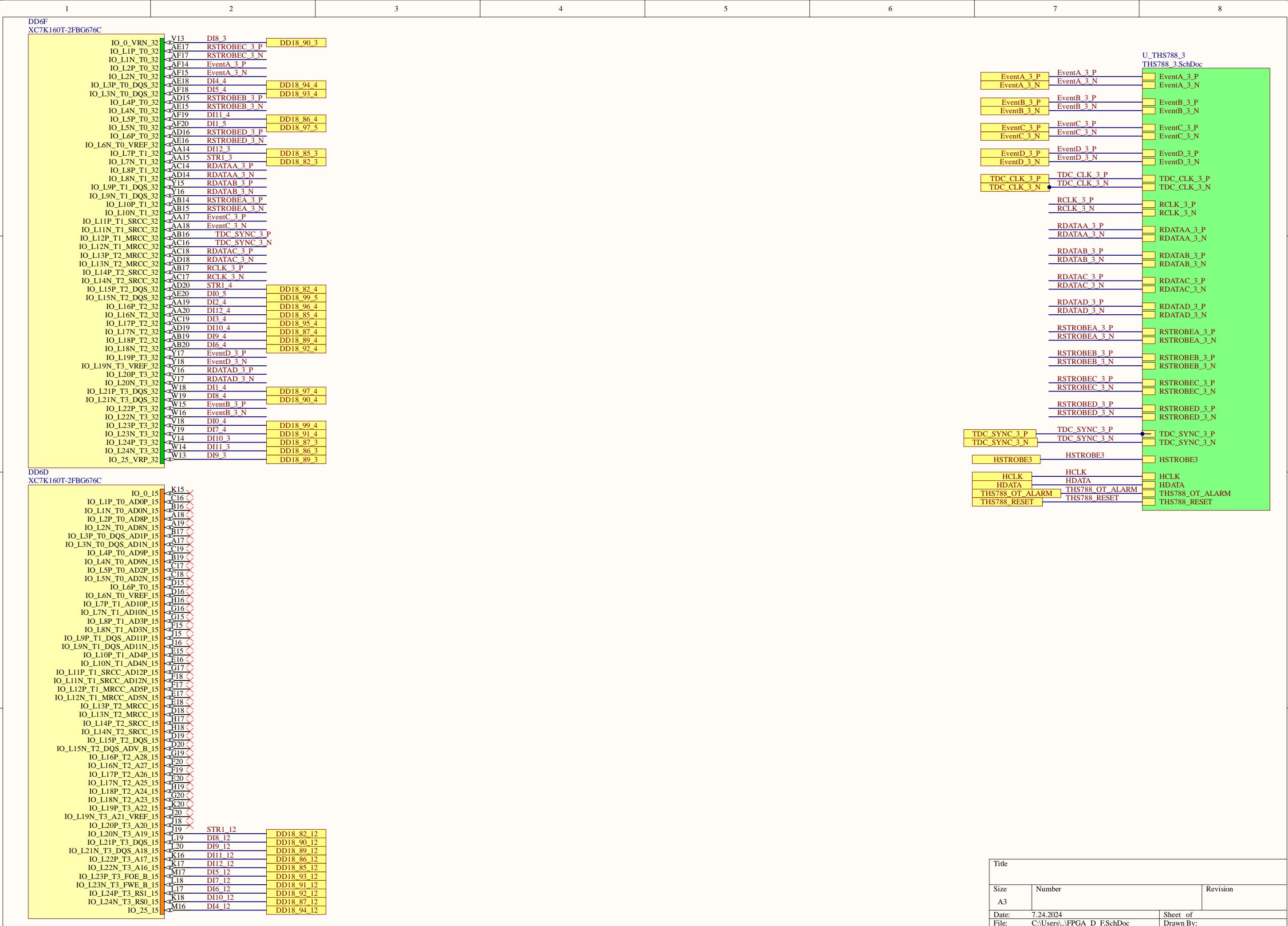


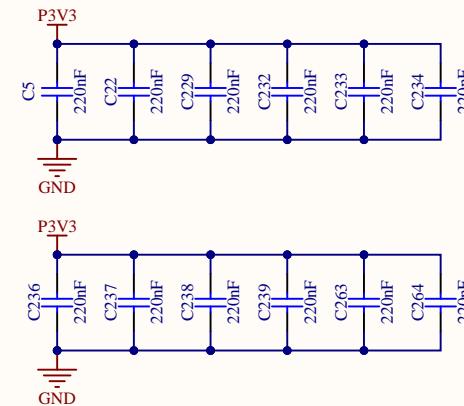
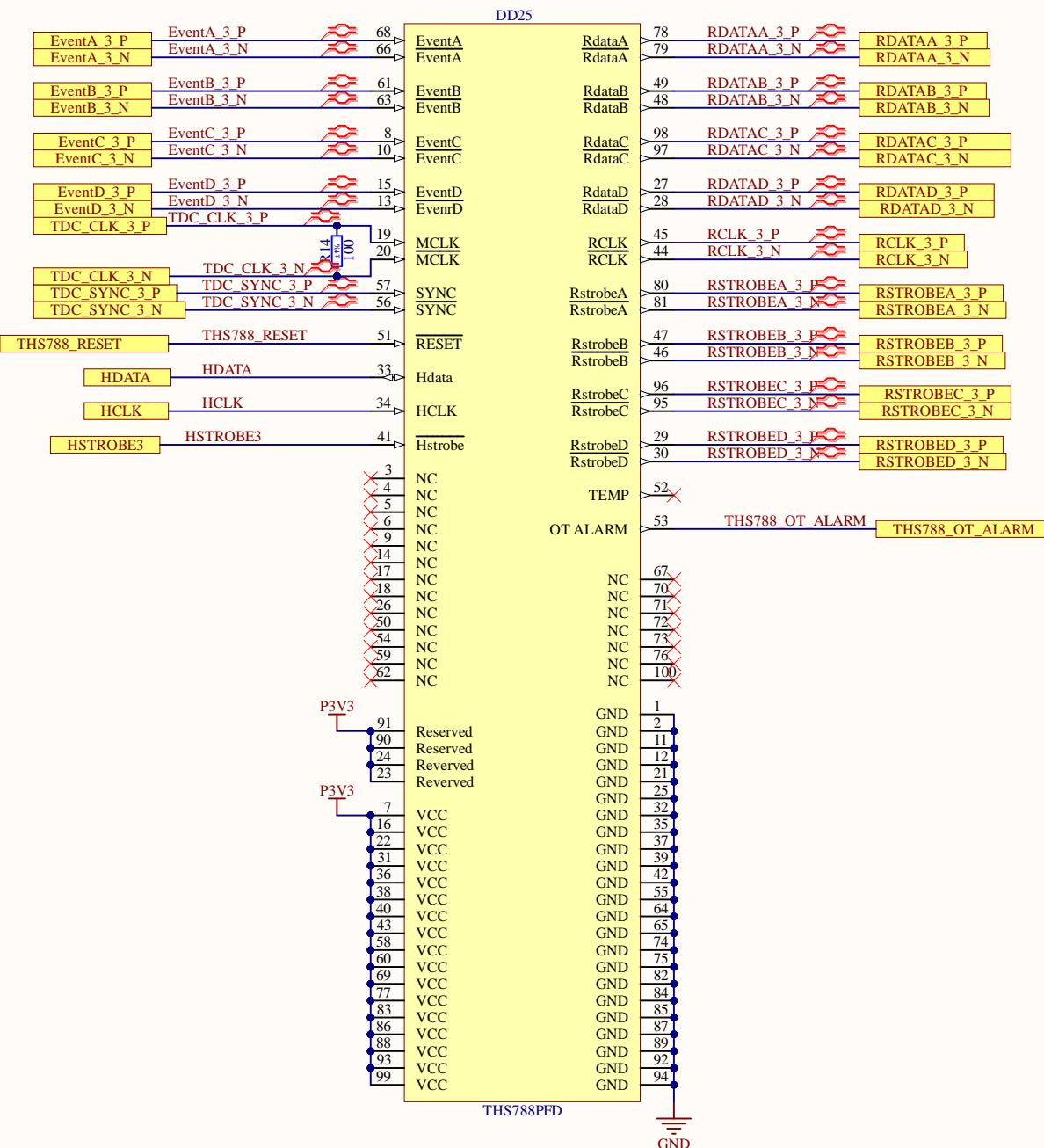
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Date: 7.24.2024 Sheet of
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Title		
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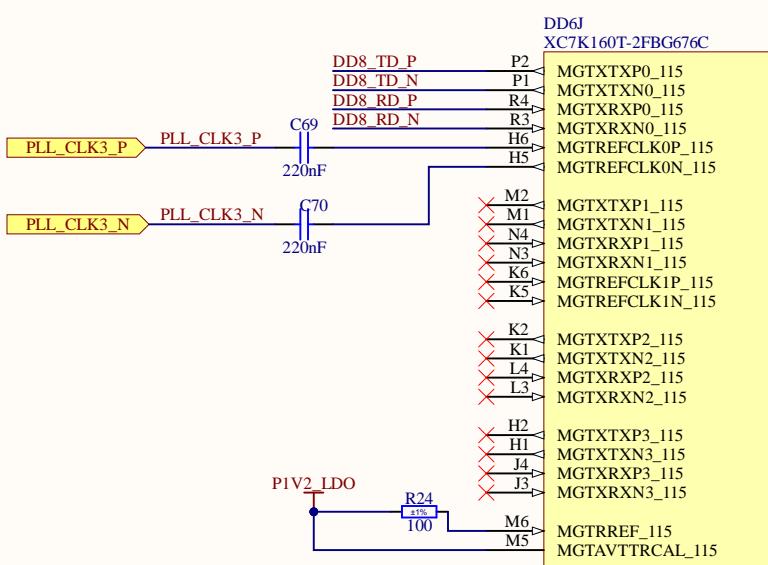
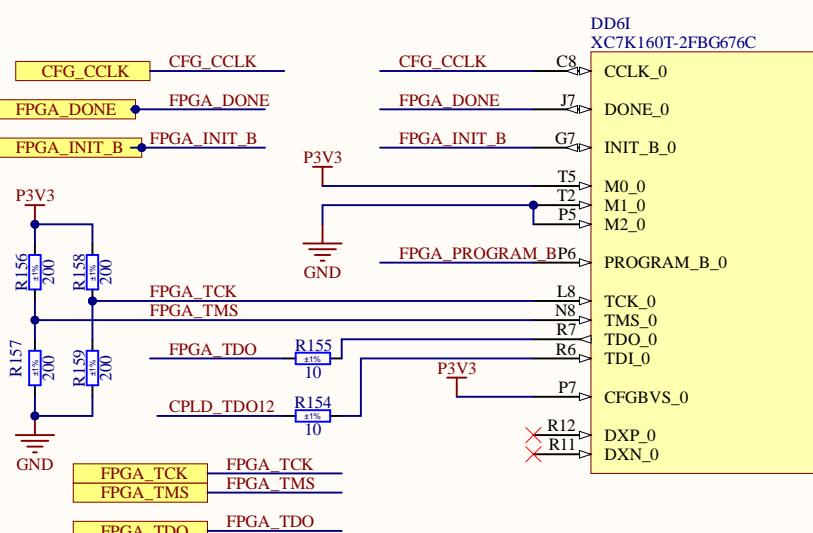
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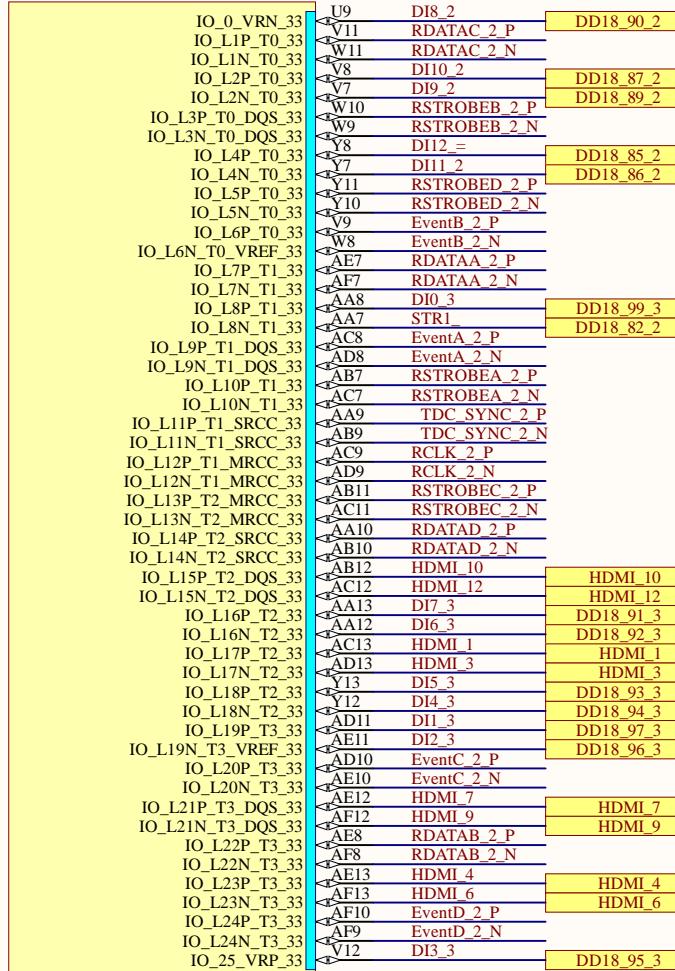
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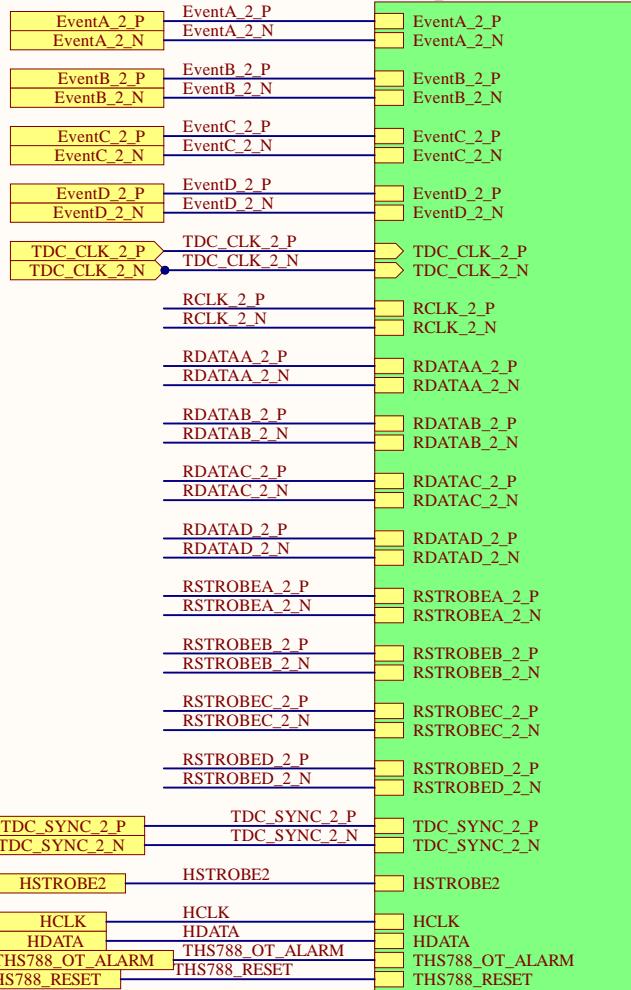
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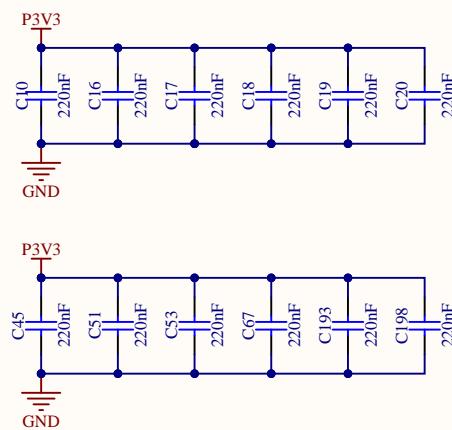
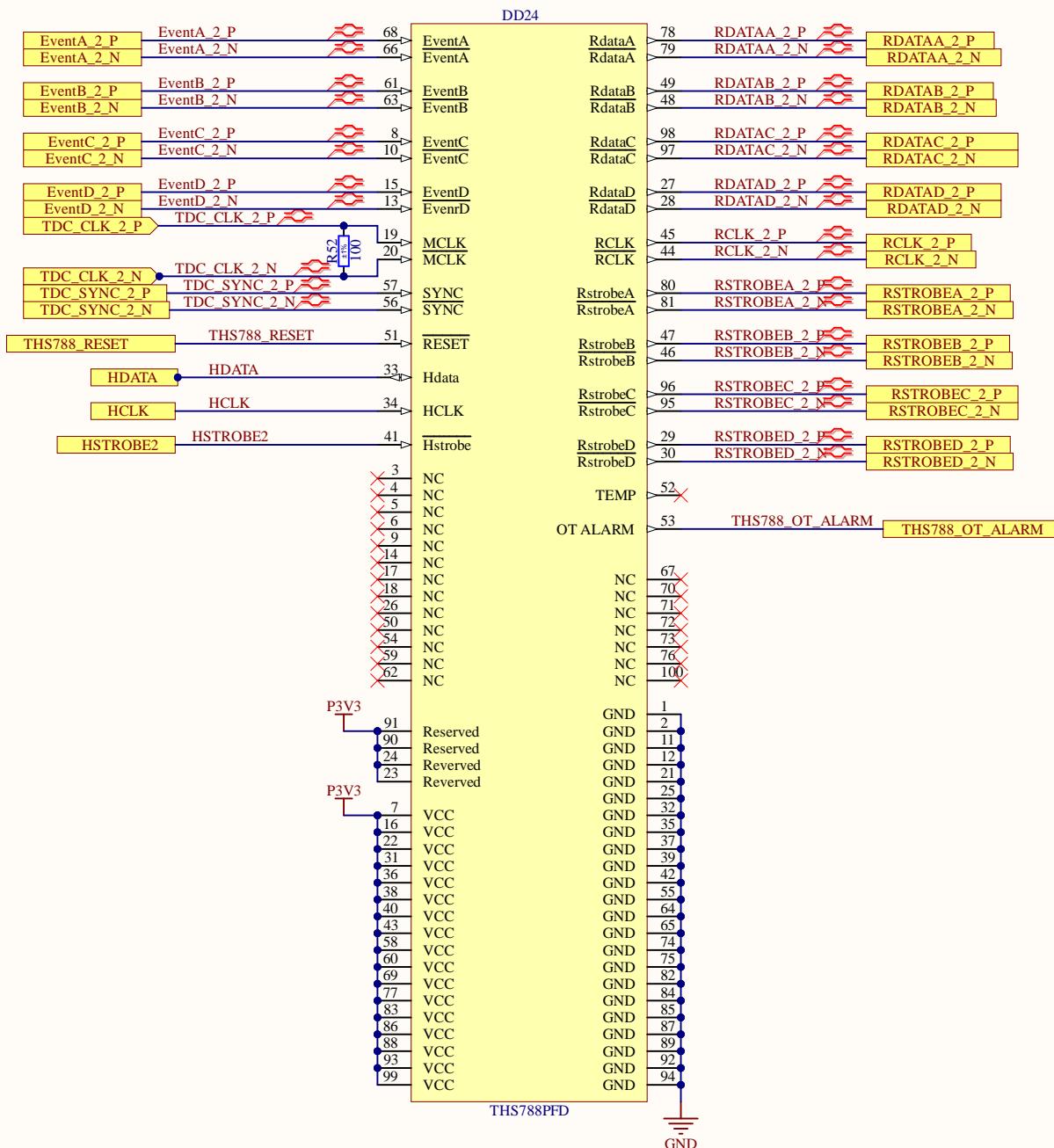
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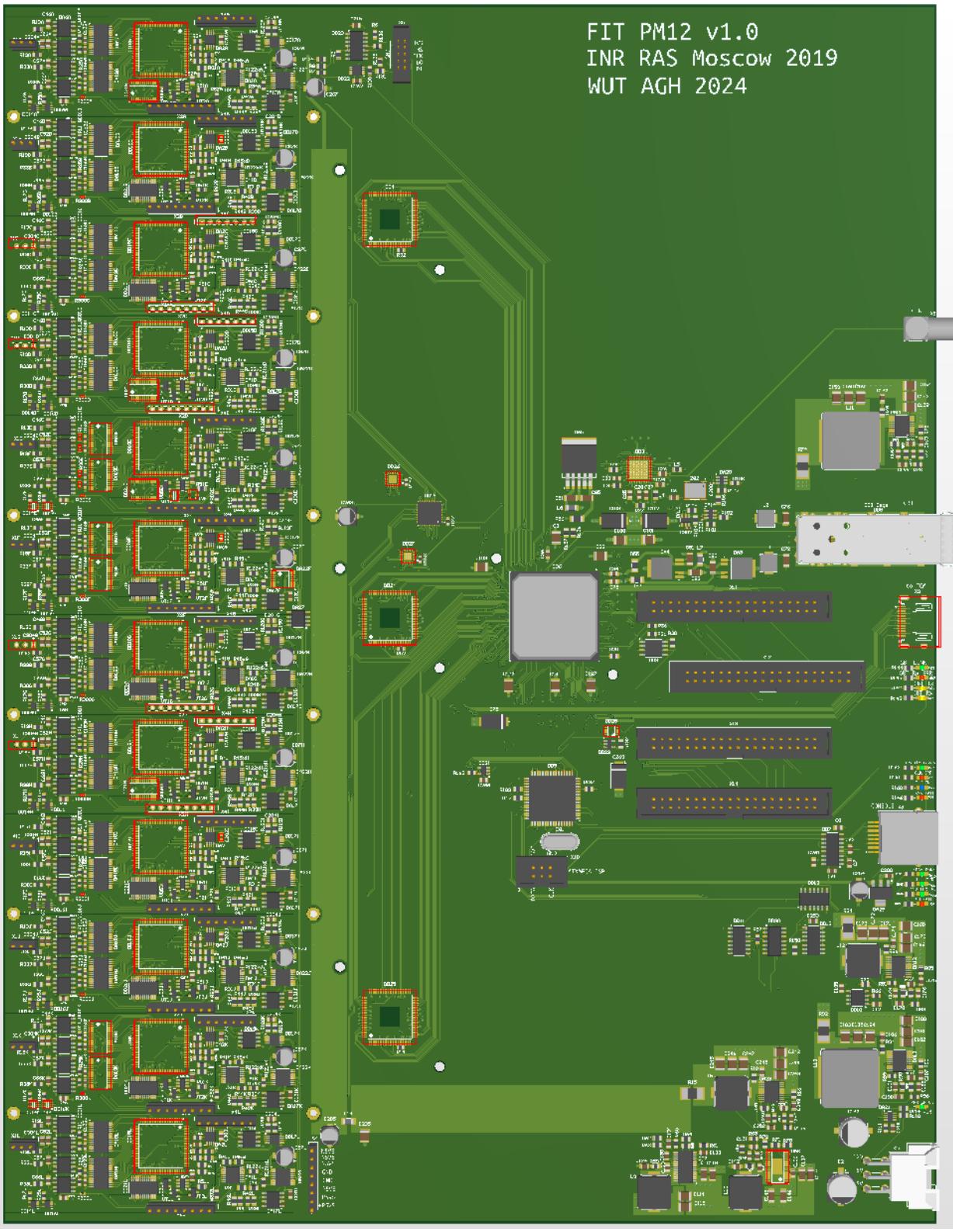
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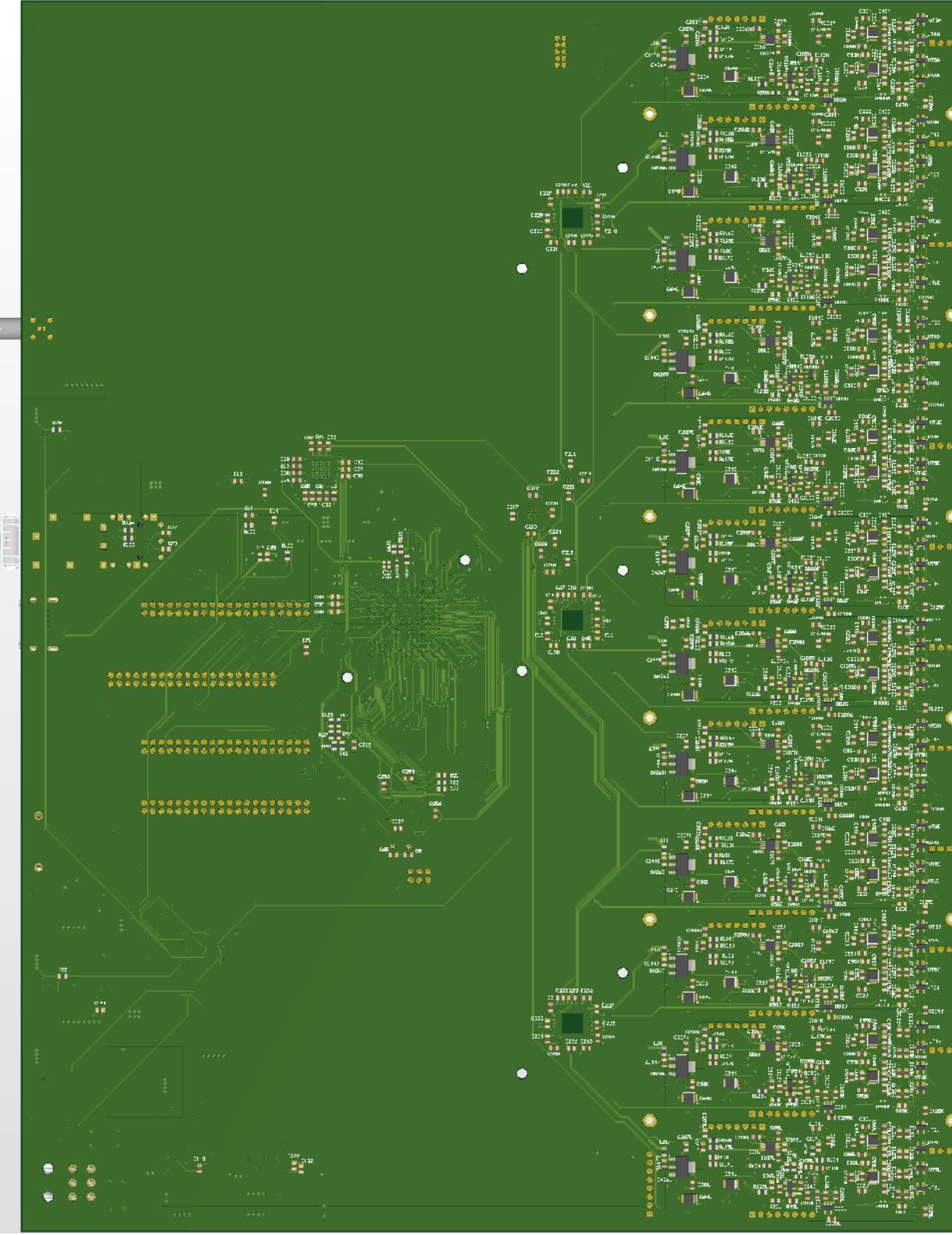


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INR RAS Moscow 2019
WUT AGH 2024








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
INR RAS Moscow 2019

WUT AGH 2024











Board Stack Report

Stack Up		Layer Stack				
Layer	Board Layer Stack	Name	Material	Thickness	Constant	Dk
1		Top Overlay		0mm		
2		Top Solder	Solder Resist	0.01016mm	3.5	3.5
3		Top Layer		0.03556mm		
4		Dielectric 1	FR-4	0.32004mm	4.8	4.8
5		Mid-Layer 1		0.03556mm		
6		Dielectric 2	FR-4	0.32004mm	4.8	4.8
7		Mid-Layer 2		0.03556mm		
8		Dielectric 3	FR-4	0.32004mm	4.8	4.8
9		Mid-Layer 3		0.03556mm		
10		Dielectric 4	FR-4	0.32004mm	4.8	4.8
11		Mid-Layer 4		0.03556mm		
12		Dielectric 5	FR-4	0.32004mm	4.8	4.8
13		Bottom Layer		0.03556mm		
14		Bottom Solder	Solder Resist	0.01016mm	3.5	3.5
15		Bottom Overlay		0mm		
	Height : 1.83388mm					