

## 21 Using shift Registers:

we know that it is possible to use shift Registers as Serial in to parallel out device. So in using shift Registers you kneed to put your R data in Shift Register one by one in the off-time interval. if you have 16 Rows, then you can use two 8 bit shift Registers one for the first & bit, and the other for second 8 bit. Leading data at shift Registers can be done in parallel. In this case the bottlenech will be loading 8 bit serial data in shift register. it you put data at each rising edge then it will take & GPIO speed to put your data at all of the R elemans.