XNOR: To0: $a=0, b=1$ 12
To1, $a=1, b=1$ 12
To2: X   ,1,2

AND: To0: 17 $j=0, c=0, x=12$
To1: 13 $d=1, e=1$
2 inv
$\overset{To2}{2x5} + 7 = 17, 2x4 + 5 = 13$

AND 3 input: To0: $22 = 3x5 + 7 \; x17$ $a=0, b=0, k=1$
$\overset{To2}{To1}: 17 = 3x4 + 5$
$\overset{To0}{To0}$ inv $a=0, b=1, k=1$

OR: To0: 17 $= 2x5 + 7$ $k=0, j=1, so$
To1: 19 $= 2x7 + 5$ $k=0, j=0, so$
To2 inv

Some times outputs was different from what we expected so I changed some inputs and delays in TB to make it same

for and:
instead of $00 \to 01$

for or:
instead of $11 \to 01$

Worst case $T.0$ for y_1: 3×5 + 7 + 5 + 7 = 34

n mose in AND   inv   T.1 Pmo'   T.0 inv

worst case $T.1$ for y_1 & 3×4 + 5 + 7 + 5 = 29

Amoses in NAND   inv   T.2Pmos   T.1 inv

in both cases, J_o will conduct and Pmos in OR gate will be charged.
So its delay ($T.0 z$ or $T.1$) won't be count.

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worst case $T.0$ for l_1 & 12 + 5 + 7 = 24

XNOR   T.1 Pmos   inv

worst case $T.1$ for e_1 & 12 + 7 + 5 = 24

XNOR   T.2 Pm.s   in

in both cases, e_o will conduct and both Pmos and nmoses will be charged
So its delay ($T.0 z$ or $T.1$ or $T.0$) won't count.

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