

1. Description

1.1. Project

Project Name	HT01_V1A2F001
Board Name	custom
Generated with:	STM32CubeMX 6.6.1
Date	09/23/2022

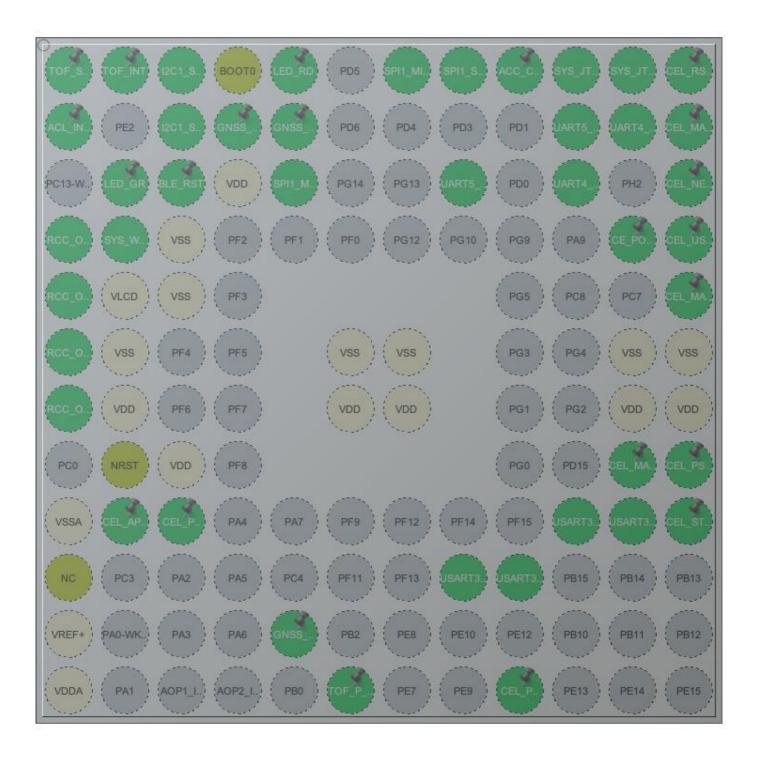
1.2. MCU

MCU Series	STM32L1
MCU Line	STM32L151/152
MCU name	STM32L151QEHx
MCU Package	UFBGA132
MCU Pin number	132

1.3. Core(s) information

Core(s)	Arm Cortex-M3

2. Pinout Configuration



UFBGA132 (Top view)

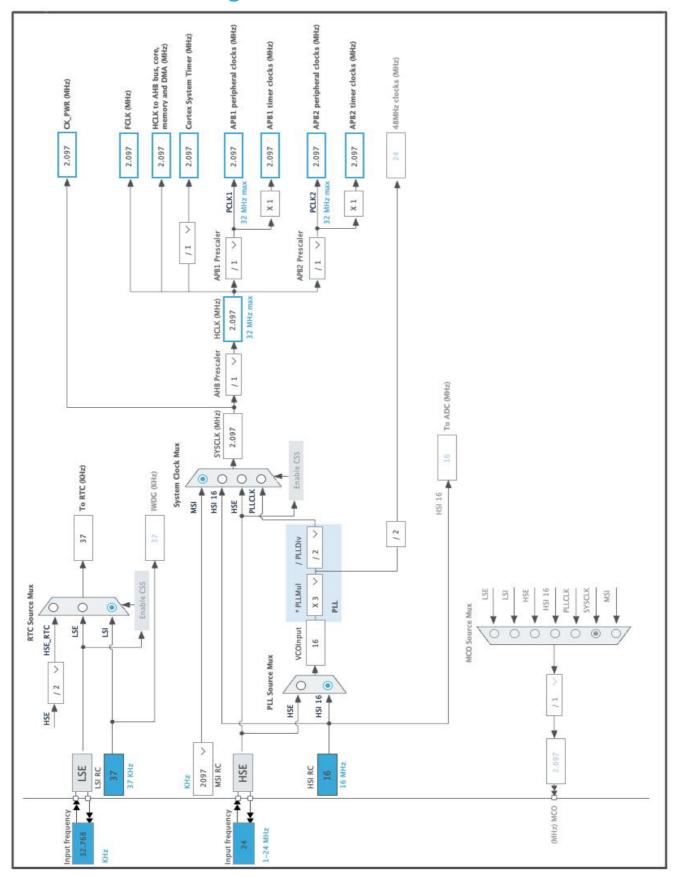
3. Pins Configuration

Pin Number UFBGA132	Pin Name (function after reset)	Pin Type	Alternate Function(s)	Label
A1	PE3 *	I/O	GPIO_Output	TOF_SHUT_N
A2	PE1 *	I/O	GPIO_Input	TOF_INT
A3	PB8	I/O	I2C1_SCL	
A4	BOOT0	Boot		
A5	PD7 *	I/O	GPIO_Output	LED_RD
A7	PB4	I/O	SPI1_MISO	
A8	PB3	I/O	SPI1_SCK	
A9	PA15 *	I/O	GPIO_Output	ACC_CS_N
A10	PA14	I/O	SYS_JTCK-SWCLK	
A11	PA13	I/O	SYS_JTMS-SWDIO	
A12	PA12 *	I/O	GPIO_Output	CEL_RST_N
B1	PE4 *	I/O	GPIO_Input	ACL_INT2
В3	PB9	I/O	I2C1_SDA	
B4	PB7 *	1/0	GPIO_Output	GNSS_RX
B5	PB6 *	I/O	GPIO_Output	GNSS_TX
B10	PC12	I/O	UART5_TX	
B11	PC10	I/O	UART4_TX	
B12	PA11 *	I/O	GPIO_Input	CEL_MAIN-DTR
C2	PE5 *	I/O	GPIO_Output	LED_GR
C3	PE0 *	I/O	GPIO_Output	BLE_RST
C4	VDD	Power		
C5	PB5	I/O	SPI1_MOSI	
C8	PD2	I/O	UART5_RX	
C10	PC11	I/O	UART4_RX	
C12	PA10 *	I/O	GPIO_Input	CEL_NET_STATUS
D1	PC14-OSC32_IN	I/O	RCC_OSC32_IN	
D2	PE6-WKUP3	I/O	SYS_WKUP3	
D3	VSS	Power		
D11	PA8 *	I/O	GPIO_Input	CE_PON_TRG
D12	PC9 *	I/O	GPIO_Output	CEL_USB_BOOT
E1	PC15-OSC32_OUT	I/O	RCC_OSC32_OUT	
E2	VLCD	Power		
E3	VSS	Power		
E12	PC6 *	I/O	GPIO_Input	CEL MAIN RI
F1	PH0-OSC_IN	I/O	RCC_OSC_IN	
F2	VSS	Power		

Pin Number UFBGA132	Pin Name (function after reset)	Pin Type	Alternate Function(s)	Label
F6	VSS	Power		
F7	VSS	Power		
F11	VSS	Power		
F12	VSS	Power		
G1	PH1-OSC_OUT	I/O	RCC_OSC_OUT	
G2	VDD	Power		
G6	VDD	Power		
G7	VDD	Power		
G11	VDD	Power		
G12	VDD	Power		
H2	NRST	Reset		
H3	VDD	Power		
H11	PD14 *	I/O	GPIO_Input	CEL_MAIN_DCD
H12	PD13 *	I/O	GPIO_Input	CEL_PSM
J1	VSSA	Power		
J2	PC1 *	I/O	GPIO_Input	CEL_AP_RDY
J3	PC2 *	I/O	GPIO_Output	CEL_PWR_KEY
J10	PD12	I/O	USART3_RTS	
J11	PD11	I/O	USART3_CTS	
J12	PD10 *	I/O	GPIO_Input	CEL_STATUS
K1	NC	NC		
K8	PD9	I/O	USART3_RX	
K9	PD8	I/O	USART3_TX	
L1	VREF+	Power		
L5	PC5 *	I/O	GPIO_Output	GNSS_PWR_EN
M1	VDDA	Power		
M6	PB1 *	I/O	GPIO_Output	TOF_P_EN
M9	PE11 *	I/O	GPIO_Output	CEL_PWR_EN

^{*} The pin is affected with an I/O function

4. Clock Tree Configuration



5. Software Project

5.1. Project Settings

Name	Value
Project Name	HT01_V1A2F001
Project Folder	/Users/gazirahman/STM32CubeIDE/workspace_1.9.0/HT01_V1A2F001
Toolchain / IDE	STM32CubeIDE
Firmware Package Name and Version	STM32Cube FW_L1 V1.10.3
Application Structure	Advanced
Generate Under Root	Yes
Do not generate the main()	No
Minimum Heap Size	0x200
Minimum Stack Size	0x400

5.2. Code Generation Settings

Name	Value
STM32Cube MCU packages and embedded software	Copy only the necessary library files
Generate peripheral initialization as a pair of '.c/.h' files	Yes
Backup previously generated files when re-generating	Yes
Keep User Code when re-generating	Yes
Delete previously generated files when not re-generated	Yes
Set all free pins as analog (to optimize the power consumption)	Yes
Enable Full Assert	No

5.3. Advanced Settings - Generated Function Calls

Rank	Function Name	Peripheral Instance Name
1	SystemClock_Config	RCC
2	MX_GPIO_Init	GPIO
3	MX_I2C1_Init	I2C1
4	MX_SPI1_Init	SPI1
5	MX_UART4_Init	UART4
6	MX_UART5_Init	UART5
7	MX_USART3_UART_Init	USART3
8	MX_RTC_Init	RTC

6. Power Consumption Calculator report

6.1. Microcontroller Selection

Series	STM32L1
Line	STM32L151/152
MCU	STM32L151QEHx
Datasheet	DS10002_Rev8

6.2. Parameter Selection

Temperature	25
Vdd	3.0

6.3. Battery Selection

Battery	Li-SOCL2(A3400)
Capacity	3400.0 mAh
Self Discharge	0.08 %/month
Nominal Voltage	3.6 V
Max Cont Current	100.0 mA
Max Pulse Current	200.0 mA
Cells in series	1
Cells in parallel	1

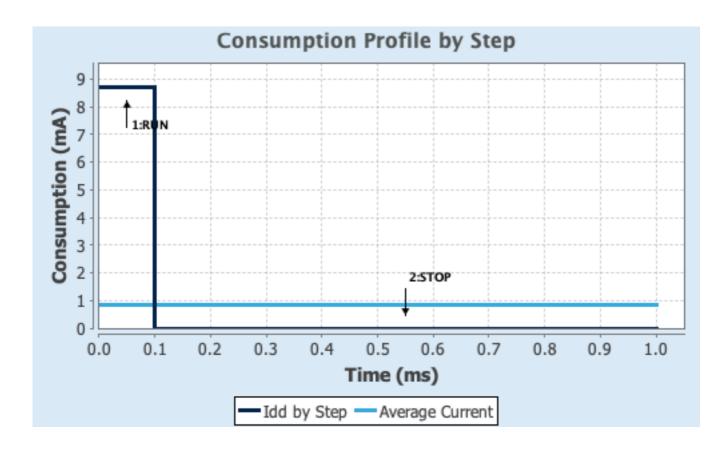
6.4. Sequence

C4am	Ct 4	Ct 0
Step	Step1	Step2
Mode	RUN	STOP
Vdd	3.0	3.0
Voltage Source	Battery	Battery
Range	Range1-High	NoRange
Fetch Type	FLASH	n/a
CPU Frequency	32 MHz	0 Hz
Clock Configuration	HSI PLL	ALL CLOCKS OFF
Clock Source Frequency	16 MHz	0 Hz
Peripherals		
Additional Cons.	0 mA	0 mA
Average Current	8.7 mA	560 nA
Duration	0.1 ms	0.9 ms
DMIPS	33.0	0.0
Ta Max	103.43	105
Category	In DS Table	In DS Table

6.5. Results

Sequence Time	1 ms	Average Current	870.5 μA
Battery Life	5 months, 9 days,	Average DMIPS	33.0 DMIPS
	22 hours		

6.6. Chart



7. Peripherals and Middlewares Configuration

7.1. I2C1 I2C: I2C

7.1.1. Parameter Settings:

Master Features:

I2C Speed Mode Standard Mode

I2C Clock Speed (Hz) 100000

Slave Features:

Clock No Stretch Mode Disabled

Primary Address Length selection 7-bit

Dual Address Acknowledged Disabled

Primary slave address 0

General Call address detection Disabled

7.2. RCC

High Speed Clock (HSE): Crystal/Ceramic Resonator Low Speed Clock (LSE): Crystal/Ceramic Resonator

7.2.1. Parameter Settings:

System Parameters:

VDD voltage (V) 3.3
Instruction Cache Enabled
Prefetch Buffer Disabled
Data Cache Enabled

Flash Latency(WS) 0 WS (1 CPU cycle)

RCC Parameters:

HSI Calibration Value 16

MSI Calibration Value 0

HSE Startup Timout Value (ms) 100

LSE Startup Timout Value (ms) 5000

Power Parameters:

Power Regulator Voltage Scale Power Regulator Voltage Scale 1

7.3. RTC

mode: Activate Clock Source mode: Activate Calendar WakeUp: Internal WakeUp 7.3.1. Parameter Settings:

General:

Hour Format Hourformat 24

Asynchronous Predivider value 127 Synchronous Predivider value 255

Calendar Time:

Data Format BCD data format

Hours 0
Minutes 0
Seconds 0

Day Light Saving: value of hour adjustment

Store Operation

Daylightsaving None

Storeoperation Reset

Calendar Date:

Week Day Monday
Month January
Date 1
Year 0

Wake UP:

Wake Up Clock RTCCLK / 16

Wake Up Counter 0

7.4. SPI1

Mode: Full-Duplex Master

7.4.1. Parameter Settings:

Basic Parameters:

Frame Format Motorola

Data Size 8 Bits

First Bit MSB First

Clock Parameters:

Prescaler (for Baud Rate) 2

Baud Rate 1.0485 MBits/s *

Clock Polarity (CPOL) Low
Clock Phase (CPHA) 1 Edge

Advanced Parameters:

CRC Calculation Disabled
NSS Signal Type Software

7.5. SYS

Debug: Serial Wire

mode: System Wake-Up 3
Timebase Source: SysTick

7.6. UART4

Mode: Asynchronous7.6.1. Parameter Settings:

Basic Parameters:

Baud Rate 115200

Word Length 8 Bits (including Parity)

Parity None Stop Bits 1

Advanced Parameters:

Data Direction Receive and Transmit

Over Sampling 16 Samples

7.7. UART5

Mode: Asynchronous

7.7.1. Parameter Settings:

Basic Parameters:

Baud Rate 115200

Word Length 8 Bits (including Parity)

Parity None Stop Bits 1

Advanced Parameters:

Data Direction Receive and Transmit

Over Sampling 16 Samples

7.8. **USART3**

Mode: Asynchronous

Hardware Flow Control (RS232): CTS/RTS

7.8.1. Parameter Settings:

Basic Parameters:

Baud Rate 115200

Word Length 8 Bits (including Parity)

Parity None Stop Bits 1

Advanced Parameters:

Data Direction Receive and Transmit

Over Sampling 16 Samples

^{*} User modified value

8. System Configuration

8.1. GPIO configuration

IP	Pin	Signal	GPIO mode	GPIO pull/up pull down	Max Speed	User Label
I2C1	PB8	I2C1_SCL	Alternate Function Open Drain	No pull-up and no pull-down	High *	
	PB9	I2C1_SDA	Alternate Function Open Drain	No pull-up and no pull-down	High *	
RCC	PC14- OSC32_IN	RCC_OSC32_IN	n/a	n/a	n/a	
	PC15- OSC32_OU T	RCC_OSC32_O UT	n/a	n/a	n/a	
	PH0- OSC_IN	RCC_OSC_IN	n/a	n/a	n/a	
	PH1- OSC_OUT	RCC_OSC_OUT	n/a	n/a	n/a	
SPI1	PB4	SPI1_MISO	Alternate Function Push Pull	No pull-up and no pull-down	High *	
	PB3	SPI1_SCK	Alternate Function Push Pull	No pull-up and no pull-down	High *	
	PB5	SPI1_MOSI	Alternate Function Push Pull	No pull-up and no pull-down	High *	
SYS	PA14	SYS_JTCK- SWCLK	n/a	n/a	n/a	
	PA13	SYS_JTMS- SWDIO	n/a	n/a	n/a	
	PE6-WKUP3	SYS_WKUP3	n/a	n/a	n/a	
UART4	PC10	UART4_TX	Alternate Function Push Pull	Pull-up	High *	
	PC11	UART4_RX	Alternate Function Push Pull	Pull-up	High *	
UART5	PC12	UART5_TX	Alternate Function Push Pull	Pull-up	High *	
	PD2	UART5_RX	Alternate Function Push Pull	Pull-up	High *	
USART3	PD12	USART3_RTS	Alternate Function Push Pull	No pull-up and no pull-down	High *	
	PD11	USART3_CTS	Alternate Function Push Pull	No pull-up and no pull-down	High *	
	PD9	USART3_RX	Alternate Function Push Pull	No pull-up and no pull-down	High *	
	PD8	USART3_TX	Alternate Function Push Pull	No pull-up and no pull-down	High *	
GPIO	PE3	GPIO_Output	Output Push Pull	Pull-up *	Very Low	TOF_SHUT_N
	PE1	GPIO_Input	Input mode	No pull-up and no pull-down	n/a	TOF_INT
	PD7	GPIO_Output	Output Push Pull	No pull-up and no pull-down	Very Low	LED_RD
	PA15	GPIO_Output	Output Push Pull	No pull-up and no pull-down	Very Low	ACC_CS_N
	PA12	GPIO_Output	Output Push Pull	Pull-up *	Very Low	CEL_RST_N
	PE4	GPIO_Input	Input mode	Pull-up *	n/a	ACL_INT2

IP	Pin	Signal	GPIO mode	GPIO pull/up pull down	Max Speed	User Label
	PB7	GPIO_Output	Output Push Pull	No pull-up and no pull-down	Very Low	GNSS_RX
	PB6	GPIO_Output	Output Push Pull	No pull-up and no pull-down	Very Low	GNSS_TX
	PA11	GPIO_Input	Input mode	No pull-up and no pull-down	n/a	CEL_MAIN-DTR
	PE5	GPIO_Output	Output Push Pull	No pull-up and no pull-down	Very Low	LED_GR
	PE0	GPIO_Output	Output Push Pull	No pull-up and no pull-down	Very Low	BLE_RST
	PA10	GPIO_Input	Input mode	No pull-up and no pull-down	n/a	CEL_NET_STATUS
	PA8	GPIO_Input	Input mode	No pull-up and no pull-down	n/a	CE_PON_TRG
	PC9	GPIO_Output	Output Push Pull	No pull-up and no pull-down	Very Low	CEL_USB_BOOT
	PC6	GPIO_Input	Input mode	No pull-up and no pull-down	n/a	CEL_MAIN_RI
	PD14	GPIO_Input	Input mode	No pull-up and no pull-down	n/a	CEL_MAIN_DCD
	PD13	GPIO_Input	Input mode	No pull-up and no pull-down	n/a	CEL_PSM
	PC1	GPIO_Input	Input mode	No pull-up and no pull-down	n/a	CEL_AP_RDY
	PC2	GPIO_Output	Output Push Pull	No pull-up and no pull-down	Very Low	CEL_PWR_KEY
	PD10	GPIO_Input	Input mode	No pull-up and no pull-down	n/a	CEL_STATUS
	PC5	GPIO_Output	Output Push Pull	No pull-up and no pull-down	Very Low	GNSS_PWR_EN
	PB1	GPIO_Output	Output Push Pull	Pull-up *	Very Low	TOF_P_EN
	PE11	GPIO_Output	Output Push Pull	No pull-up and no pull-down	Very Low	CEL_PWR_EN

8.2. DMA configuration

nothing configured in DMA service

8.3. NVIC configuration

8.3.1. NVIC

Interrupt Table	Enable Preenmption Priority		SubPriority		
Non maskable interrupt	true	0	0		
Hard fault interrupt	true	0	0		
Memory management fault	true	0	0		
Pre-fetch fault, memory access fault	true	0	0		
Undefined instruction or illegal state	true	0	0		
System service call via SWI instruction	true	0	0		
Debug monitor	true	0	0		
Pendable request for system service	true	0	0		
System tick timer	true	15	0		
RTC wake-up interrupt through EXTI line 20	unused				
Flash global interrupt	unused				
RCC global interrupt	unused				
I2C1 event interrupt		unused			
I2C1 error interrupt		unused			
SPI1 global interrupt	unused				
USART3 global interrupt	unused				
UART4 global interrupt	unused				
UART5 global interrupt	unused				

8.3.2. NVIC Code generation

Enabled interrupt Table	Select for init sequence ordering	Generate IRQ handler	Call HAL handler
Non maskable interrupt	false	true	false
Hard fault interrupt	false	true	false
Memory management fault	false	true	false
Pre-fetch fault, memory access fault	false	true	false
Undefined instruction or illegal state	false	true	false
System service call via SWI instruction	false	true	false
Debug monitor	false	true	false
Pendable request for system service	false	true	false
System tick timer	false	true	true

* User modified value

9. System Views

9.1. Category view

9.1.1. Current



System Core	Analog	Timers	Connectivity	Multimedia	Computing
DMA		RTC ♥	I2C1 ⊘		
GPIO ♥			SPI1 ⊘		
NVIC 🕏			UART4 ♥		
RCC ♥			UART5 ♥		
sys 🤡			USART3 ⊘		

10. Docs & Resources

Type Link

Presentations https://www.st.com/resource/en/product_presentation/stm32-

stm8_embedded_software_solutions.pdf

Presentations https://www.st.com/resource/en/product_presentation/stm32_eval-

tools_portfolio.pdf

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stm32f0f1f3gxlx-series-dma-controller-stmicroelectronics.pdf

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microcontroller-system-memory-boot-mode-stmicroelectronics.pdf

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recommendations-and-package-information-for-leadfree-ecopack-mcus-

and-mpus-stmicroelectronics.pdf

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best-adc-accuracy-in-stm32-microcontrollers-stmicroelectronics.pdf

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