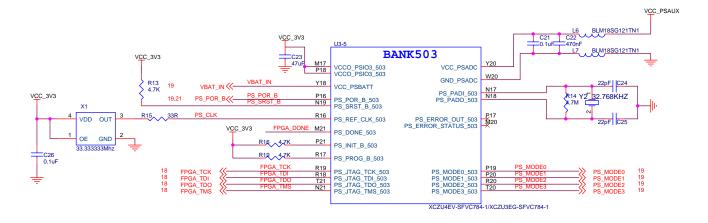
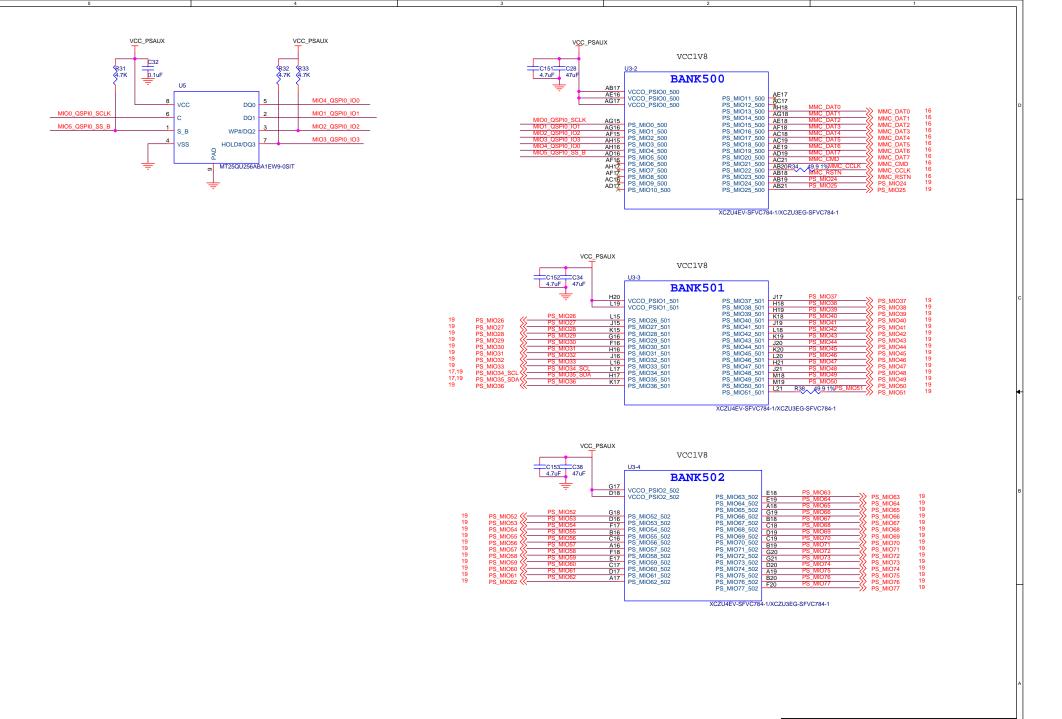


MODE[3:0]	BOOT MODE	Descritpion
0000	PS JTAG	PS JTAG Interface
0001	Quad_SPI(24b)	24-Bit addresssing(QSPI24)
0010	Quad_SPI(32b)	32-Bit addresssing(QSPI32)
0011	SD0(2.0)	SD2.0
0100	NAND	Requires 8-bit data bus width
0101	SD1(2.0)	SD2.0
0110	eMMC(1.8V)	eMMC version 4.5 at 1.8V
0111	USB0(2.0)	USB 2.0 only
1000	PJTAG(MIO #0)	PJTAG connection 0 option
1001	PJTAG(MIO #1)	PJTAG connection 1 option
1110	SD1 LS(3.0)	SD 3.0

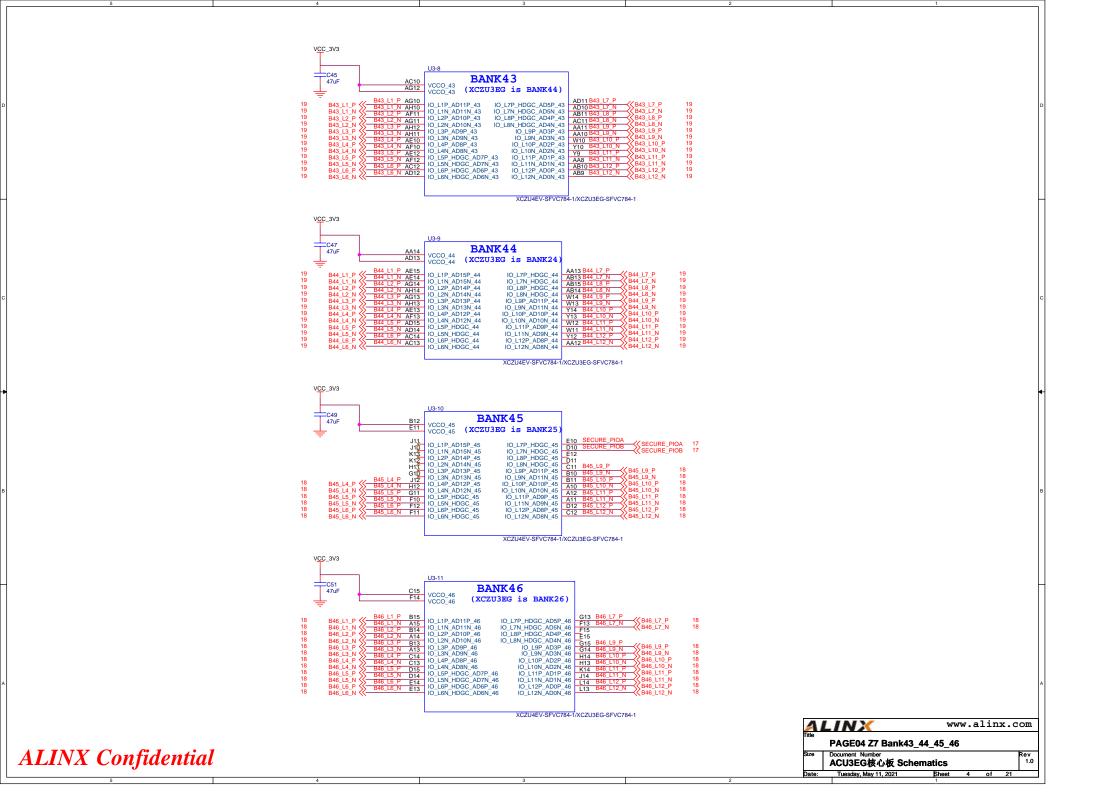


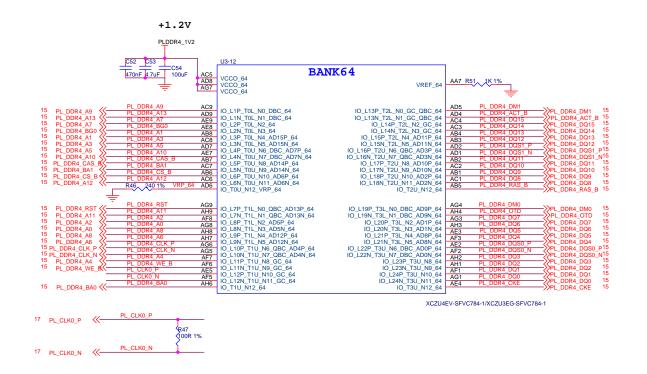






www.alinx.com
PAGE03 Z7 MIO-Config
Size Document Number
ACU3EG核心板 Schematics 1.0
Date: Tuesday,May11,2021 Sheet 3 of 21



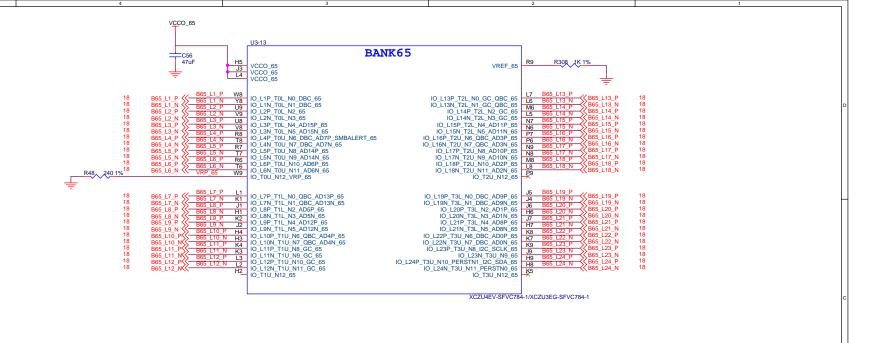


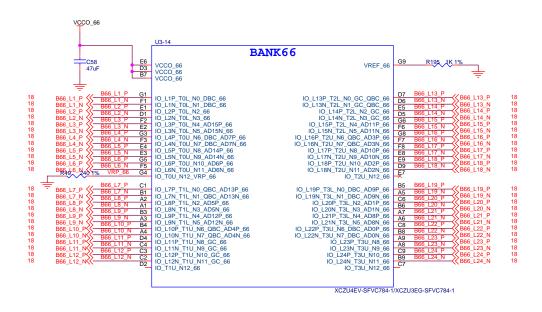
www.alinx.com

Fiftle PAGE05 Z7 Bank64

Size Document Number ACU3EG核心板 Schematics

Date: Tuesday,May11,2021 Sheet 5 of 21





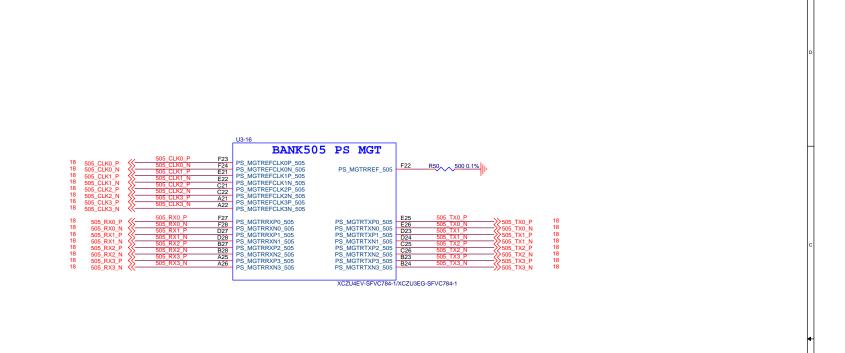
Www.alinx.com
Title

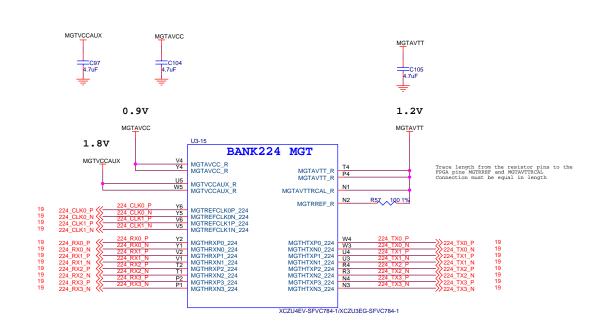
PAGE06 Z7 Bank65_66

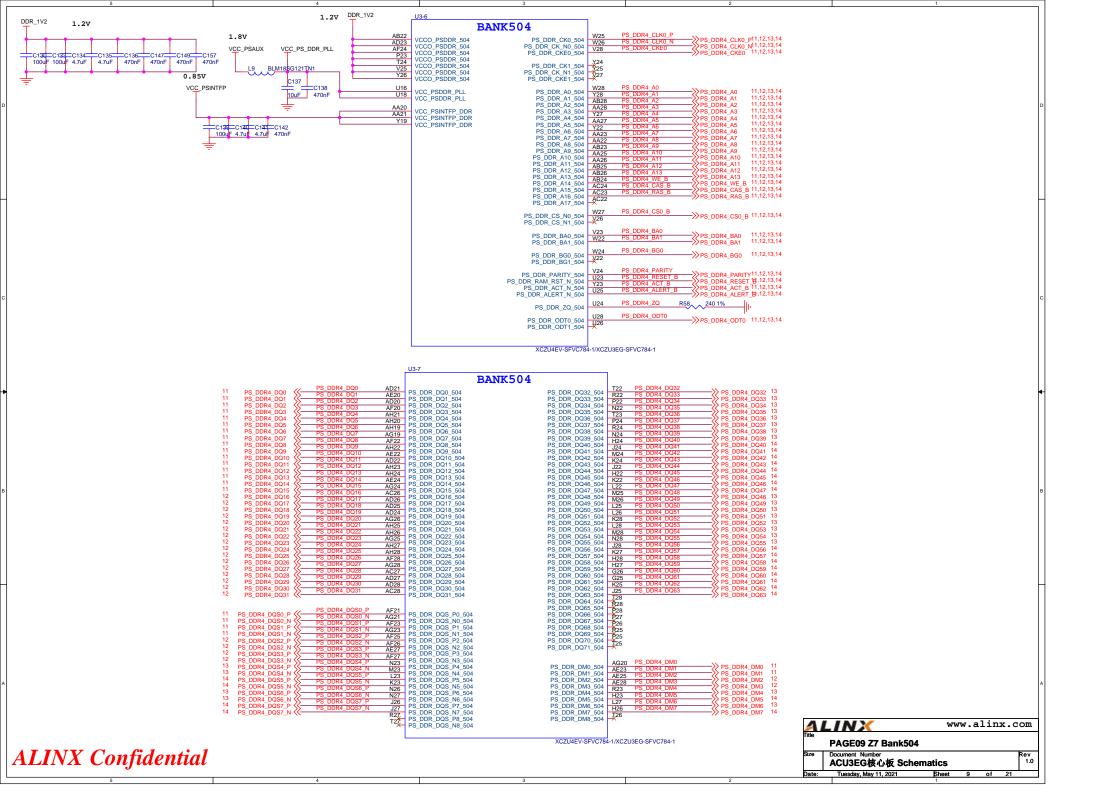
Size Document Number ACU3EG核心板 Schematics 1.0

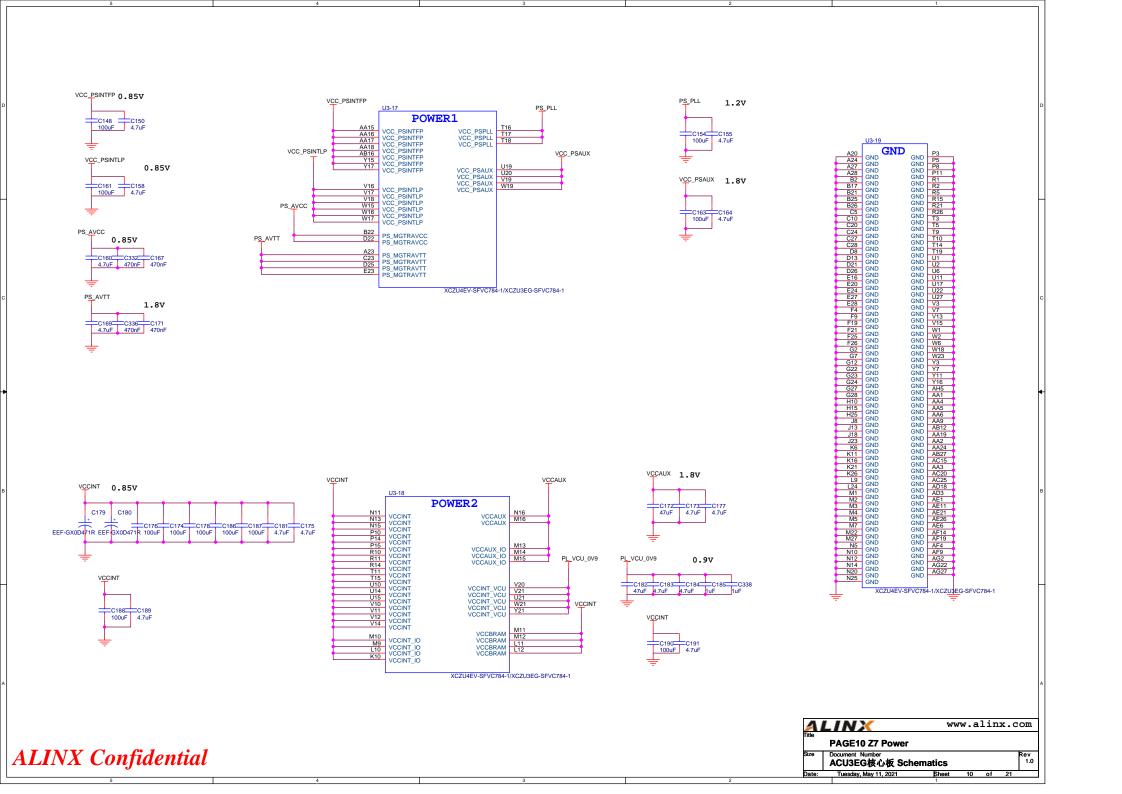
Date: Tuesday, May 11, 2021 Sheet 6 of 21

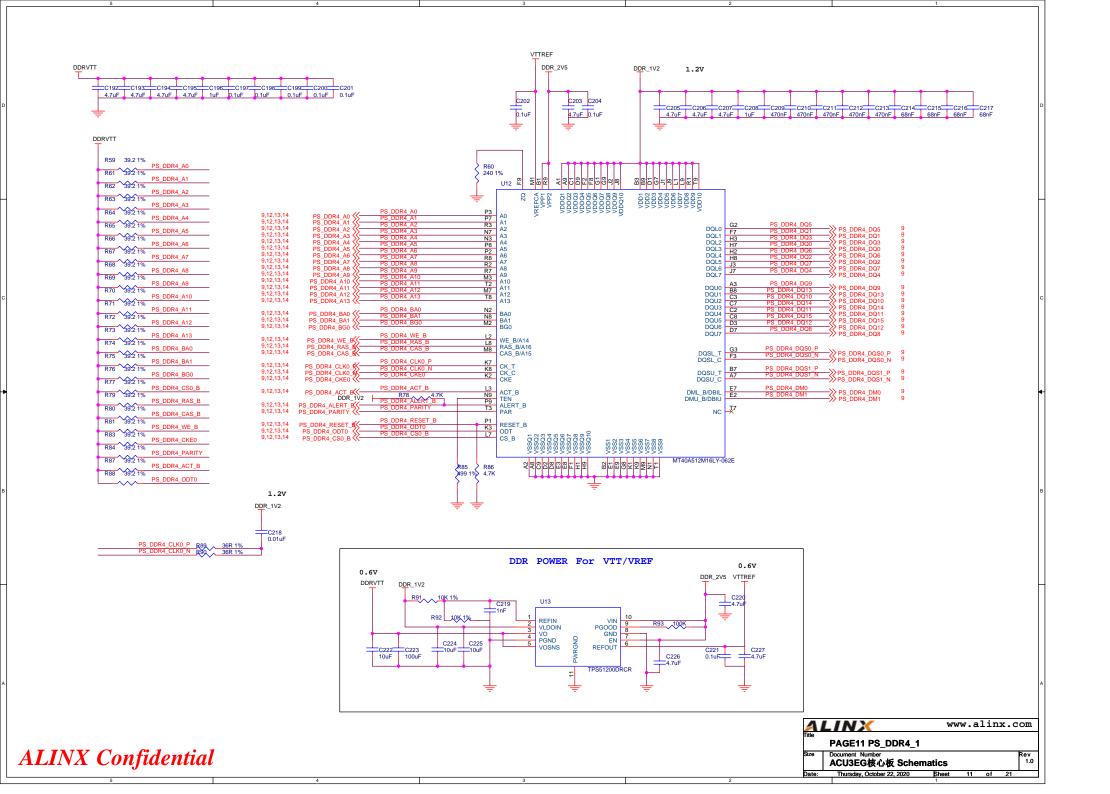
ALINX Confidential

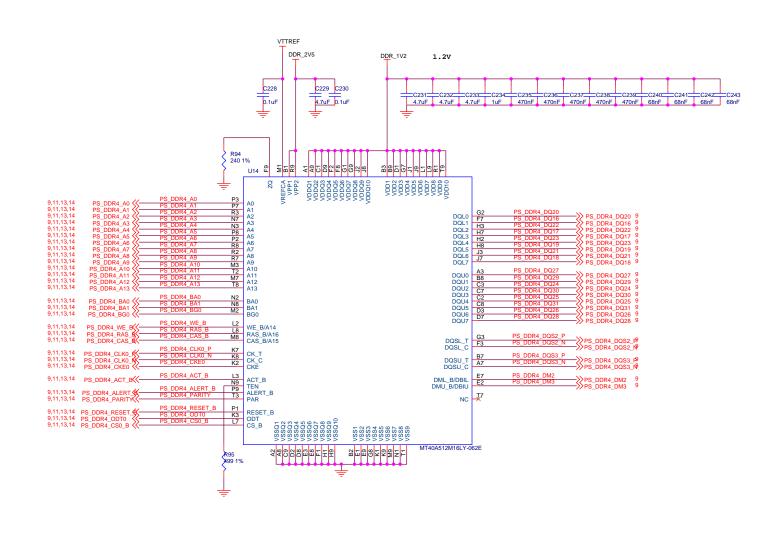




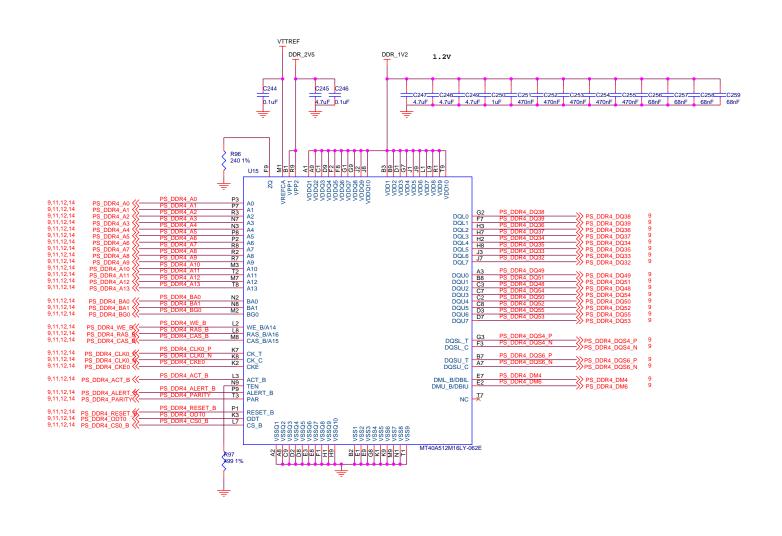




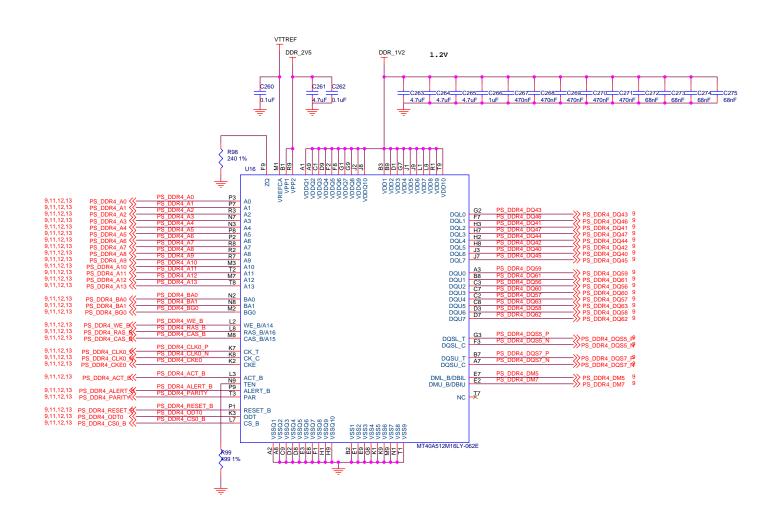


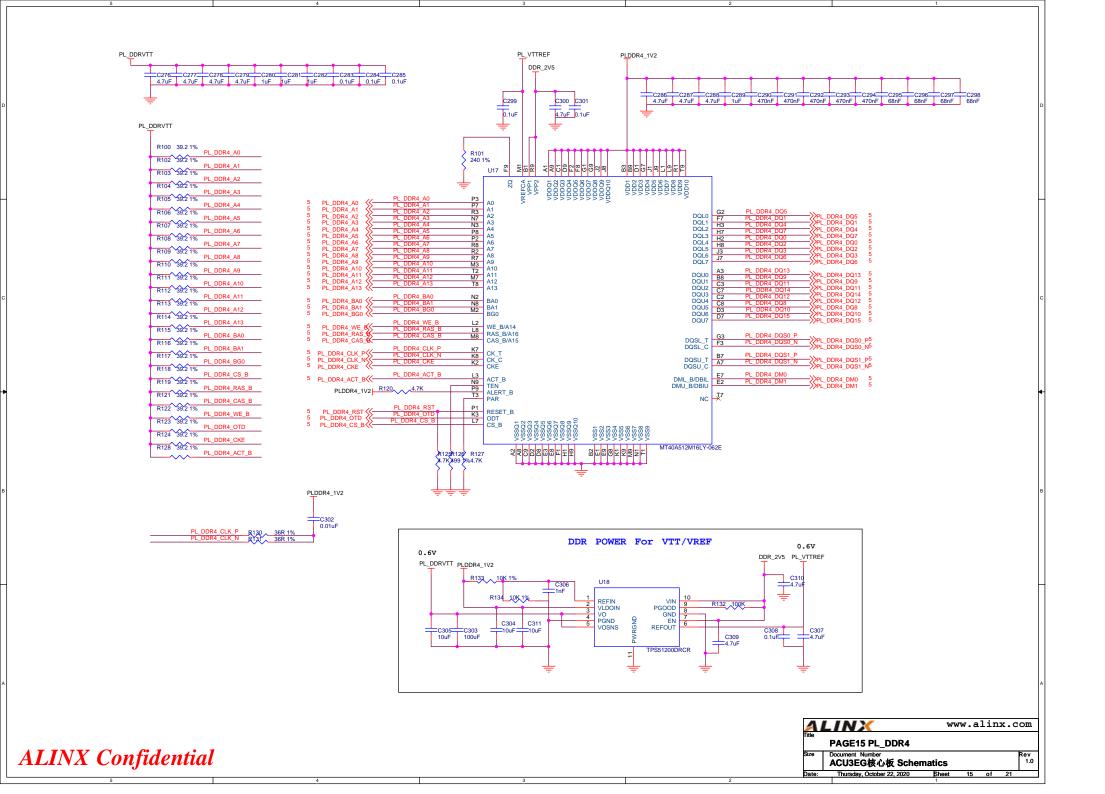


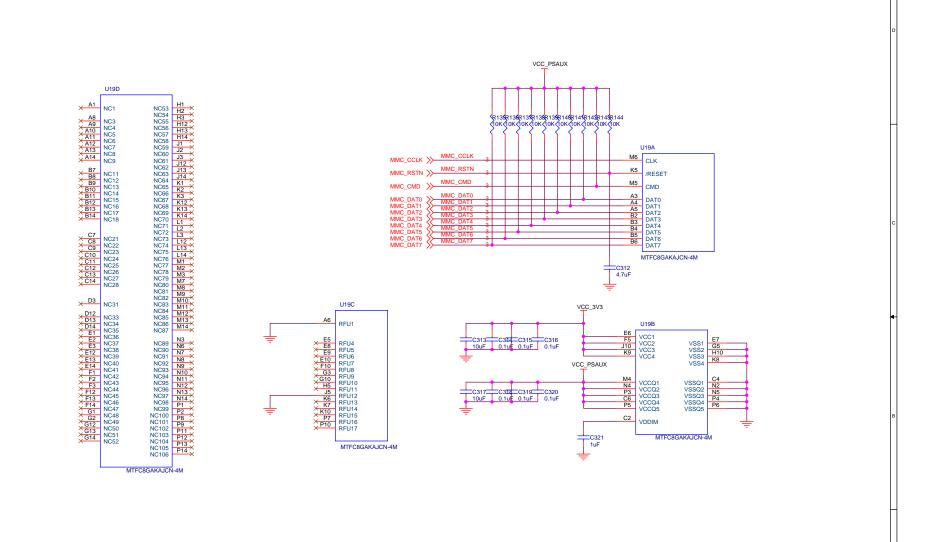
www.alinx.com
Title PAGE12 PS_DDR4_2
Size Document Number ACU3EG核心板 Schematics 1.0
Date: Thursday, October 22, 2020 Sheet 12 of 21



www.alinx.com
Title
PAGE13 PS_DDR4_3
Size Document Number
ACU3EG核心板 Schematics
Date: Thursday, October 22, 2020 Sheet 13 of 21







Www.alinx.com

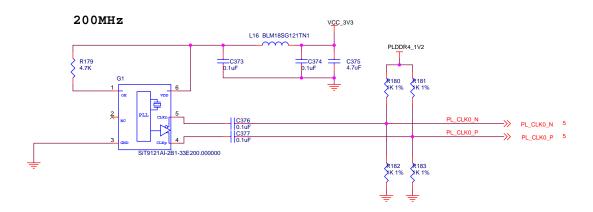
PAGE16 eMMC

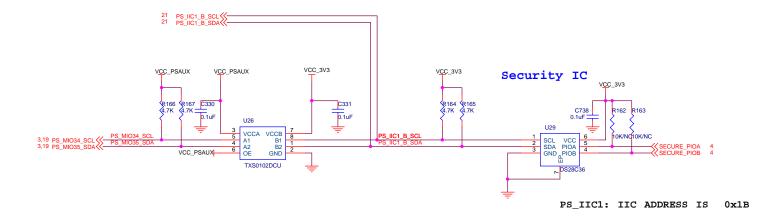
Size Document Number ACU3EG核心板 Schematics 1.0

Date: Tuesday, March 10, 2020 Sheet 16 of 21

ALINX Confidential

PL SYSTEM CLOCK



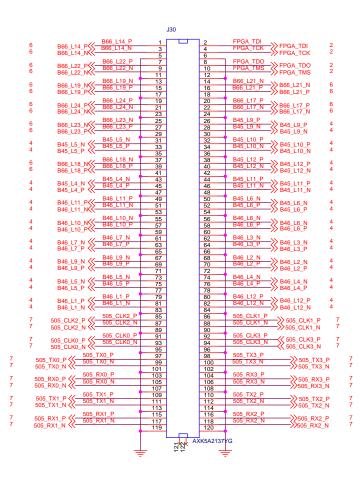


Www.alinx.com
Fille
PAGE17 CLOCK & SECURITY
Size Document Number ACU3EG核心板 Schematics
Date: Tuesday, March 10, 2020 Sheet 17 of 21



BANK45, BANK46 IO Voltage is +3.3V Standard

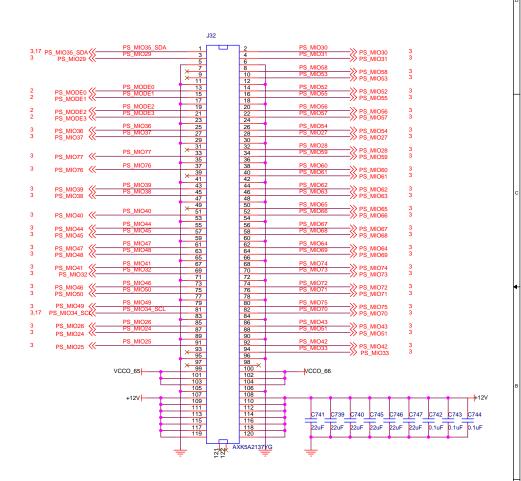
J29									
_		B65 L2 N	1		2	B65 L22 P	_		
6	B65_L2_N 55-	B65_L2_P	3		4	B65_L22_N	6 6		
· ·	B65_L2_P <<-		5		6	>>> B65_L22_N	·		
6	B65 L4 N <<-	B65_L4_N B65_L4_P	7		8 10	B65_L20_P B65_L20_N >>> B65_L20_P	6		
6	B65_L4_N	B03_L4_F	11		12	>>> B65_L20_N	6		
6	DOT 14 N //	B65_L1_N	13		14	B65_L6_N N Dos Lo N	6		
6	B65_L1_N	B65_L1_P	15		16	B65_L6_P	6		
		B65_L7_P	17 19		18 20	B65_L17_P \\ \\ \			
6	B65_L7_P <	B65 L7 N	21		22	B65 17 N ->> B65_L17_P	6		
6	B65_L7_N <<-		23		24	>>> B65_L17_N	6		
6	B65_L15_P	B65_L15_P	25		26	B65_L9_P B65_L9_N >>> B65_L9_P	6		
6	B65_L15_N	B65_L15_N	27 29		28 30	B65_L9_N	6		
_		B65 L16 P	31		32	B65 I 3 N	_		
6 6	B65_L16_P	B65_L16_N	33		34	B65_L3_P >> B65_L3_N	6 6		
O	B65_L16_N<<		35		36	>>> B65_L3_P	o		
6	B65_L14_P <<-	B65_L14_P B65_L14_N	37 39		38 40	B65_L19_P B65_L19_N >> B65_L19_P	6		
6	B65_L14_N<<	D05_L14_IV	39 41		40	B65_L19_N ≥ B65_L19_N	6		
6		B65_L5_N	43		44	B65_L18_P \ \			
6	B65_L5_N	B65_L5_P	45		46	B65_L18_N B65_L18_P B65_L18_N	6 6		
•	D00_E0_1 ((DOE LAA N	47		48	B65 L8 P	Ŭ		
6	B65_L11_N<<-	B65_L11_N B65_L11_P	49 51		50 52	B65 I 8 NI >> B65 L8 P	6		
6	B65_L11_P <<-	D00_E11_1	53		54	—————————————————————————————————————	6		
6	B65_L10_N<<-	B65_L10_N	55		56	B65_L24_N N PGF L24 N	6		
6	B65 L10 P	B65_L10_P	57		58	B65_L24_P	6		
	**	B66_L3_P	59 61		60 62	DOE LAD D			
6	B66_L3_P <>-	B66_L3_N	63		64	B65_L12_N >> B65_L12_P	6		
6	B66_L3_N <<-		65		66	>>> B65_L12_N	6		
6	B66_L1_P <<-	B66_L1_P	67		68	B65_L13_N	6		
6	B66_L1_N <<-	B66_L1_N	69 71		70 72	B65_L13_P	6		
6		B66 L6 P	73		74	B65 L21 P 💉	6		
6	B66_L6_P S	B66_L6_N	75		76	B65_L21_N B65_L21_P B65_L21_N	6		
•	D00_L0_14 ((Dec Life D	77		78	B65 L23 P	Ŭ		
6	B66_L16_P <<-	B66_L16_P B66_L16_N	79 81		80 82	B65_L23_P >>> B65_L23_P	6		
6	B66_L16_N<<-	500_510_11	83		84	>>> B65_L23_N	6		
6	Dec 145 D//-	B66_L15_P	85		86	B66_L5_N >>> B66_L5_N	6		
6	B66_L15_P	B66_L15_N	87		88	B66_L5_P	6		
		B66 L4 P	89 91		90 92	B66 L2 P			
6 6	B66_L4_P \$\$	B66_L4_N	93		94	B66 12 N >> B66_L2_P	6 6		
O	B66_L4_N <<-		95		96	>>> B66_L2_N	o		
6	B66_L11_P\$\$	B66_L11_P B66_L11_N	97		98	B66_L20_P B66_L20_N >>> B66_L20_P	6		
6	B66_L11_N(DOO_ETT_IV	99 101		100 102	>>> B66_L20_N	6		
6	//	B66_L12_P	103		104	B66_L7_P \\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \	6		
6	B66_L12_P	B66_L12_N	105		106	B66_L7_N	6		
	D00_E12_IV((B66 L13 N	107 109		108 110	B66 L10 P 33			
6	B66_L13_N\\	B66_L13_P	111		112	B66 I 10 N >> B66_L10_P	6		
6	B66_L13_P	,	113		114		6		
6	B66_L8_N <<-	B66_L8_N	115		116	B66_L9_P B66_L9_N →>> B66_L9_P	6		
6	B66_L8_P \	B66_L8_P	117		118 120	B66_L9_N	6		
	**		119		120	· · · · · · · · · · · · · · · · · · ·			
			'	A)	K5A2137	rG			
		_	L	5,5		<u></u>			
		7	-		- 7	-			



BANK43, BANK44 IO Voltage is +3.3V Standard

The MIO Voltage is +1.8V Standard

			J	31				
	,,	B44_L10_P	1 Г		2	B44_L7_P		
4	B44_L10_P	B44_L10_N	3		4	B44_L7_N	—≫ B44_L7_P —≫ B44_L7_N	4
		B44 L6 P	5		6 8	B43 L6 P		
4	B44_L6_P	B44_L6_P	7 9		10	B43_L6_P	—————————————————————————————————————	4
4	B44_L6_N <<-		11		12		—->>> B43_L6_N	4
4	B44 15 ₽ </td <td>B44_L5_P</td> <td>13</td> <td></td> <td>14</td> <td>B43_L7_P</td> <td>—>>> B43_L7_P</td> <td>4</td>	B44_L5_P	13		14	B43_L7_P	—>>> B43_L7_P	4
4	B44_L5_P	B44_L5_N	15 17		16	B43_L7_N	—————————————————————————————————————	4
		B44_L1_P	19		20	B43 L8 N	X	
4	B44_L1_P	B44_L1_N	21		22	B43_L8_P	—≫ B43_L8_N —≫ B43_L8_P	4
•	D44_L1_N \\	B44 L12 P	23 25		24	B44 L2 P	// D43_L0_P	
4	B44_L12_P\$\$ B44_L12_N\$\$	B44_L12_P	25		28	B44_L2_P	—>>> B44_L2_P	4
4	B44_L12_N\\		29		30	BTT_CE_T	—>>> B44_L2_N	4
4	D44 12 D //-	B44_L3_P	31		32 ×			
4	B44_L3_P	B44_L3_N	33		34			
		B43 L12 N	35 37		36 ^	B43 L9 P		
4	B43_L12_N	B43_L12_P	39		40	B43_L9_N	—≫ B43_L9_P —≫ B43_L9_N	4
-	B43_L12_P <<		41		42		—->> B43_L9_N	-
4	B43 L10 NKK-	B43_L10_N B43_L10_P	43 45		44	B43_L3_P B43_L3_N	—>>> B43_L3_P	4
4	B43_L10_N B43_L10_P	D43_L10_F	47		48	D43_L3_N	——>>> B43_L3_N	4
4	Dec 144 N//	B44_L11_N	49		50	B43_L1_N	N DOLLAN	
4	B44_L11_N\\ B44_L11_P\\	B44_L11_P	51		52	B43_L1_P	—≫ B43_L1_N —≫ B43_L1_P	4
		B44 I 9 N	53 55		54 56	B44 I 4 P		
4	B44_L9_N ————————————————————————————————————	B44_L9_P	57		58	B44_L4_N	—≫ B44_L4_P —≫ B44_L4_N	4
4	B44_L9_P <<-		59		60		—>>> B44_L4_N	4
4	B44 18 P ((-	B44_L8_P B44_L8_N	61		62	B43_L5_P B43_L5_N	—>>> B43 L5 P	4
4	B44_L8_P ————————————————————————————————————	D44_L0_IN	63 65		66	D43_L5_N	—≫ B43_L5_N	4
4		B43_L2_N	67		68	B43_L4_P	XX	
4	B43_L2_N	B43_L2_P	69		70	B43_L4_N	—≫ B43_L4_P —≫ B43_L4_N	4
		VBAT_IN	71		72 74	B43_L11_P	// B43_L4_N	
2	VBAT_IN	MR	75		76	B43_L11_P	—≫ B43_L11_P —≫ B43_L11_N	4
21	MR <<	THE CONTRACTOR OF THE CONTRACT	77		78		—>>> B43_L11_N	4
			79		80	PS_POR_B	—≫ PS POR B	2.21
			81 83		82 84		//	
			85		86			
			87		88			
		224 CLK0 P	89		90	224 CLK1 P		
8	224_CLK0_P <<	224_CLK0_F	91		92	224_CLK1_F	—>>> 224_CLK1_P	8
8	224_CLK0_N <<		95		96		—>>>224_CLK1_N	8
8	224 TY3 N((-	224_TX3_N	97		98	224_RX3_N	—>>> 224 RX3 N	8
8	224_TX3_N \\ 224_TX3_P \\	224_TX3_P	99		100	224_RX3_P	—>> 224_RX3_P	8
8		224 TX2 N	103		104	224 RX2 N	***	
8	224_TX2_N	224_TX2_P	105		106	224_RX2_P	—>>> 224_RX2_N —>>> 224_RX2_P	8
Ŭ	224_1A2_F \\	224 TV4 N	107		108	224 DV4 N	// 224_RX2_P	Ŭ
8	224_TX1_N <<	224_TX1_N 224_TX1_P	109 111		110 112	224_RX1_N 224_RX1_P	—>>> 224_RX1_N	8
8	224_TX1_P <<		113		114		—>>> 224_RX1_P	8
8	224_TX0_N<<	224_TX0_N	115		116	224_RX0_N	—>>> 224_RX0_N	8
8	224_TX0_P	224_TX0_P	117 119		118 120	224_RX0_P	——————————————————————————————————————	8
	**		119		120			
		_		- A	XK5A2137YG	_		
		-	Ŧ	55	=			



VCCO_65 VCCO_66 Power supply can not exceed 1.8V

