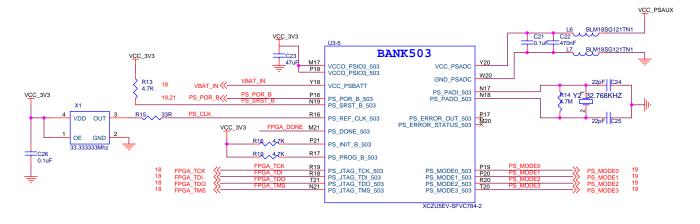
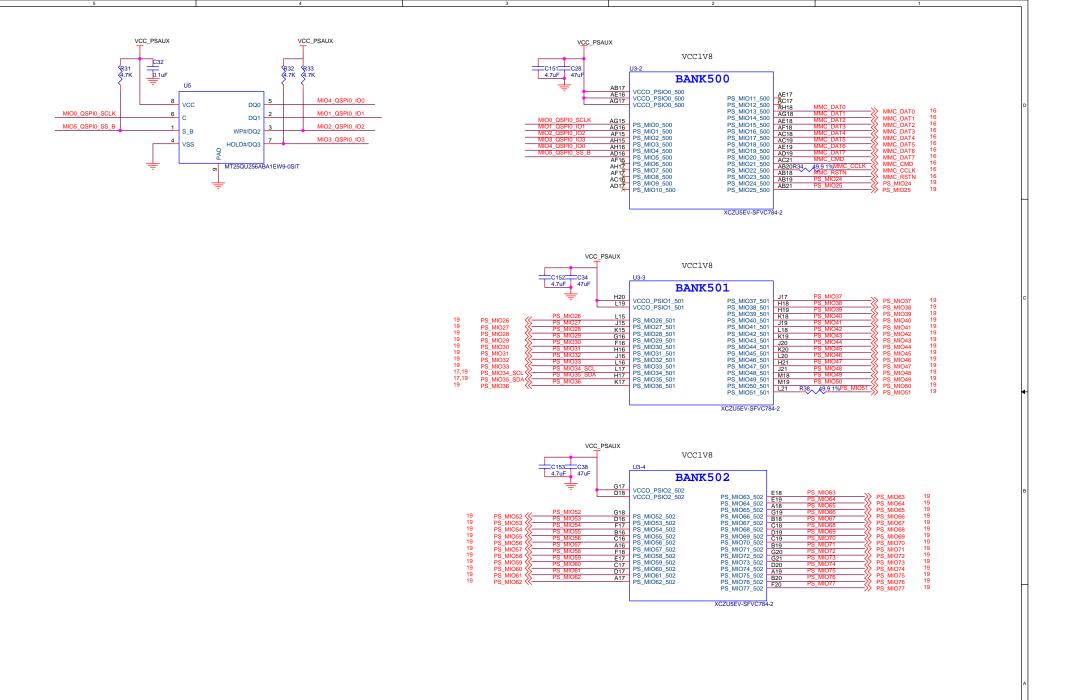


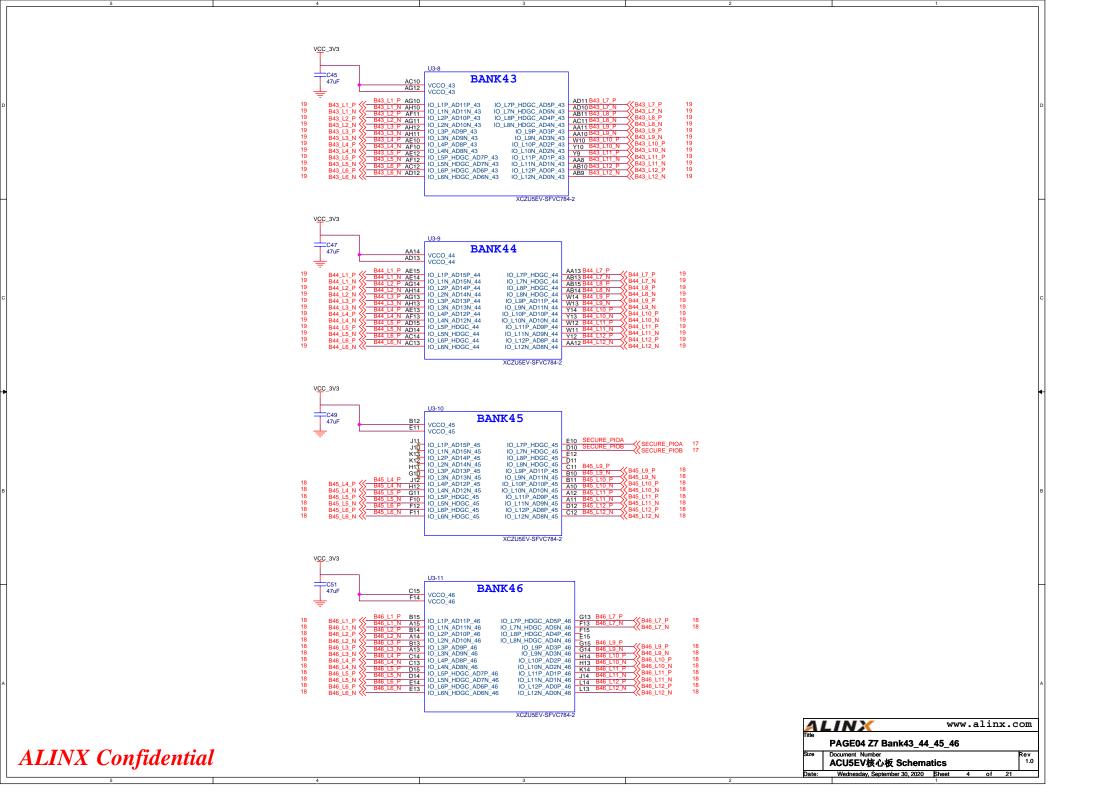
MODE[3:0]	BOOT MODE	Descritpion
0000	PS JTAG	PS JTAG Interface
0001	Quad_SPI(24b)	24-Bit addresssing(QSPI24)
0010	Quad_SPI(32b)	32-Bit addresssing(QSPI32)
0011	SD0(2.0)	SD2.0
0100	NAND	Requires 8-bit data bus width
0101	SD1(2.0)	SD2.0
0110	eMMC(1.8V)	eMMC version 4.5 at 1.8V
0111	USB0(2.0)	USB 2.0 only
1000	PJTAG(MIO #0)	PJTAG connection 0 option
1001	PJTAG(MIO #1)	PJTAG connection 1 option
1110	SD1 LS(3.0)	SD 3.0

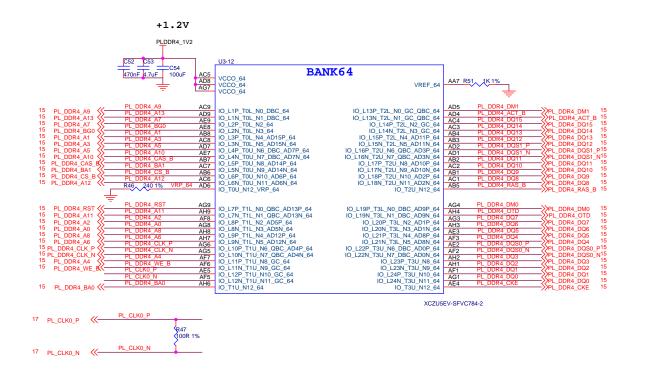


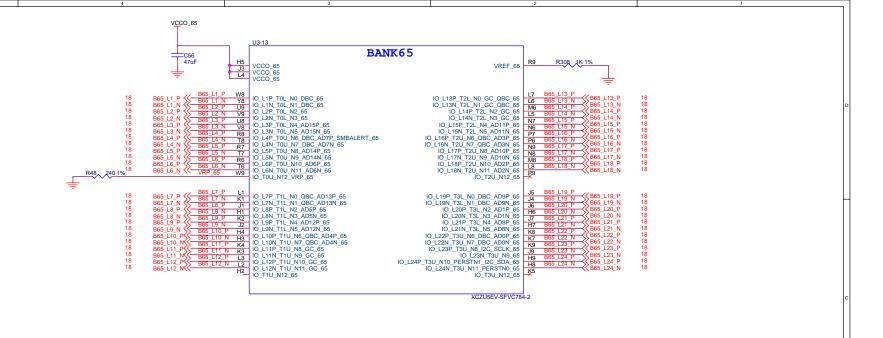


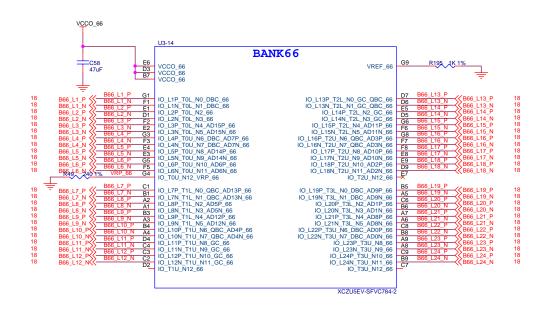




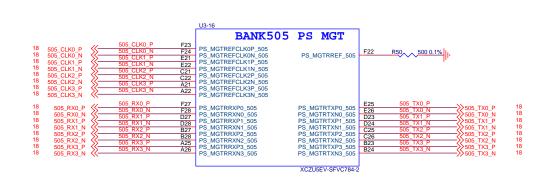






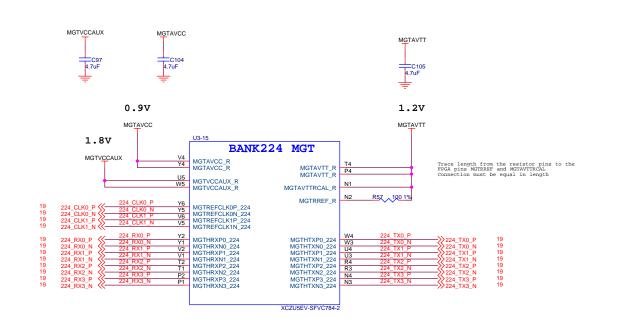






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PAGE07 Z7 Bank505

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Date: Wednesday, September 30, 2020 Sheet 7 of 21

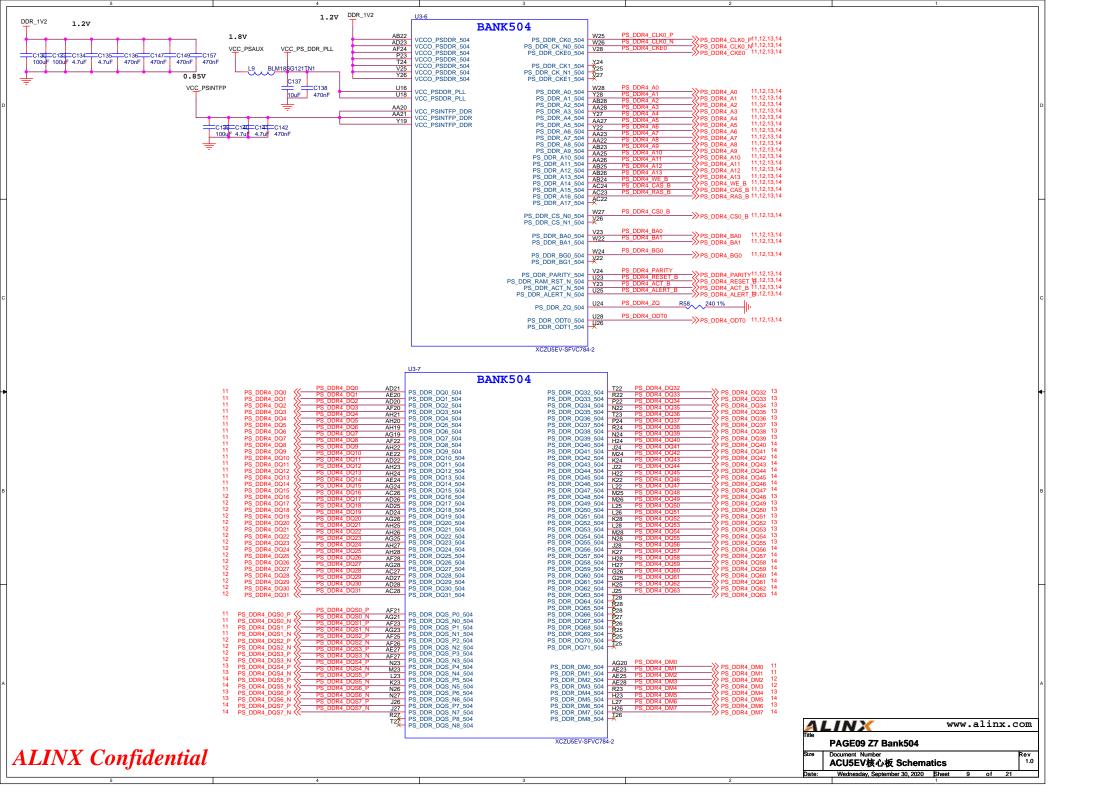


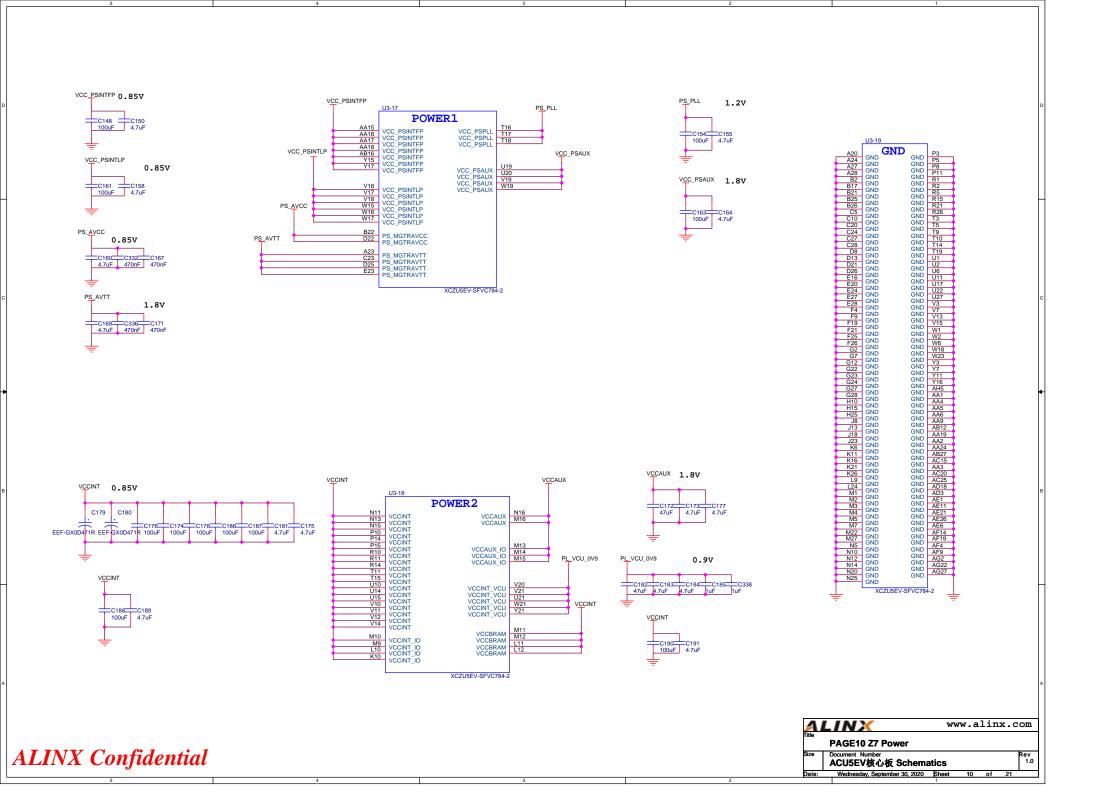
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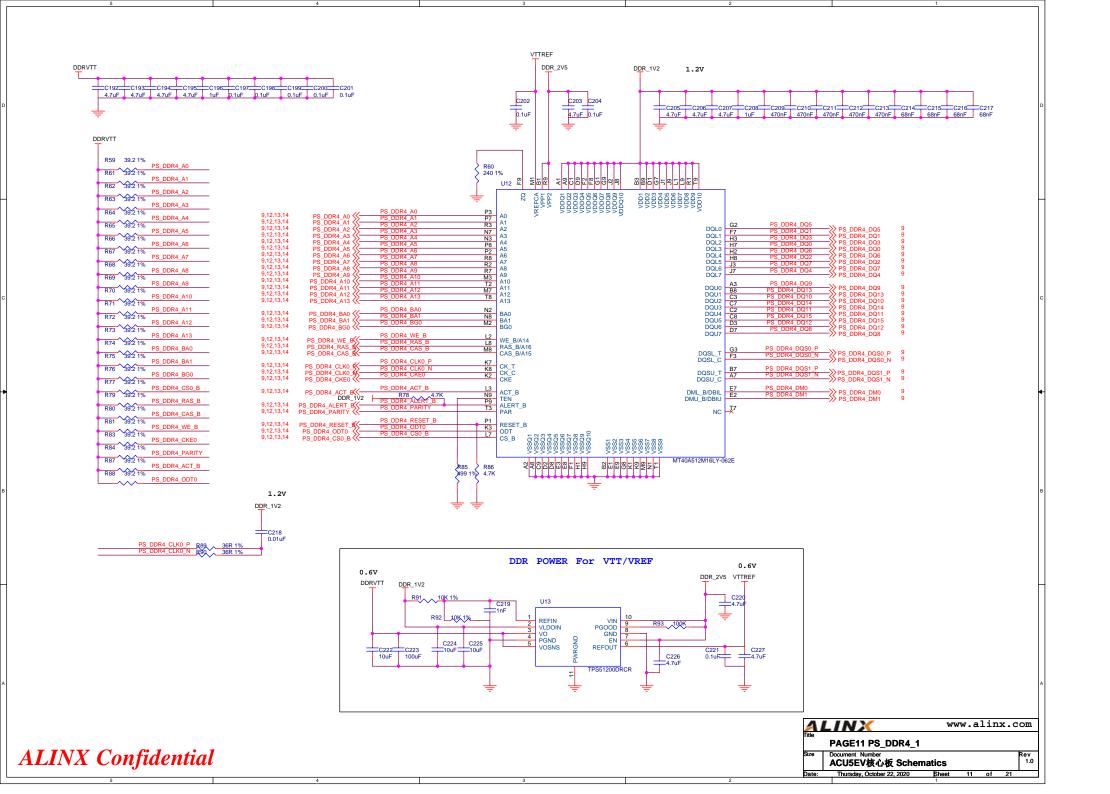
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PAGE08 Z7 Bank224

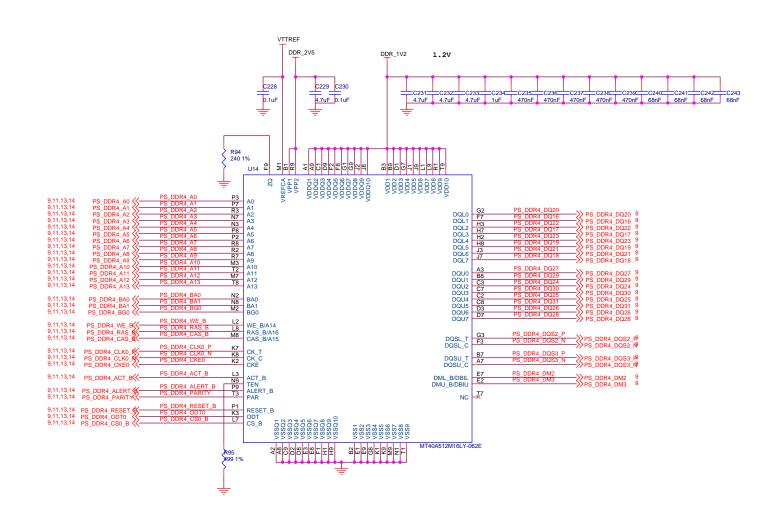
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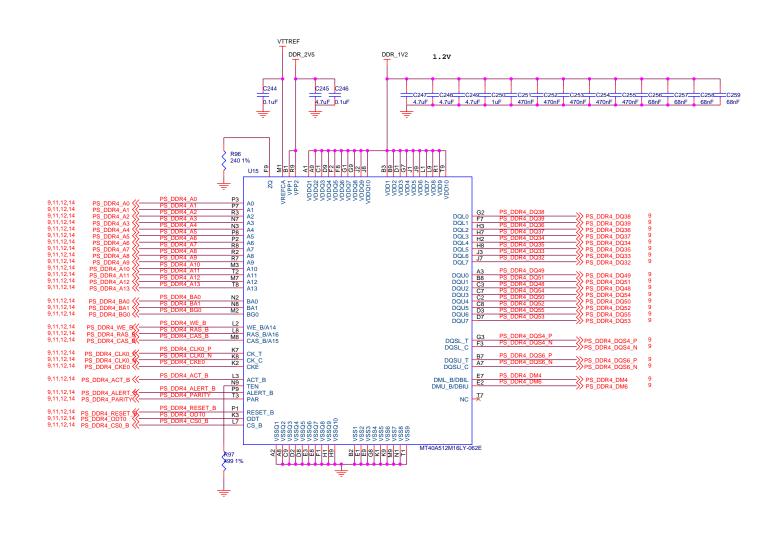
Date: Wednesday, September 30, 2020 Sheet 8 of 21







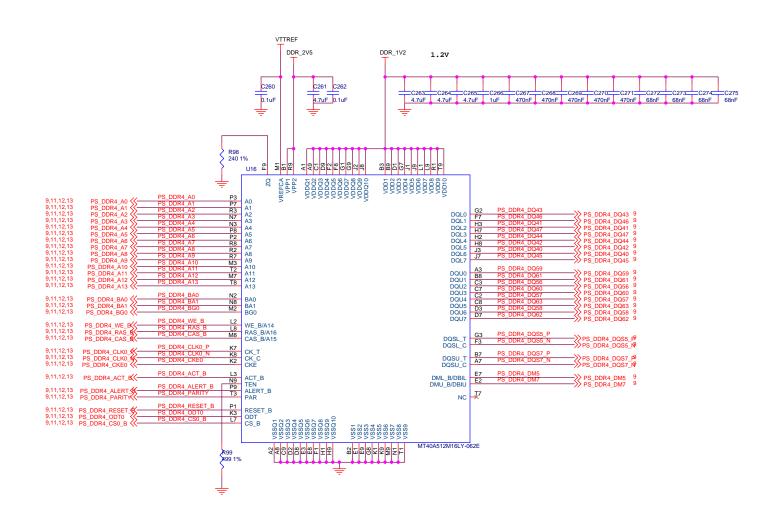


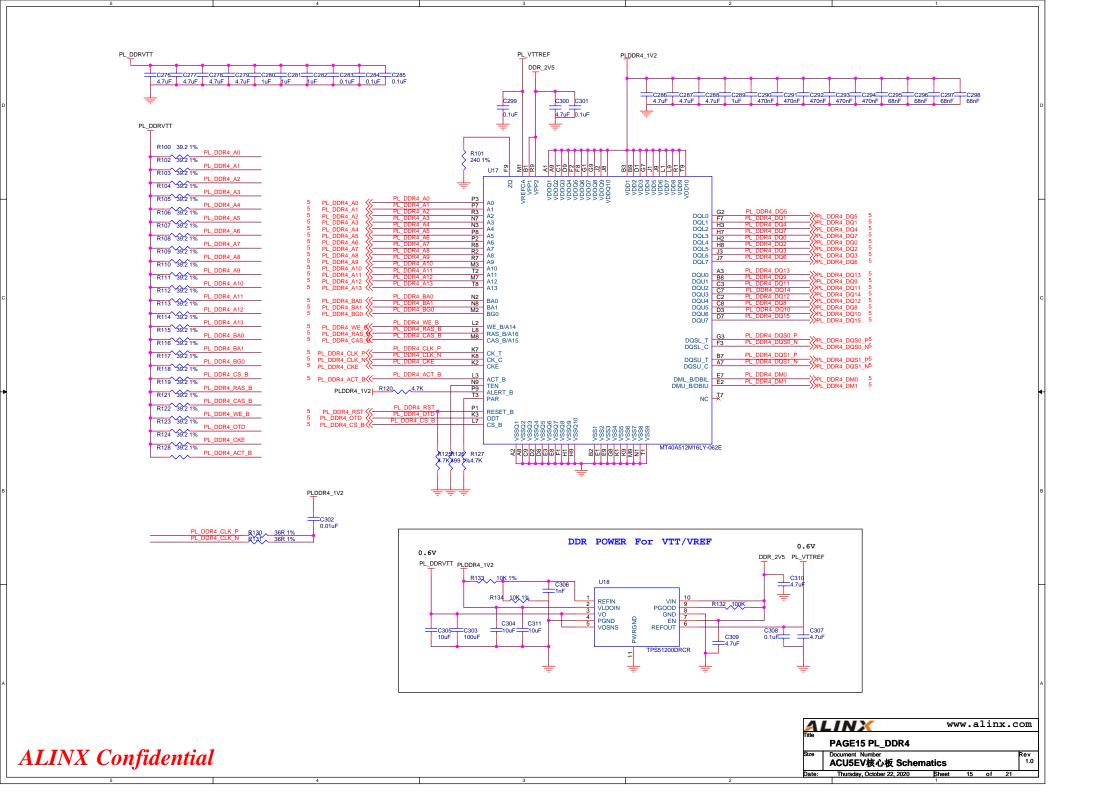


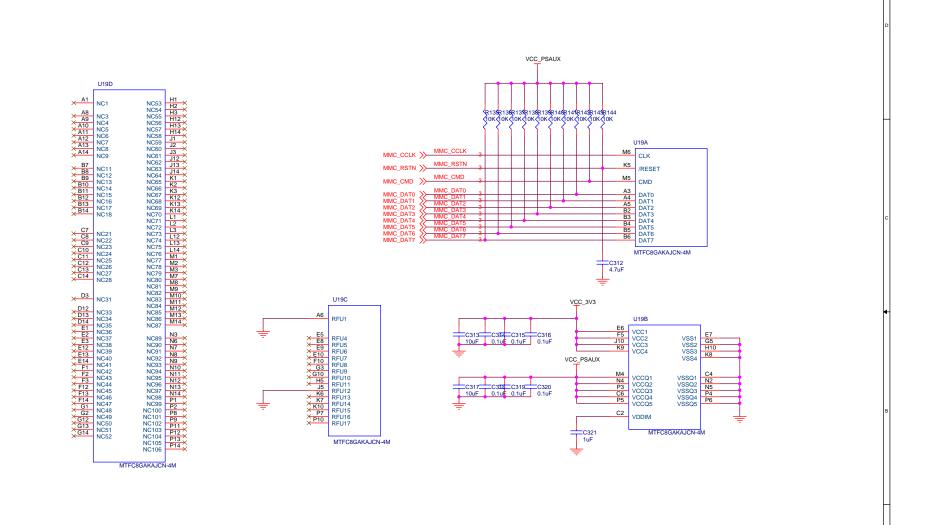
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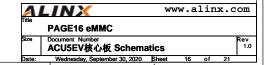
PAGE13 PS_DDR4_3

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Date: Thursday, October 22, 2020 Sheet 13 of 21

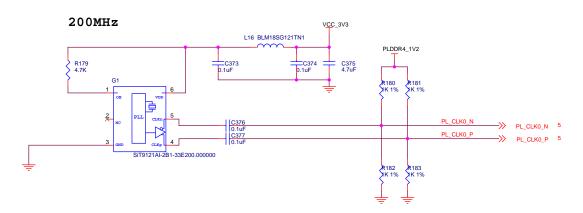


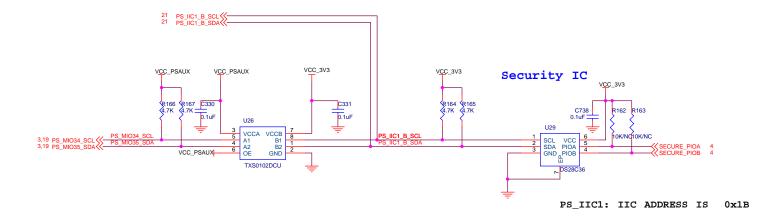






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PAGE17 CLOCK & SECURITY

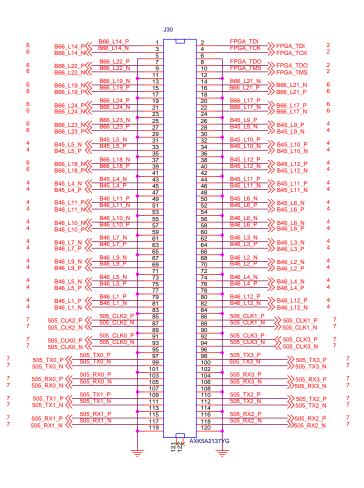
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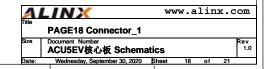
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BANK45, BANK46 IO Voltage is +3.3V Standard

J29									
_		B65 L2 N	1		2	B65 L22 P			
6	B65_L2_N \$	B65_L2_P	3		4	B65_L22_N	B65_L22_P	6 6	
· ·	B65_L2_P <<-		5		6		─>>> B65_L22_N	·	
6	B65_L4_N <<-	B65_L4_N B65_L4_P	7		8	B65_L20_P B65_L20_N	→>> B65_L20_P	6	
6	B65_L4_P <<	D05_L4_P	9		10 12	D05_L20_IV	→>> B65_L20_N	6	
		B65_L1_N	13		14	B65 L6 N	X		
6	B65_L1_N	B65_L1_P	15		16	B65_L6_P	→>> B65_L6_N →>> B65_L6_P	6 6	
· ·		DOE 17 D	17		18	DOE 147 D	-// B03_L0_P	·	
6	B65_L7_P <<-	B65_L7_P B65_L7_N	19 21		20 22	B65_L17_P B65_L17_N	→>> B65_L17_P	6	
6	B65_L7_N <<-	D03_E/_I4	23		24	D03_E17_14	→>> B65_L17_N	6	
6		B65_L15_P	25		26	B65_L9_P	W === . = =	6	
6	B65_L15_P	B65_L15_N	27		28	B65_L9_N	→>> B65_L9_P →>> B65_L9_N	6	
-		B65_L16_P	29		30	B65 L3 N	// B00_E9_N		
6		B65_L16_N	31 33		32 34	B65_L3_N	→>> B65_L3_N	6	
6	B65_L16_N<<		35		36		−>> B65_L3_P	6	
6		B65_L14_P	37		38	B65_L19_P	N 205 140 B	6	
6	B65_L14_N	B65_L14_N	39		40	B65_L19_N	→>> B65_L19_P →>> B65_L19_N	6	
		B65 L5 N	41 43		42 44	B65_L18_P	// D00_E10_IV		
6		B65_L5_N	45		46	B65_L18_N	→>> B65_L18_P	6	
6	B65_L5_P <<-		47		48		→>> B65_L18_N	6	
6	R65 L11 N//-	B65_L11_N	49		50	B65_L8_P	-W per 10 p	6	
6	B65_L11_N	B65_L11_P	51		52	B65_L8_N	→>> B65_L8_P →>> B65_L8_N	6	
		B65_L10_N	53 55		54 56	B65 L24 N	// 500_50_11		
6	B65_L10_N((-	B65_L10_P	57		58	B65 L24 P	→>>> B65_L24_N	6	
6	B65_L10_P <<-		59		60		─>>> B65_L24_P	6	
6		B66_L3_P	61		62	B65_L12_P	→>> B65_L12_P	6	
6	B66_L3_N <	B66_L3_N	63		64	B65_L12_N	→S B65 L12 N	6	
	**	B66_L1_P	65 67		66 68	B65 L13 N	//		
6	B66_L1_P <<	B66 L1 N	69		70	B65 L13 P	SB65_L13_N	6	
6	B66_L1_N <<-		71		72		⟨⟨B65_L13_P	6	
6		B66_L6_P	73		74	B65_L21_P	→>> B65_L21_P	6	
6	B66_L6_N <	B66_L6_N	75 77		76 78	B65_L21_N	→ B65_L21_N	6	
	;;	B66 L16 P	79		80	B65 L23 P			
6	B66_L16_P\\	B66_L16_N	81		82	B65_L23_N	→>> B65_L23_P	6	
6	B66_L16_N<<		83		84		→>> B65_L23_N	6	
6		B66_L15_P	85		86	B66_L5_N	→>> B66_L5_N	6	
6	B66_L15_N<	B66_L15_N	87 89		90	B66_L5_P	→>> B66_L5_P	6	
		B66 L4 P	91		92	B66 L2 P	**		
6	B66_L4_P	B66_L4_N	93		94	B66_L2_N	→ B66_L2_P	6 6	
· ·	B66_L4_N <<	D00 144 D	95		96	D00 100 D	─>>> B66_L2_N	·	
6		B66_L11_P B66_L11_N	97 99		98 100	B66_L20_P B66_L20_N	→>> B66_L20_P	6	
6	B66_L11_N<<	DOO_ETT_IV	101		100	D00_E20_14	→>> B66_L20_N	6	
6	//	B66_L12_P	103		104	B66_L7_P	W === := =		
6	B66_L12_P	B66_L12_N	105		106	B66_L7_N	B66_L7_P	6 6	
•		D00 140 N	107		108	D00 140 D	─>>> B66_L7_N		
6	B66 L13 N<<	B66_L13_N B66_L13_P	109 111		110 112	B66_L10_P B66_L10_N	→>> B66_L10_P	6	
6	B66_L13_P(\	500_E10_F	113		114	200_E10_IV	→>> B66_L10_N	6	
6		B66_L8_N	115		116	B66_L9_P	-N pec 10 p	6	
6	B66_L8_N SEE	B66_L8_P	117		118	B66_L9_N	→>> B66_L9_P →>> B66_L9_N	6	
	200_20_1 ((119		120		// DOO_ES_IN	-	
				Δ	(K5A2137)	rG			
				2,84	110/12/13/	•			
		=	₹		=	=			

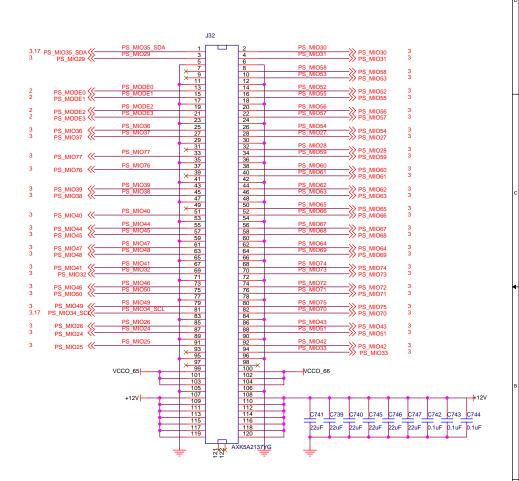




BANK43, BANK44 IO Voltage is +3.3V Standard

The MIO Voltage is +1.8V Standard

			J	31				
		B44 L10 P	1 Г		2	B44_L7_P		
4	B44_L10_P <<	B44 L10 N	3		4	B44 L7 N	—>>> B44_L7_P	4
4	B44_L10_N\\		5		6		—>>> B44_L7_N	4
4	D44 10 D //	B44_L6_P	7		8	B43_L6_P	W 040 40 0	4
4	B44_L6_P	B44_L6_N	9		10	B43_L6_N	—≫ B43_L6_P —≫ B43_L6_N	4
7	D44_L0_N \\		11		12			7
4	B44 15 P <<-	B44_L5_P B44_L5_N	13		14	B43_L7_P B43_L7_N	—>>> B43 L7 P	4
4	B44_L5_P	D44_L3_IN	15 17		16	D43_L7_IN	—≫ B43_L7_P —≫ B43_L7_N	4
		B44 L1 P	19		20	B43 L8 N		
4	B44_L1_P	B44_L1_N	21		22	B43_L8_P	—— → → → → → B43_L8_N	4
4	B44_L1_N <<		23		24		—⋙ B43_L8_P	4
4	D44 140 D//-	B44_L12_P	25		26	B44_L2_P	-Way 100	4
4	B44_L12_P	B44_L12_N	27		28	B44_L2_N	—≫ B44_L2_P —≫ B44_L2_N	4
	D44_L12_W\\	D44 10 D	29		30		// D44_L2_IN	
4	B44 13 P <<─	B44_L3_P B44_L3_N	31		32 34 ×			
4	B44_L3_P	D44_L3_N	33 35		34 36			
		B43 L12 N	37		38	B43 L9 P		
4	B43_L12_N \\	B43_L12_P	39		40	B43 L9 N	—≫ B43_L9_P —≫ B43_L9_N	4
4	B43_L12_P <<		41		42		—>>> B43_L9_N	4
4	D40 140 N//-	B43_L10_N	43		44	B43_L3_P	N pag 12 p	4
4	B43_L10_N B43_L10_P	B43_L10_P	45		46	B43_L3_N	—≫ B43_L3_P —≫ B43_L3_N	4
	D45_E10_F ((Date Late N	47		48	D40 14 N	// B43_E3_IV	
4	B44 L11 NKK-	B44_L11_N B44_L11_P	49 51		50 52	B43_L1_N B43_L1_P	—≫ B43 L1 N	4
4	B44_L11_N B44_L11_P	D44_L11_F	53		54	D43_L1_F	—≫ B43_L1_N —≫ B43_L1_P	4
		B44 L9 N	55		56	B44 L4 P		
4	B44_L9_N ————————————————————————————————————	B44 L9 P	57		58	B44_L4_N	—≫ B44_L4_P —≫ B44_L4_N	4
4	B44_L9_P <<		59		60		—>>> B44_L4_N	4
4	D44 10 D //-	B44_L8_P	61		62	B43_L5_P	W 040 15 D	4
4	B44_L8_P	B44_L8_N	63		64	B43_L5_N	—≫ B43_L5_P —≫ B43_L5_N	4
	D44_L0_N \\	D40 10 N	65		66	D40 14 D	// D43_L5_IV	
4	B43 12 N <<∕─	B43_L2_N B43_L2_P	67		68	B43_L4_P B43_L4_N	—>>> B43 I 4 P	4
4	B43_L2_N	D43_L2_P	69 71		70 72	D43_L4_IN	—≫ B43_L4_P —≫ B43_L4_N	4
_		VBAT_IN	73		74	B43_L11_P		
2 21	VBAT_IN ∰ MR	MR	75		76	B43_L11_N	—≫ B43_L11_P —≫ B43_L11_N	4
21	MR <<		77		78		—>>> B43_L11_N	4
			79		80	PS_POR_B	—≫ PS POR B	2.21
			81		82		// 1 0_1 OK_B	_,
			83 85		84			
			87		88			
			89		90			
		224_CLK0_P	91		92	224_CLK1_P	W	
8	224_CLK0_P	224_CLK0_N	93		94	224_CLK1_N	224_CLK1_P	8 8
·	224_CLKU_N \\		95		96		—>>>224_CLK1_N	Ü
8	224_TX3_N <<	224_TX3_N	97		98	224_RX3_N	—>>> 224 RX3 N	8
8	224 TX3 P	224_TX3_P	99		100	224_RX3_P	—>>> 224_RX3_P	8
		224_TX2_N	101		102	224 RX2 N	·	
8	224_TX2_N	224_TX2_P	105		104	224 RX2 P	——>>> 224_RX2_N	8
8	224_TX2_P << —		107		108		—>>> 224_RX2_P	8
8		224_TX1_N	109		110	224_RX1_N	N	8
8	224_TX1_N 224_TX1_P	224_TX1_P	111		112	224_RX1_P	—>>> 224_RX1_N —>>> 224_RX1_P	8
3	224_1X1_P\\	004 TV0 N	113		114	OOA DVO N	// 224_RXI_P	3
8	224_TX0_N<<	224_TX0_N 224_TX0_P	115		116	224_RX0_N 224_RX0_P	—>>> 224_RX0_N	8
8	224_TX0_P	224_1AU_P	117		118	224_RAU_P	224_RX0_N 224_RX0_P	8
	"		119		120			
			1 -	A	XK5A2137YG			
		-	=	28		-		
				—				



VCCO_65 VCCO_66 Power supply can not exceed 1.8V

