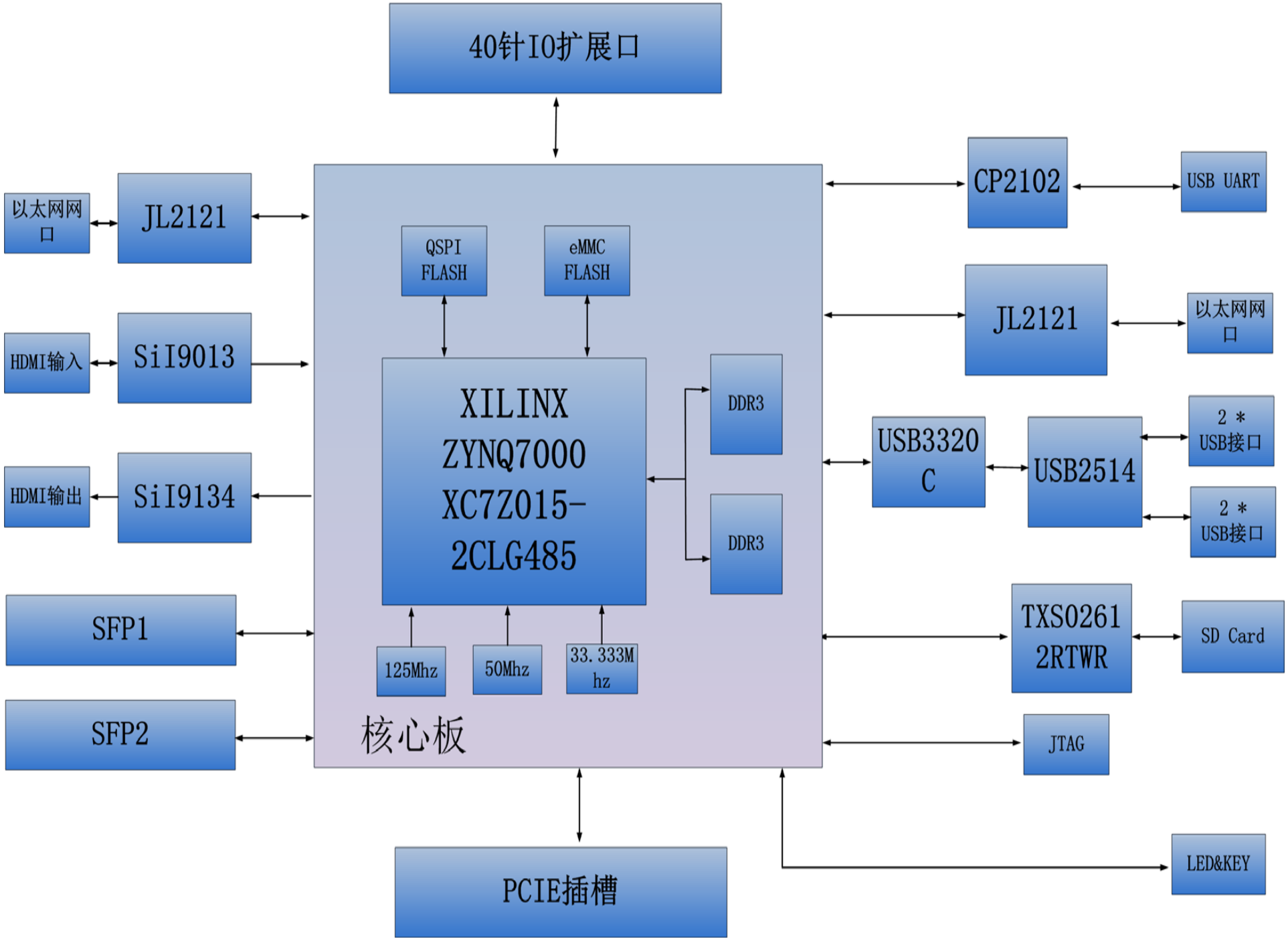

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Title	PAGE01 Block Diagram
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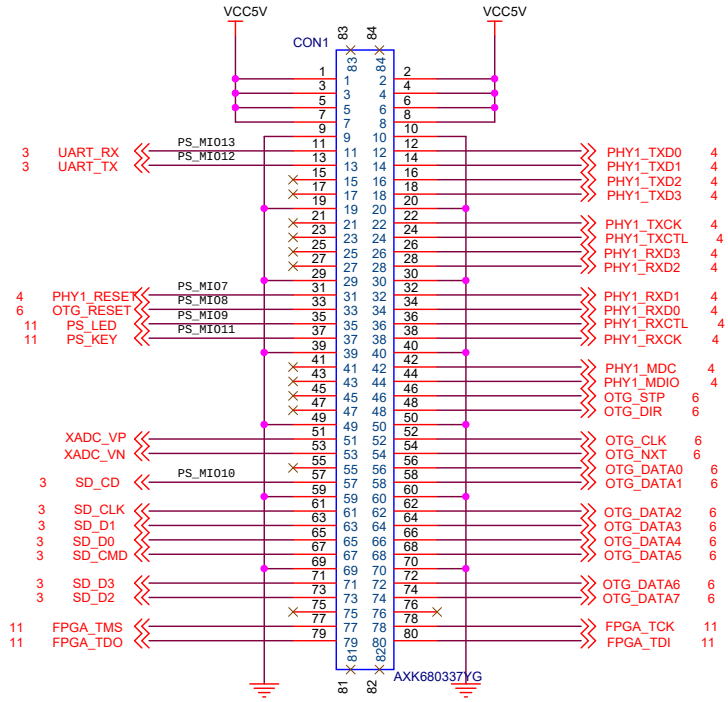

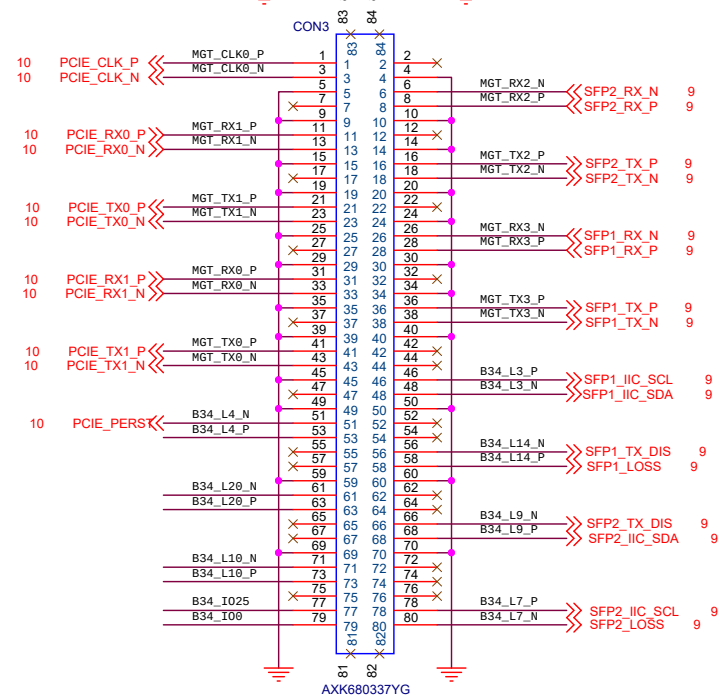

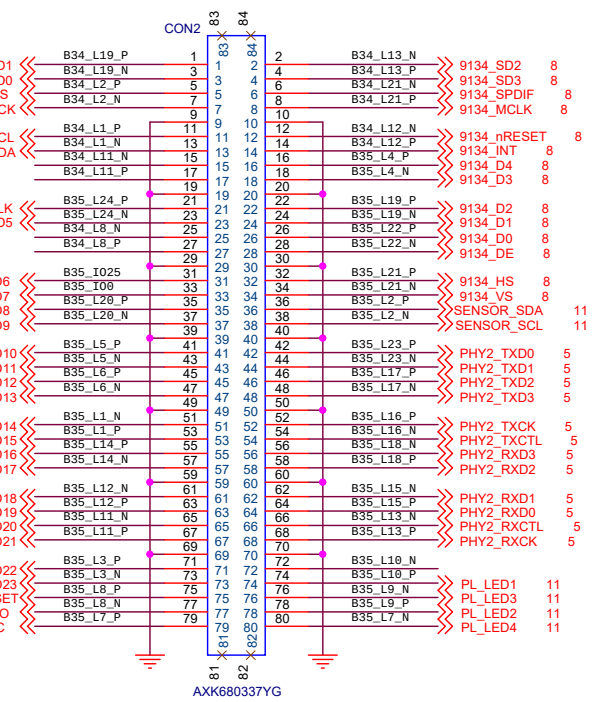

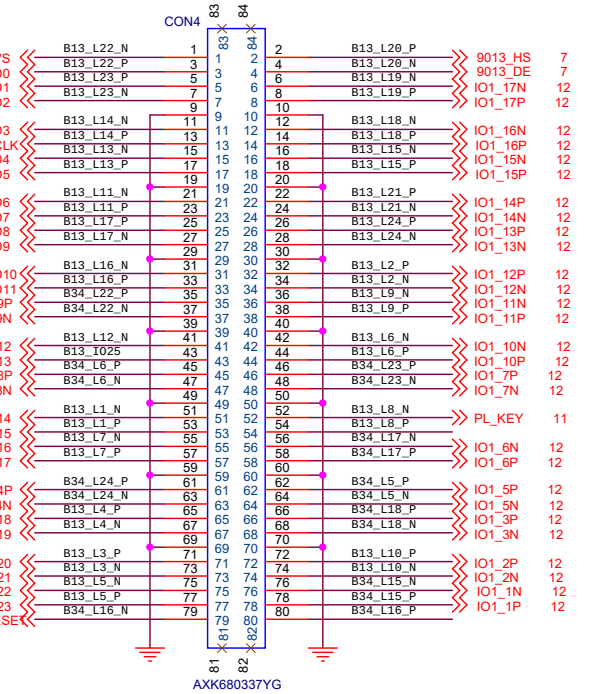

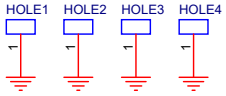

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Title	PAGE02 Board Connector
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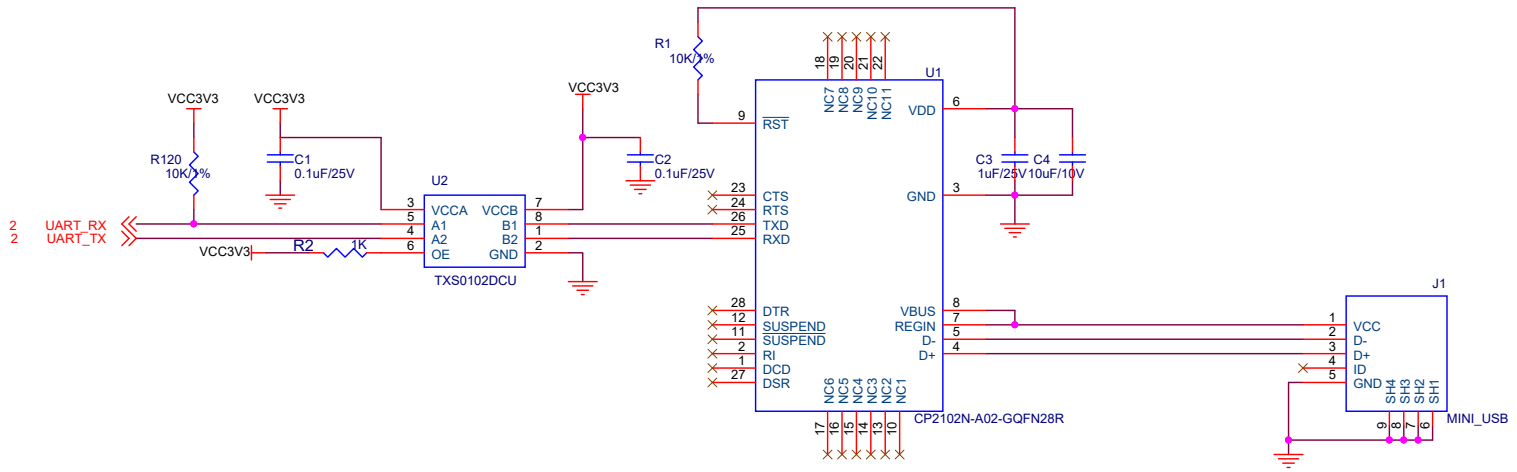
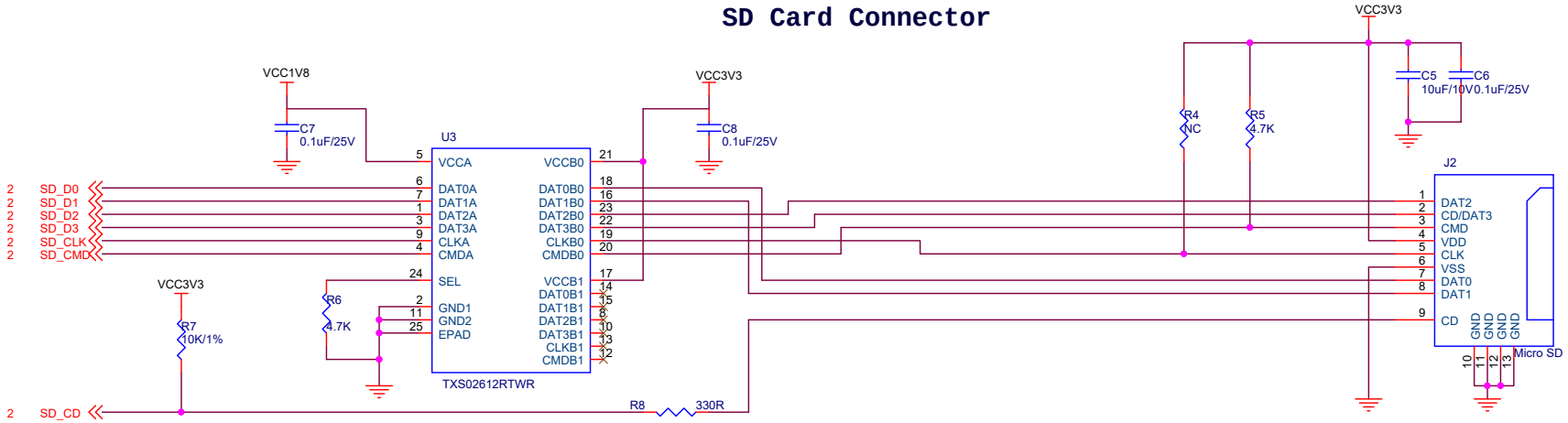
USB Uart



SD Card Connector



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Title: PAGE03 SD Card and UART

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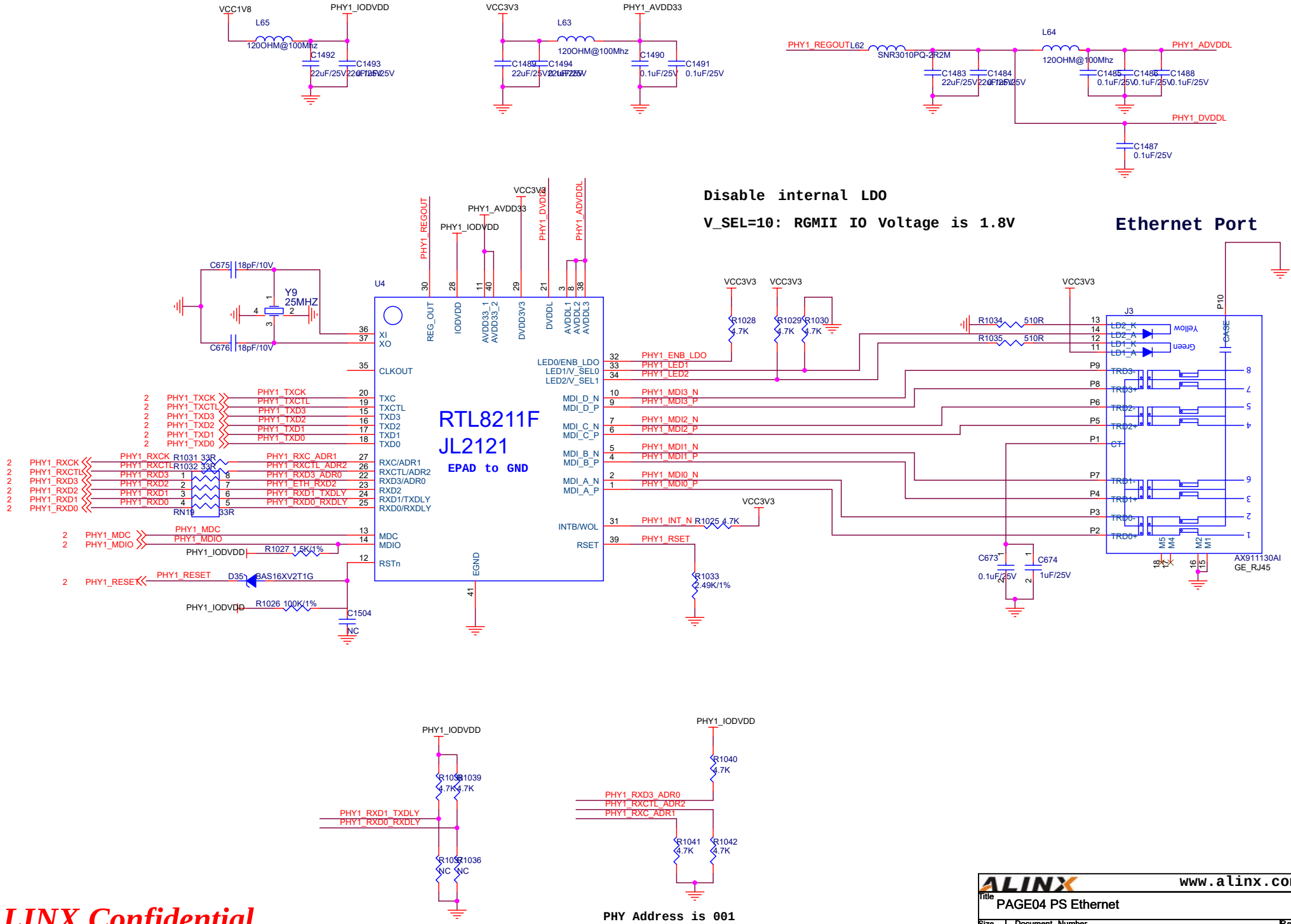
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V_SEL=10: RGMII IO Voltage is 1.8V
Ethernet Port
RTL8211F
JL2121
EPAD to GND
PHY Address is 001
AX911130AI
GE_RJ45
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PAGE04 PS Ethernet
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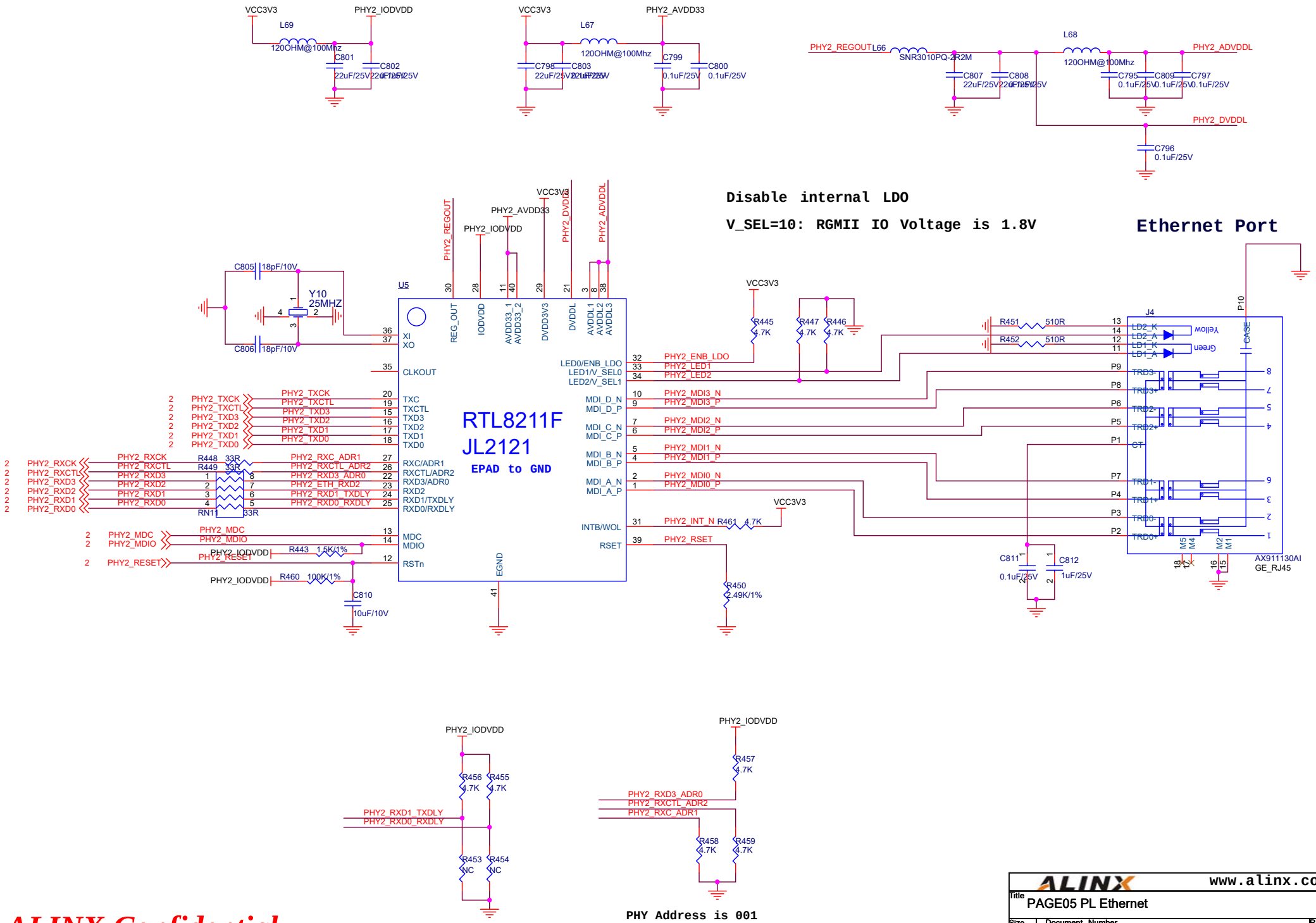
Disable internal LDO
V_SEL=10: RGMII IO Voltage is 1.8V
Ethernet Port
RTL8211F
JL2121
EPAD to GND
PHY Address is 001
U5
J4
P10
Y10
25MHZ
C805 18pF/10V
C806 18pF/10V
L69 120OHM@100Mhz
L67 120OHM@100Mhz
L68 120OHM@100Mhz
L66 SNR3010PQ-2R2M
C801 22uF/25V
C802 0.1uF/25V
C798 22uF/25V
C803 0.1uF/25V
C799 0.1uF/25V
C800 0.1uF/25V
C807 22uF/25V
C808 0.1uF/25V
C795 0.1uF/25V
C809 0.1uF/25V
C797 0.1uF/25V
C796 0.1uF/25V
C810 10uF/10V
C811 0.1uF/25V
C812 1uF/25V
R443 1.5K/1%
R460 100K/1%
R450 2.49K/1%
R461 4.7K
R445 4.7K
R447 4.7K
R446 4.7K
R451 510R
R452 510R
R448 33R
R449 33R
RN1 33R
R456 4.7K
R455 4.7K
R453 NC
R454 NC
R457 4.7K
R458 4.7K
R459 4.7K
VCC3V3
PHY2_IODVDD
PHY2_AVDD33
PHY2_REGOUT
PHY2_DVDDL
PHY2_ADVDDL
PHY2_RESET
PHY2_INT_N
PHY2_RSET
PHY2_MDC
PHY2_MDIO
PHY2_TXCK
PHY2_TXCTL
PHY2_TXD3
PHY2_TXD2
PHY2_TXD1
PHY2_TXD0
PHY2_RXCK
PHY2_RXCTL
PHY2_RXD3
PHY2_RXD2
PHY2_RXD1
PHY2_RXD0
PHY2_RXC_ADR1
PHY2_RXCTL_ADR2
PHY2_RXD3_ADR0
PHY2_ETH_RXD2
PHY2_RXD1_TXDLY
PHY2_RXD0_RXDLY
PHY2_ENB_LDO
PHY2_LED1
PHY2_LED2
PHY2_MDI3_N
PHY2_MDI3_P
PHY2_MDI2_N
PHY2_MDI2_P
PHY2_MDI1_N
PHY2_MDI1_P
PHY2_MDI0_N
PHY2_MDI0_P
XI
XO
CLKOUT
RSTn
EGND
AX911130AI
GE_RJ45
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PAGE05 PL Ethernet
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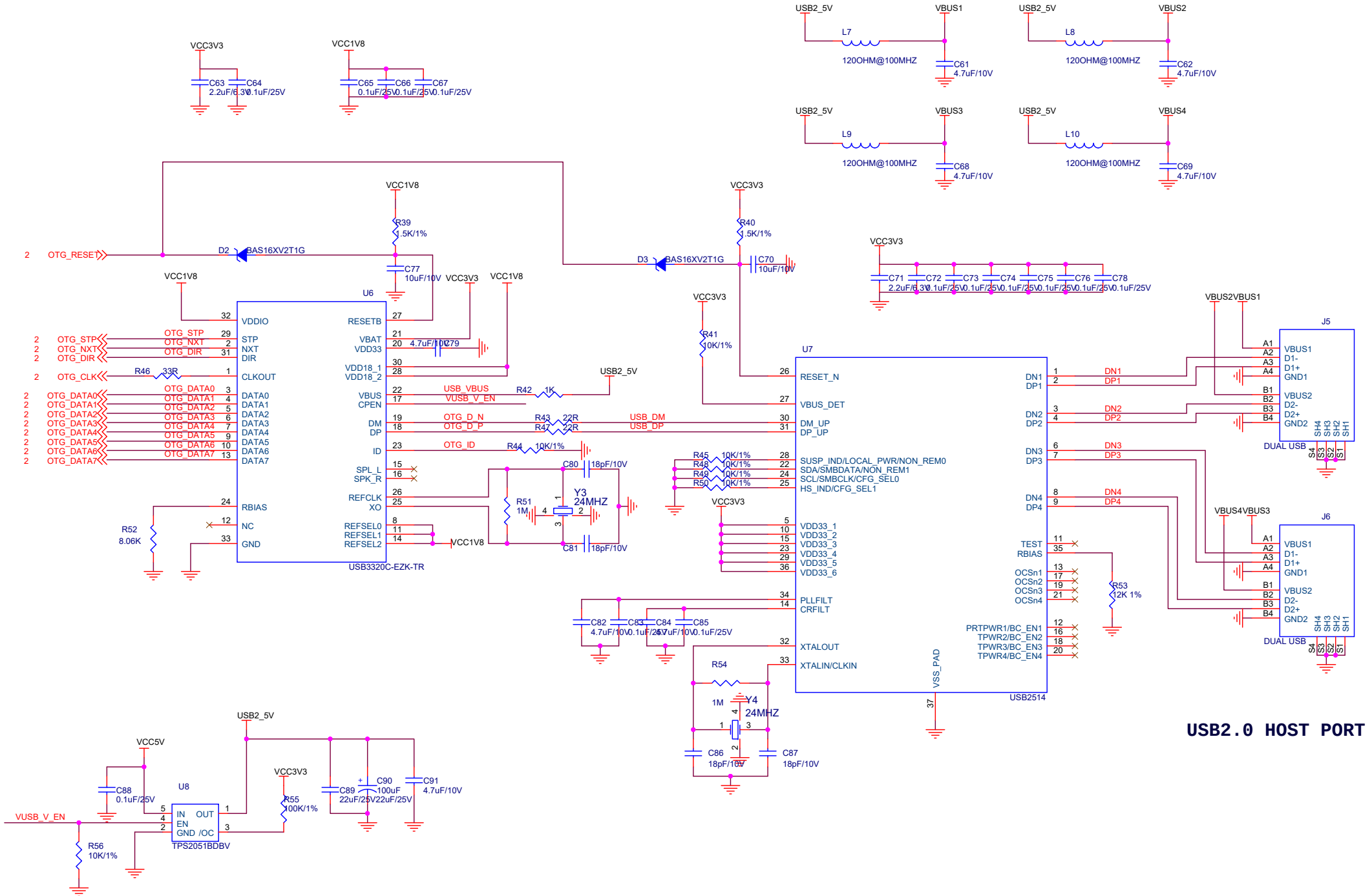

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Title: PAGE06 USB2.0		
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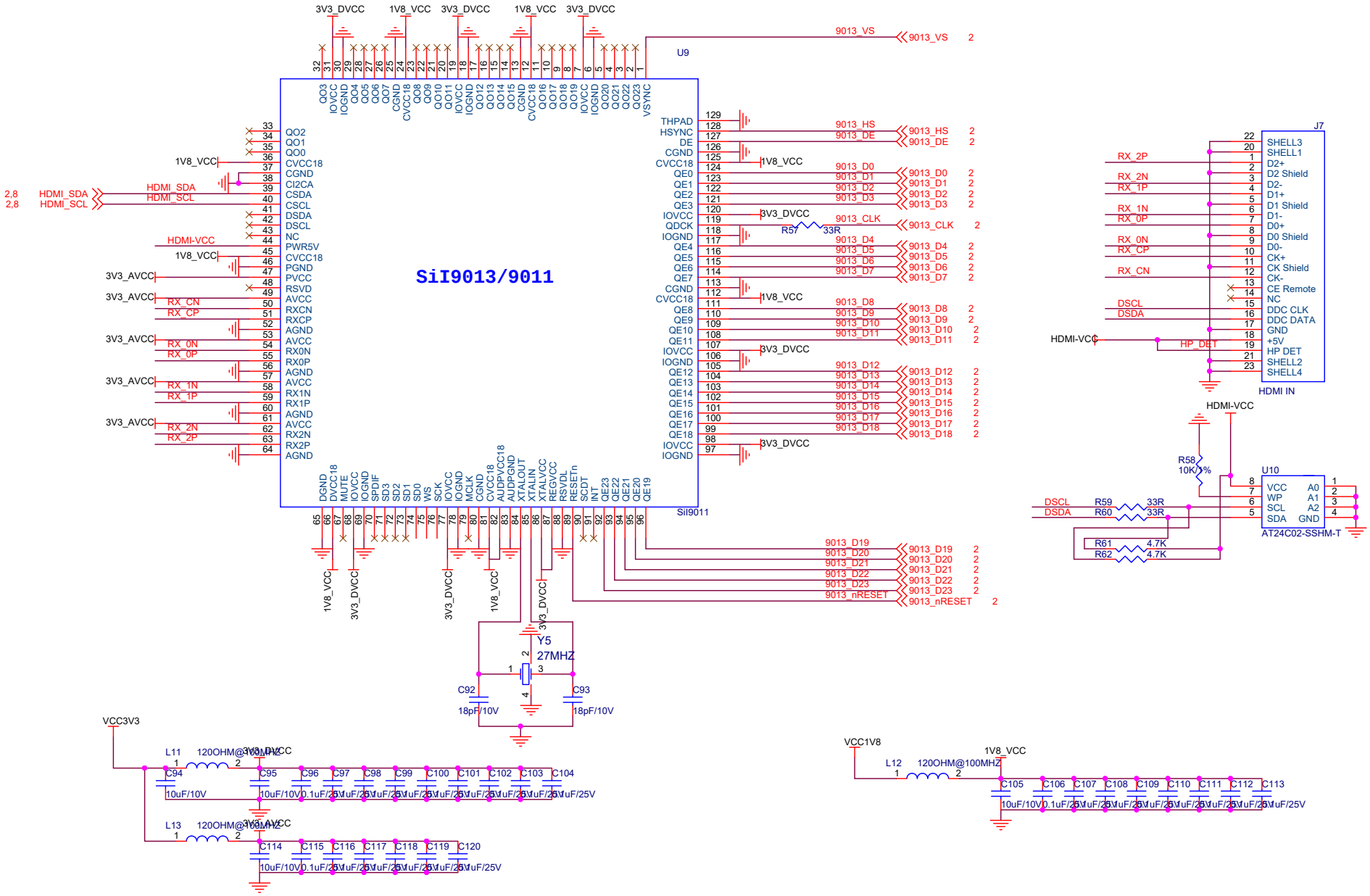

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ALINX	www.alinx.com
Title	PAGE07 HDMI_IN
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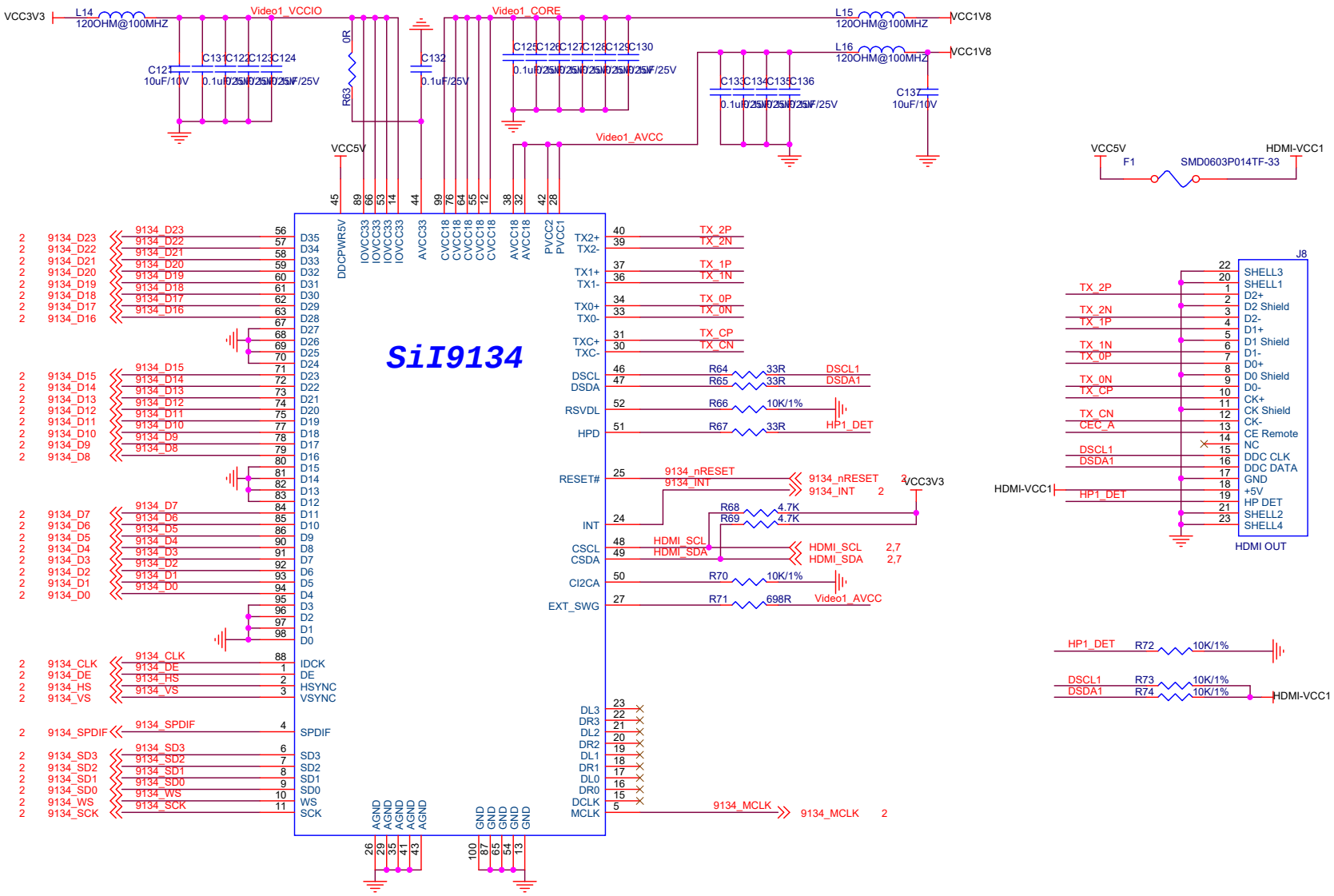

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ALINX	www.alinx.com
Title	PAGE08 HDMI OUT
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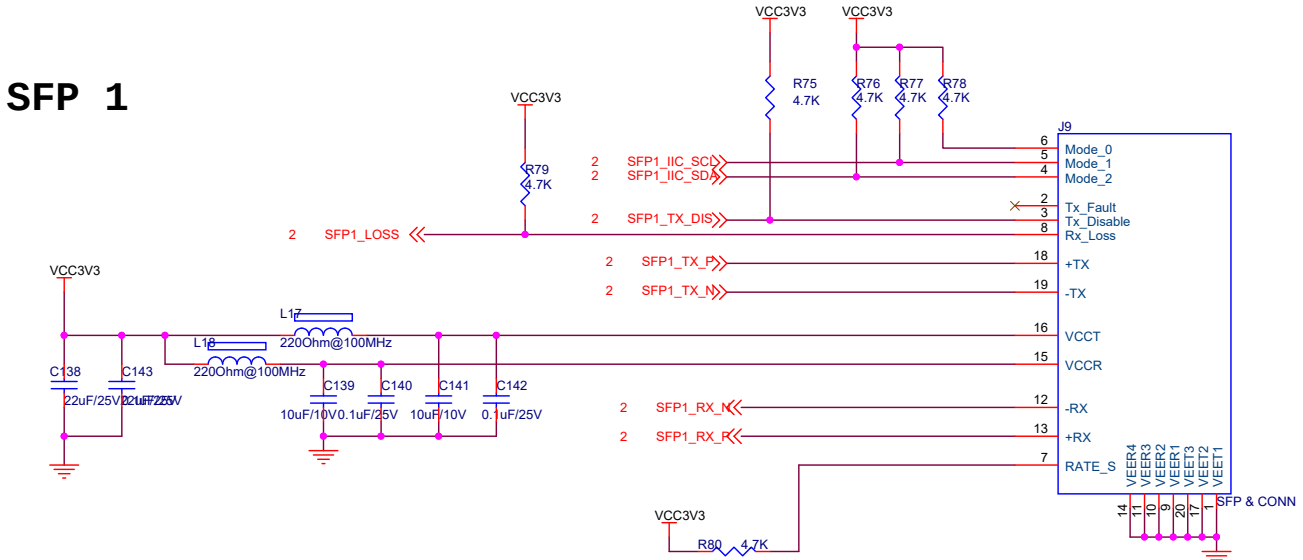
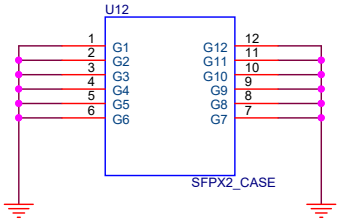
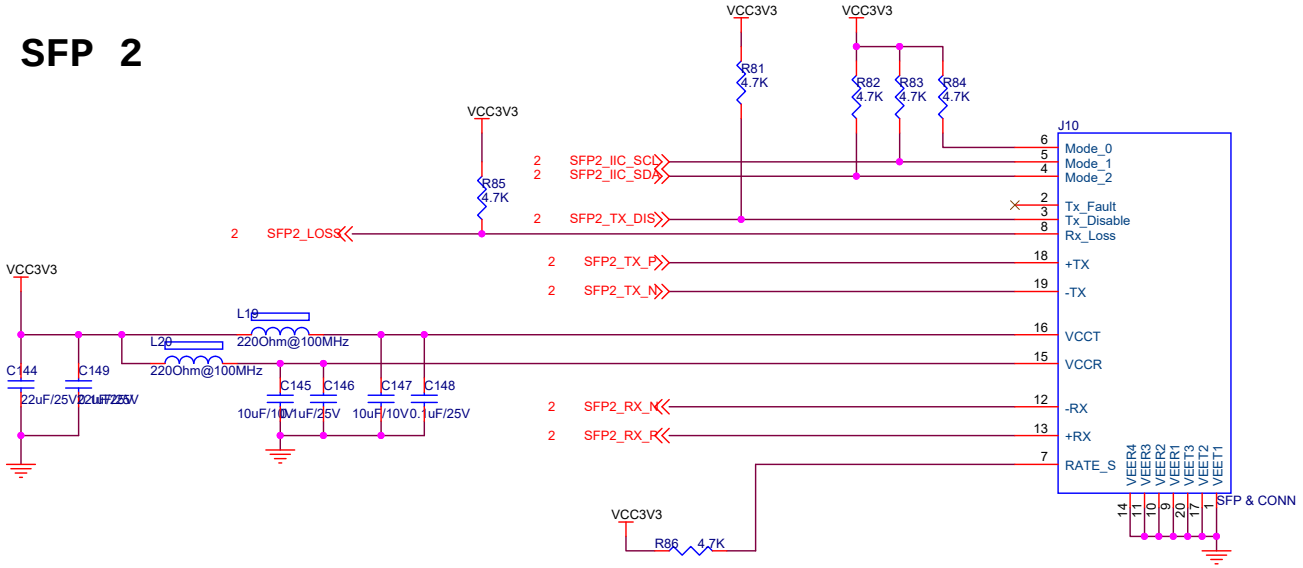
SFP 1





SFP 2



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Title: PAGE09 SFP1 & SPF2		
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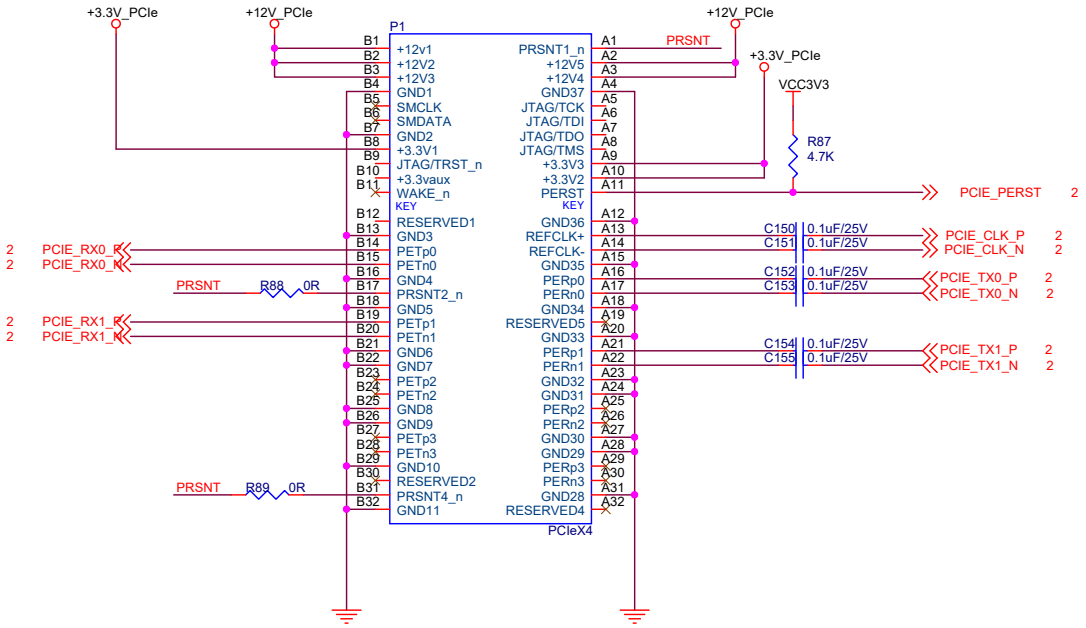
PCIE X2 SLOT



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Title: PAGE10 PCIE

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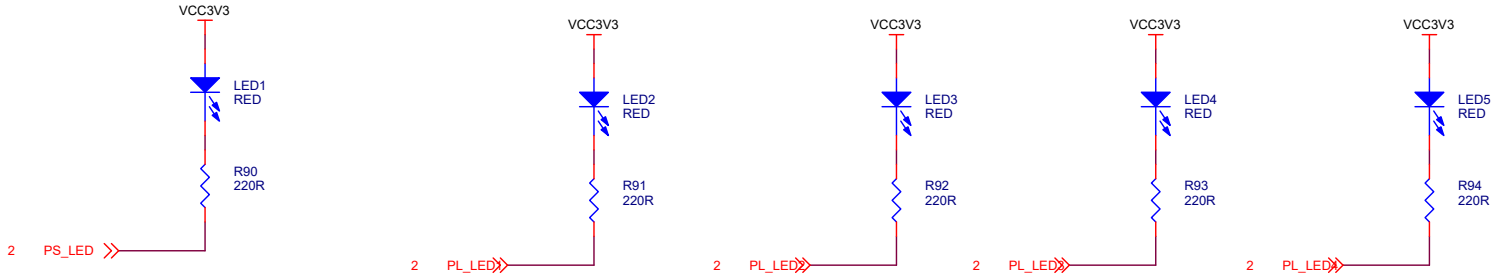

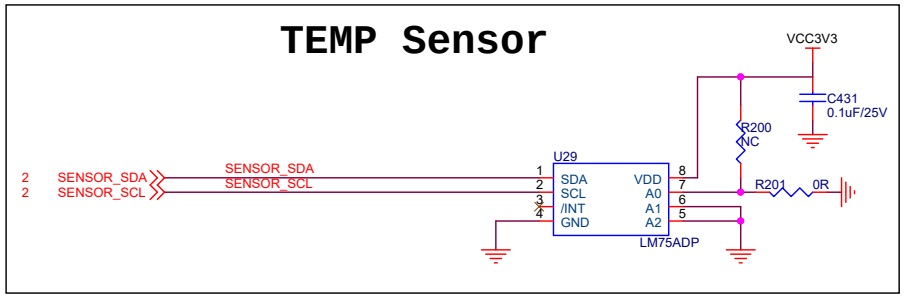

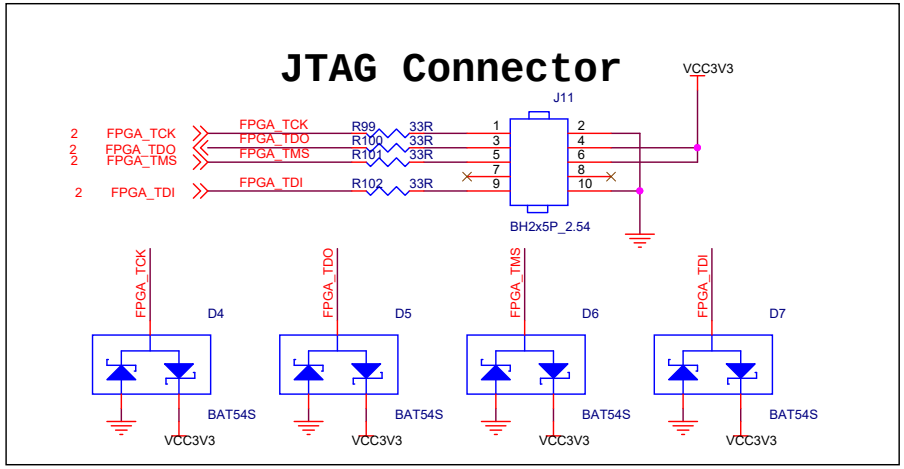

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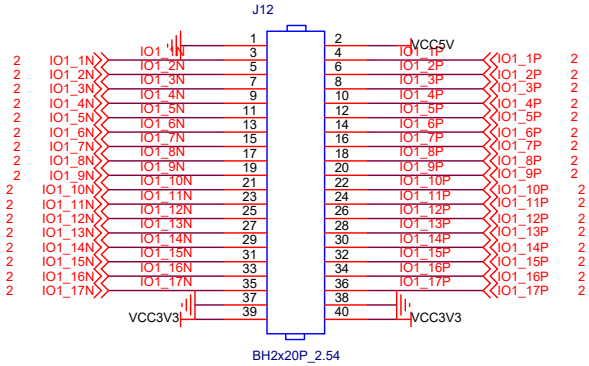
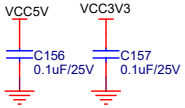
FPGA 40 PIN External IO





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Title: PAGE12 EX Connector		
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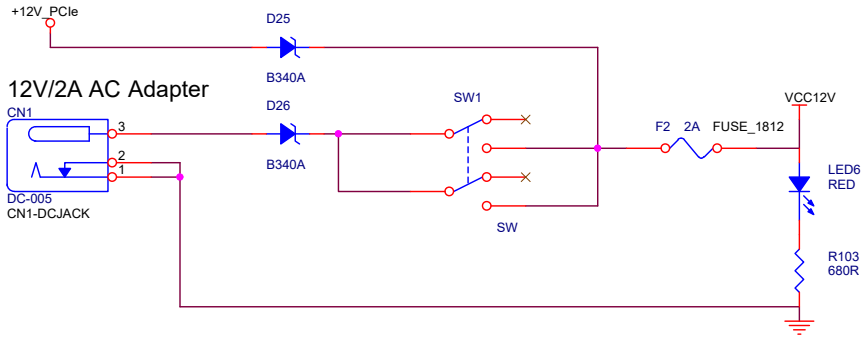
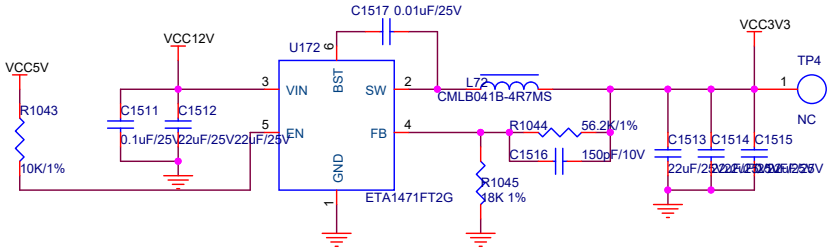
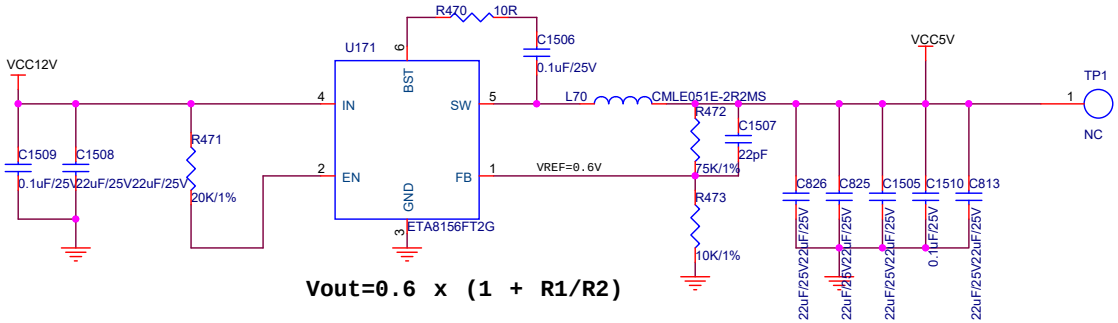


+3.3V POWER



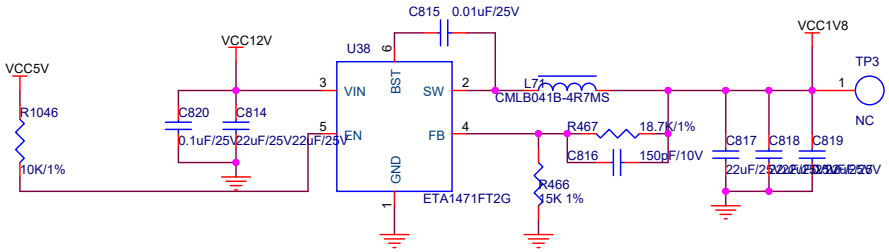
Vout=0.8 x (1 + R1/R2)

+5V POWER



Vout=0.6 x (1 + R1/R2)

+1.8V POWER



Vout=0.8 x (1 + R1/R2)

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Title: PAGE13 Power

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