

BOOT OPTION

+3.3V

R9 20K PS_MIO8
R8 20K CSPI_D0
R10 20K CSPI_D1
R11 20K CSPI_SCK
R12 20K PS_MIO7

SW1
SW DIP-2

MIO[8] =1 ----MIO bank1 voltage=1.8V
MIO[2] =0 ----cascaded JTAG
MIO[3] =0 ----JTAG/NAND/Quad-SPI/SD
MIO[6] =0 ----PLL used
MIO[7] =0 ----MIO bank0 voltage=3.3V

+3.3V

R75 10K R78 10K
R76 20K CSPI_D3
R77 20K CSPI_D2

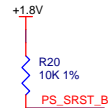
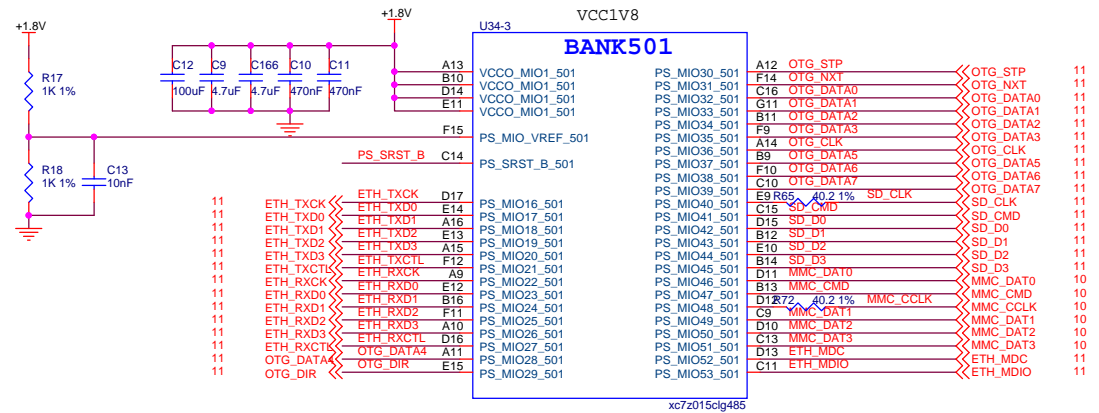
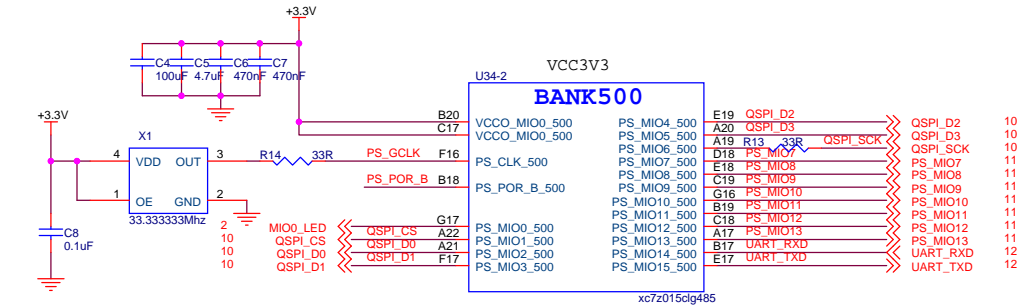
SW1
SW DIP-2

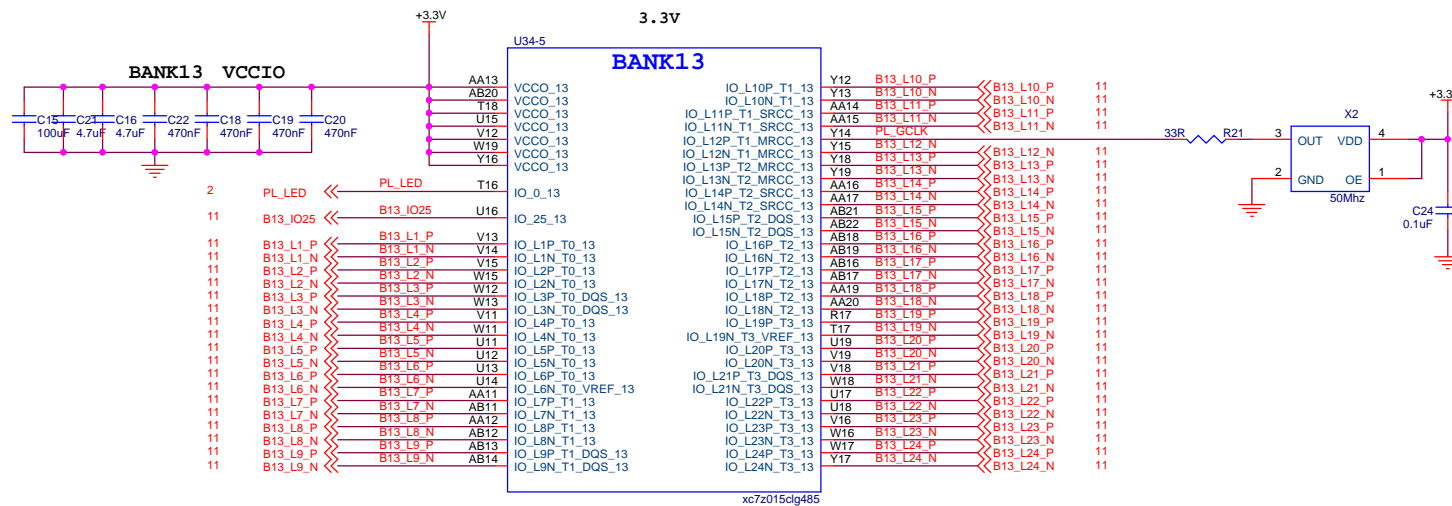
Boot Mode	MIO[5] (QSPI_D3)	MIO[4] (QSPI_D2)
JTAG	0	0
NAND	0	1
QSPI-FLASH	1	0
SD Card	1	1

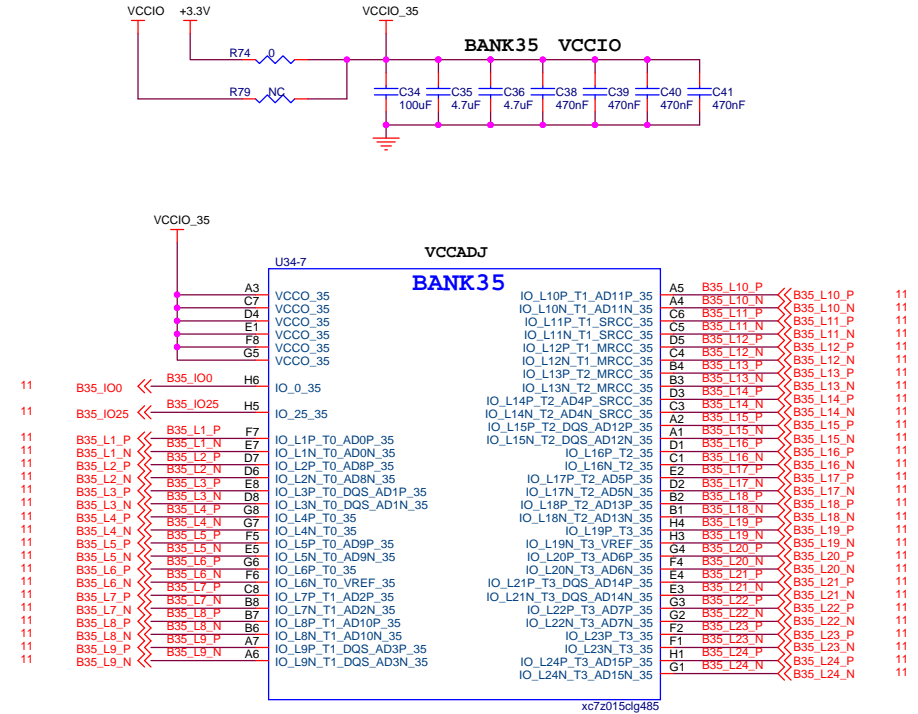
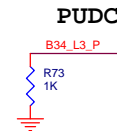
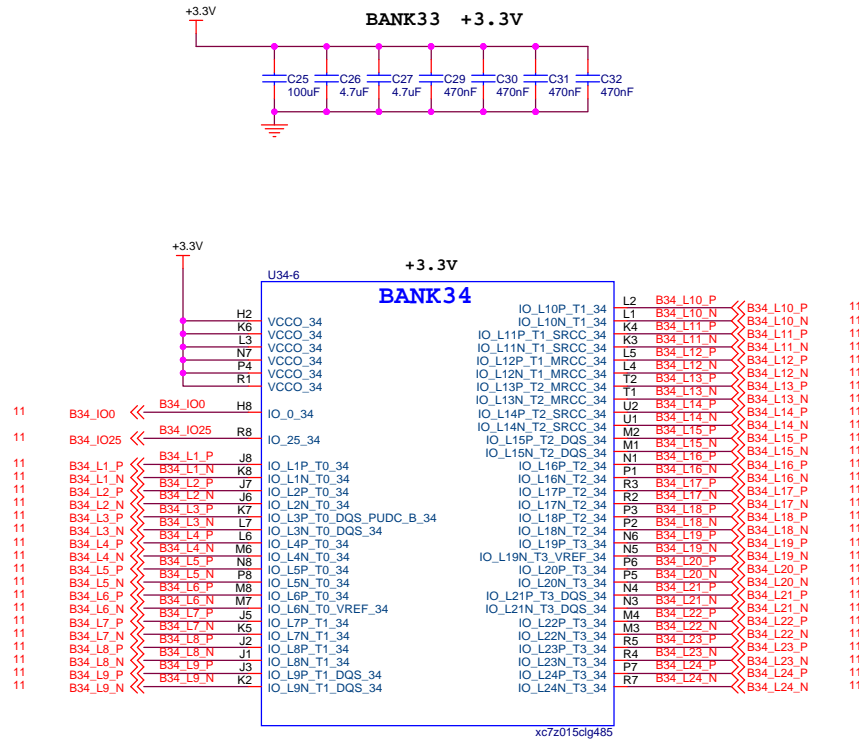
POWER ON RESET

The diagram illustrates a Power On Reset (POR) circuit. It features a TCM8111TETCTR chip (U3) with the following connections:

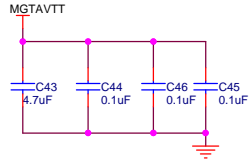
- Pin 3 (#MR):** Connected to a push-button switch labeled KEY1, which is also labeled POR RST. The other side of the switch is connected to ground.
- Pin 4 (VDD):** Connected to a +3.3V supply. A capacitor C14 (0.1uF) is connected between this pin and ground.
- Pin 1 (GND):** Connected to ground.
- Pin 2 (#RESET):** Connected to a +3.3V supply through a resistor R19 (4.7K). This output is labeled PS_POR_B.



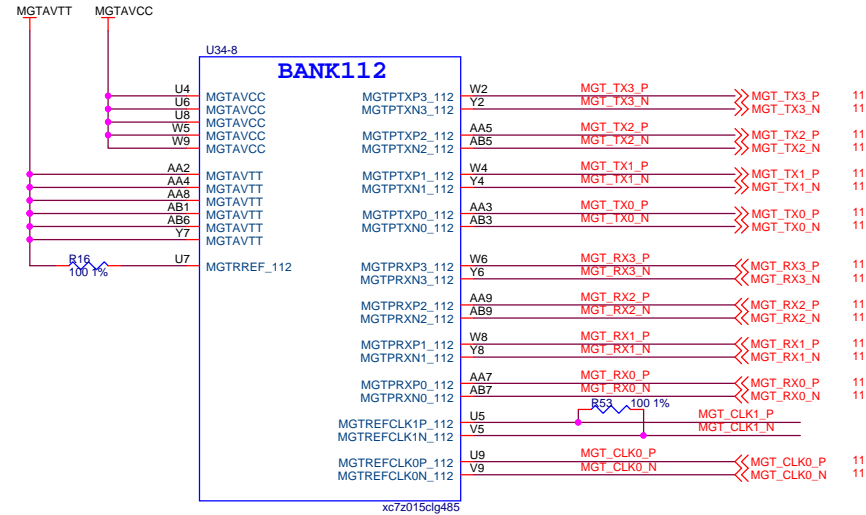
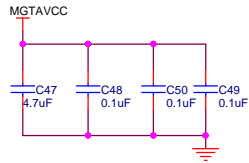




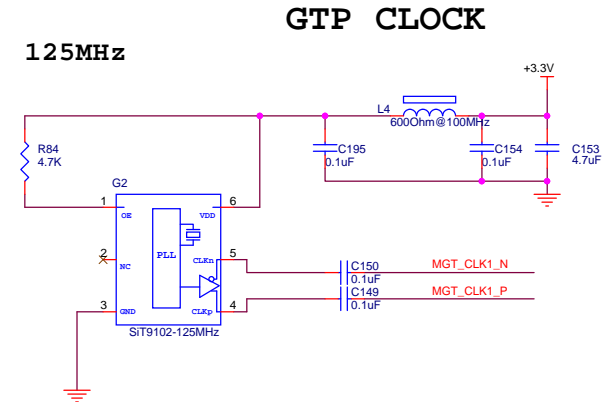
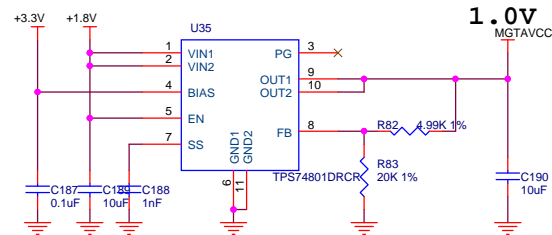
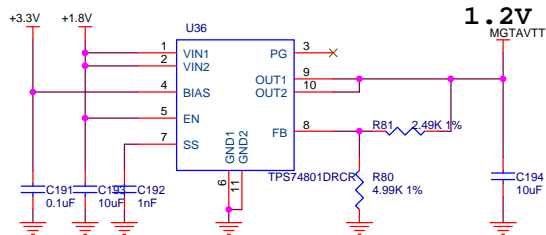
MGTAVTT 4.7uF(1) 0.1uF(2)

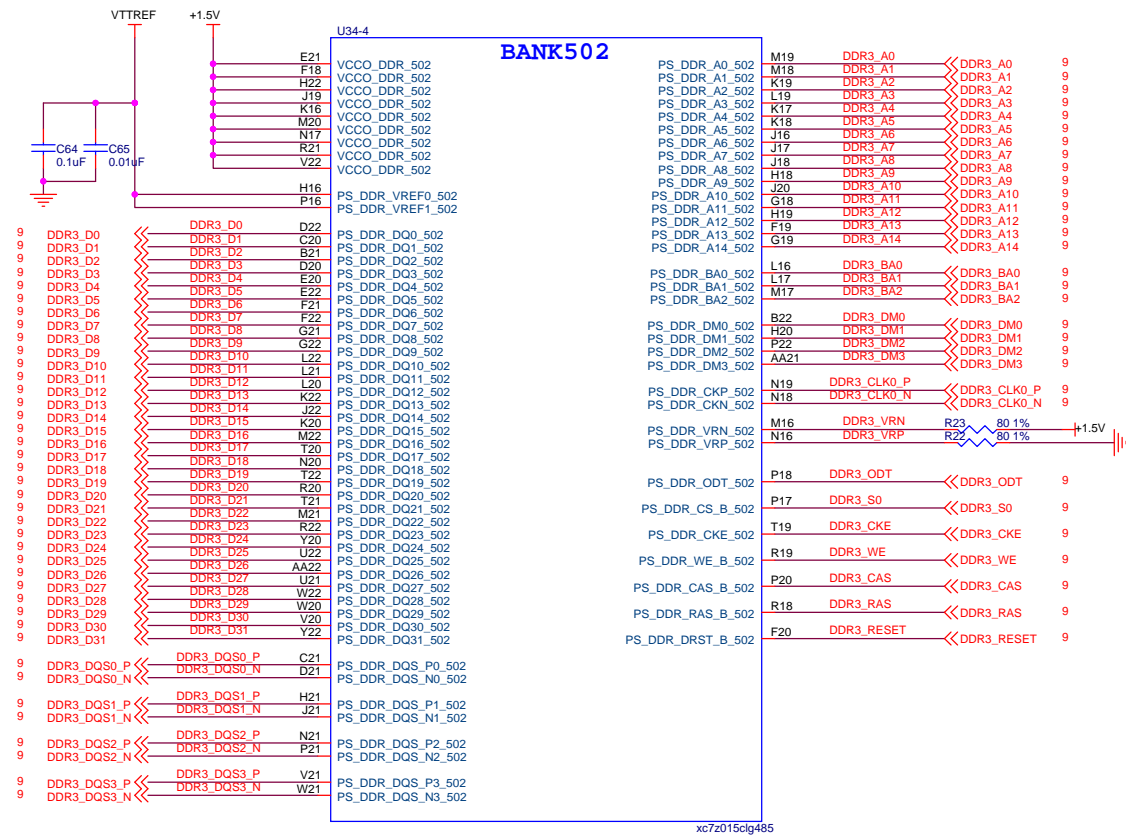



MGTAVCC 4.7uF(1) 0.1uF(2)

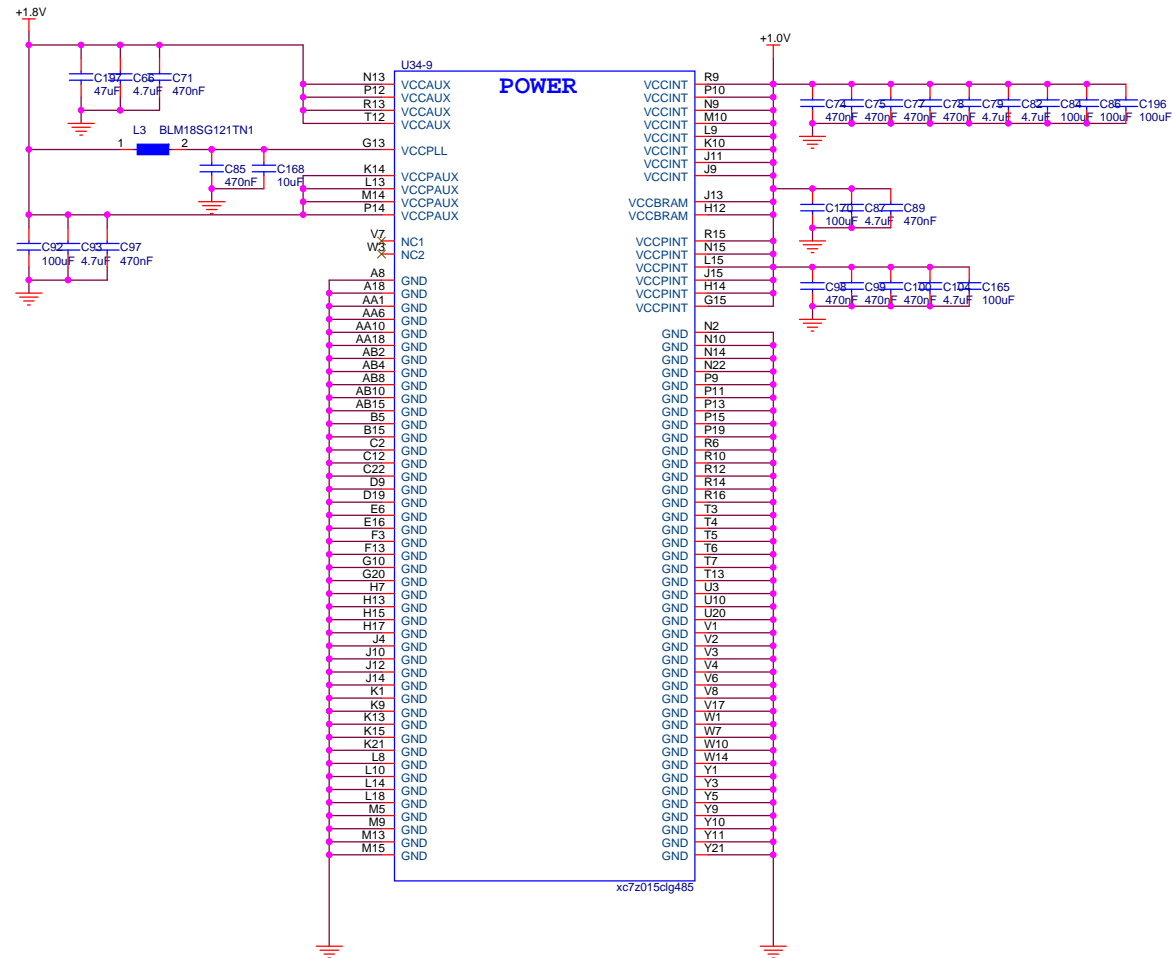


POWER ON: VCCINT(1.0V)->VMGTAVCC(1.0V)->VMGTAVTT(1.2V)





		www.alinx.com	
Title			
AC7015 Bank502			
Size	Document Number		Rev
	AC7015核心板 Schematics		1.0
Date:	Wednesday, June 24, 2020	Sheet	7 of 12



为了PCB走线方便，DDR3数据组内的0qs除外)可以任意交换。

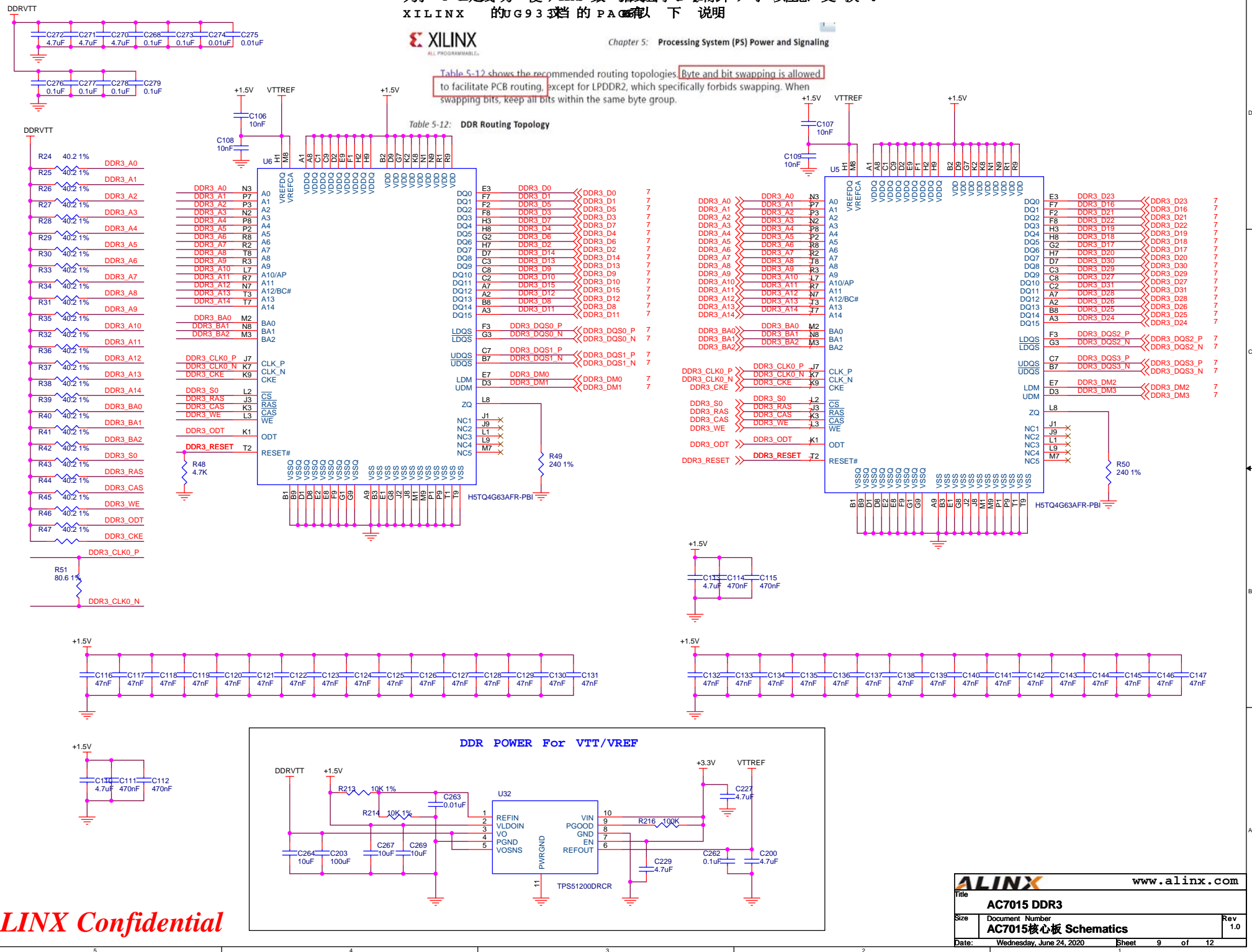
XILINX 的UG93 3档的PA以下说明

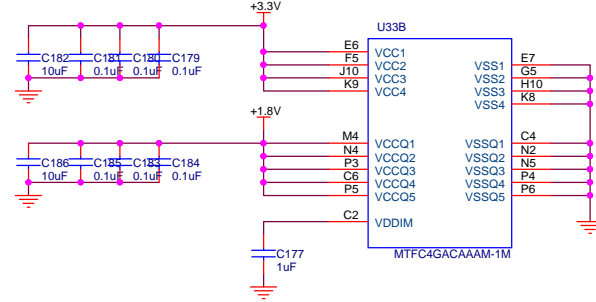
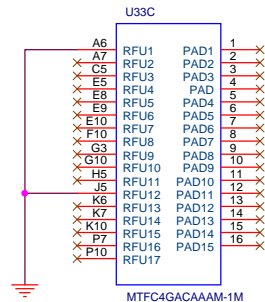
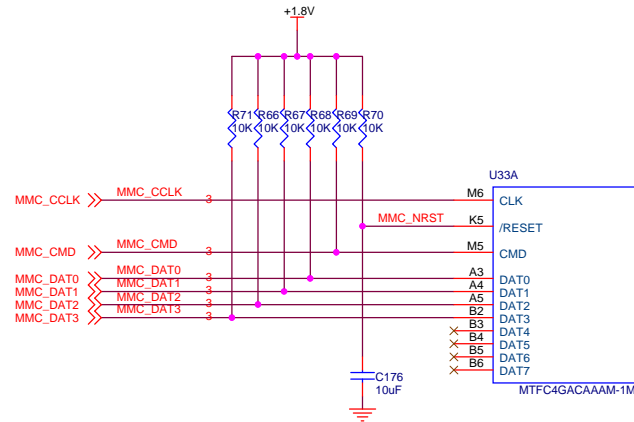
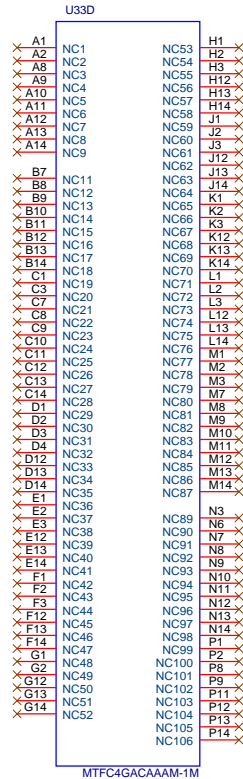
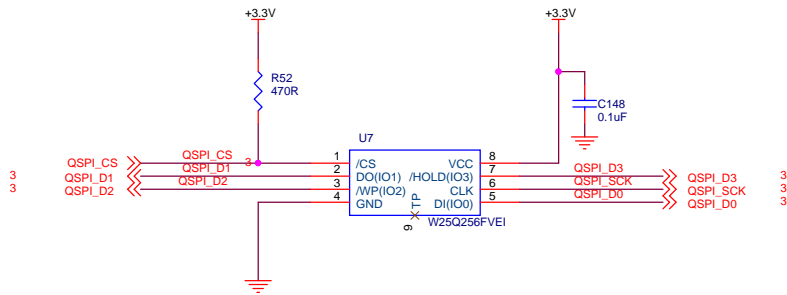


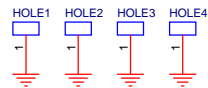
Chapter 5: Processing System (PS) Power and Signaling

Table 5-12 shows the recommended routing topologies. Byte and bit swapping is allowed to facilitate PCB routing, except for LPDDR2, which specifically forbids swapping. When swapping bits, keep all bits within the same byte group.

Table 5-12: DDR Routing Topology

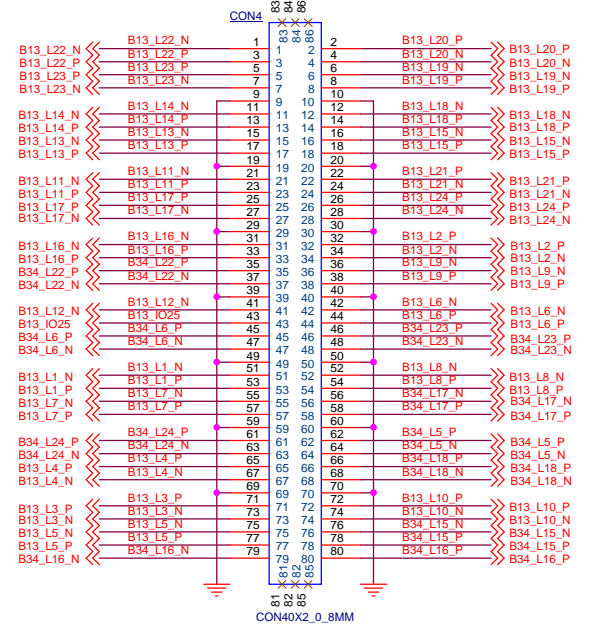
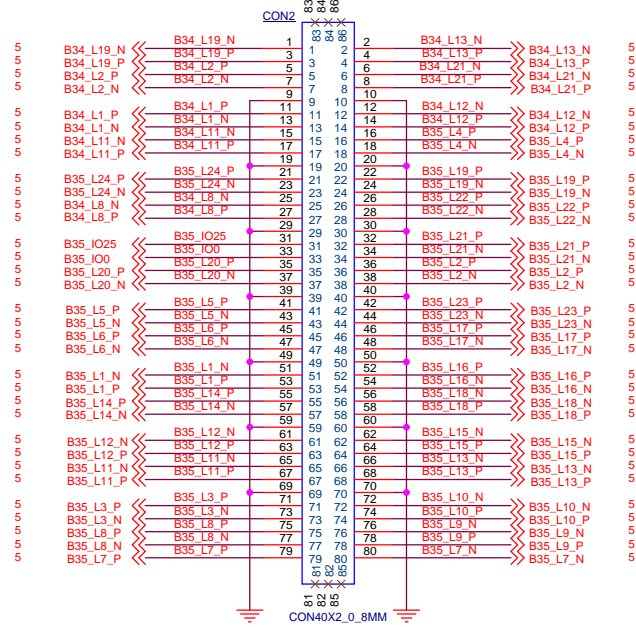
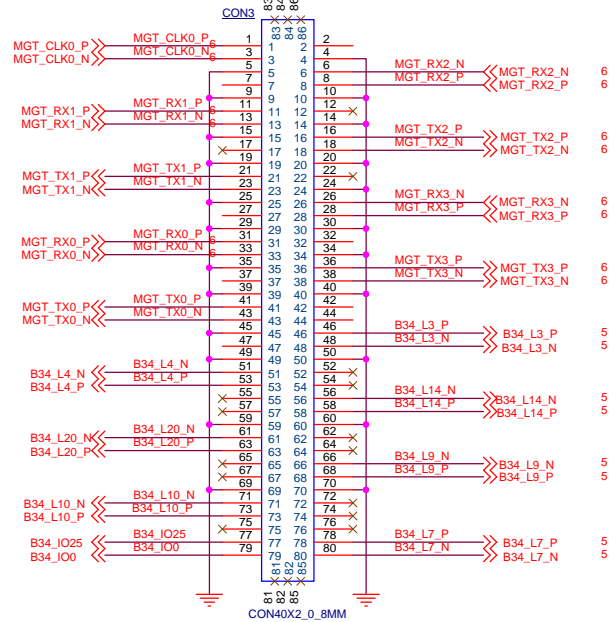
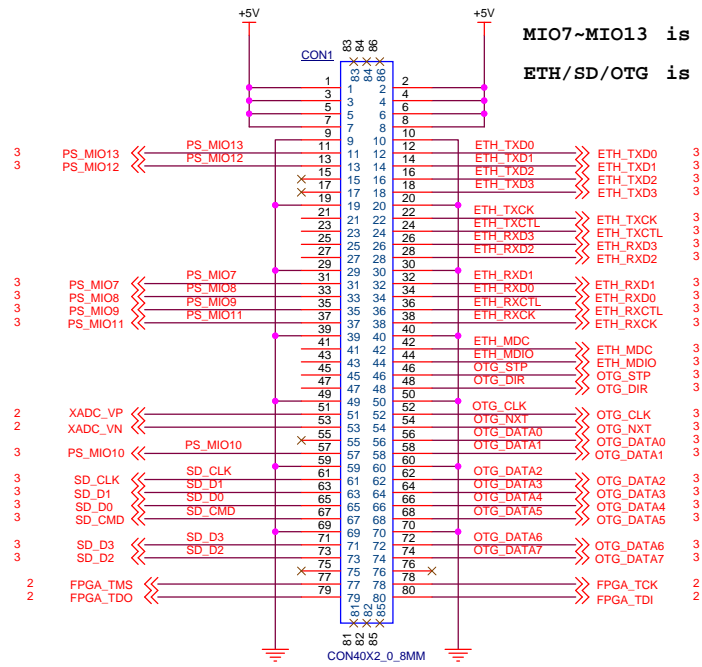




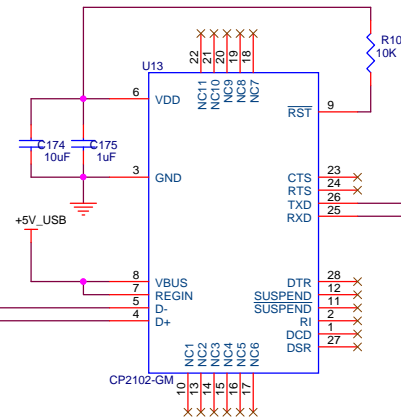
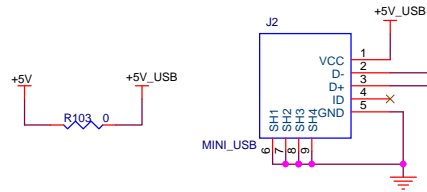


BANK35 IO Voltage is adjustable

MIO7~MIO13 is 3.3V Voltage Standard
ETH/SD/OTG is 1.8V Voltage Standard



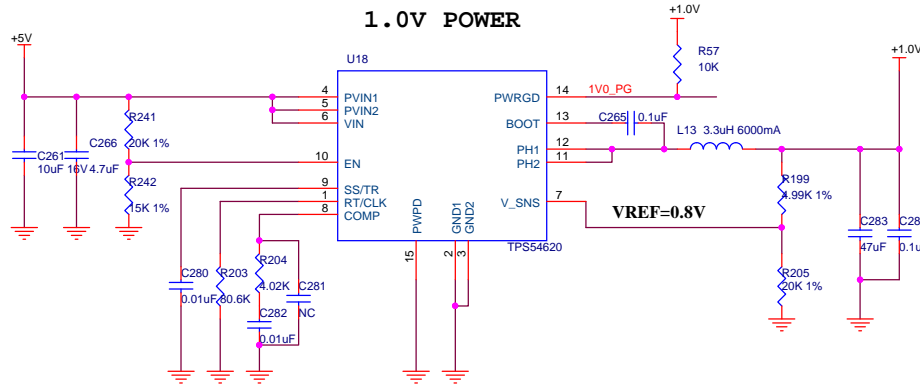
USB Uart



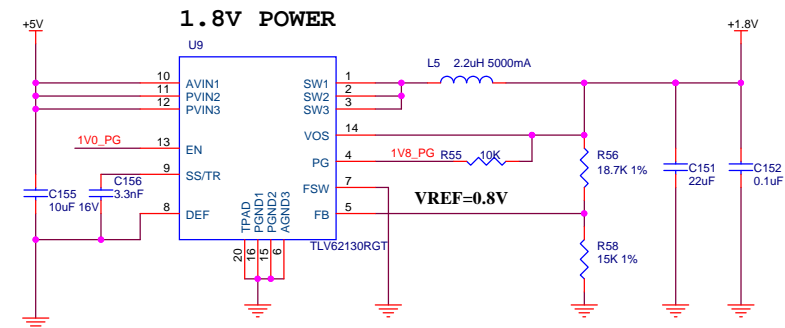
Power On Sequence:

1.0V -> 1.8V -> 1.5 V/3.3V -> VCCIO

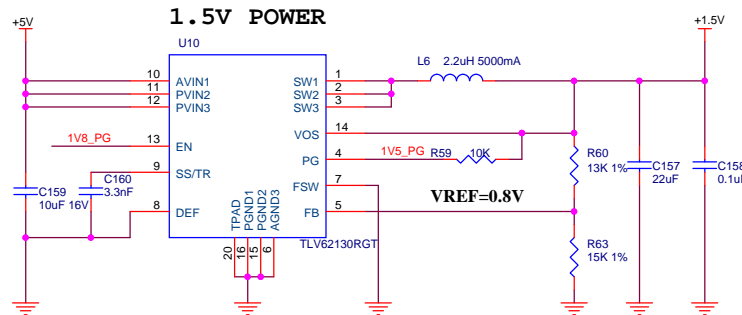
1.0V POWER



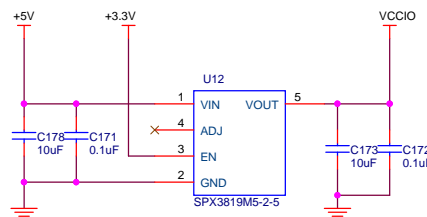
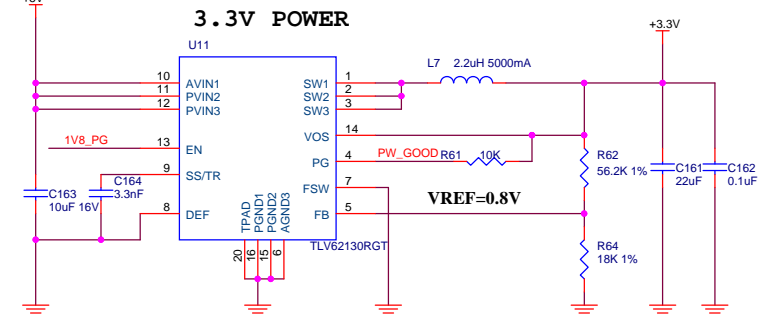
1.8V POWER



1.5V POWER



3.3V POWER



ALINX Confidential