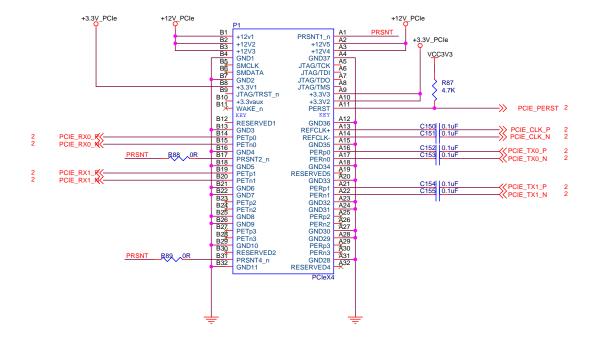


PCIE X2 SLOT

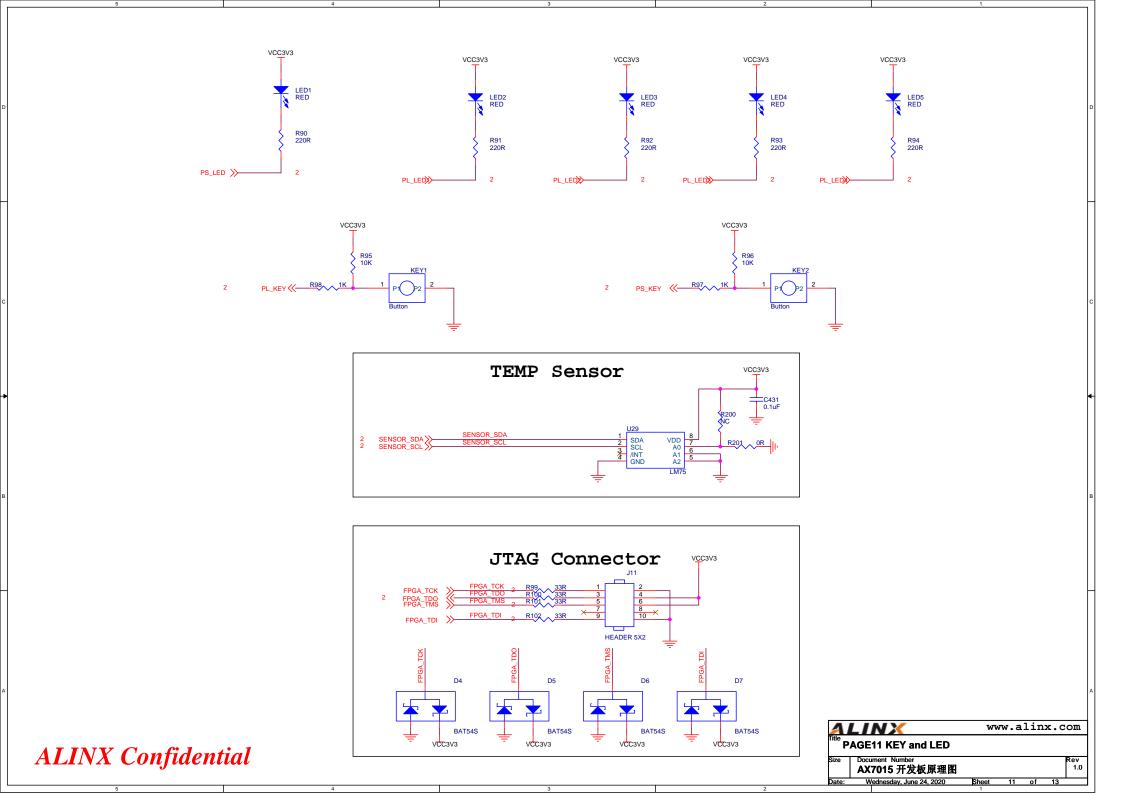


Www.alinx.com

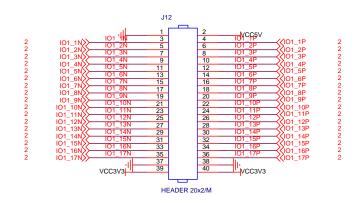
Title PAGE10 PCIE

Size Document Number AX7015 开发板原理图 1.0

Date: Wednesday, June 24, 2020 Sheet 10 of 13



FPGA 40 PIN External IO







ALINX Confidential

