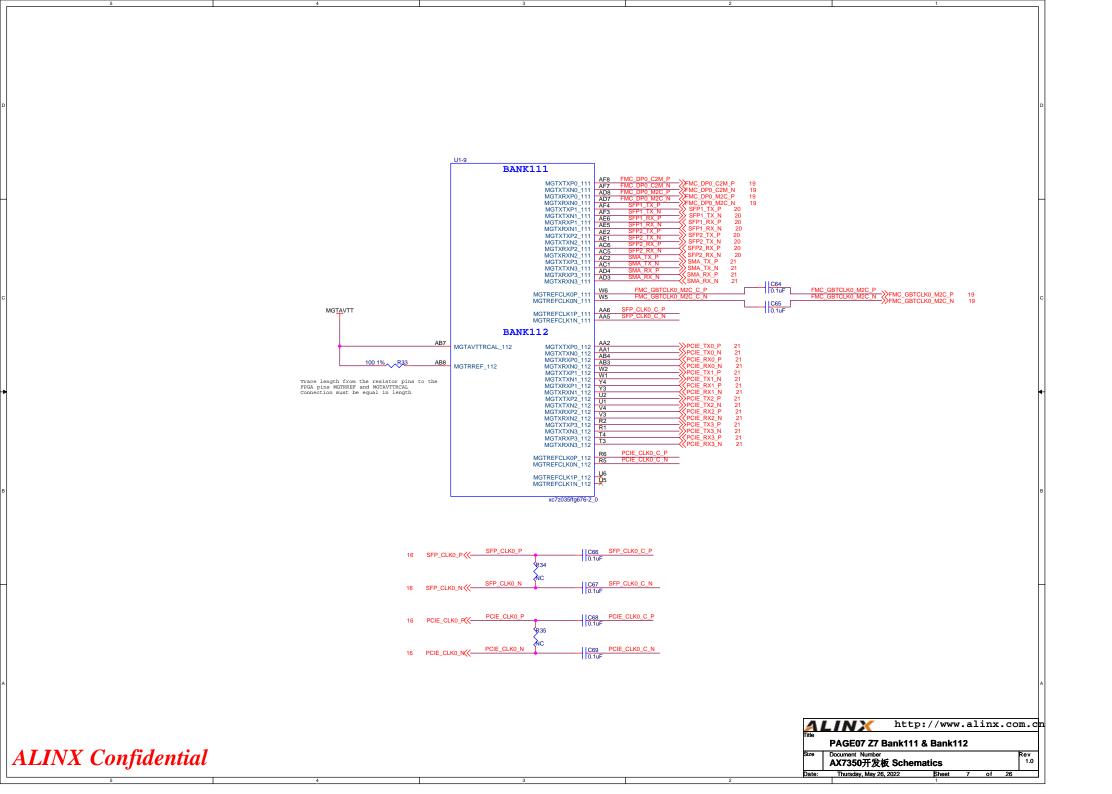
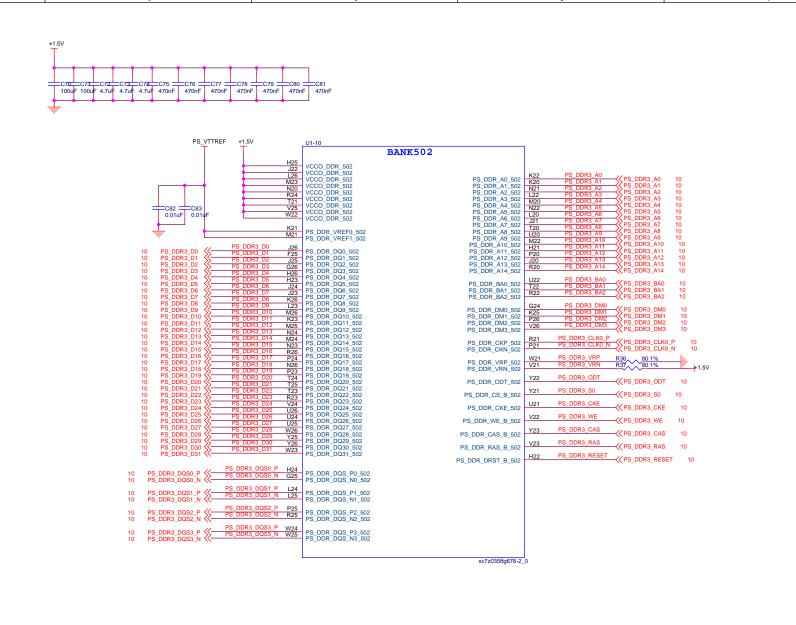
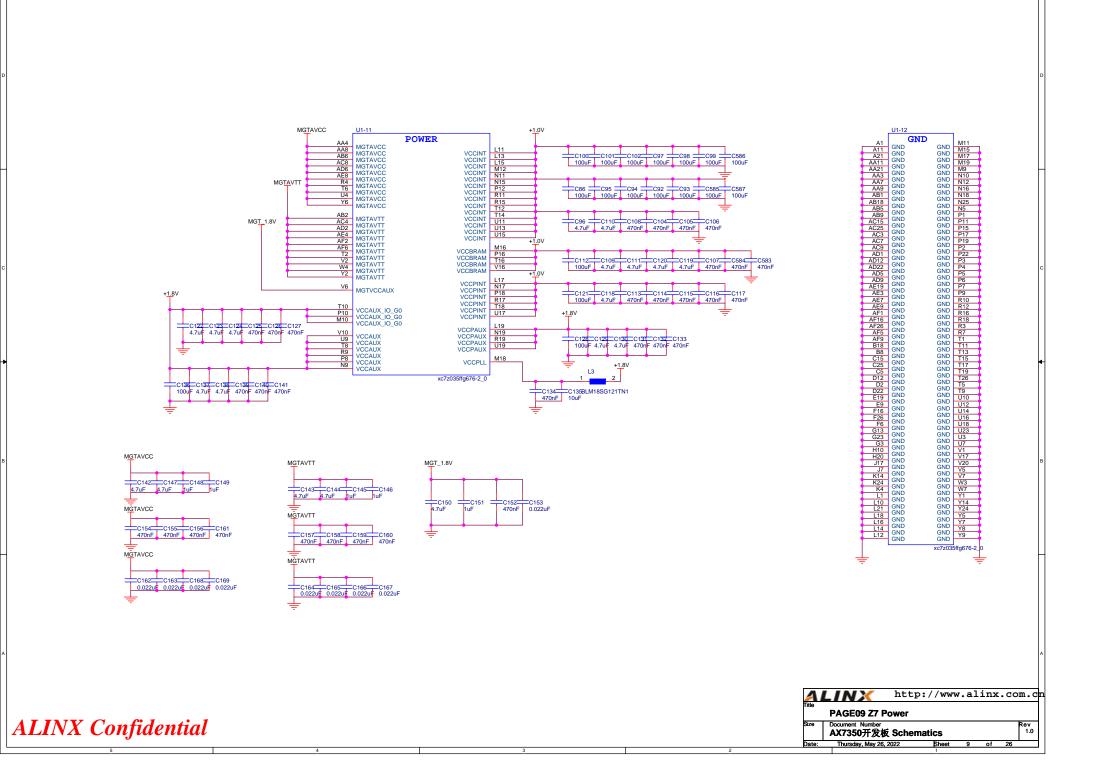


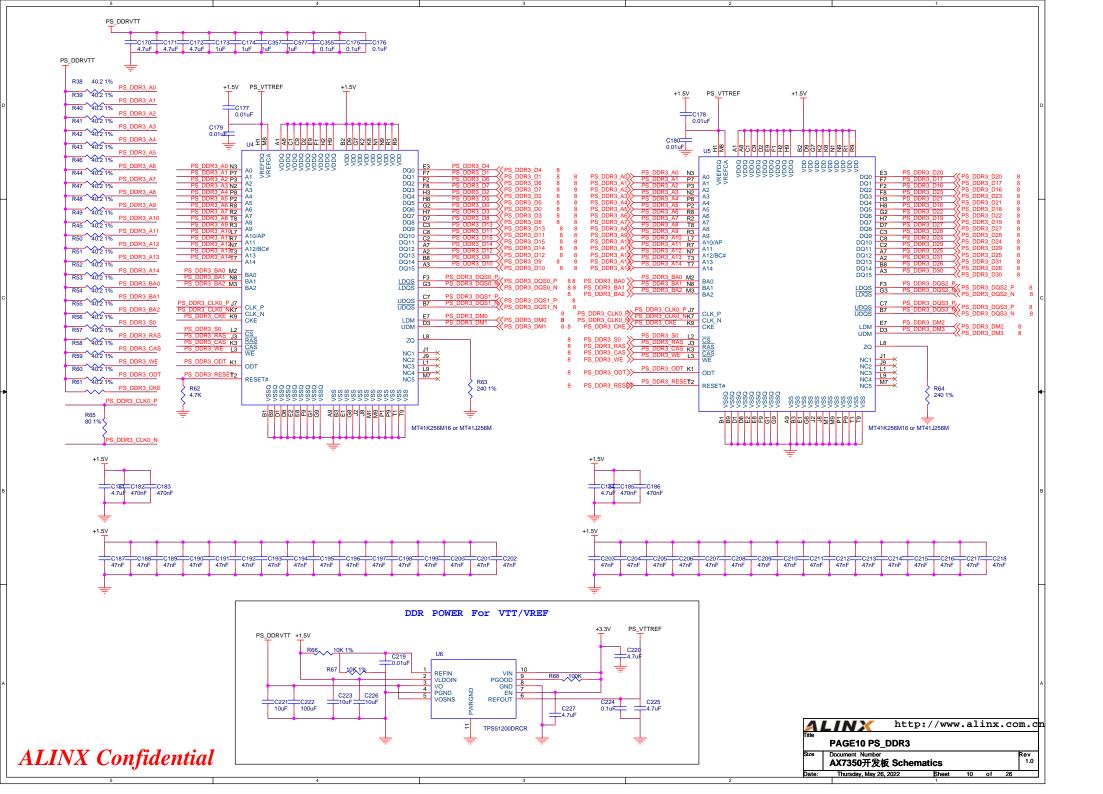
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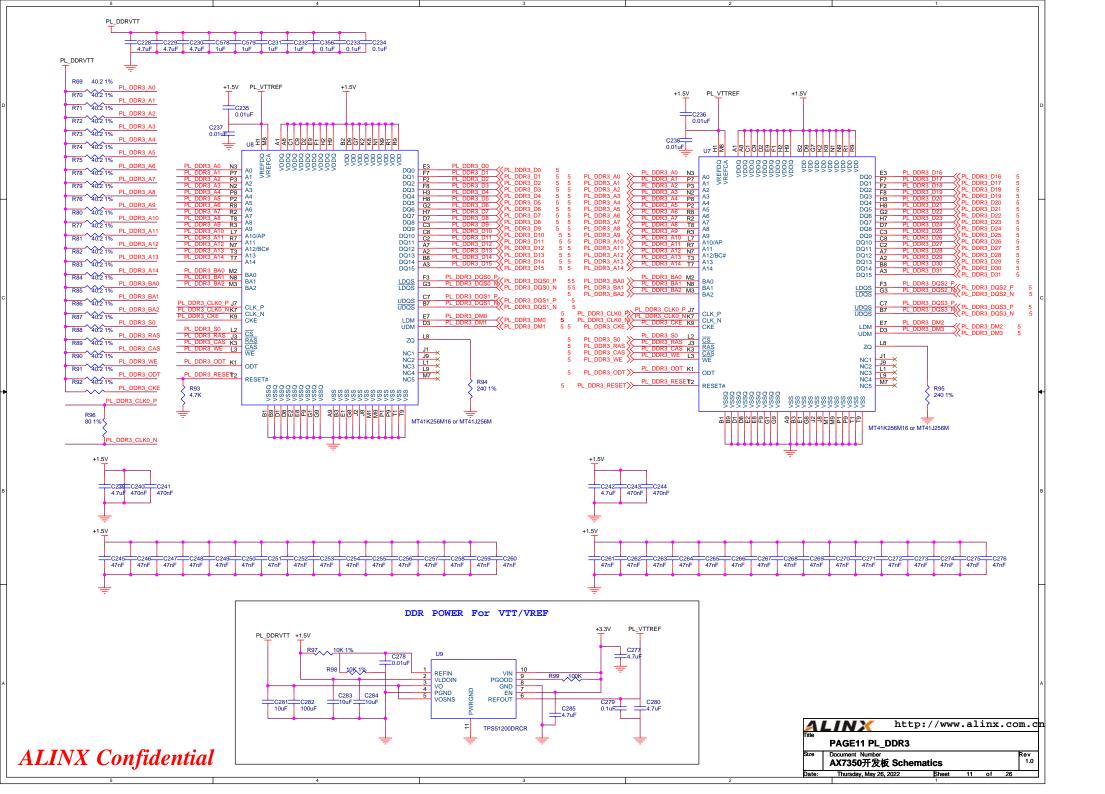
| PAGE06 Z7 Bank35 | Document Number | AX7350开发板 Schematics | AX7350开发板 Schematics | Date: Thursday,May 26, 2022 | Sheet 6 of 26

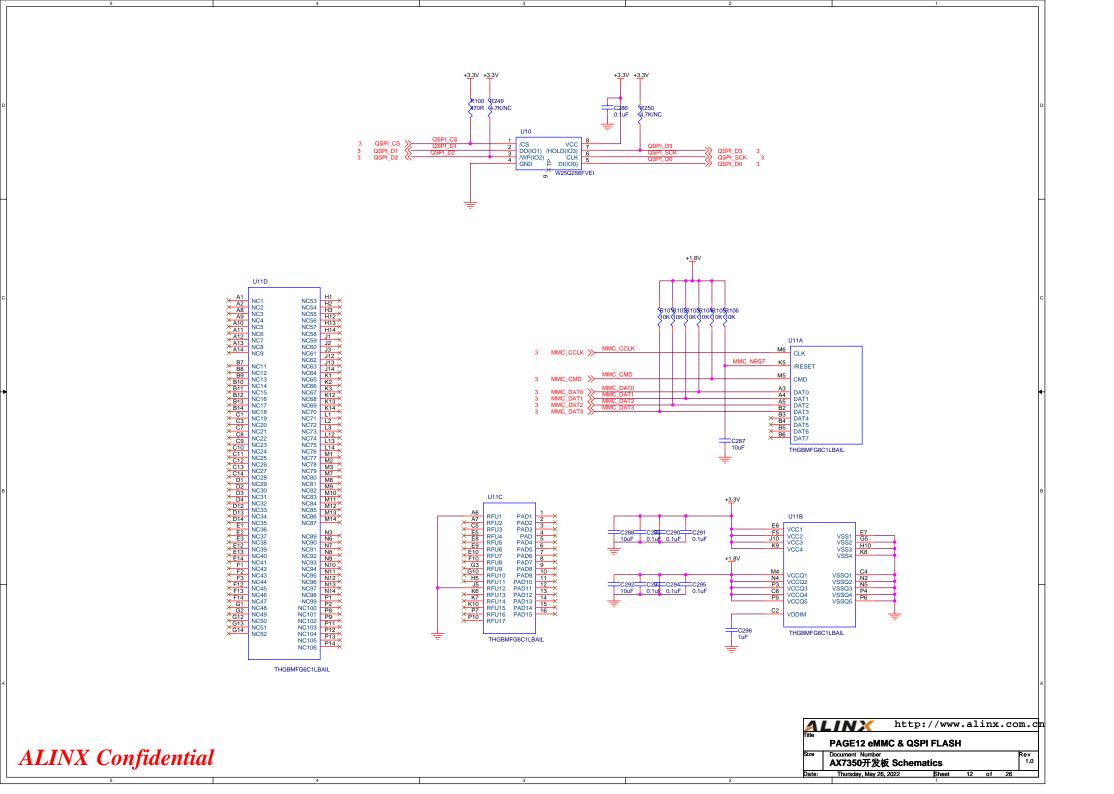


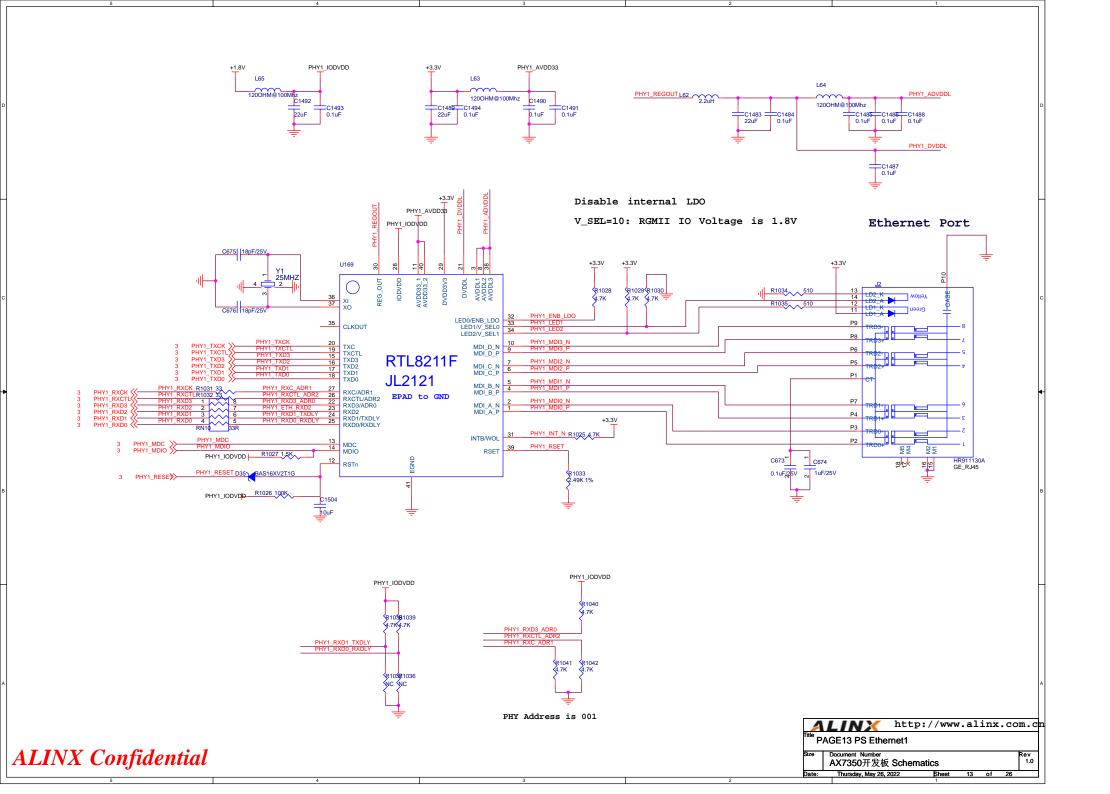


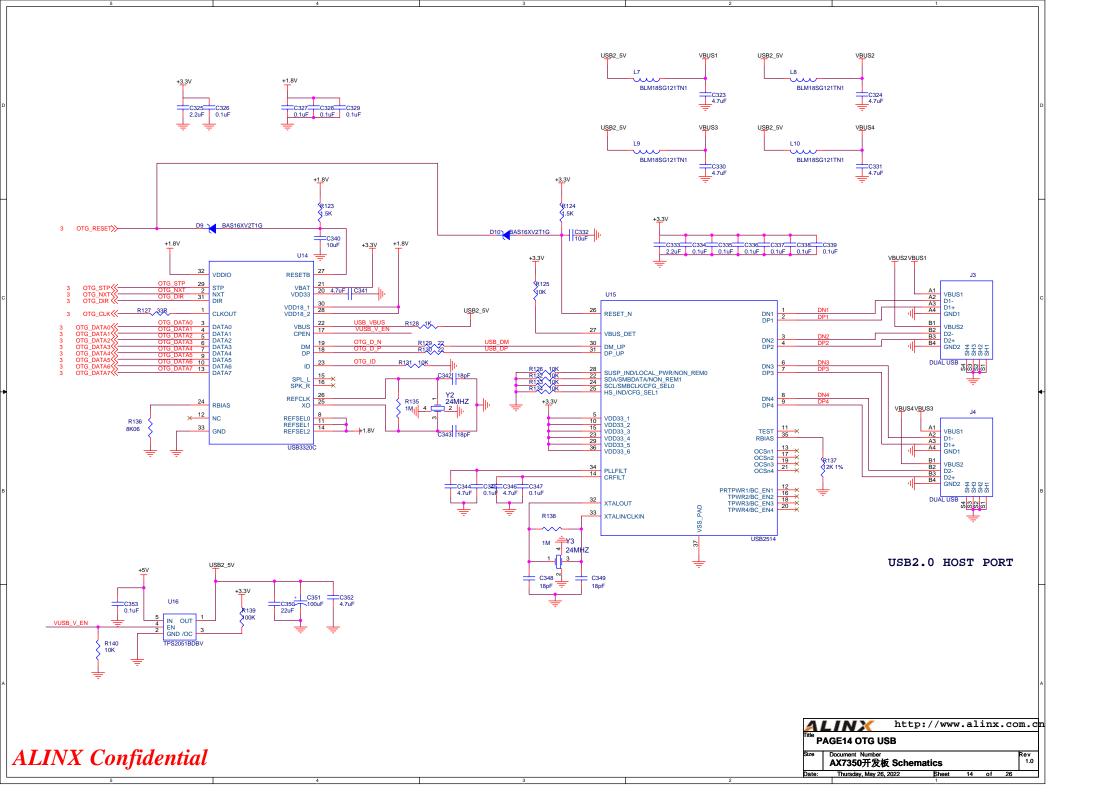


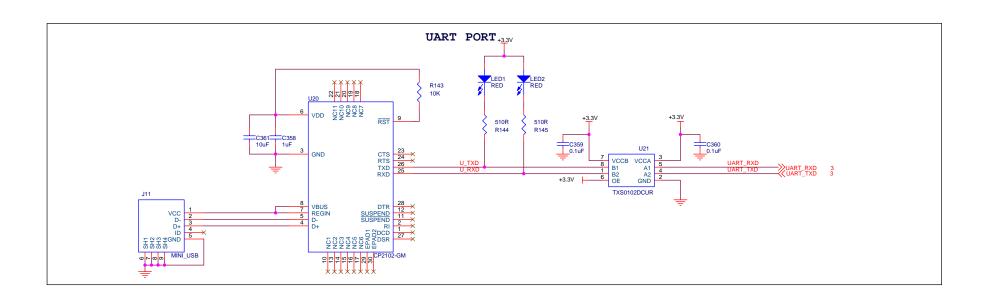


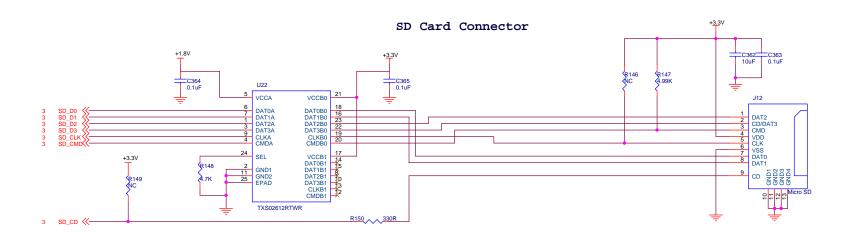










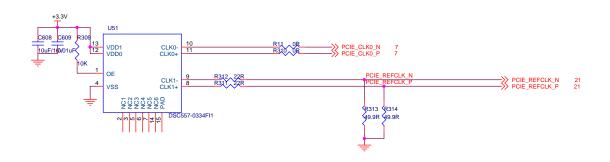


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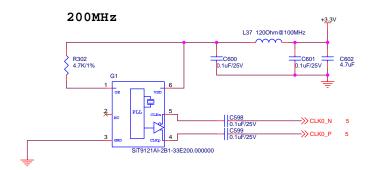
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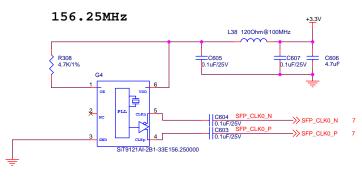
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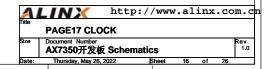


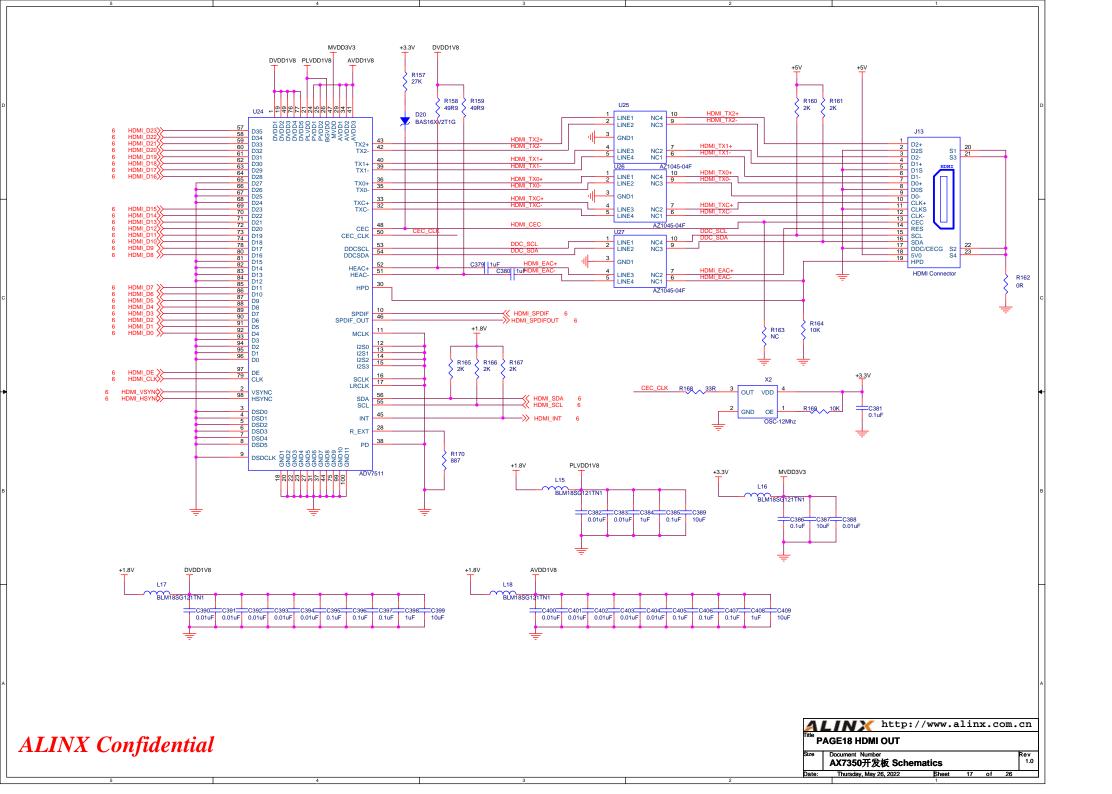
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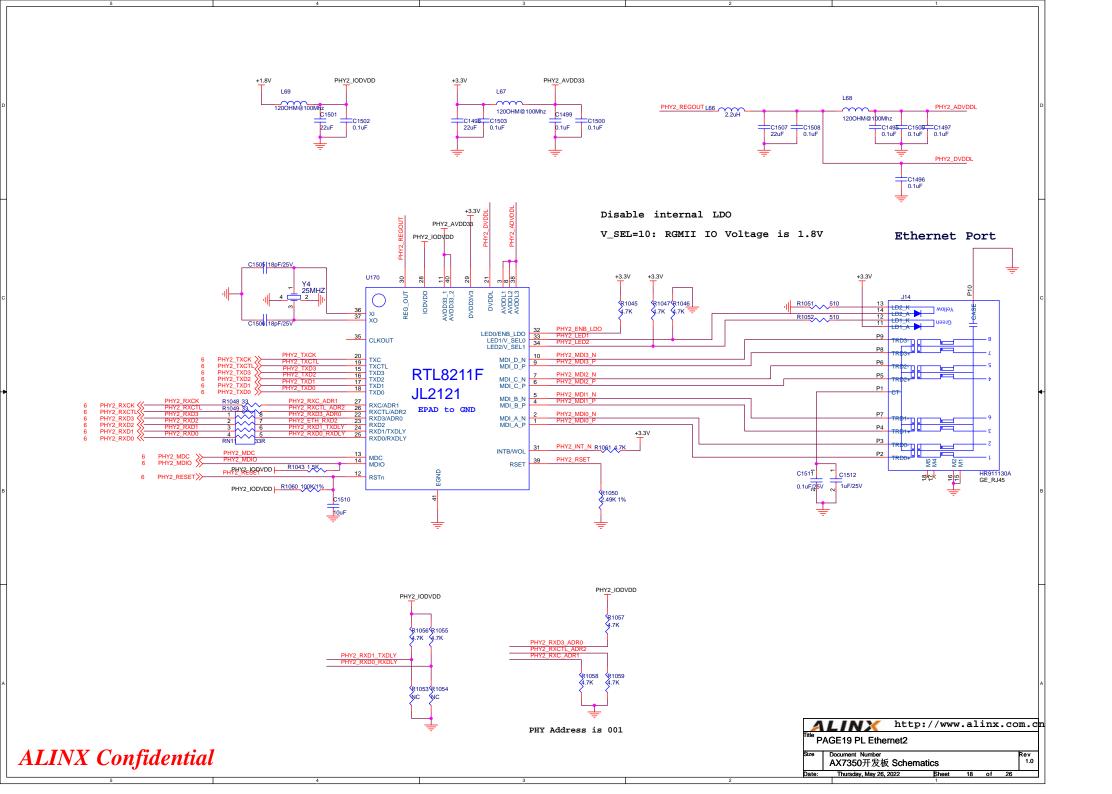


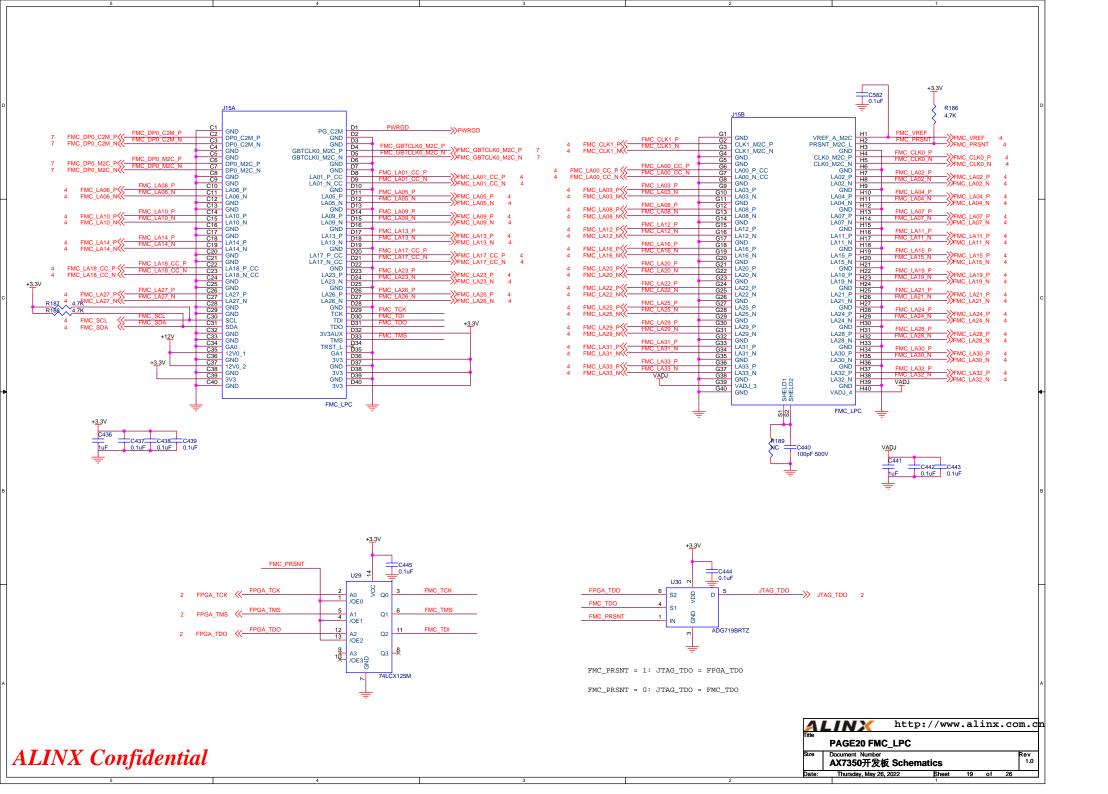
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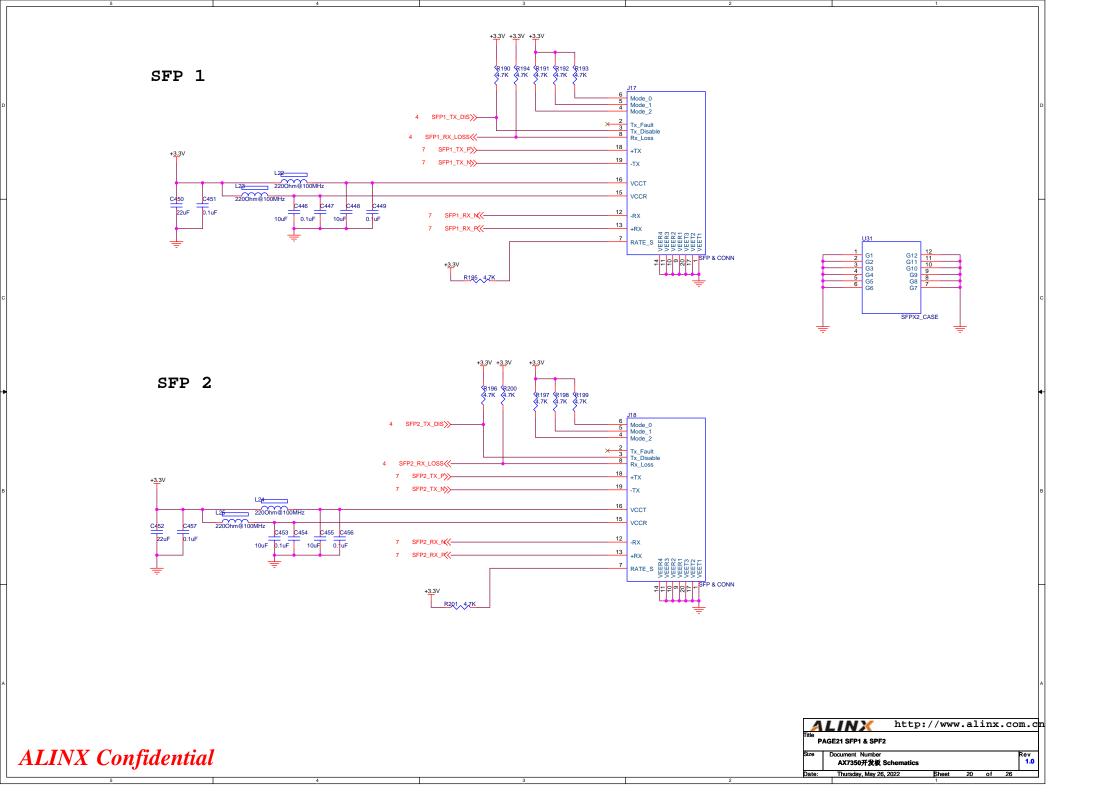


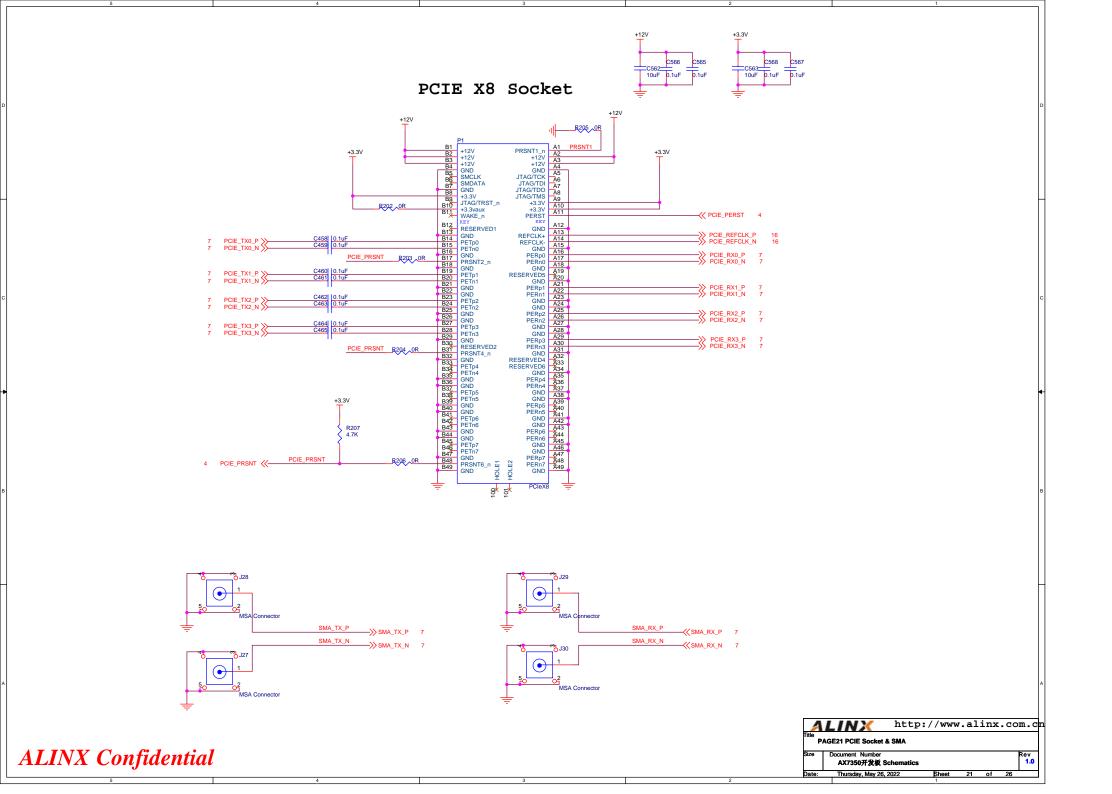


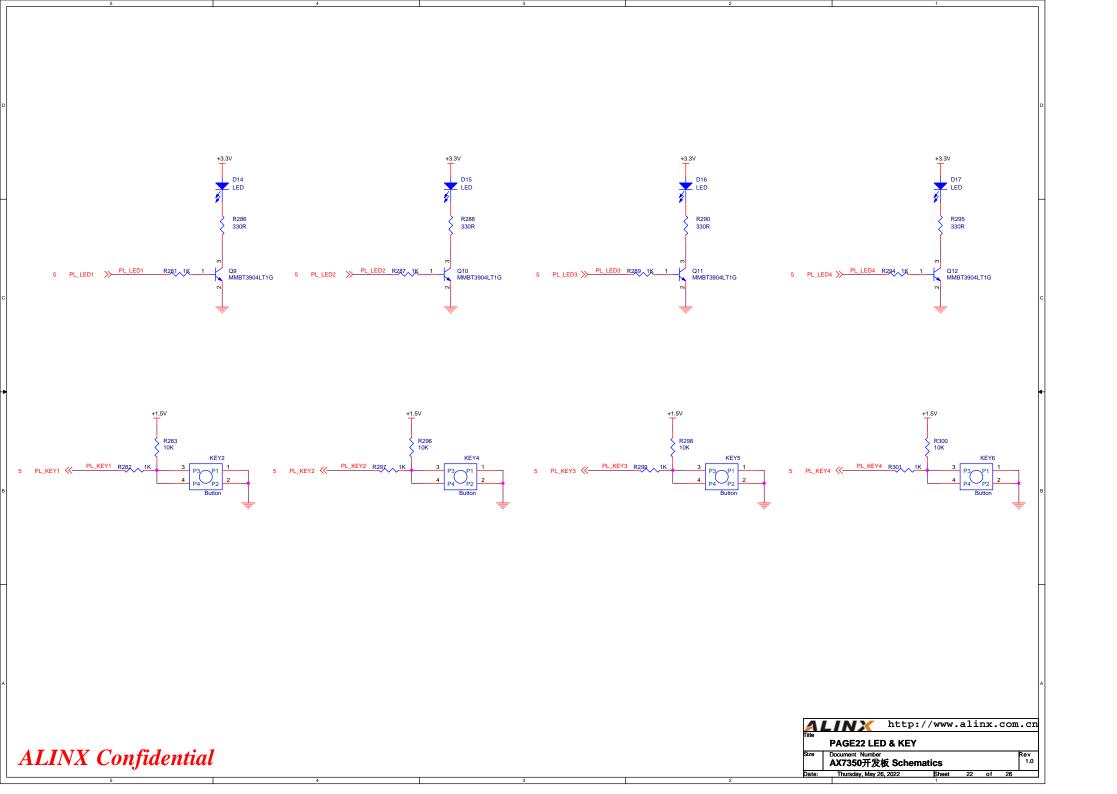


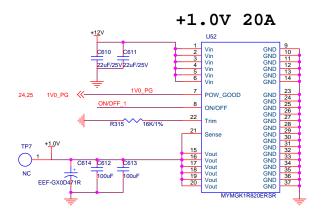




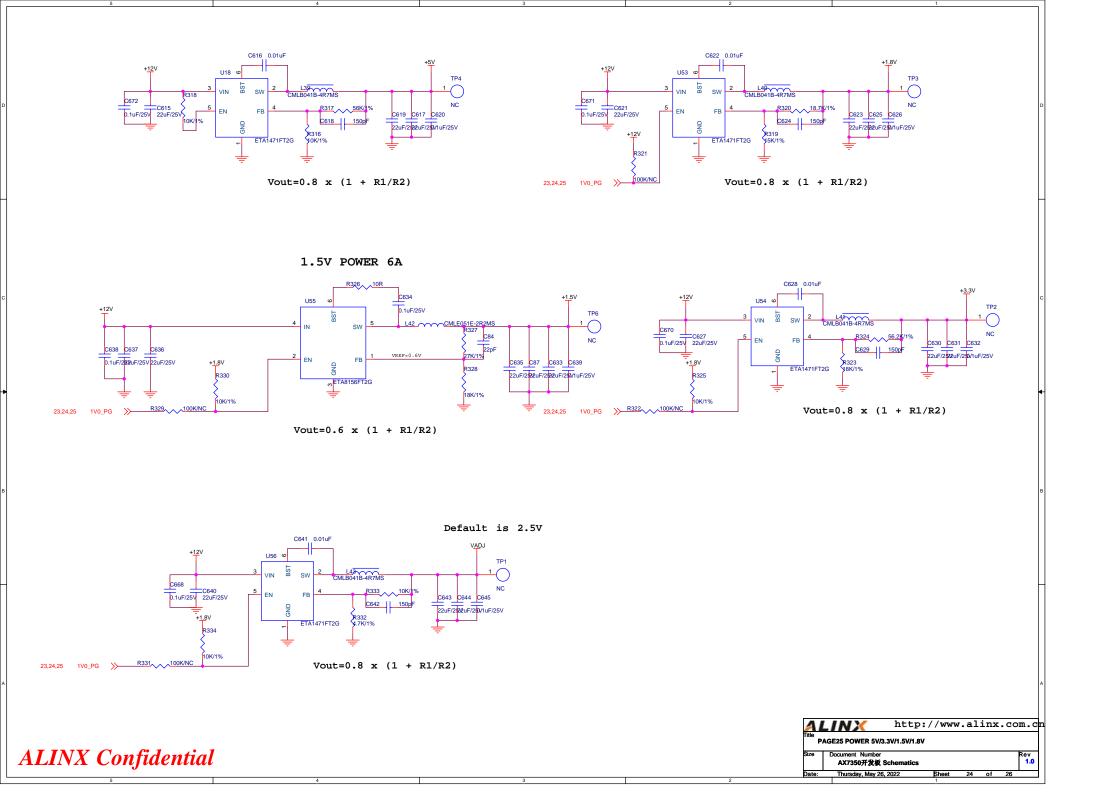




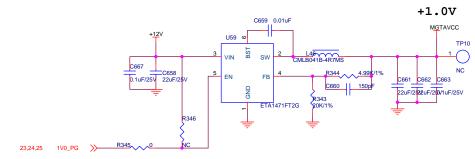




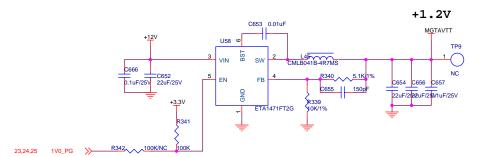
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 $Vout=0.8 \times (1 + R1/R2)$



 $Vout=0.8 \times (1 + R1/R2)$

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