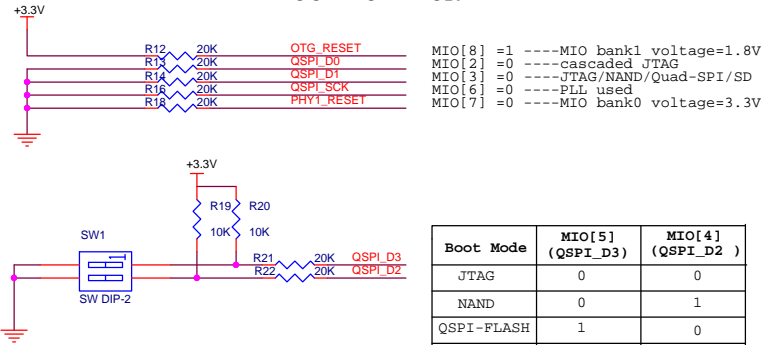
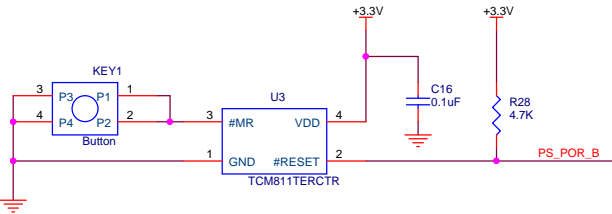


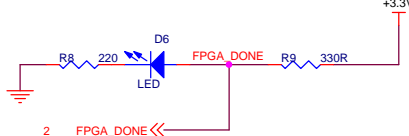
BOOT OPTION



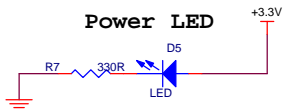
POWER ON RESET



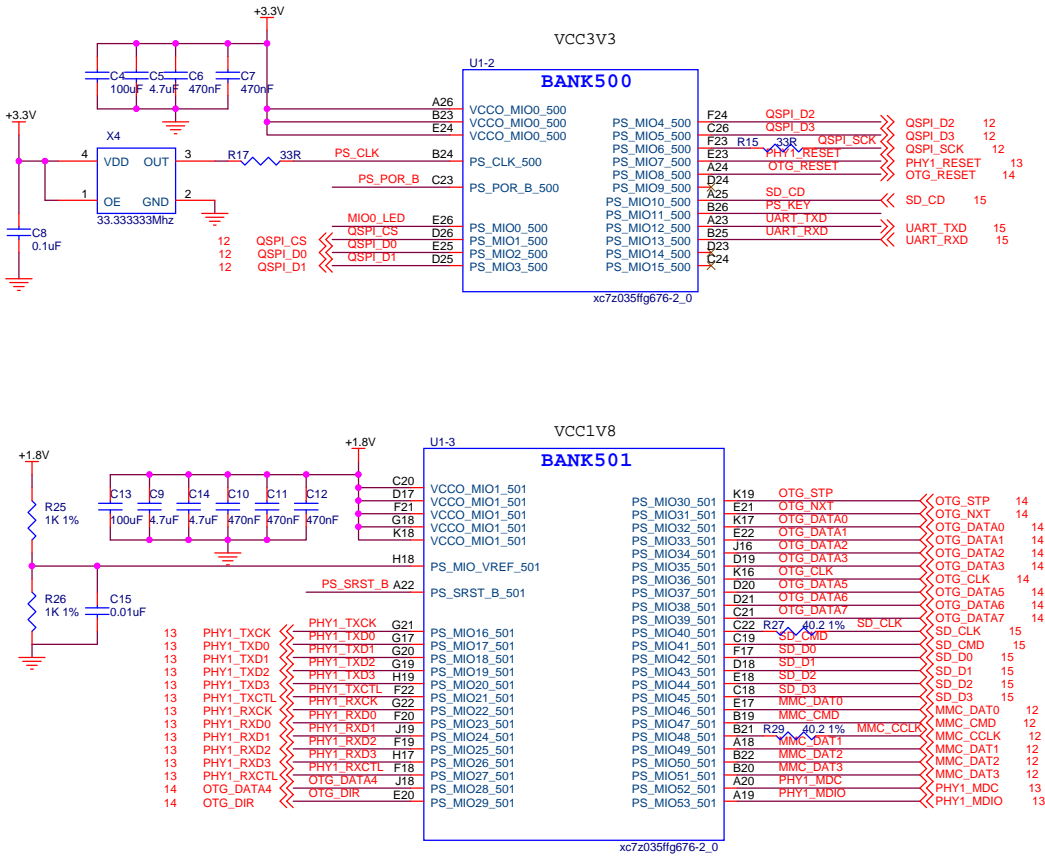
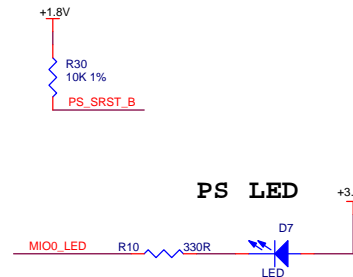
FPGA DONE LED

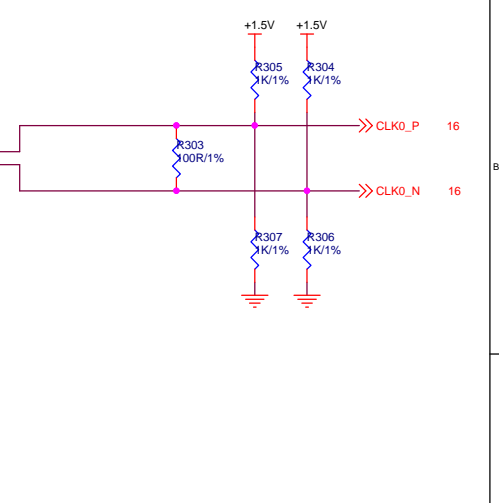
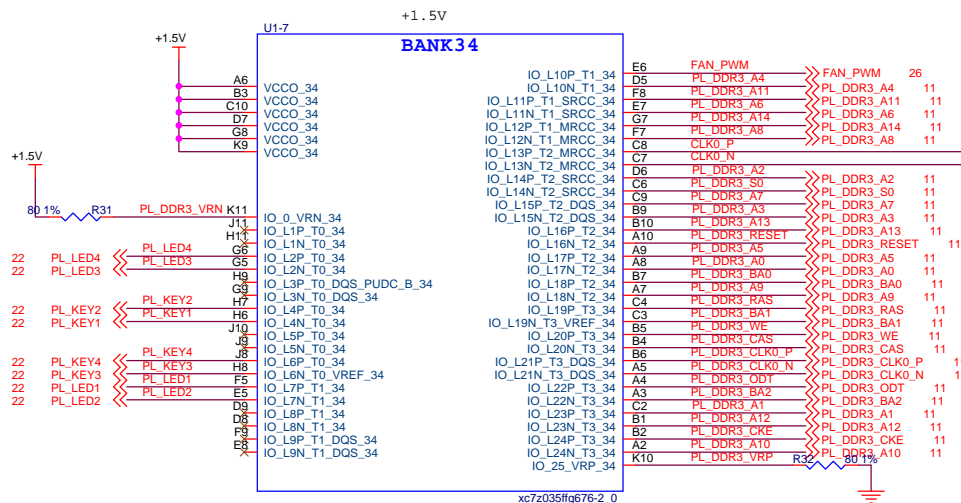
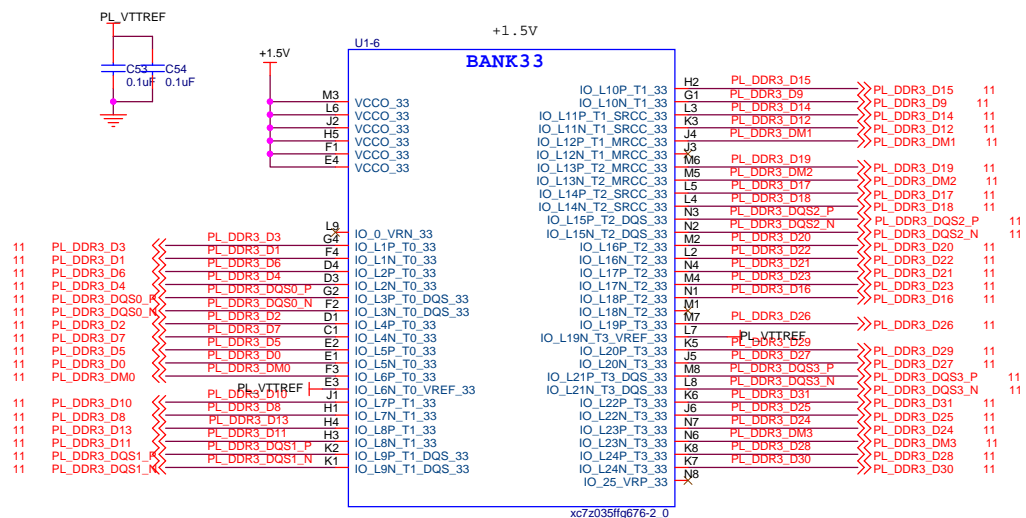
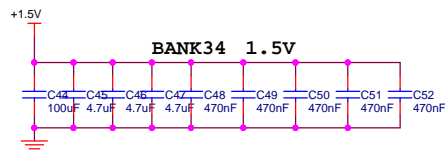
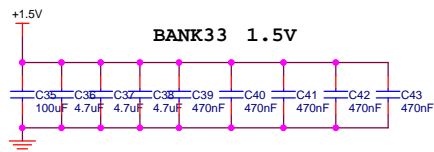


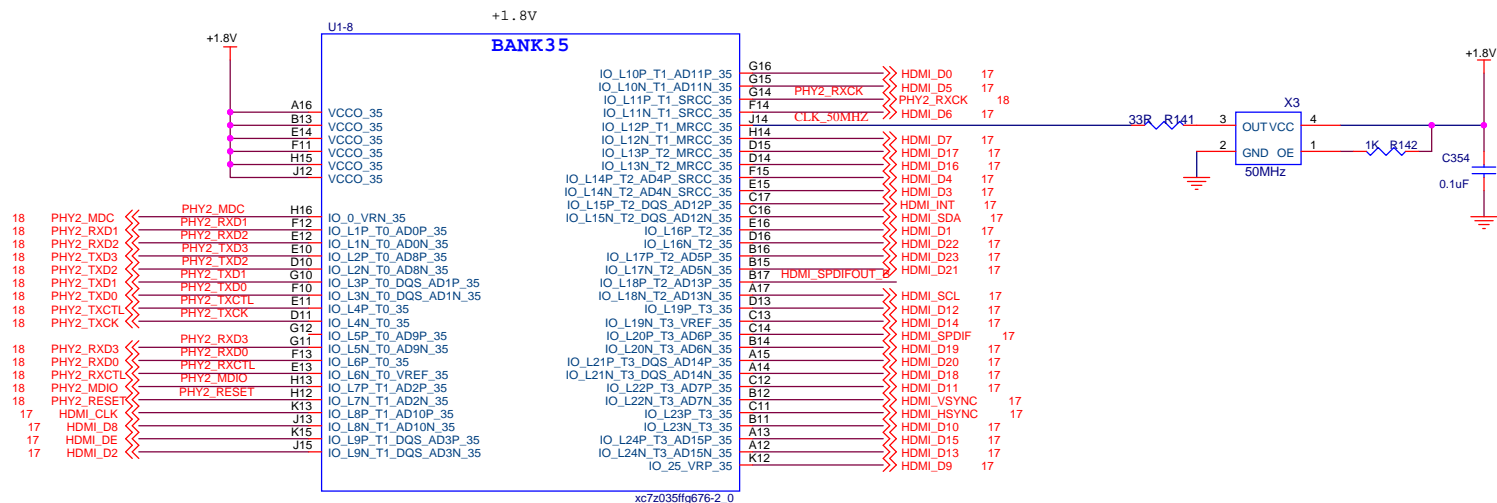
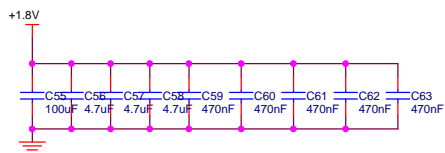
Power LED

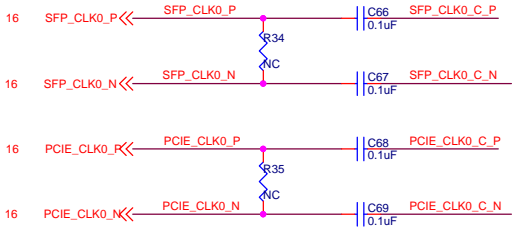
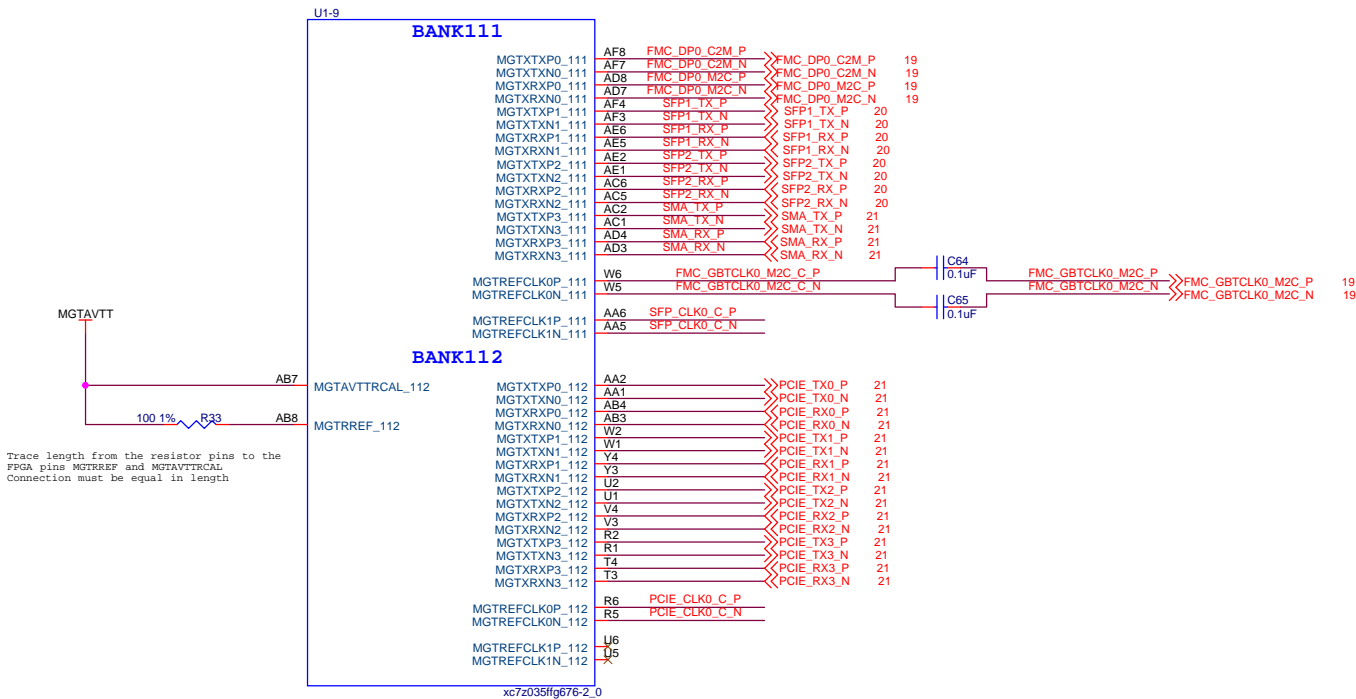


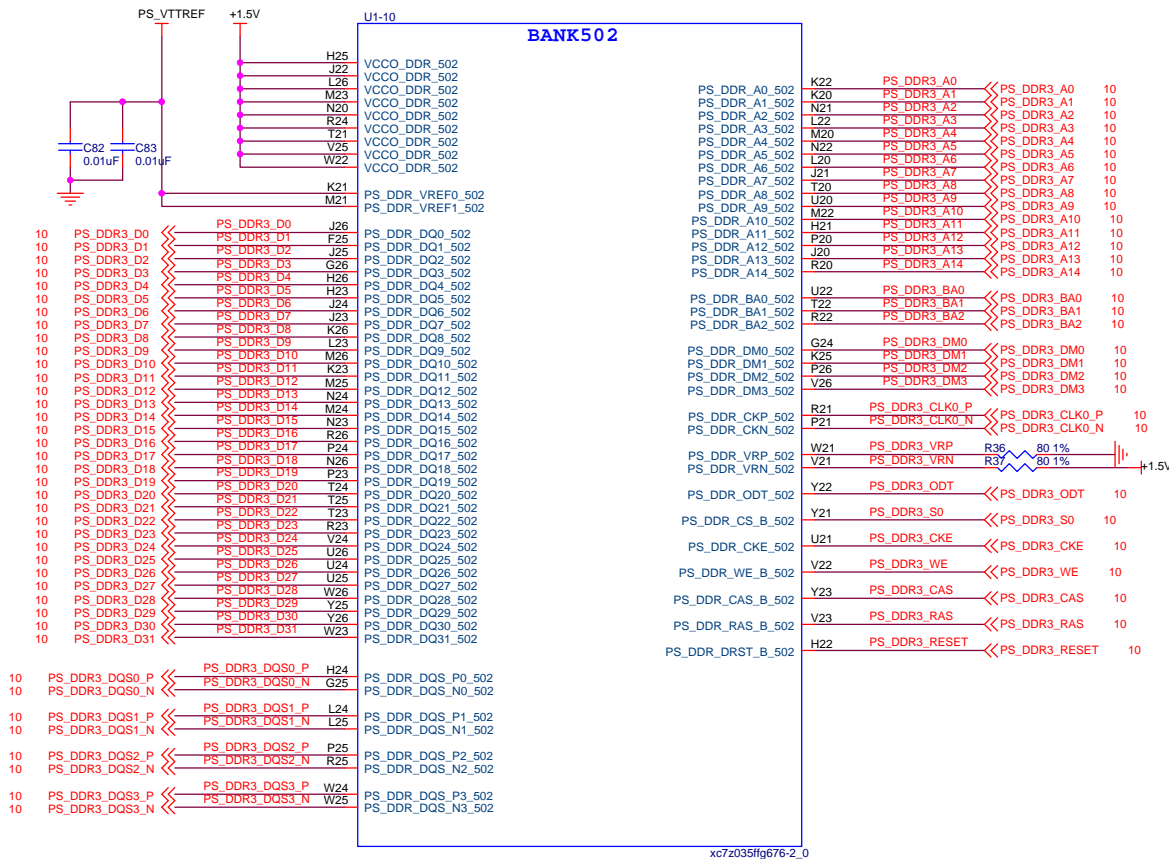
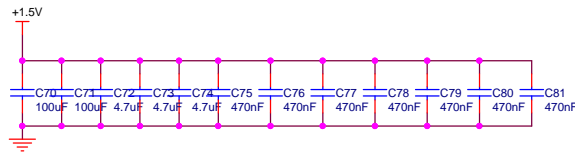
PS LED

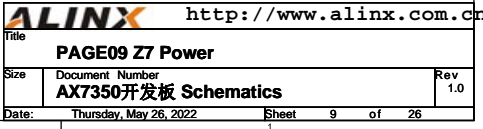


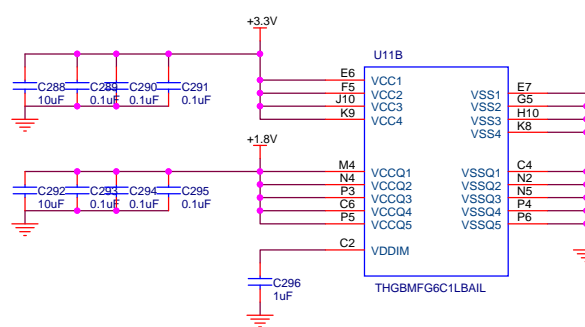
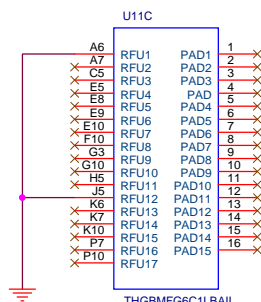
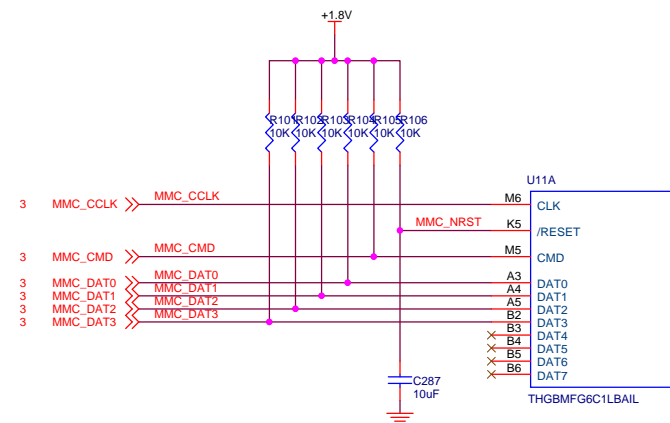
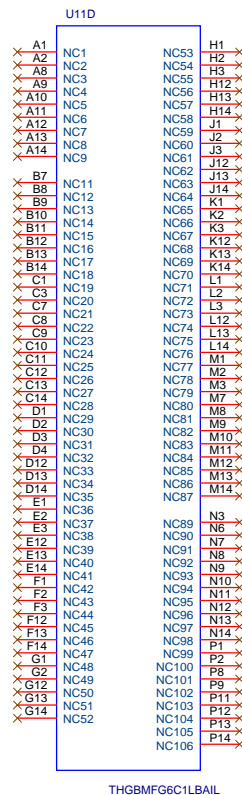
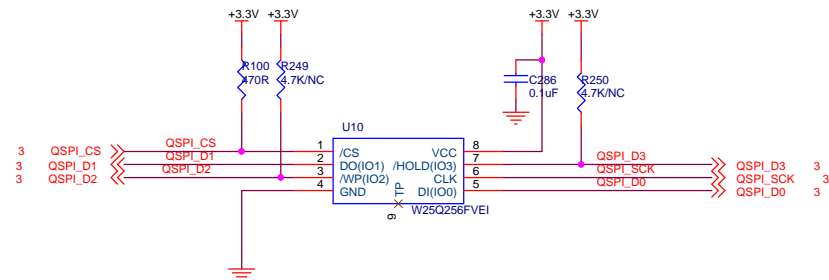


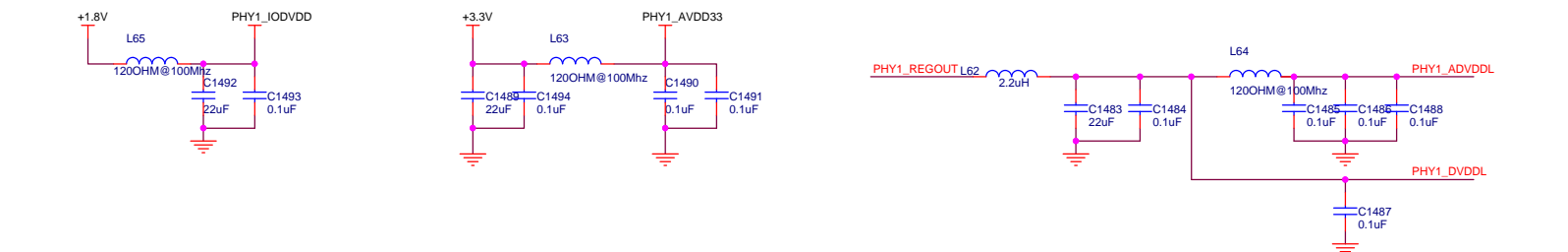








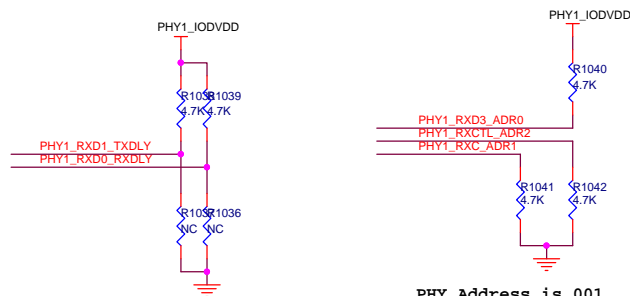
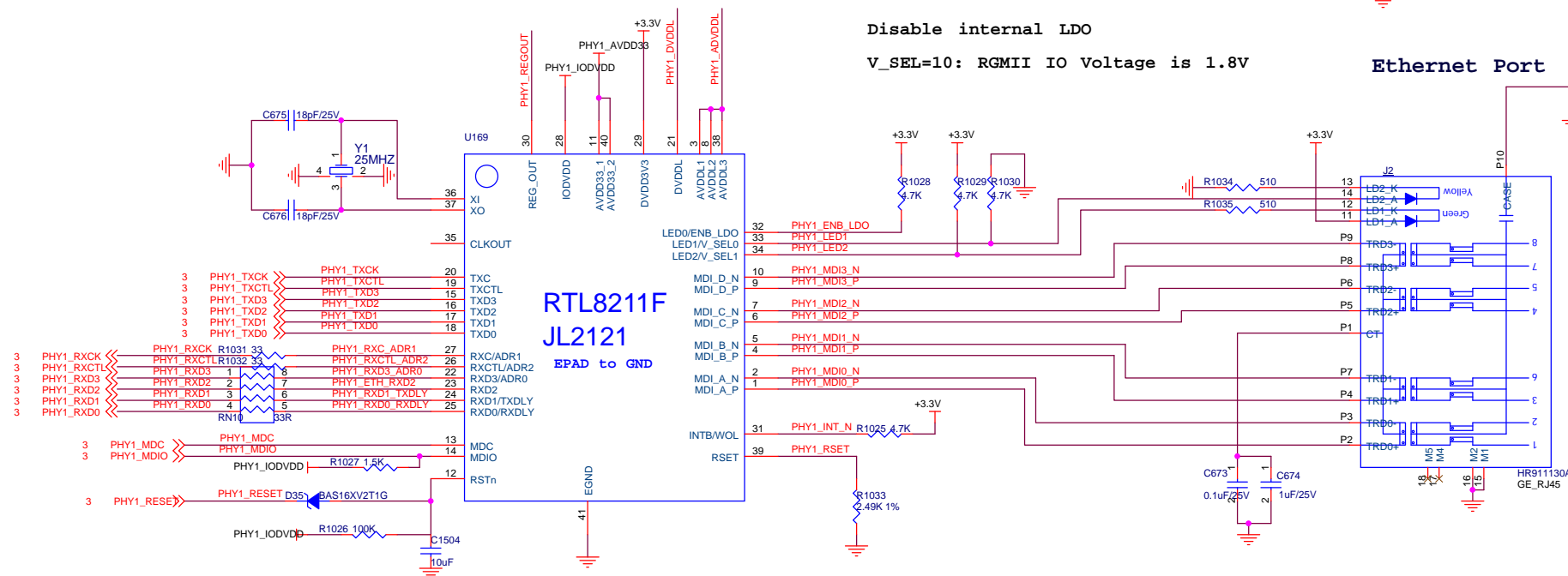




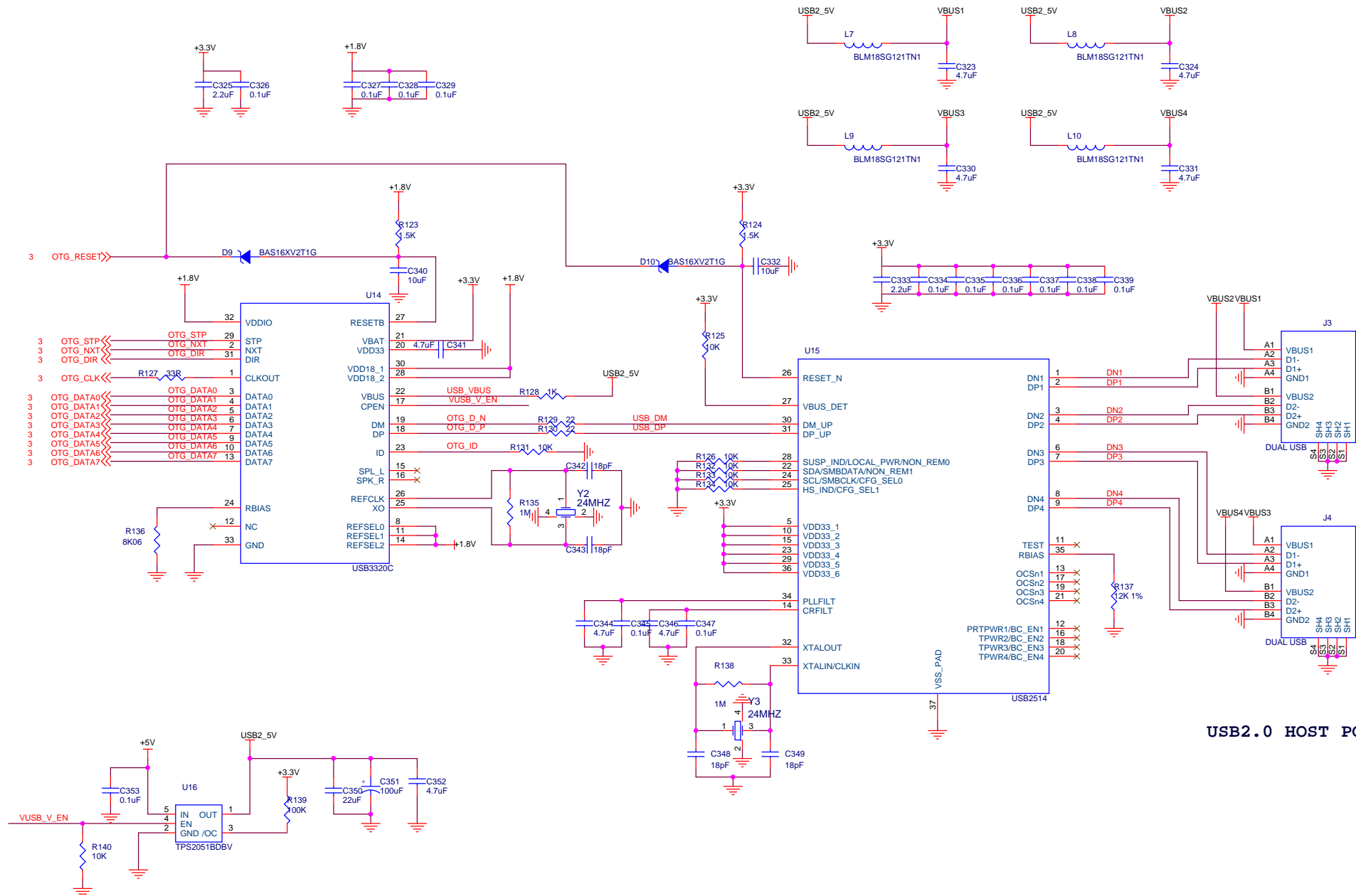
Disable internal LDO

V_SEL=10: RGMII IO Voltage is 1.8V

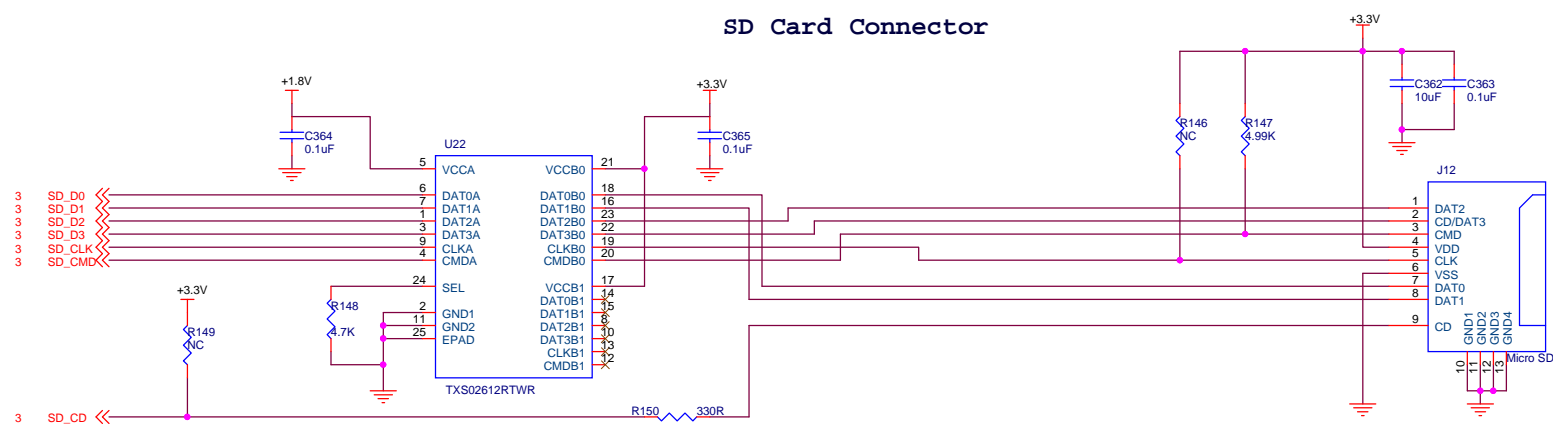
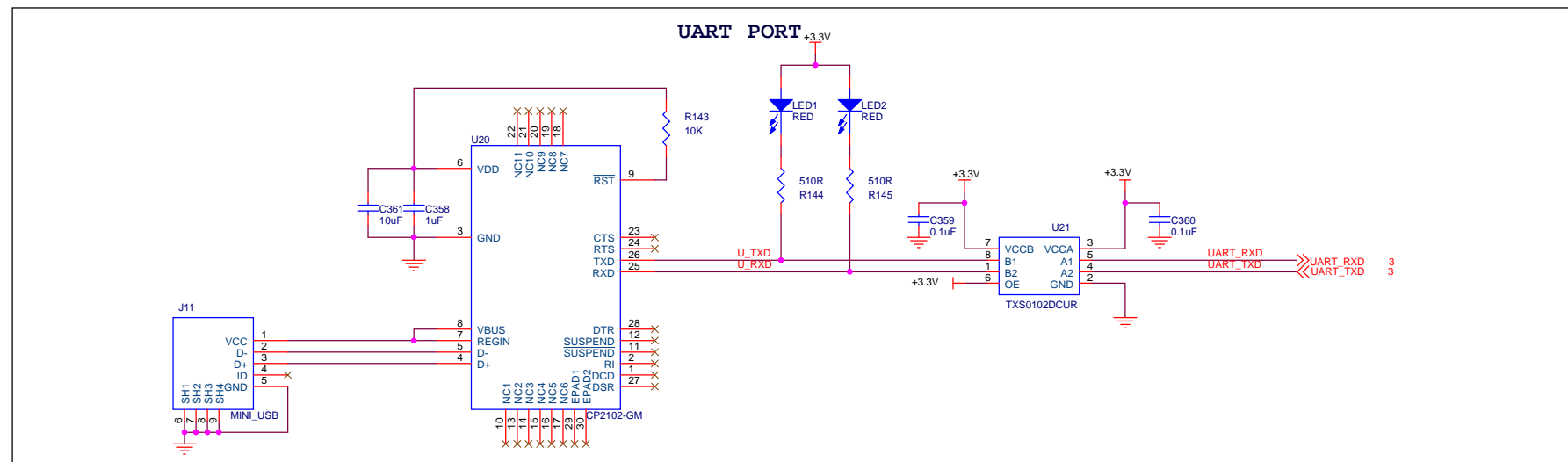
Ethernet Port

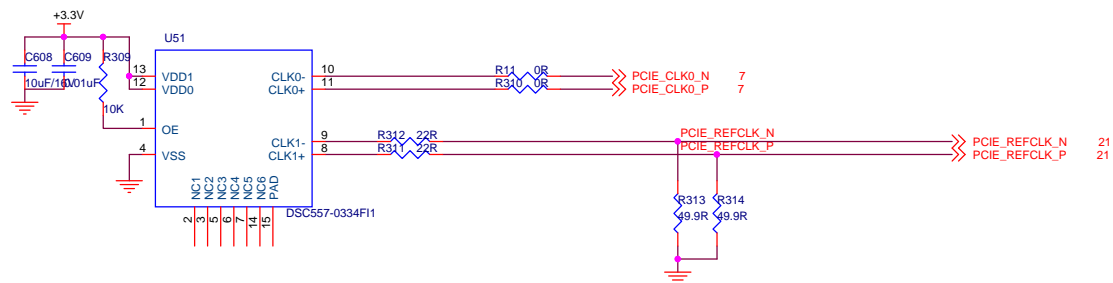


PHY Address is 001



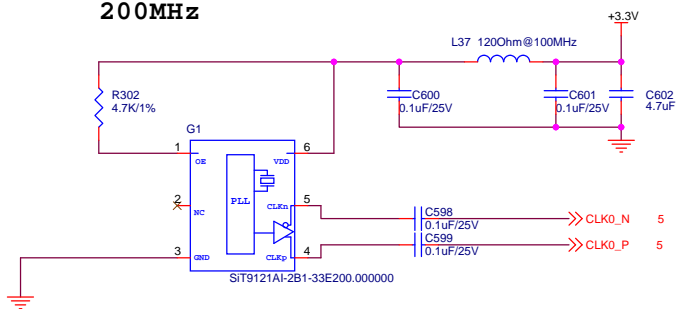
USB2.0 HOST PORT





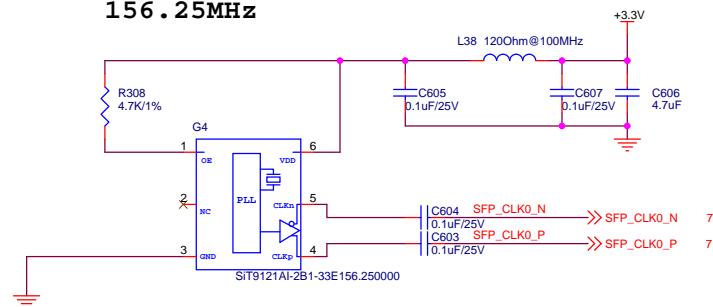
SYSTEM CLOCK

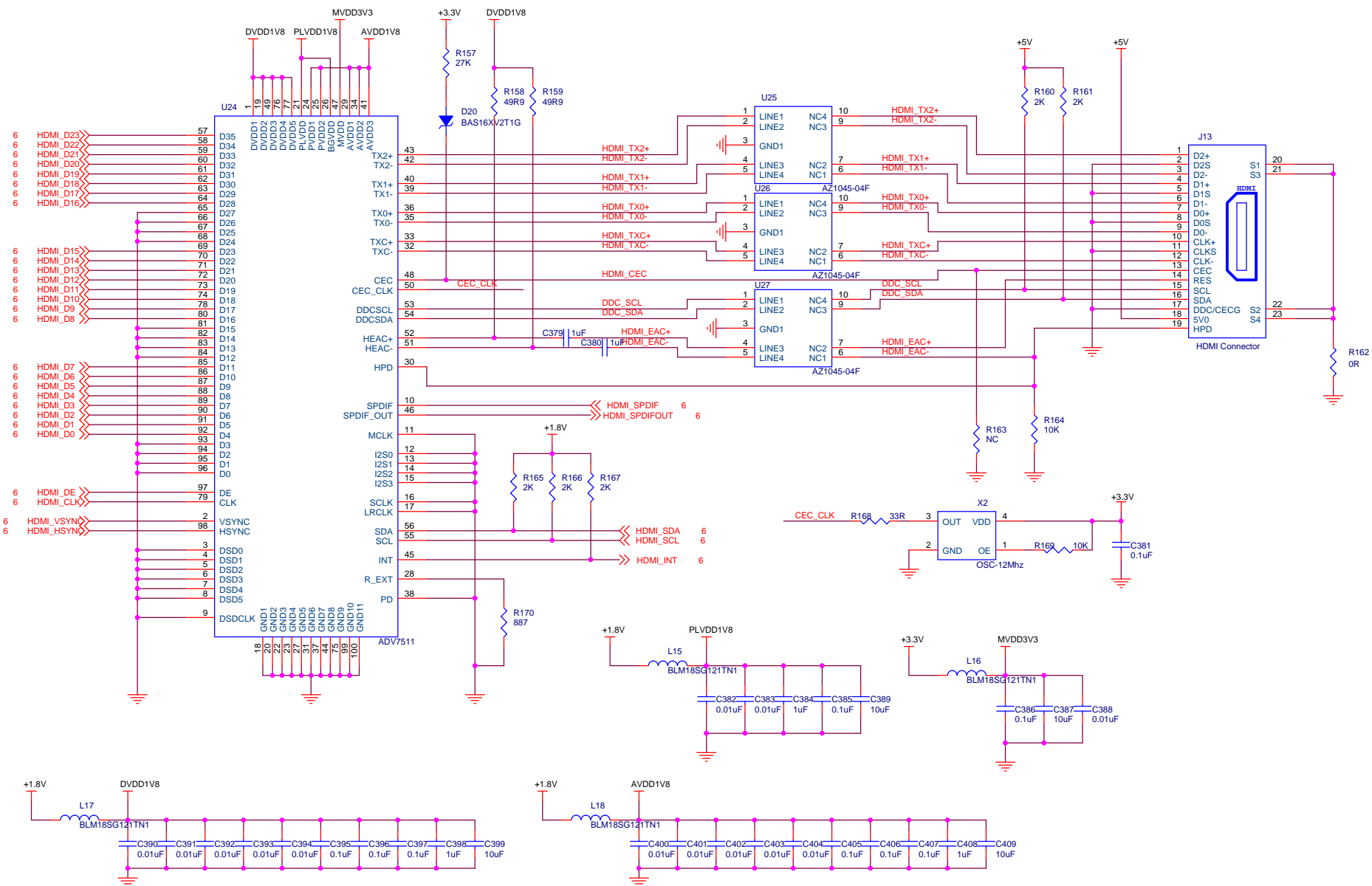
200MHz



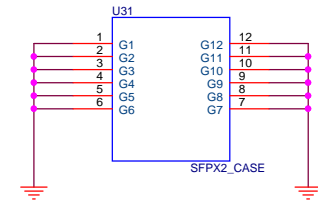
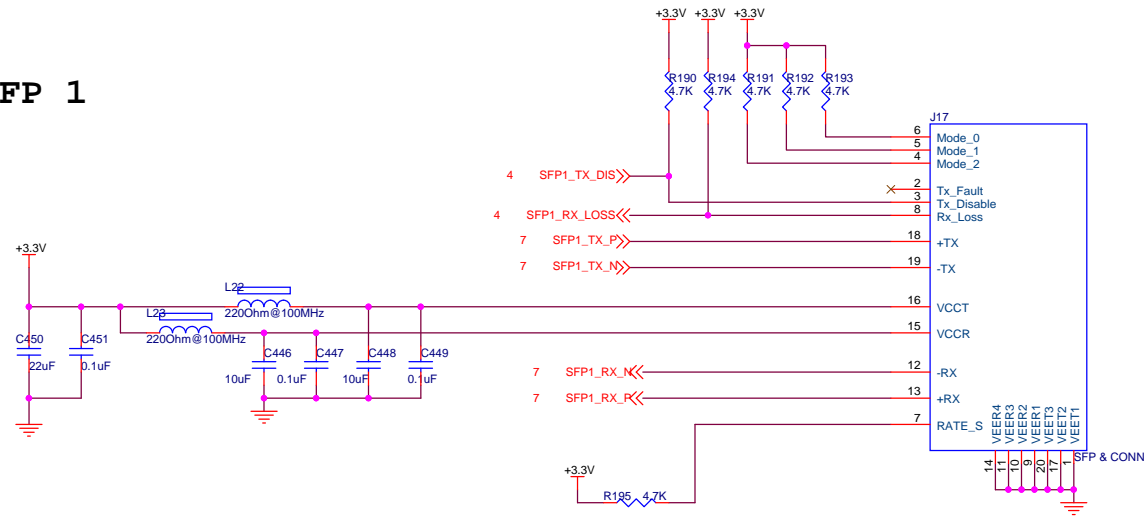
156.25Mhz for SFP

156.25MHz

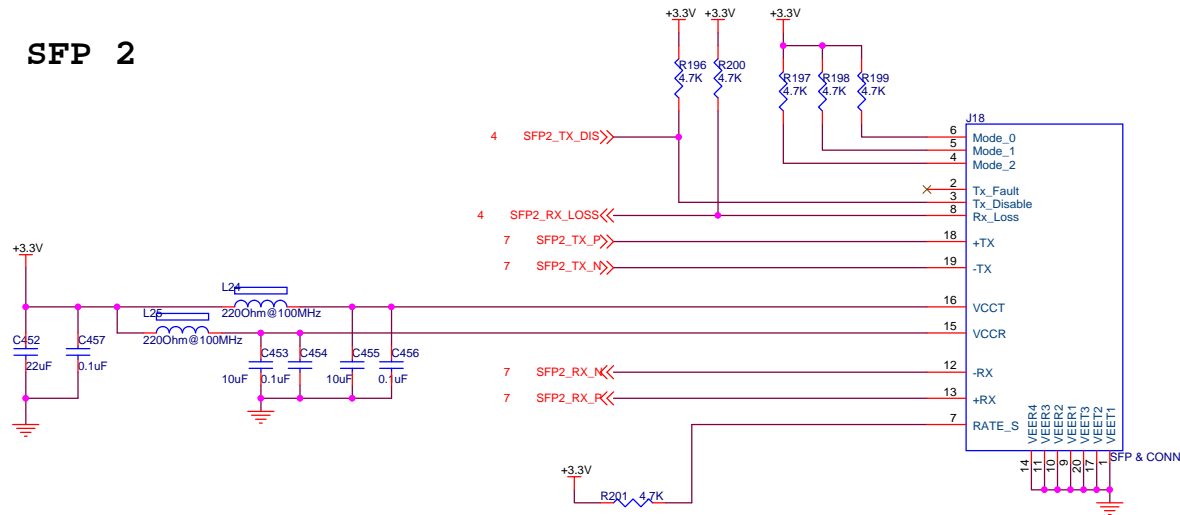




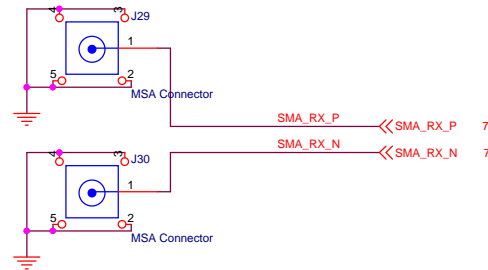
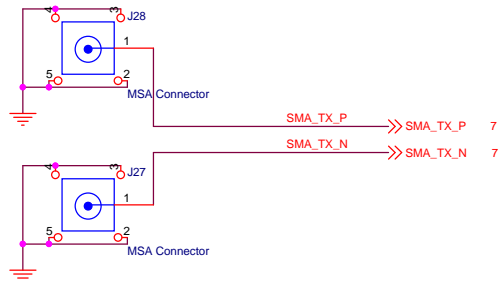
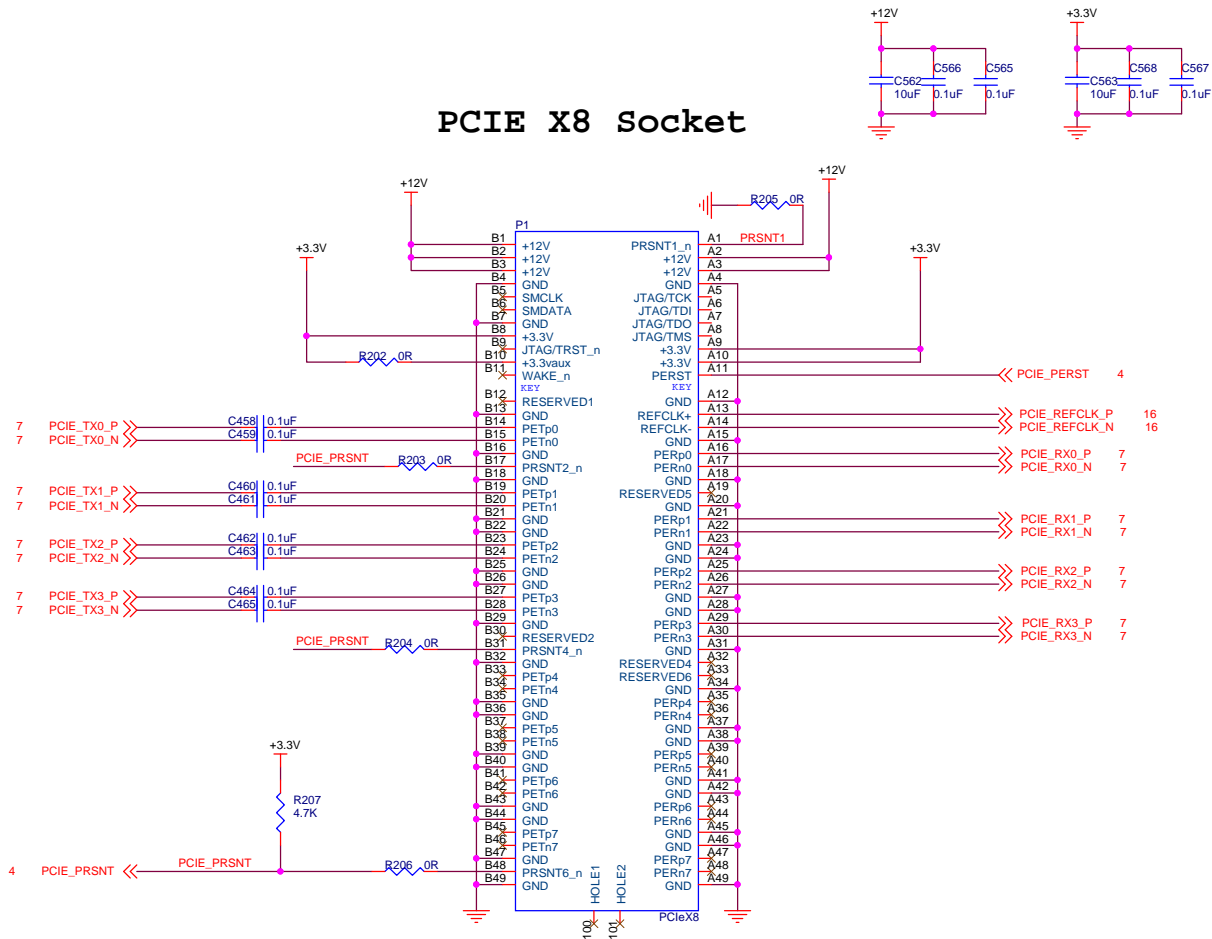
SFP 1

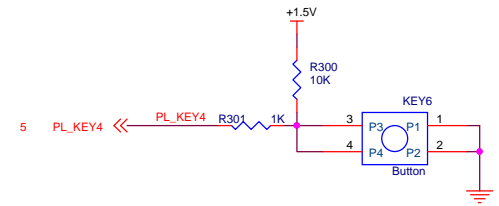
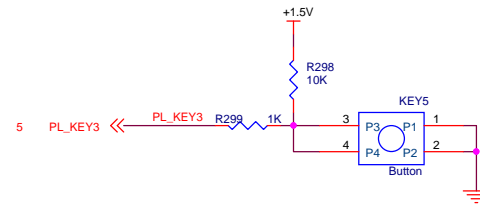
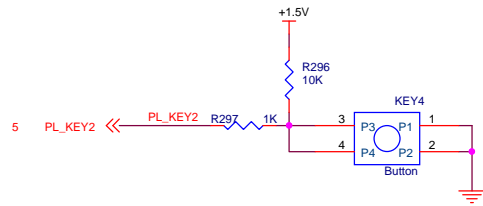
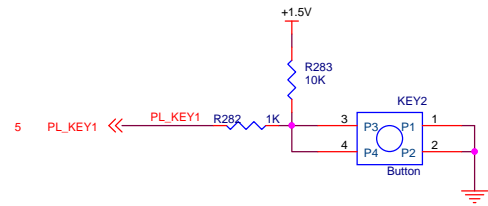
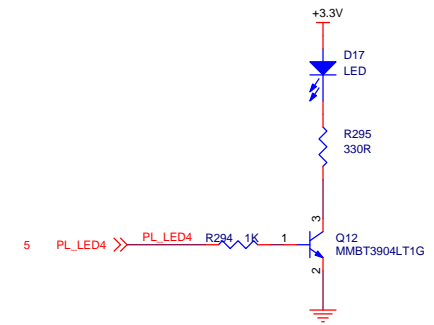
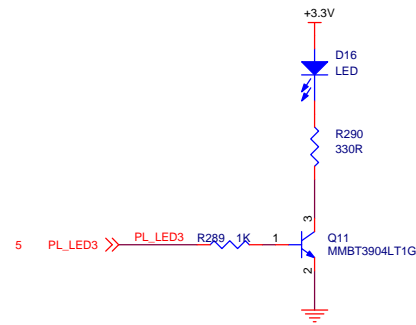
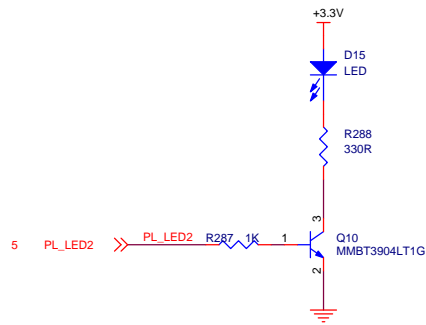
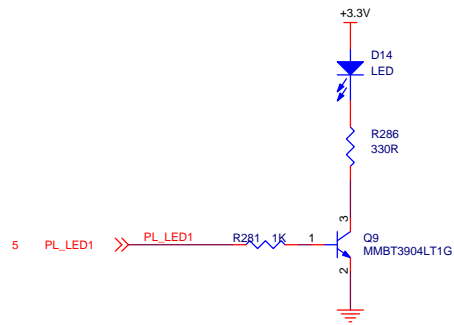


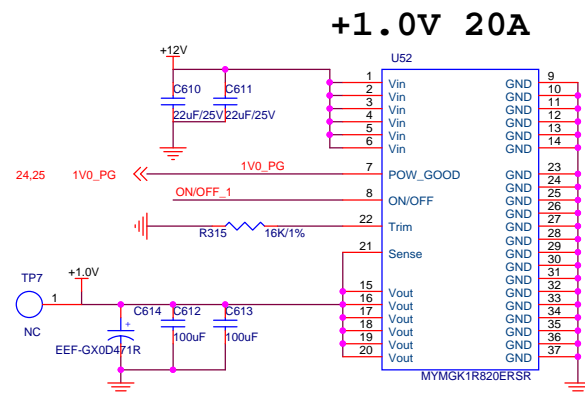
SFP 2

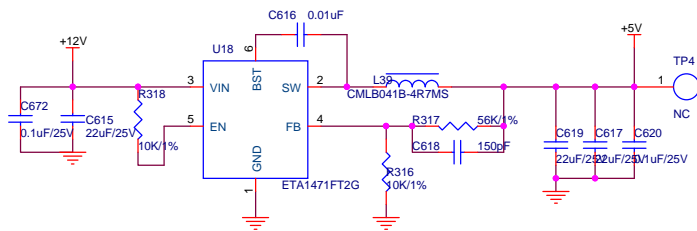


PCIE X8 Socket

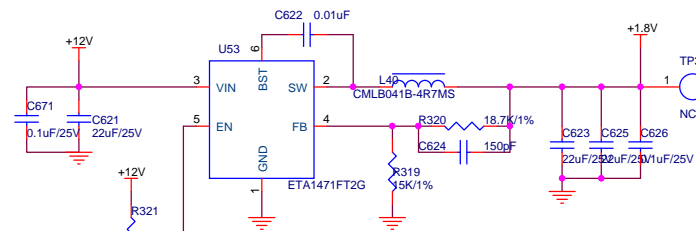






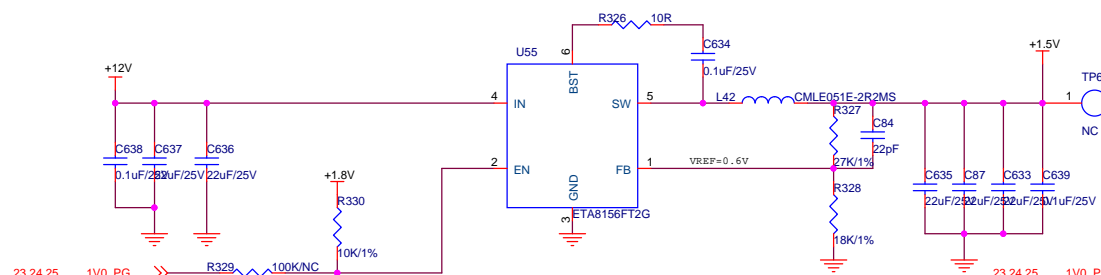


$$V_{out}=0.8 \times (1 + R1/R2)$$

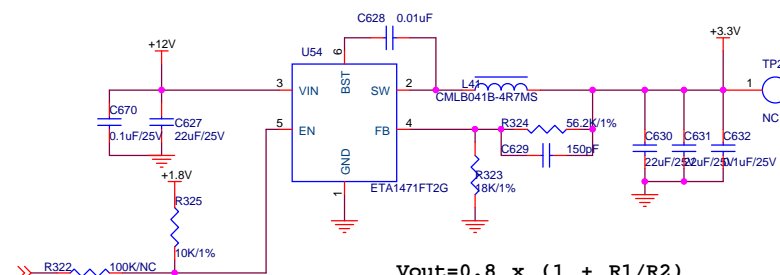


$$V_{out}=0.8 \times (1 + R1/R2)$$

1.5V POWER 6A

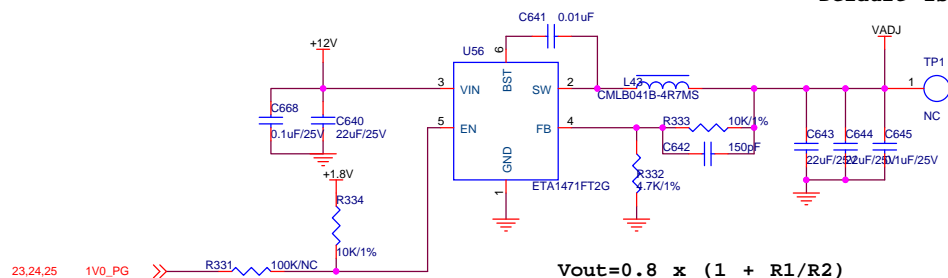


$$V_{out}=0.6 \times (1 + R1/R2)$$



$$V_{out}=0.8 \times (1 + R1/R2)$$

Default is 2.5V



$$V_{out}=0.8 \times (1 + R1/R2)$$

