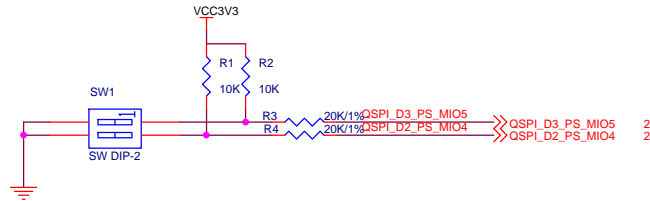


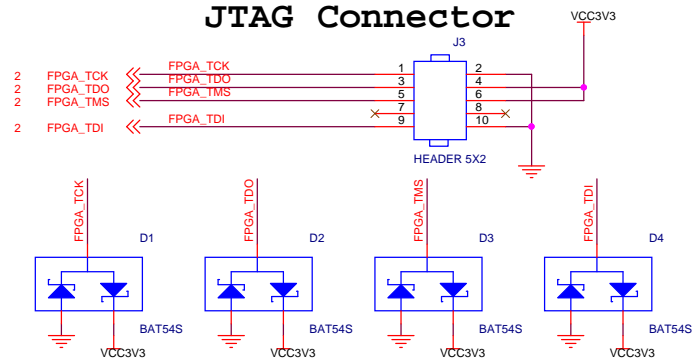
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BOOT OPTION

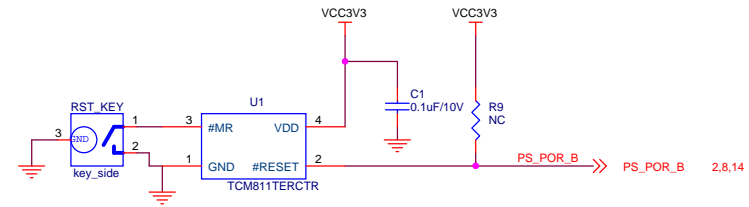


Boot Mode	MIO[5] (QSPI_D3)	MIO[4] (QSPI_D2)
JTAG	0	0
NAND	0	1
QSPI-FLASH	1	0
SD Card	1	1

JTAG Connector

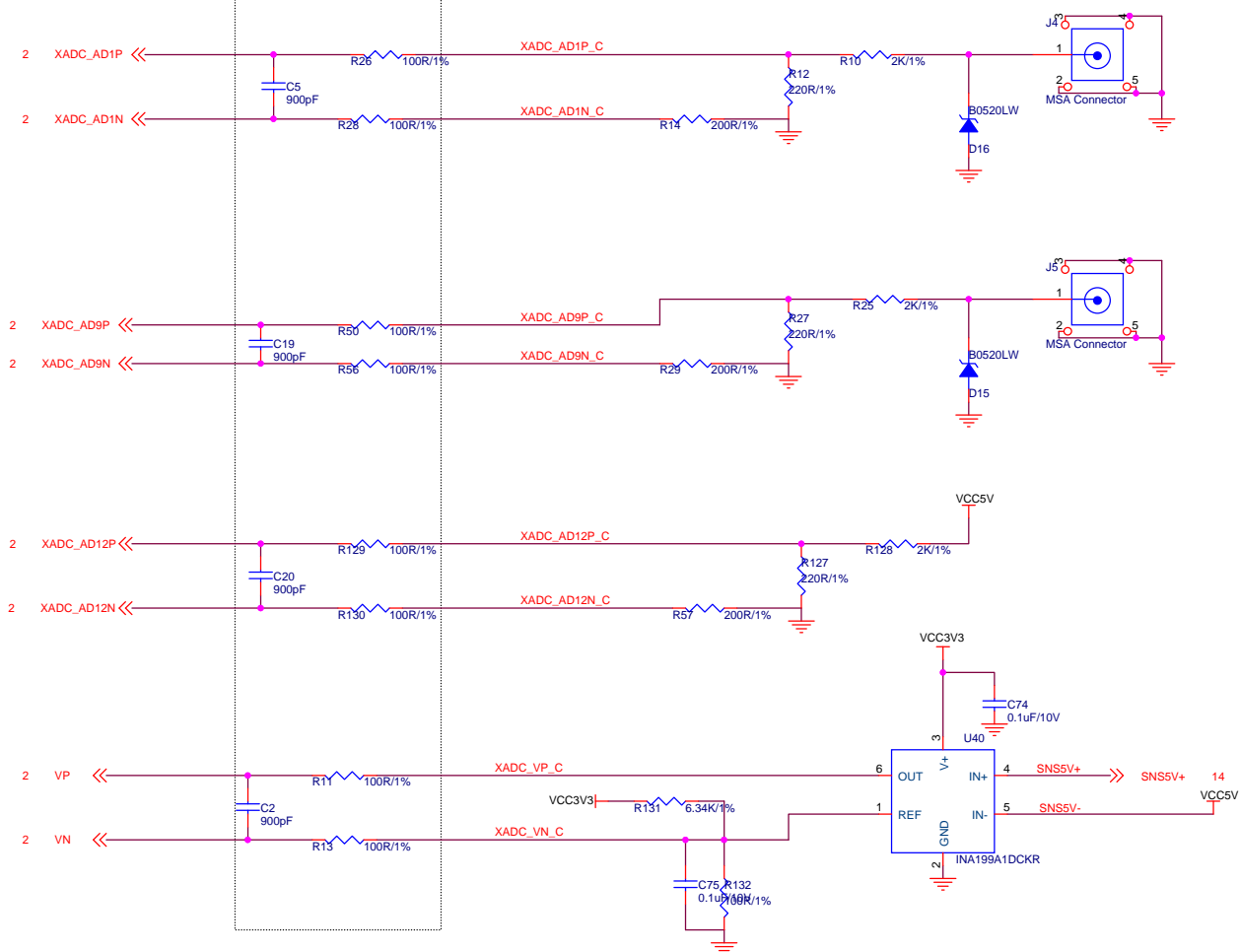



POWER ON RESET

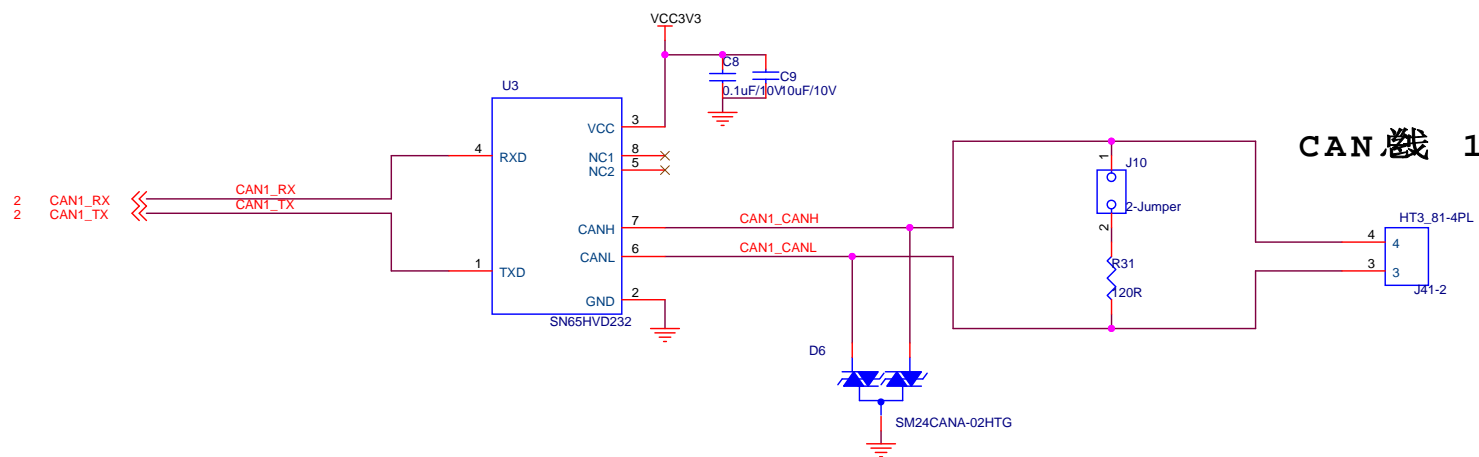
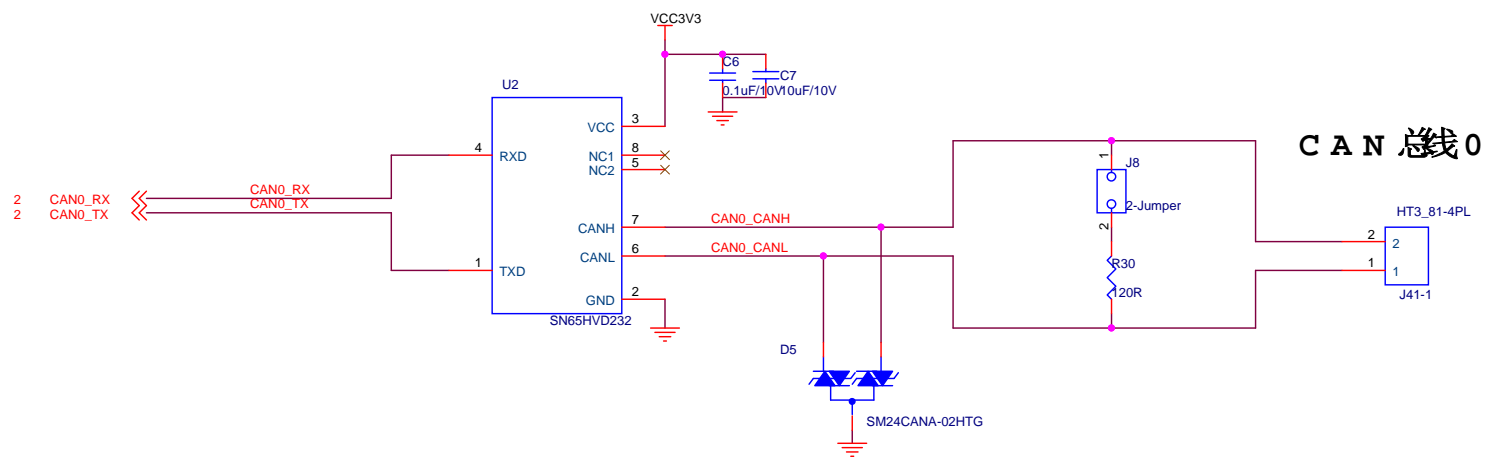


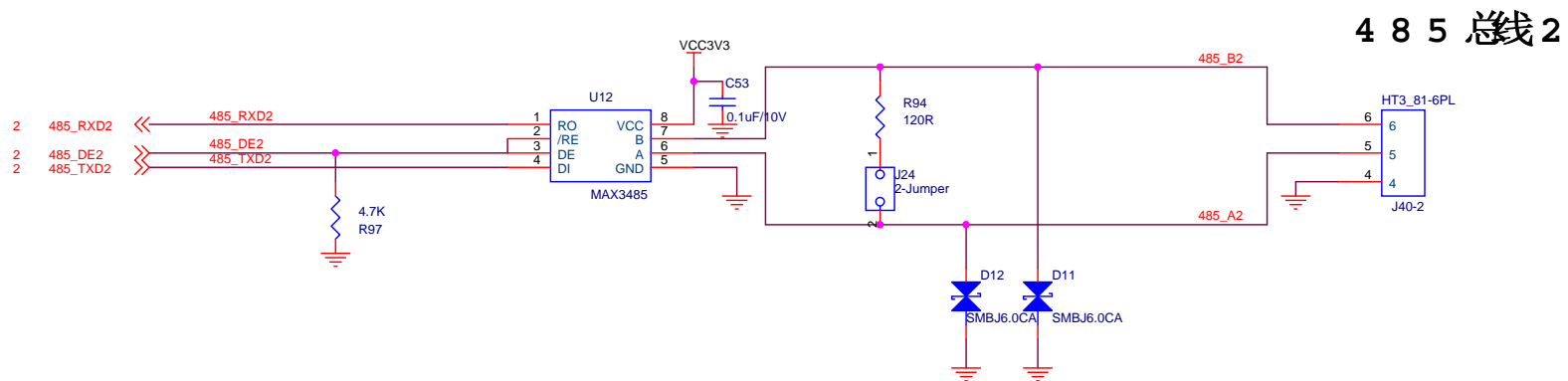
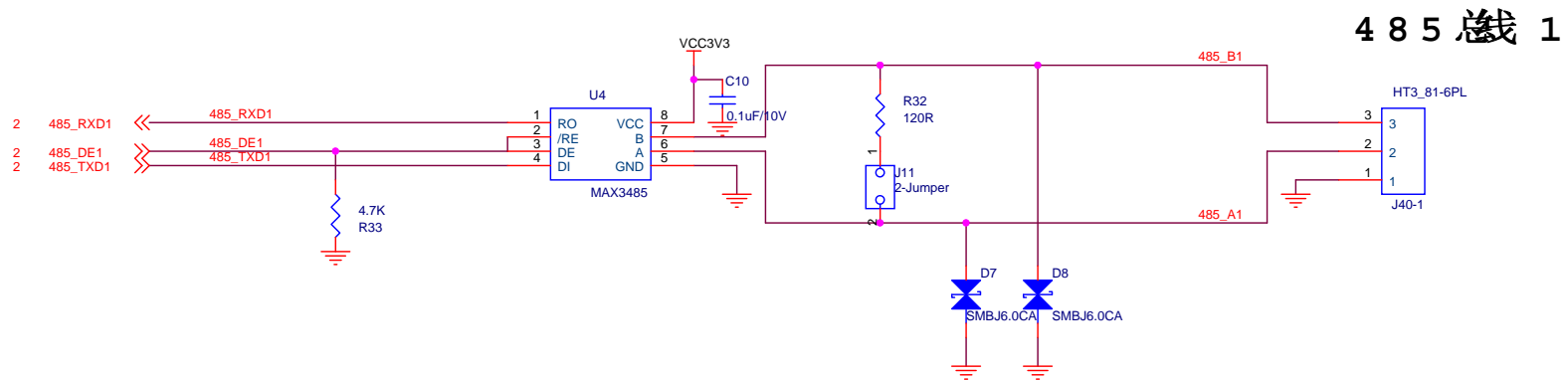
The figure displays four circuit diagrams, each representing a different configuration for a 100R/1% resistor network. Each diagram shows a central node connected to four other nodes, with resistors and capacitors placed between them.

- Diagram 1 (Top):** A central node is connected to four other nodes. The top and bottom nodes are connected to the central node via resistors R26 and R28, both labeled 100R/1%. The left and right nodes are connected to the central node via capacitors C5 and C19, both labeled 900pF.
- Diagram 2:** A central node is connected to four other nodes. The top and bottom nodes are connected to the central node via resistors R50 and R56, both labeled 100R/1%. The left and right nodes are connected to the central node via capacitors C19 and C20, both labeled 900pF.
- Diagram 3:** A central node is connected to four other nodes. The top and bottom nodes are connected to the central node via resistors R129 and R130, both labeled 100R/1%. The left and right nodes are connected to the central node via capacitors C20 and C2, both labeled 900pF.
- Diagram 4 (Bottom):** A central node is connected to four other nodes. The top and bottom nodes are connected to the central node via resistors R11 and R13, both labeled 100R/1%. The left and right nodes are connected to the central node via capacitors C2 and C19, both labeled 900pF.

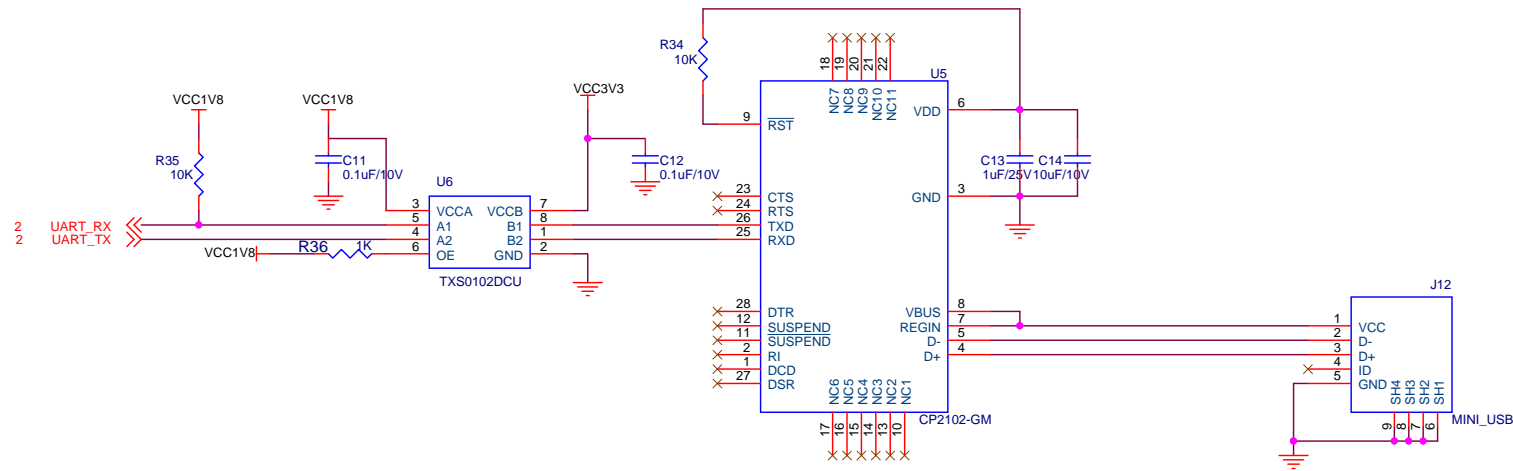


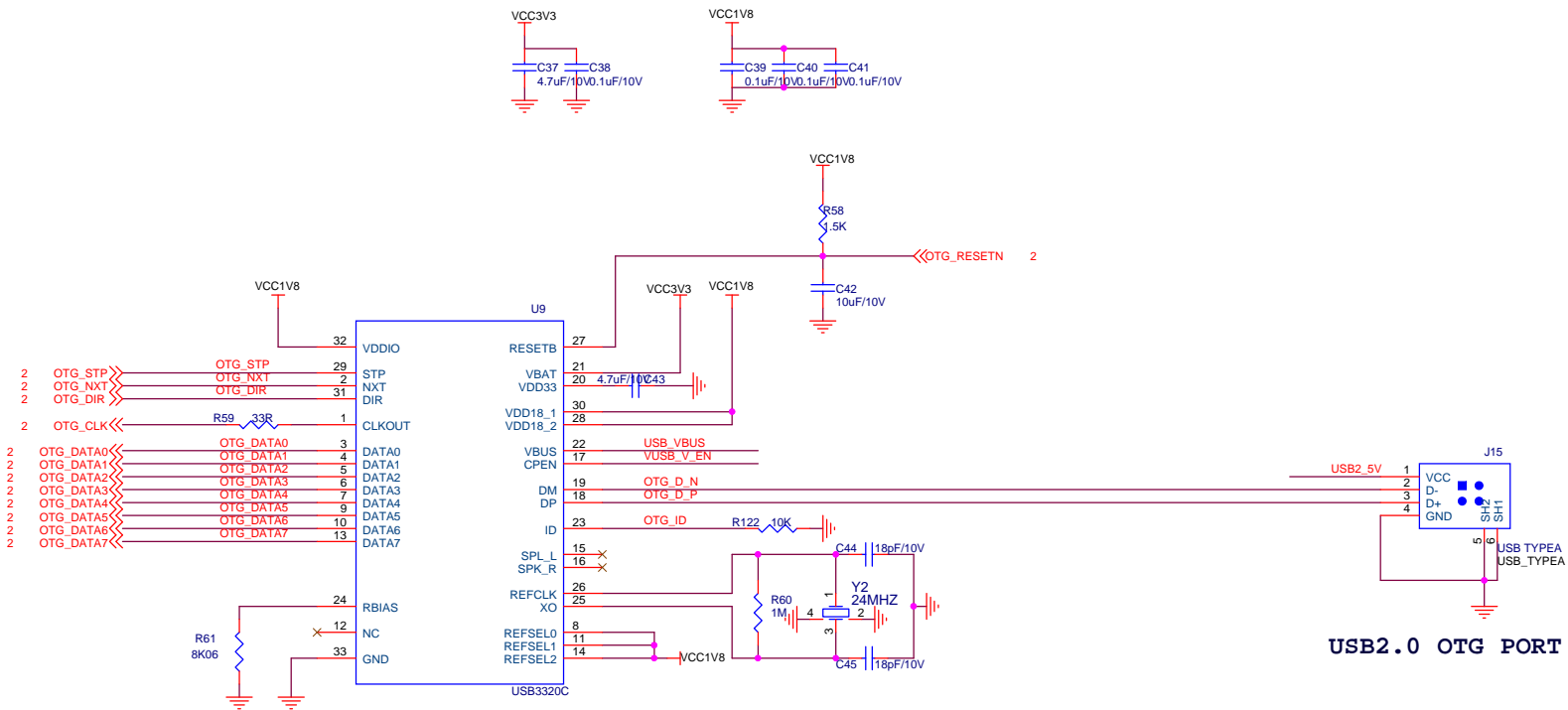
		Http://www.alinx.com	
Title PAGE04 XADC			
Size	Document Number AX7Z010B/AX7Z020B 开发板原理图		Rev 1.0
Date:	Friday, November 25, 2022	Sheet 4	of 14



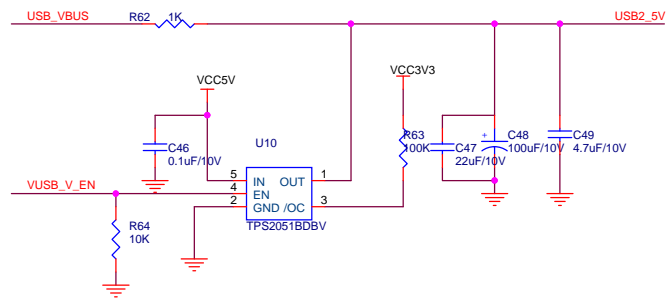


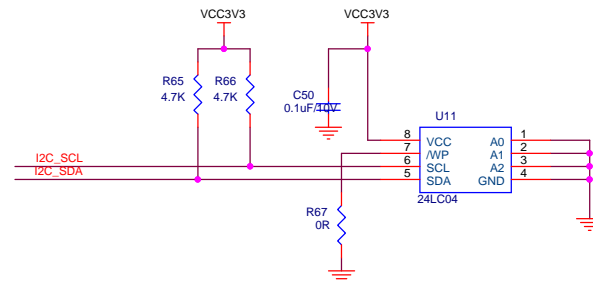
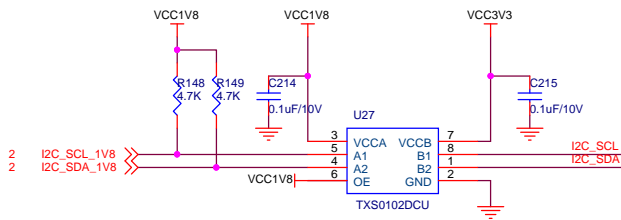
USB Uart



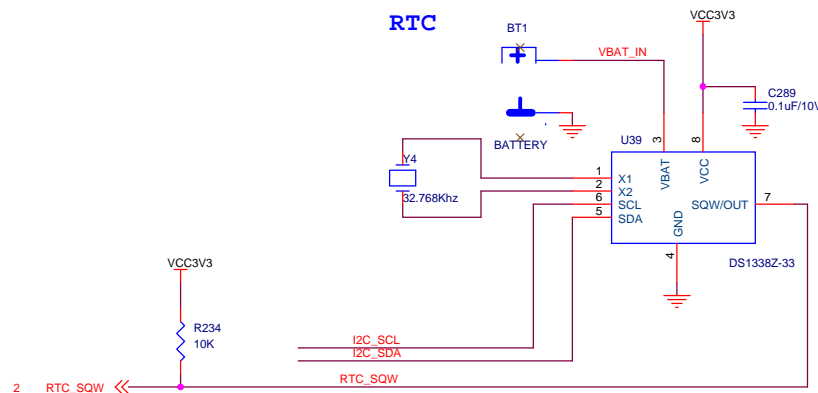


USB2.0 OTG PORT

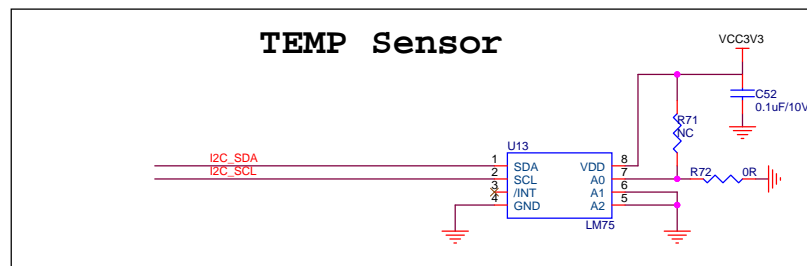




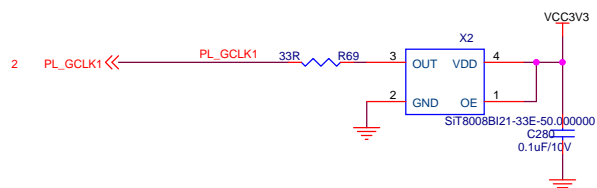
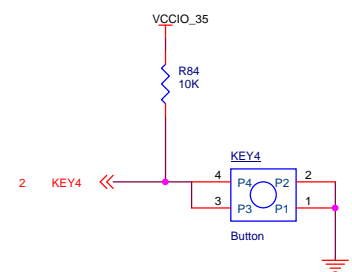
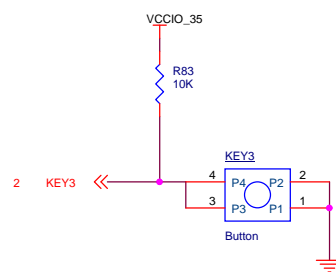
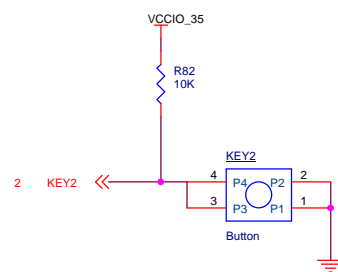
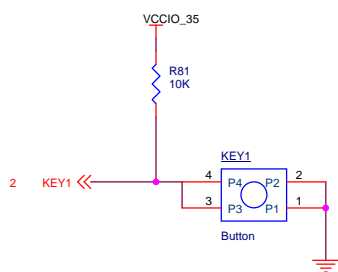
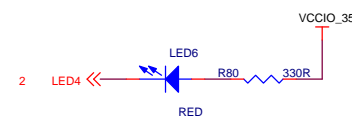
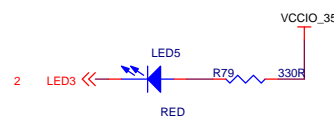
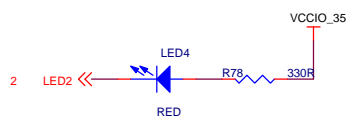
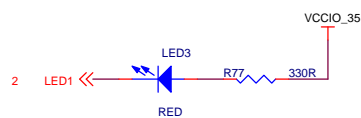
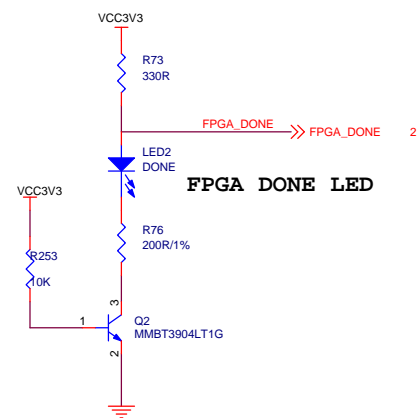
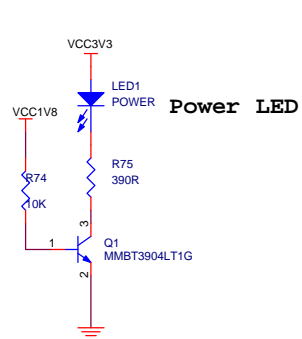
EEPROM

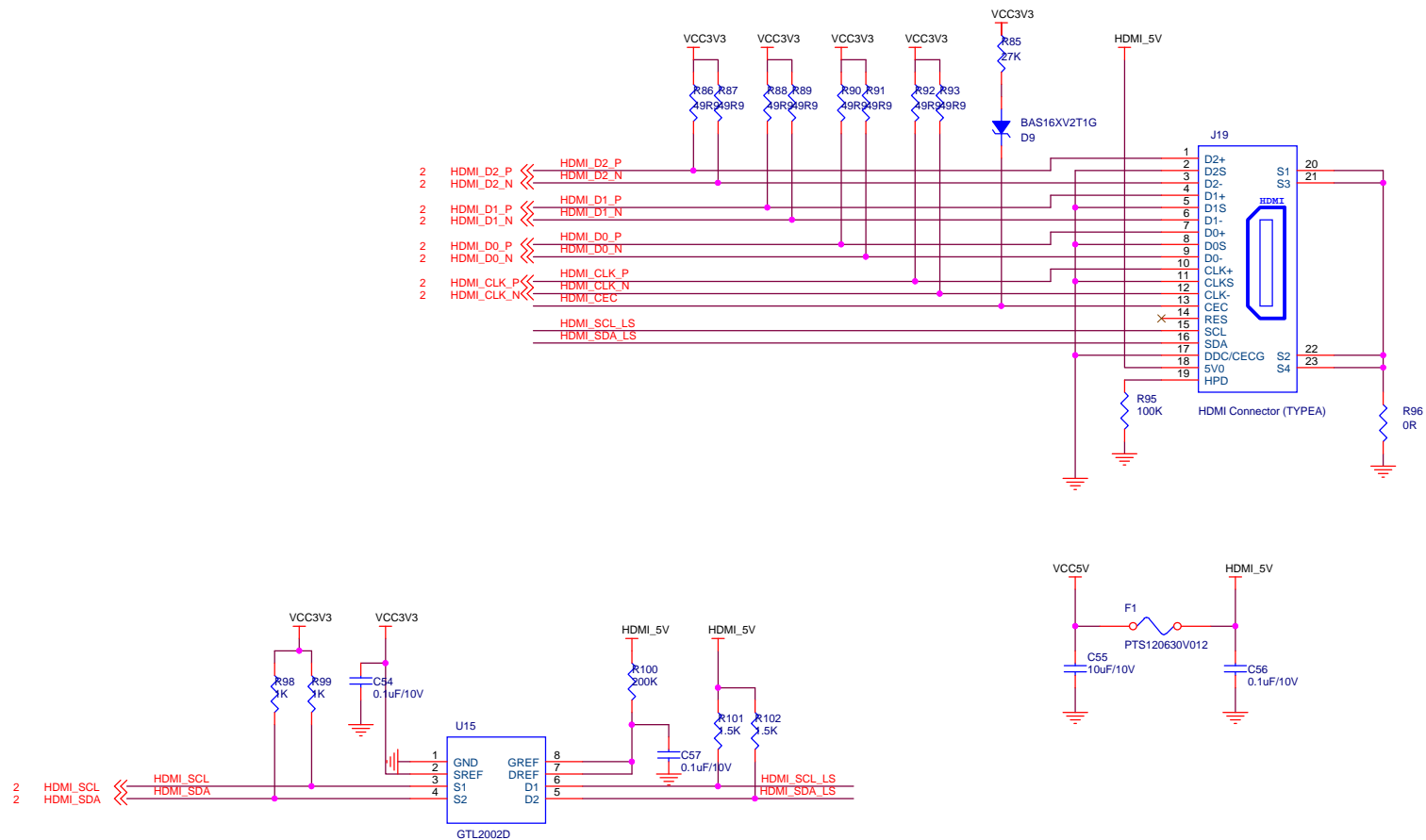


RTC

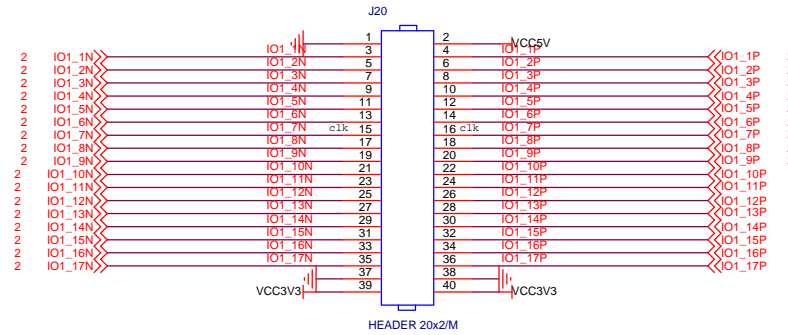
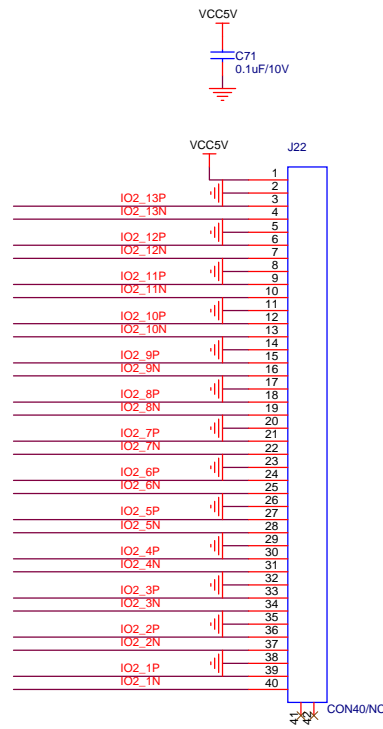


TEMP Sensor

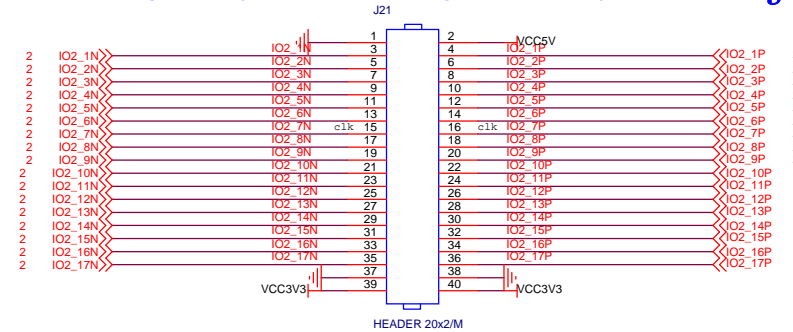




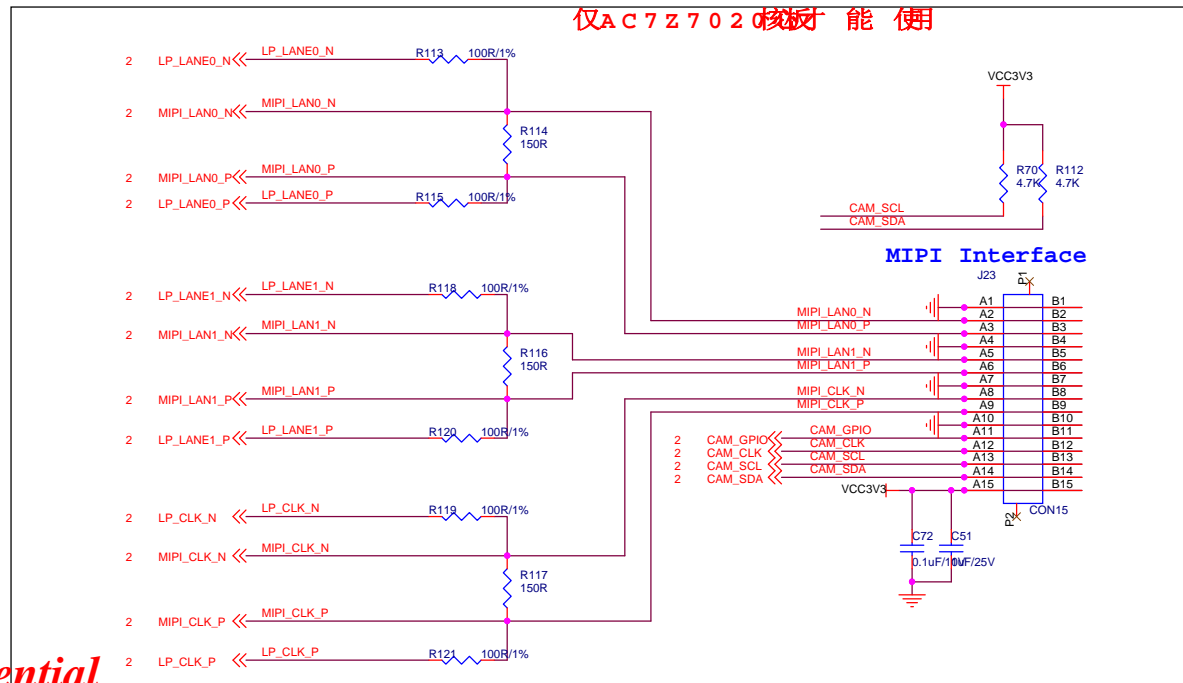
FPGA 40 PIN External IO ----3.3V Level



FPGA 40 PIN External IO ----ADJ

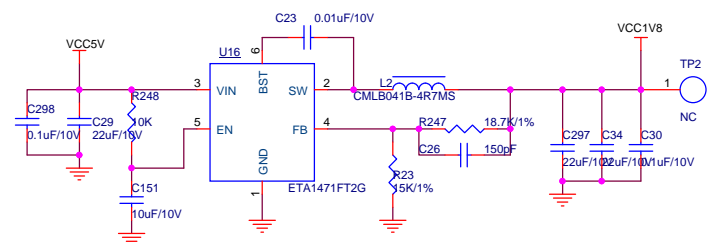
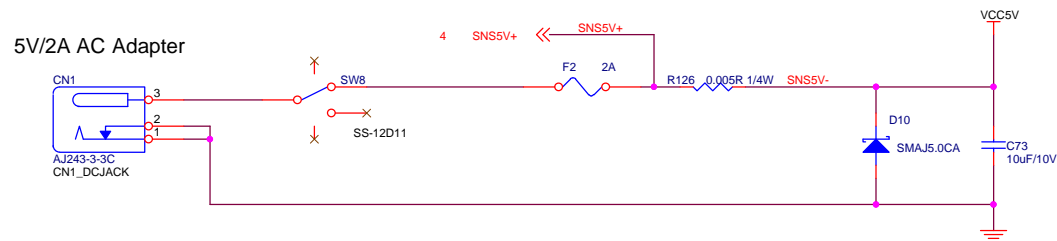


仅AX7Z020板能用

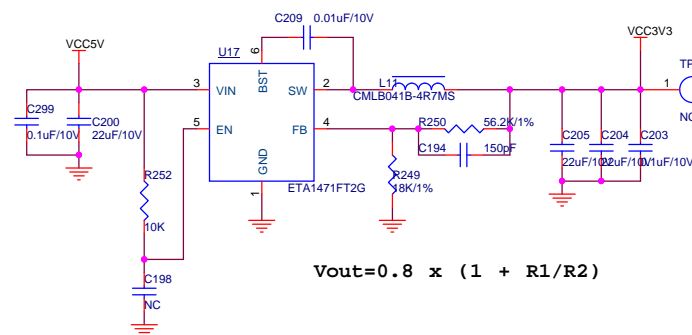


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5V/2A AC Adapter

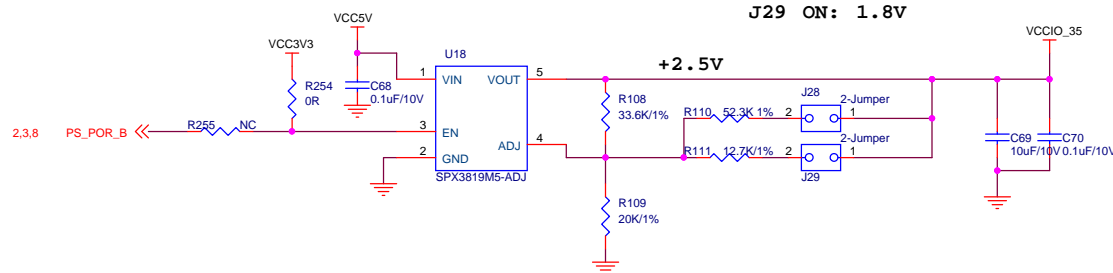


$$V_{out}=0.8 \times (1 + R1/R2)$$



$$V_{out}=0.8 \times (1 + R1/R2)$$

J28/J29 OFF: 3.3V
J28 ON: 2.5V
J29 ON: 1.8V



$$V_{OUT} = 1.235V \times [1 + R108/R109]$$

FAN

