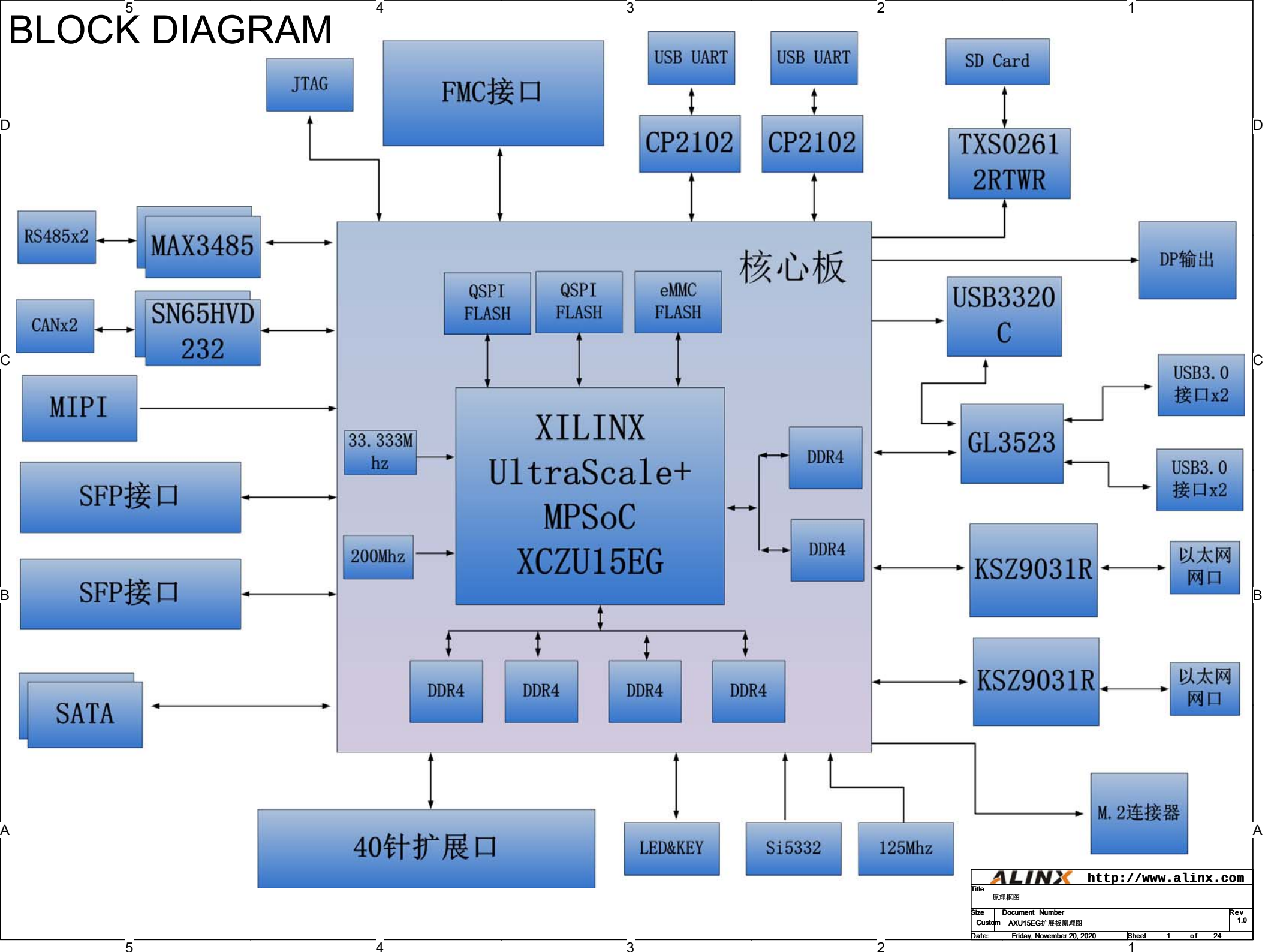
BLOCK DIAGRAM



ALINX http://www.alinx.com

Title	原理框图	
Size	Document Number	Rev
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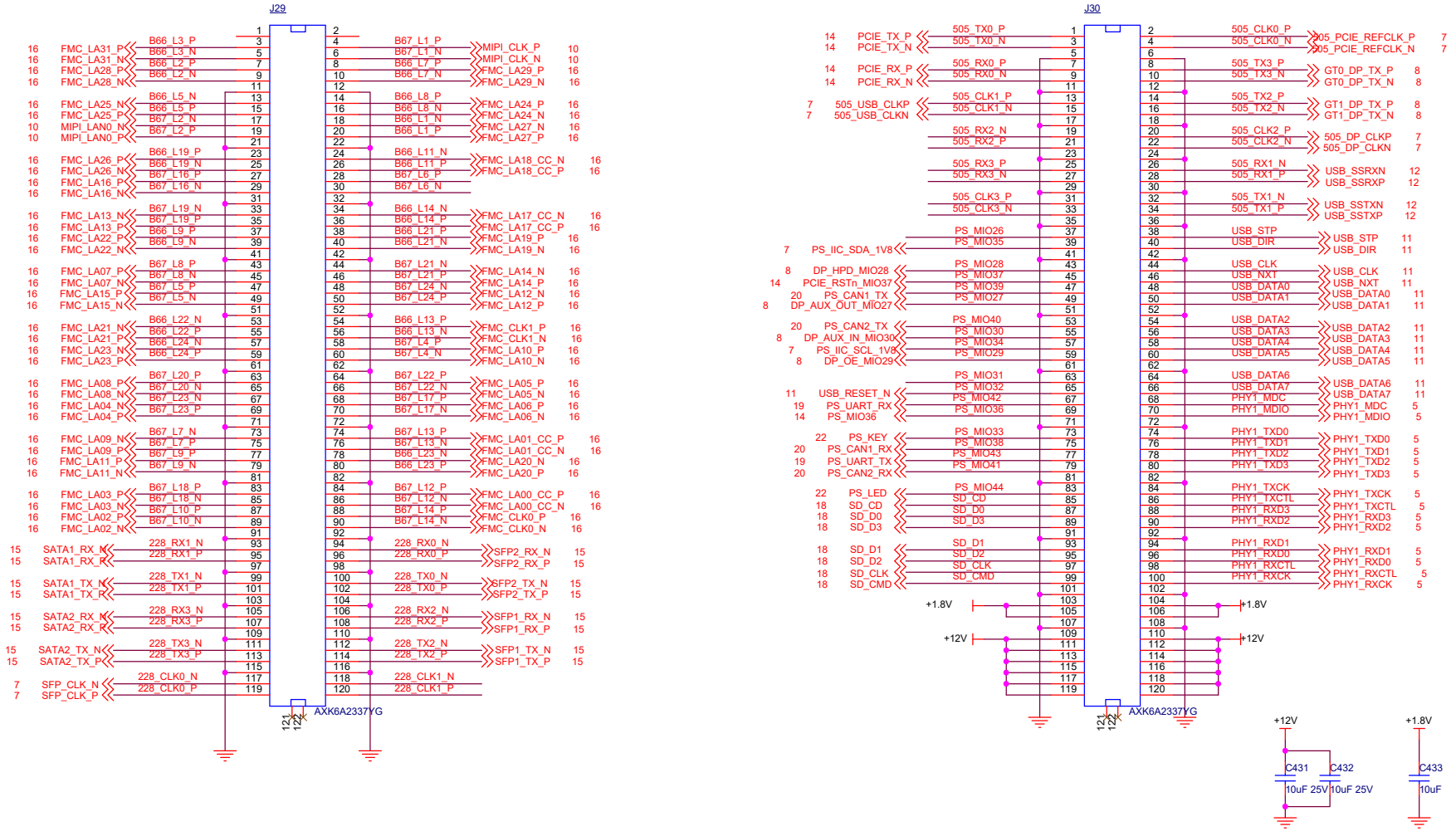

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ALINX http://www.alinx.com		
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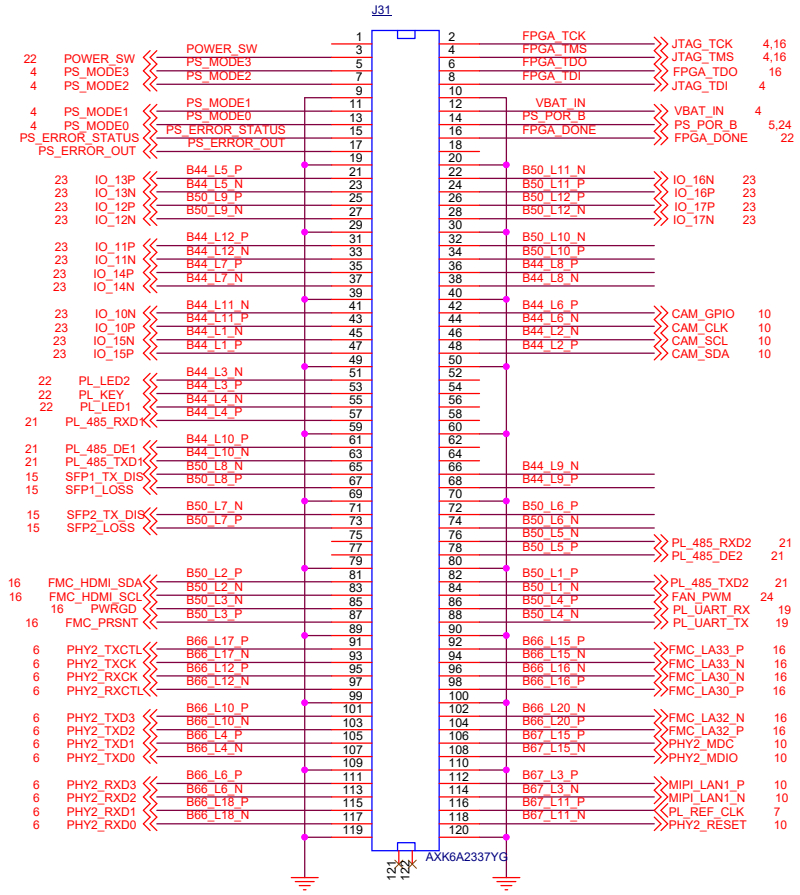
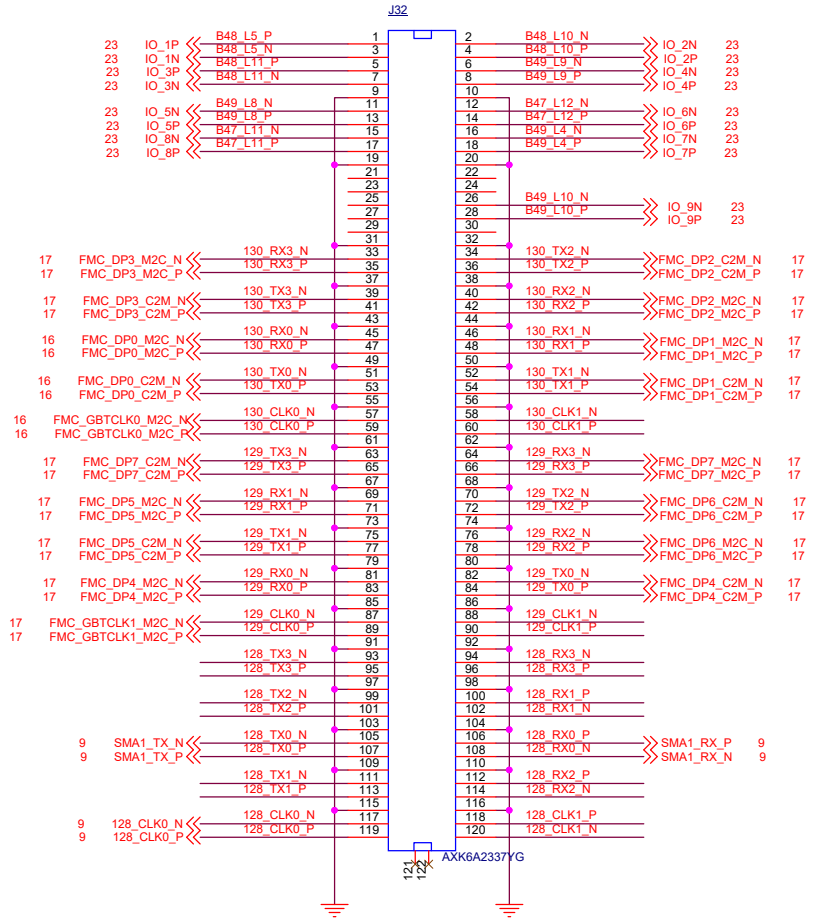
Size: A3	Document Number: AXU15EG扩展板原理图	Rev: 1.0
Date: Friday, November 20, 2020	Sheet 2 of 24	





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ALINX http://www.alinx.com		
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Size: A3	Document Number: AXU15EG扩展板原理图	Rev: 1.0
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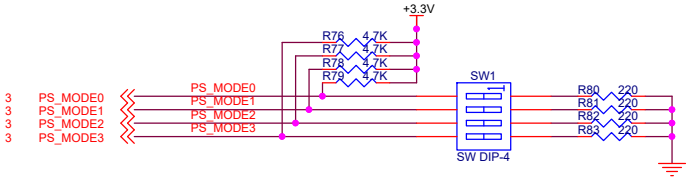
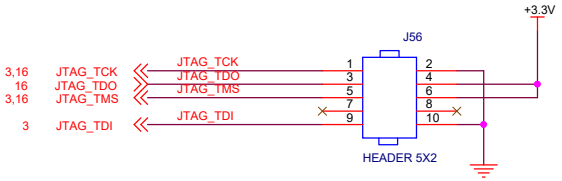
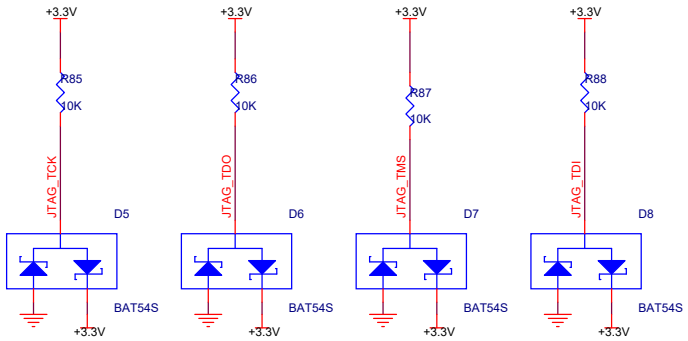
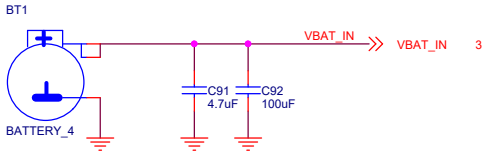


MODE[3:0]	BOOT MODE	Descritpion
0000	PS JTAG	PS JTAG Interface
0001	Quad_SPI(24b)	24-Bit addresssing(QSPI24)
0010	Quad_SPI(32b)	32-Bit addresssing(QSPI32)
0011	SD0(2.0)	SD2.0
0100	NAND	Requires 8-bit data bus width
0101	SD1(2.0)	SD2.0
0110	eMMC(1.8V)	eMMC version 4.5 at 1.8V
0111	USB0(2.0)	USB 2.0 only
1000	PJTAG(MIO #0)	PJTAG connection 0 option
1001	PJTAG(MIO #1)	PJTAG connection 1 option
1110	SD1 LS(3.0)	SD 3.0

JTAG Connector







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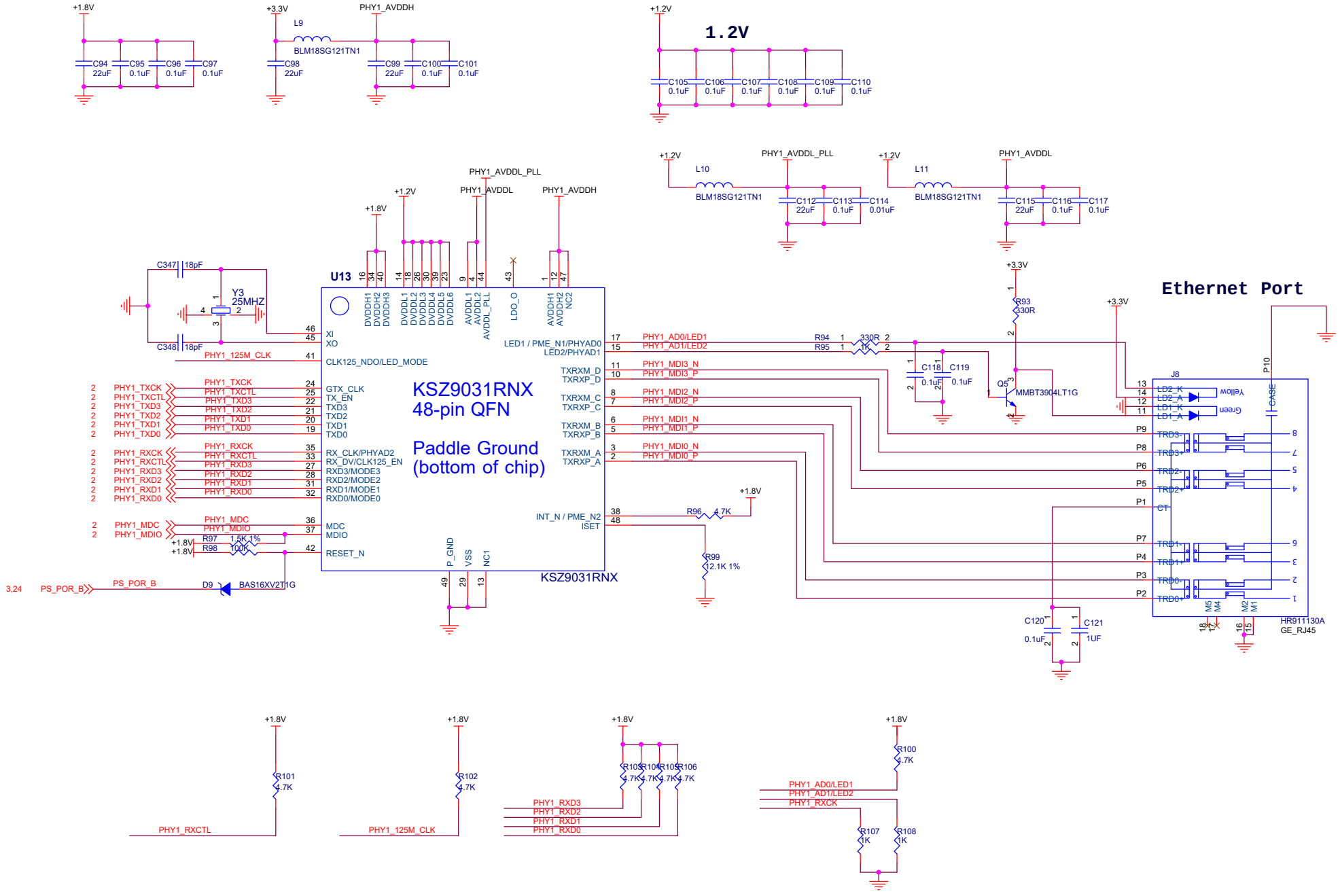
ALINX http://www.alinx.com

Title: 04 JTAG		
Size: A3	Document Number: AXU15EG扩展板原理图	Rev: 1.0
Date: Friday, November 20, 2020	Sheet 4 of 24	



ALINX http://www.alinx.com

Title	05 PS Ethernet	
Size A3	Document Number AXU15EG扩展板原理图	Rev 1.0
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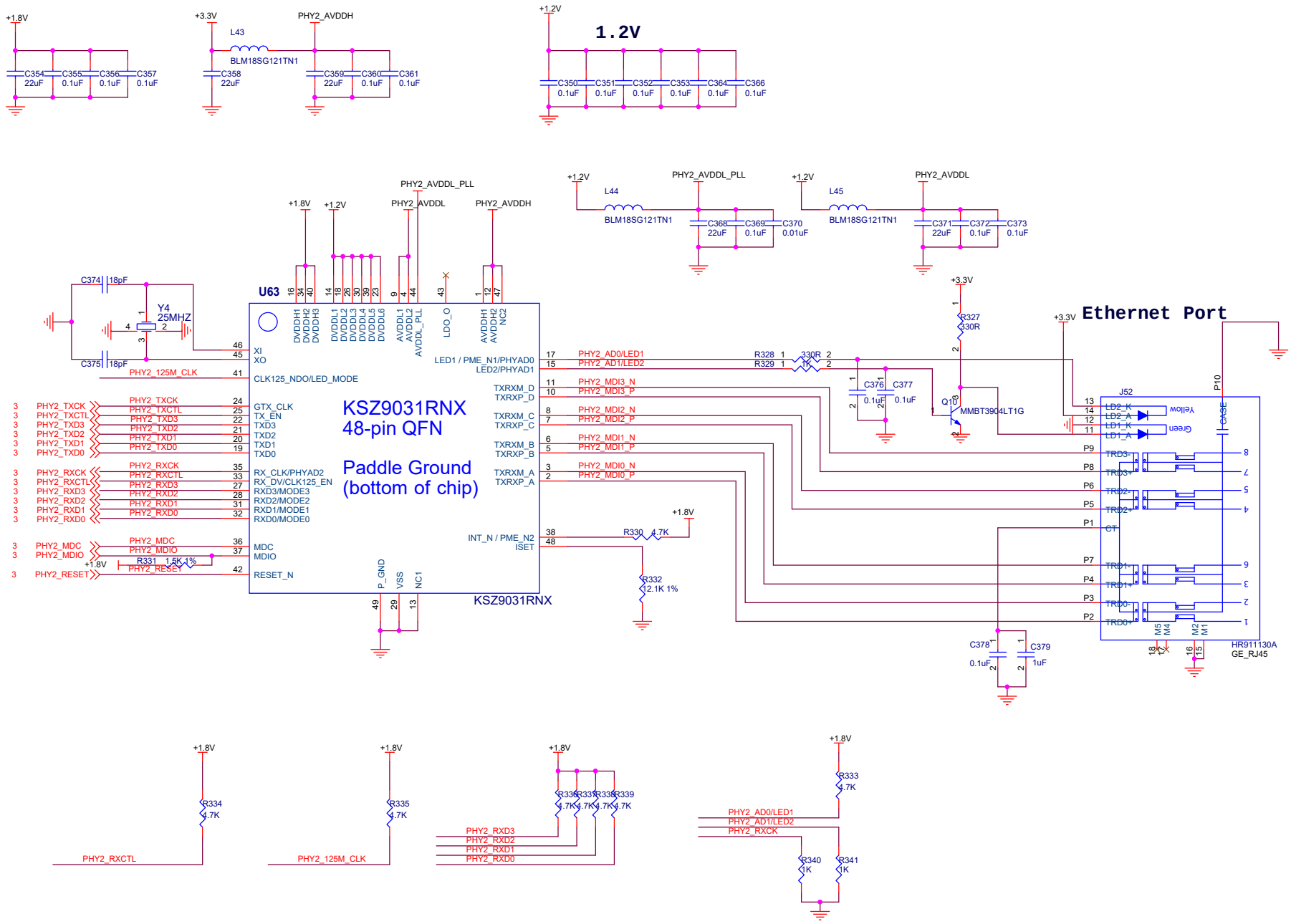

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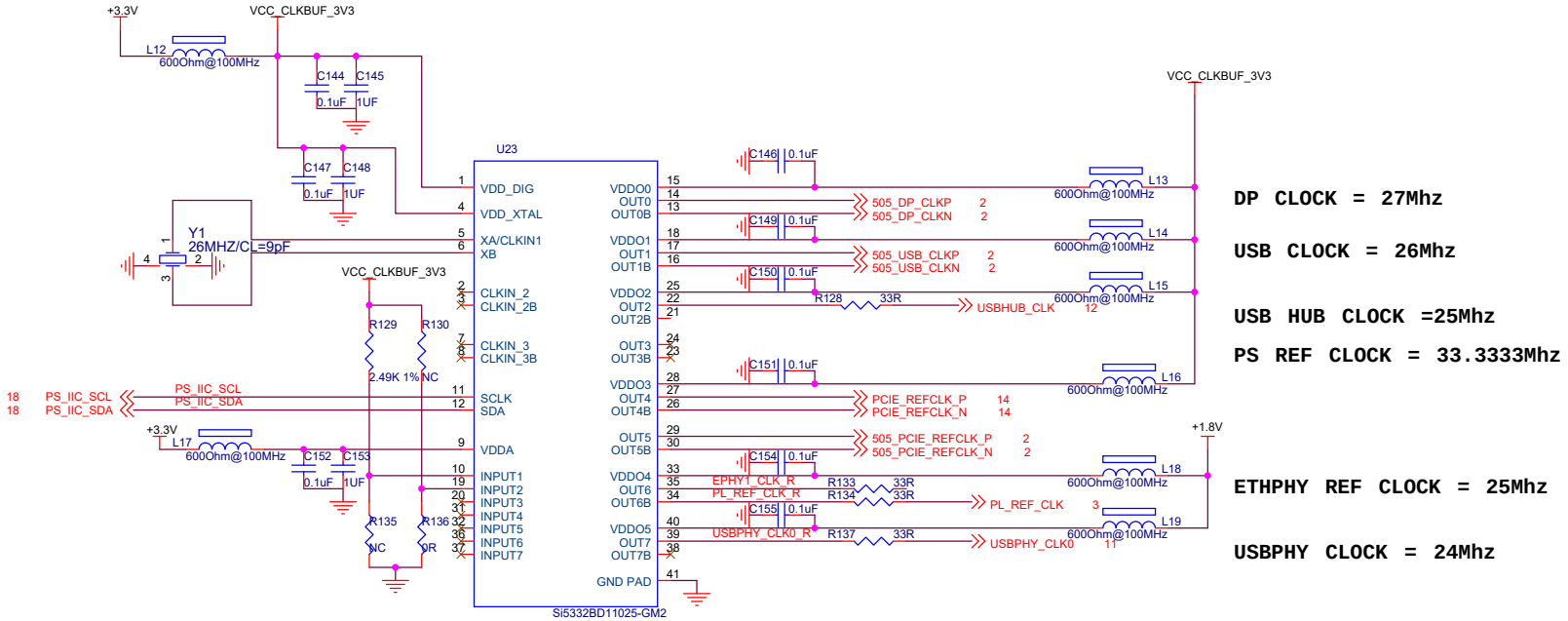
ALINX	www.alinx.com
Title	06 PL Ethernet2
Size	Document Number: AXU15EG扩展板原理图 — Rev: 1.0
Date:	Friday, November 20, 2020 — Sheet 6 of 24



DP CLOCK = 27Mhz

USB CLOCK = 26Mhz

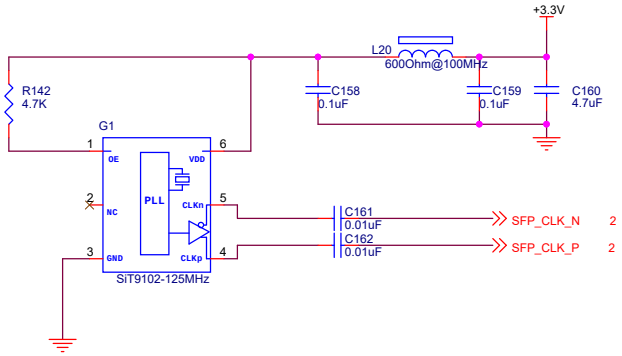
USB HUB CLOCK =25Mhz

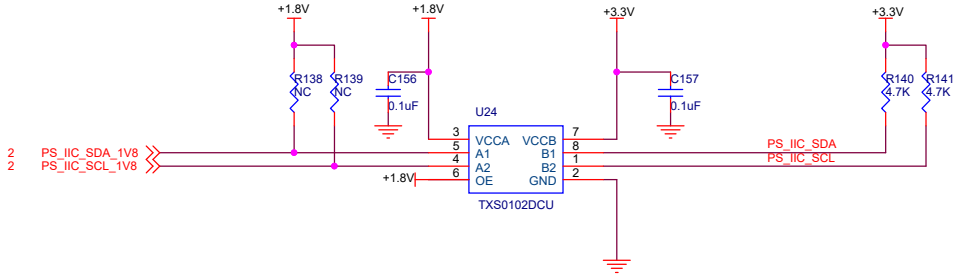
PS REF CLOCK = 33.3333Mhz

ETHPHY REF CLOCK = 25Mhz

USBPHY CLOCK = 24Mhz

PS_IIC1: IIC ADDRESS IS 0x76





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Title: 07 CLOCK		
Size: A3	Document Number: AXU15EG扩展板原理图	Rev: 1.0
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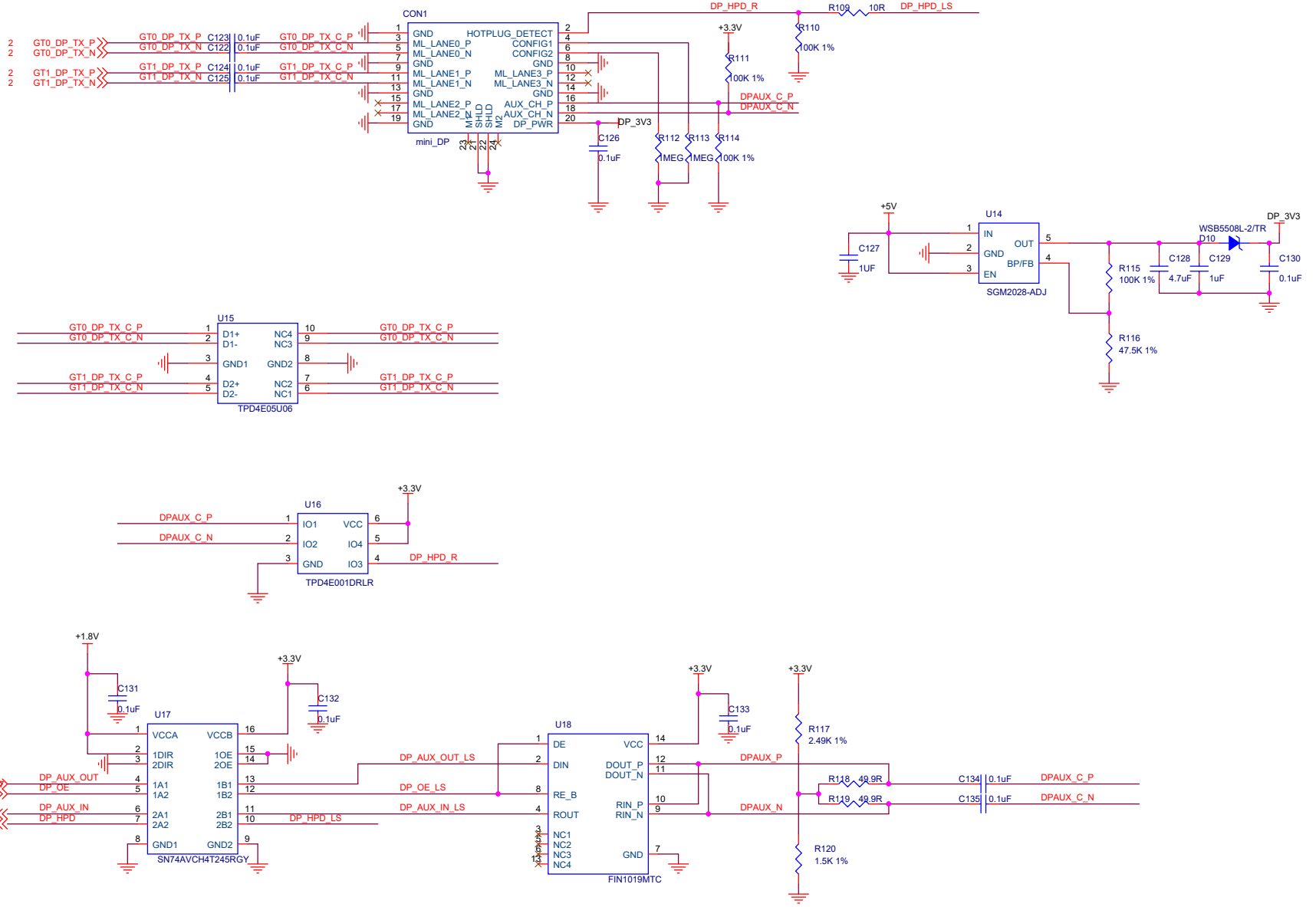

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ALINX	www.alinx.com
Title	08 Display Port
Size	Document Number: AXU15EG扩展板原理图
Rev	1.0
Date:	Friday, November 20, 2020
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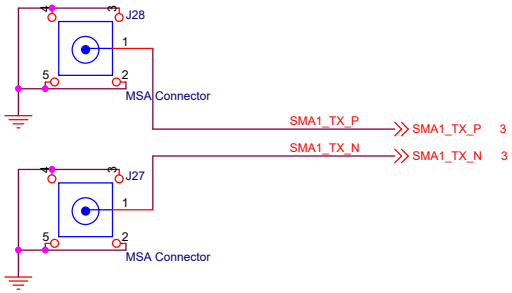

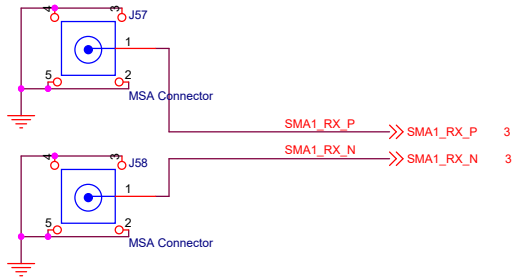

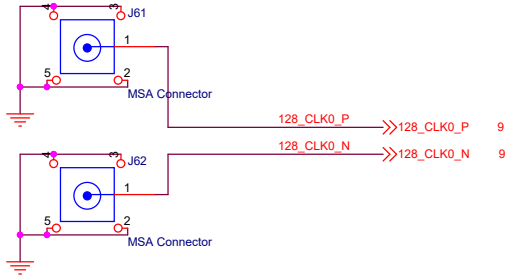

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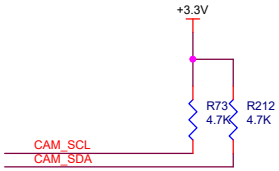
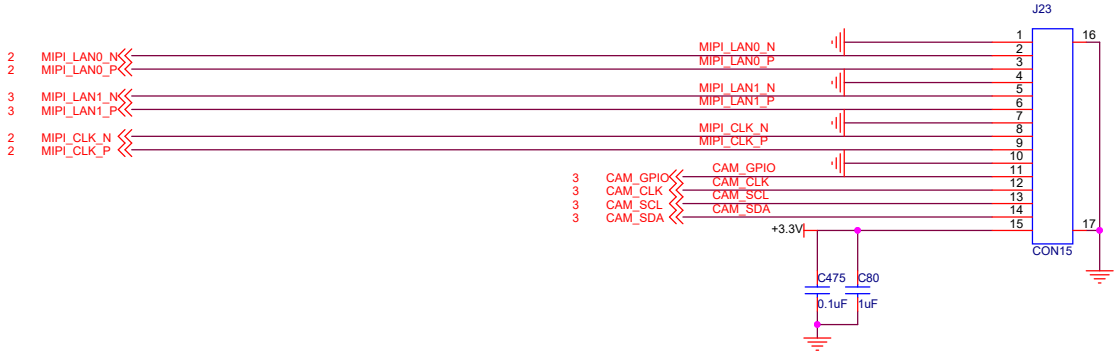
ALINX http://www.alinx.com		
Title: 09 SMA GTH		
Size: A3	Document Number: AXU15EG扩展板原理图	Rev: 1.0
Date: Friday, November 20, 2020	Sheet 9 of 24	



MIPI Interface



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Title: 10 MIPI		
Size: A3	Document Number: AXU15EG扩展板原理图	Rev: 1.0
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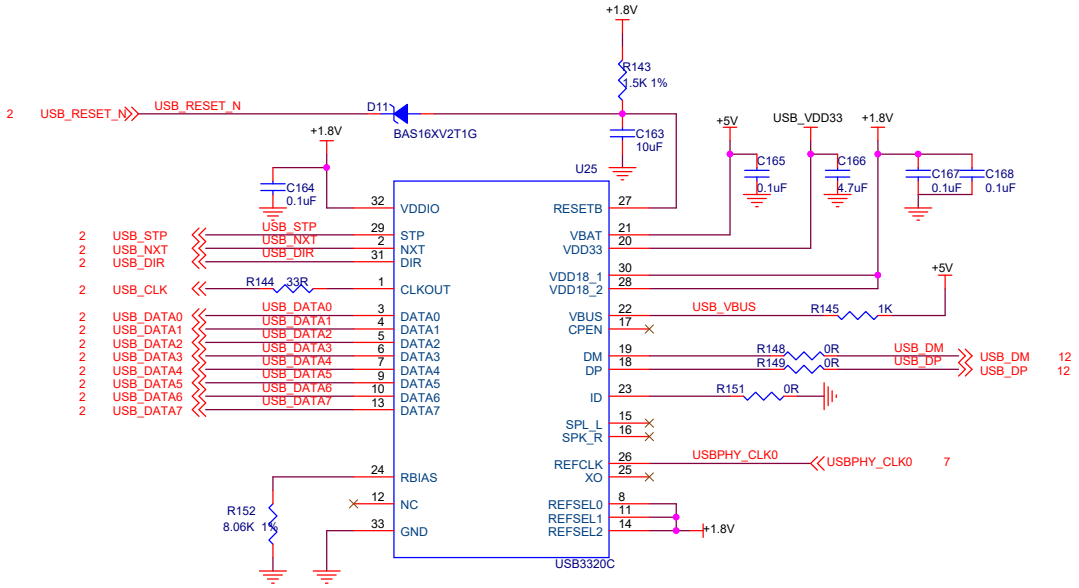

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Title: 11 USB2.0 PHY		
Size: A3	Document Number: AXU15EG扩展板原理图	Rev: 1.0
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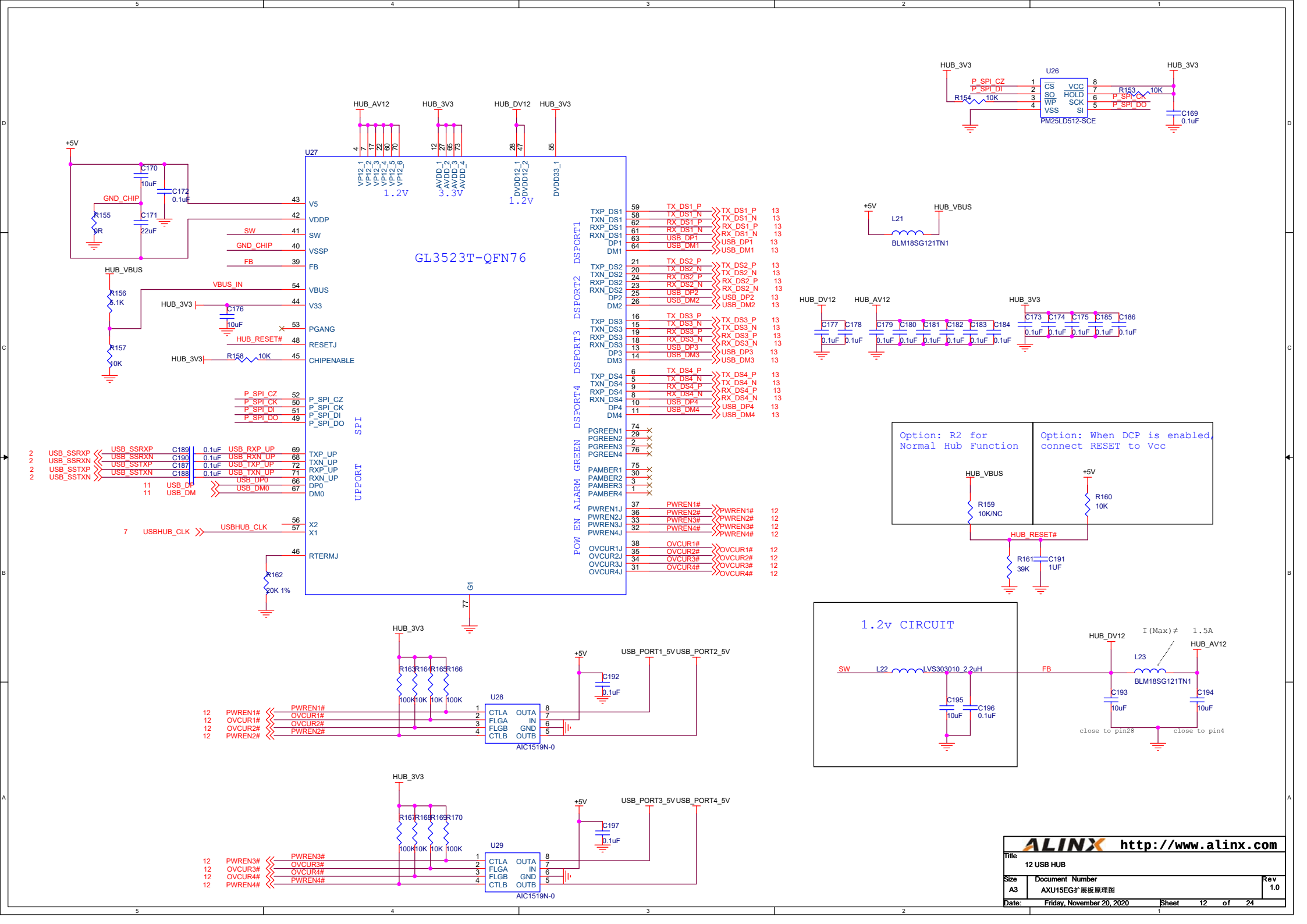

ALINX http://www.alinx.com		
Title: 12 USB HUB		
Size: A3	Document Number: AXU15EG扩展板原理图	Rev: 1.0
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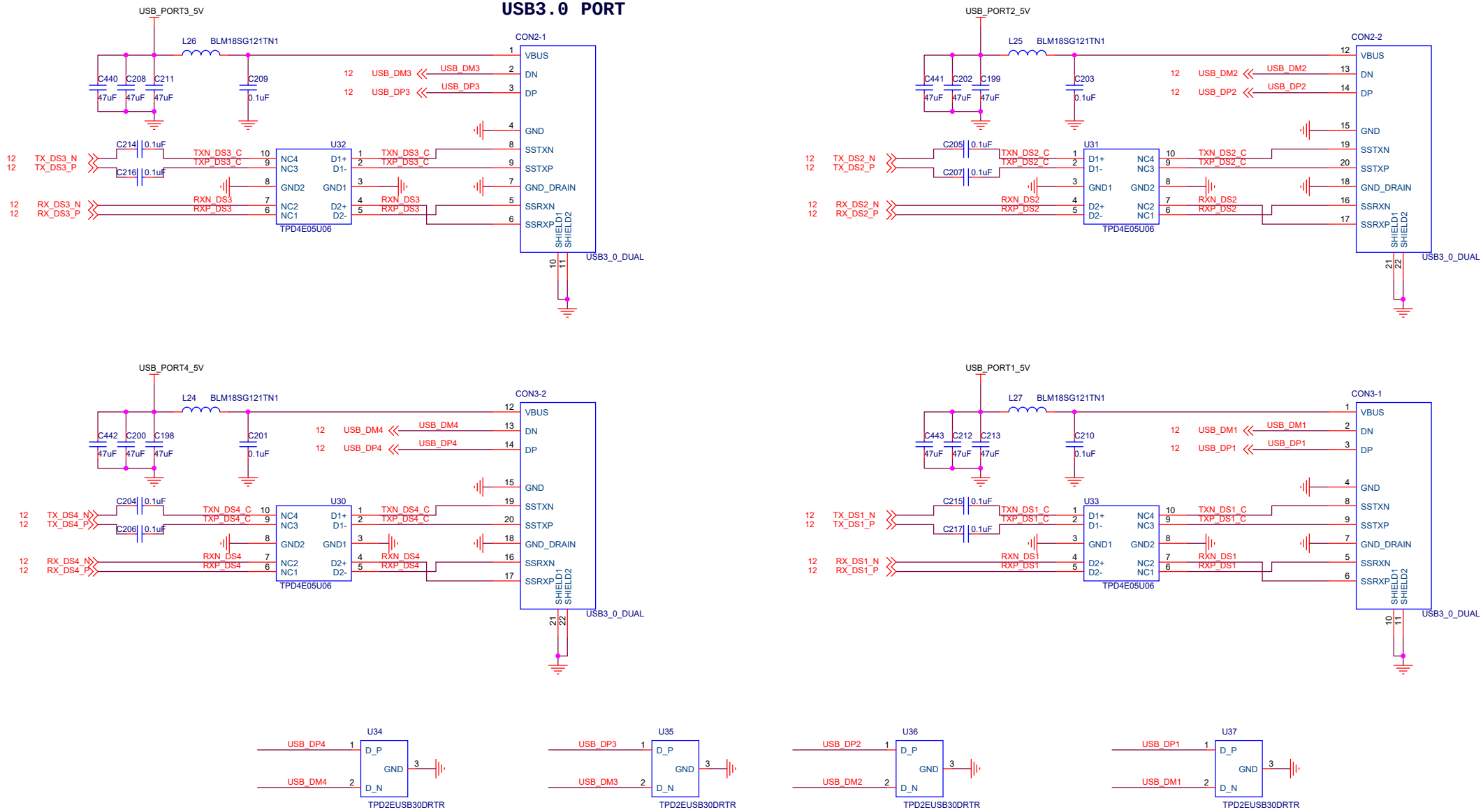
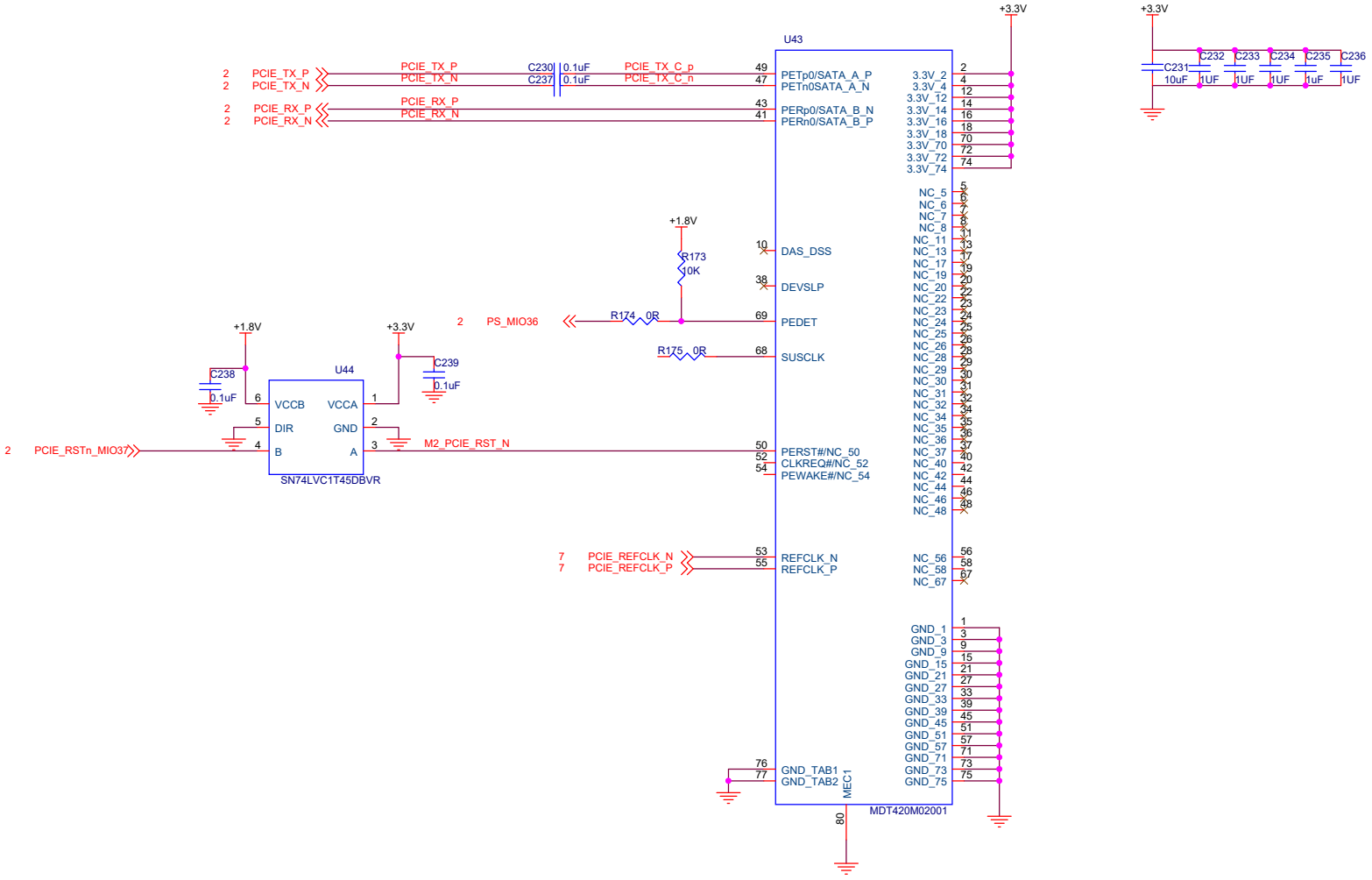
USB3.0 PORT



ALINX http://www.alinx.com

Title	13 USB CONN	
Size A3	Document Number AXU15EG扩展板原理图	Rev 1.0
Date: Friday, November 20, 2020	Sheet 13 of 24	



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ALINX http://www.alinx.com		
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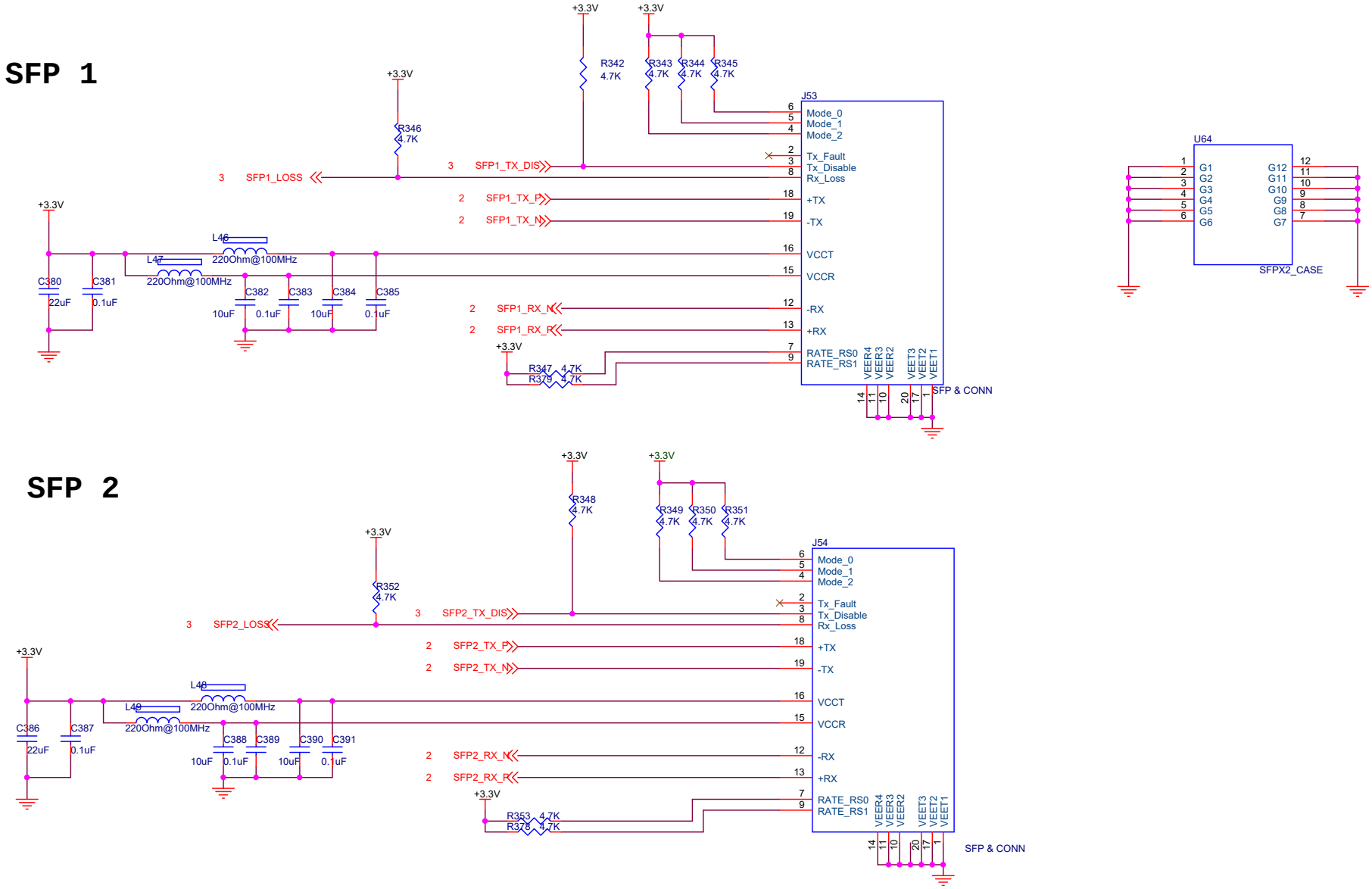
Size: A3	Document Number: AXU15EG扩展板原理图	Rev: 1.0
Date: Friday, November 20, 2020	Sheet 14 of 24	



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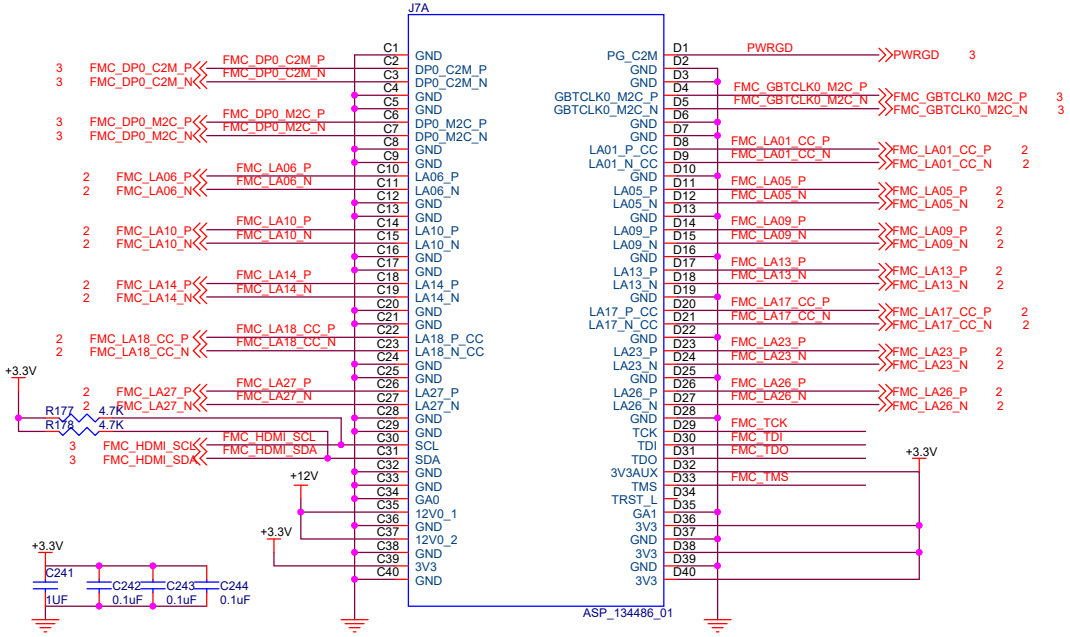
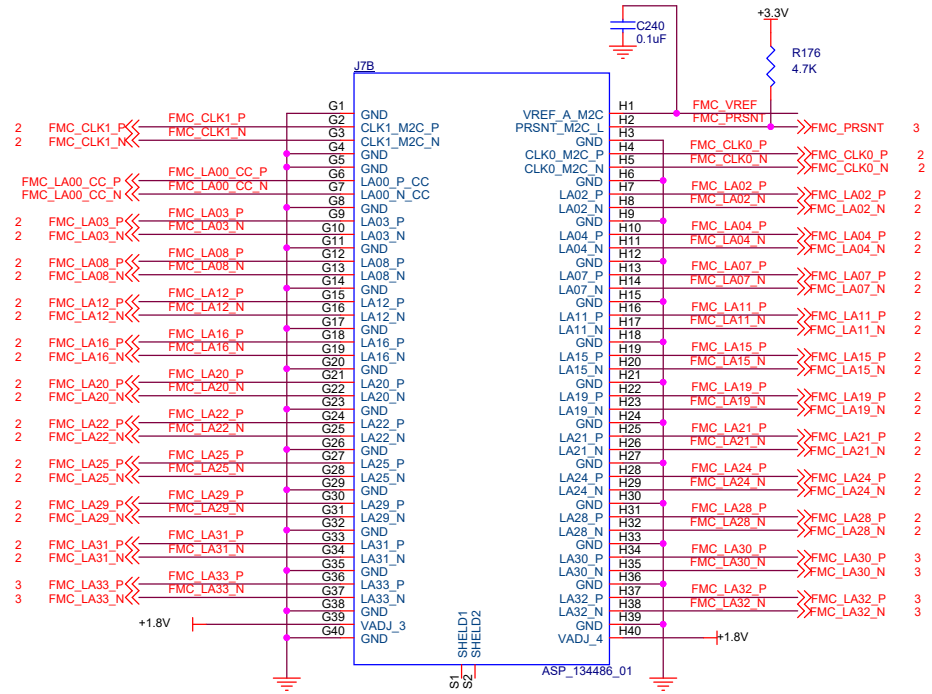
ALINX www.alinx.com

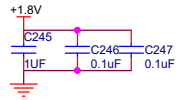
Title: 15 SFP1 & SPF2

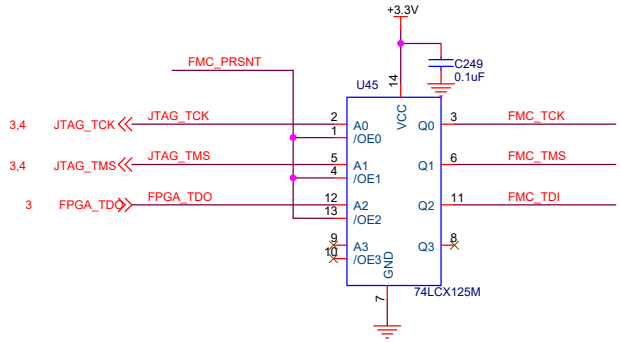
Size | Document Number: AXU15EG扩展板原理图 | Rev: 1.0

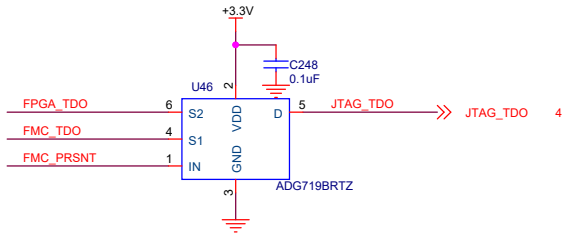
Date: Friday, November 20, 2020 | Sheet 15 of 24











FMC_PRSNT = 1: JTAG_TDO = FPGA_TDO

FMC_PRSNT = 0: JTAG_TDO = FMC_TDO

ALINX http://www.alinx.com

Title: 16 FMC_LPC		
Size: A3	Document Number: AXU15EG扩展板原理图	Rev: 1.0
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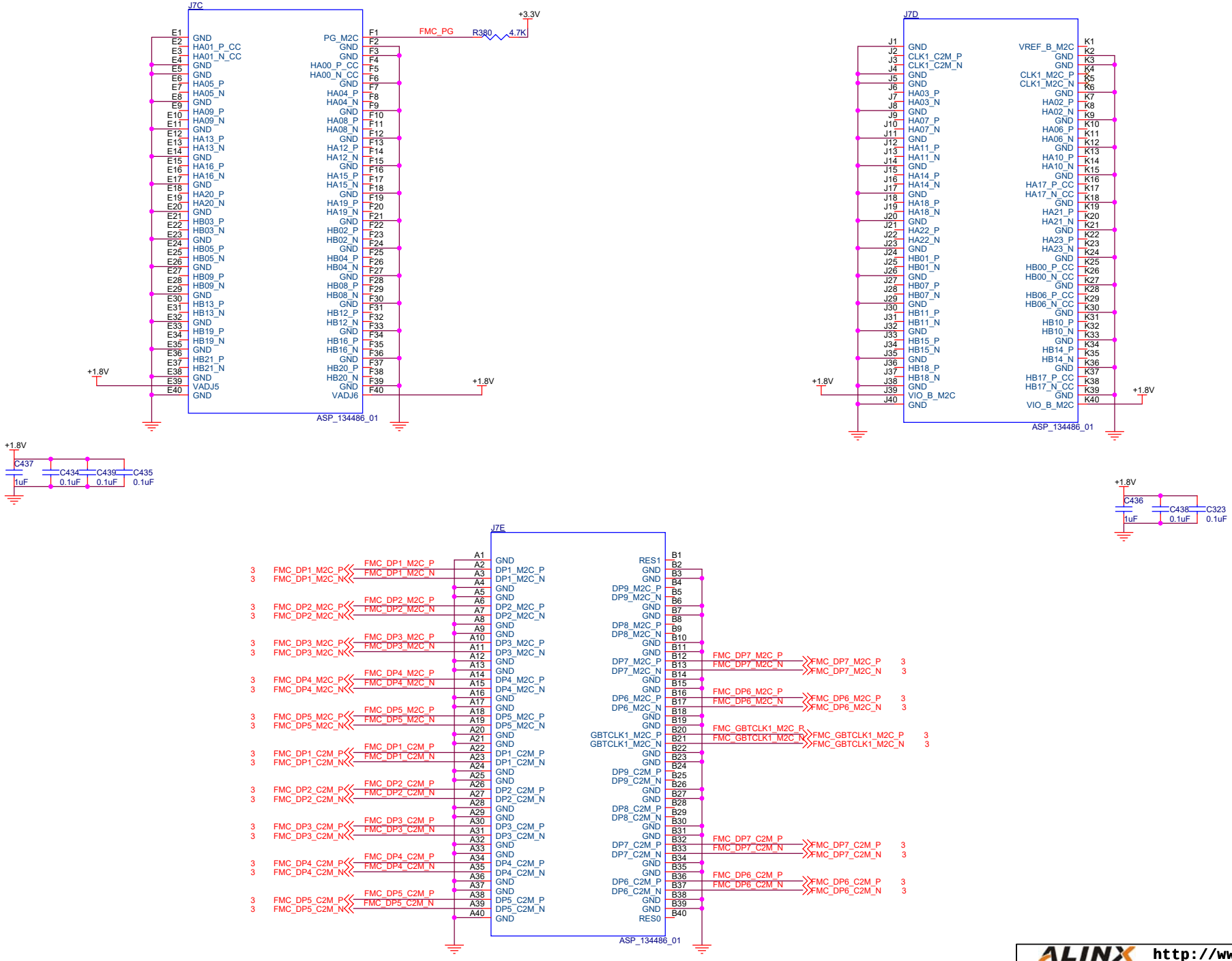

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ALINX http://www.alinx.com		
Title: 16 FMC_LPC		
Size: A3	Document Number: AXU15EG扩展板原理图	Rev: 1.0
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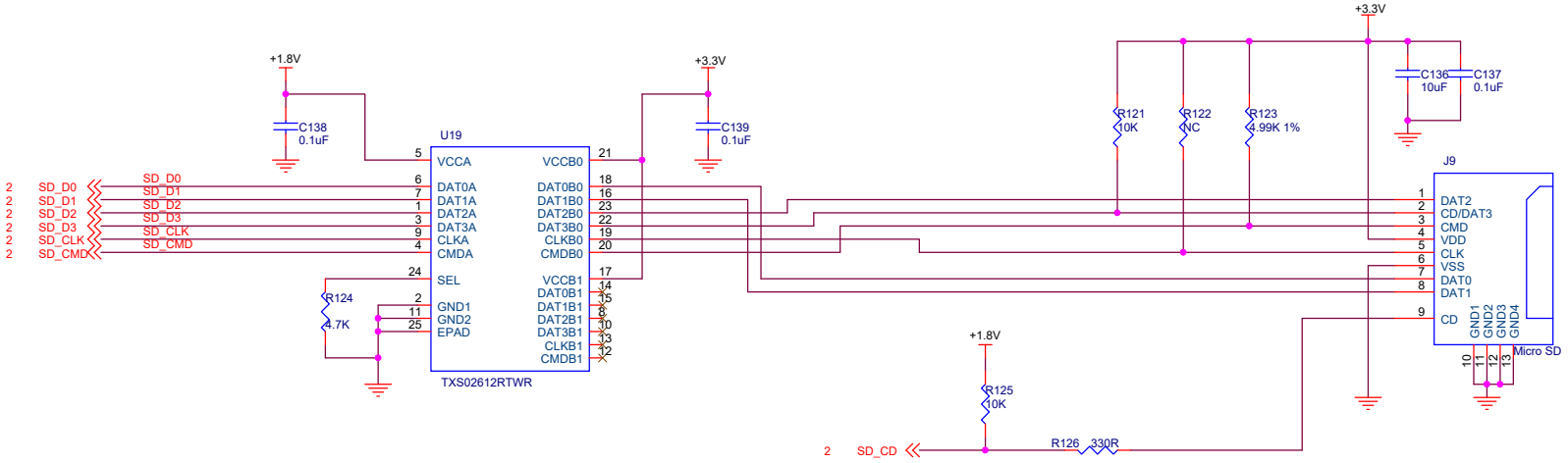
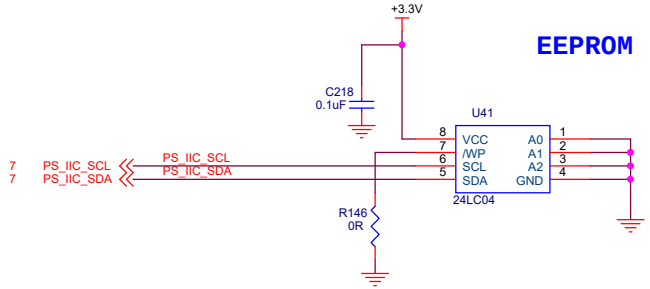
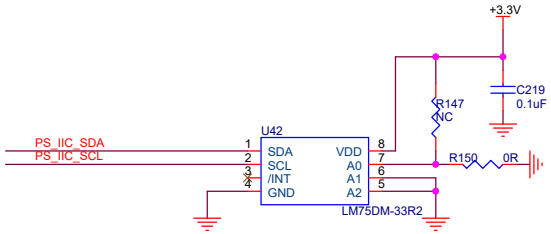
SD Card Connector



EEPROM



SENSOR



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ALINX http://www.alinx.com		
Title: 17 SD CARD		
Size: A3	Document Number: AXU15EG扩展板原理图	Rev: 1.0
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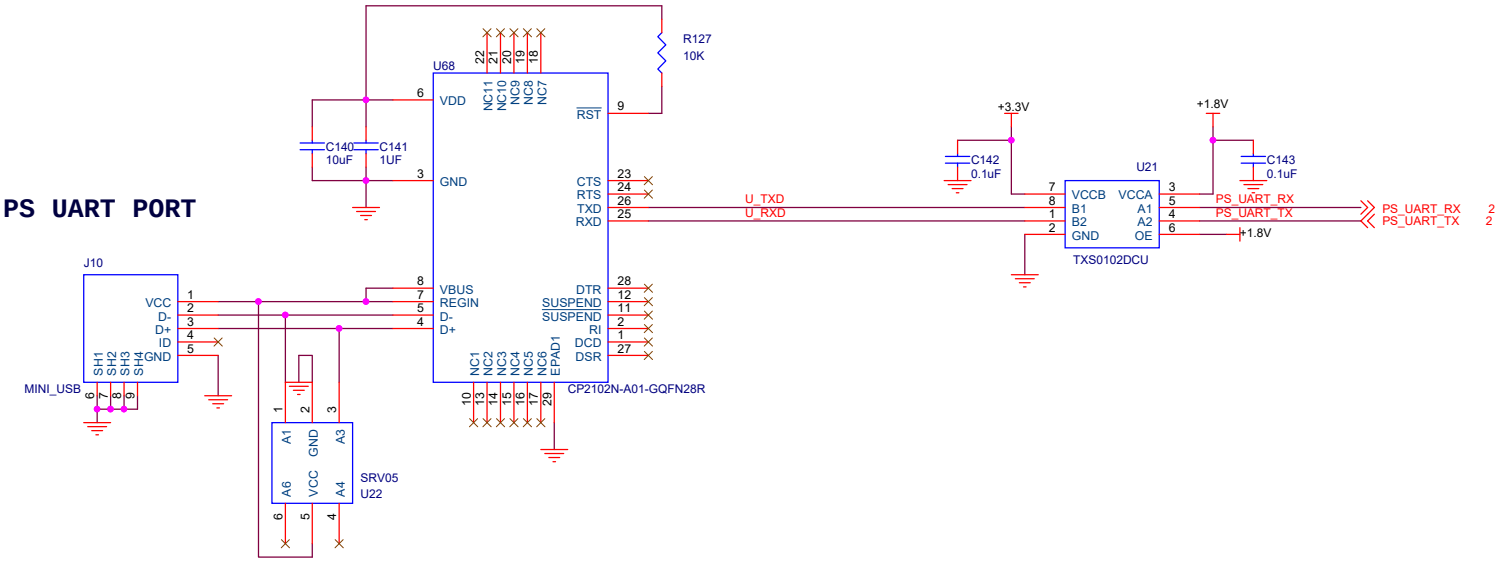
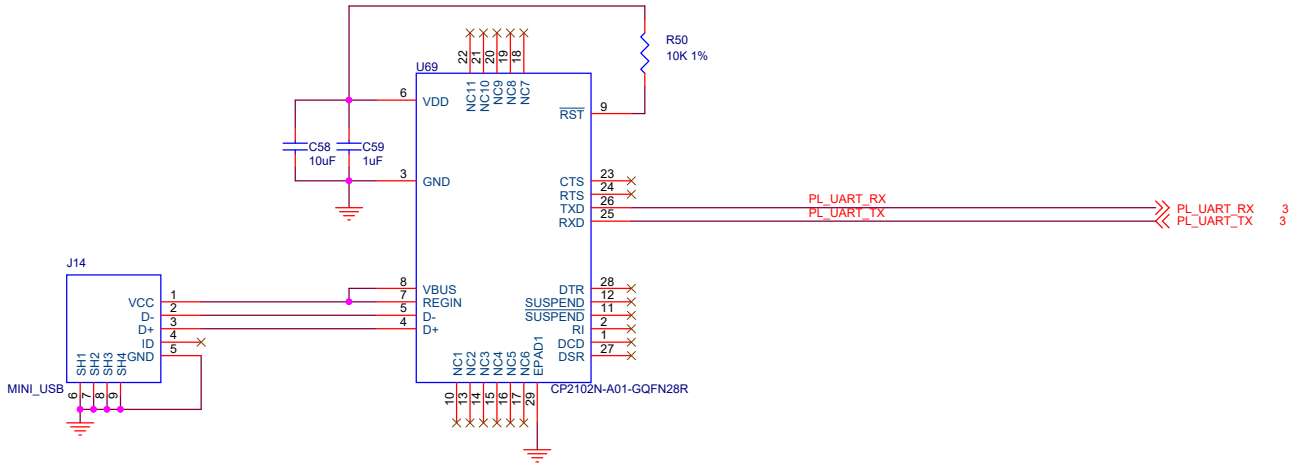
PS UART PORT



PL UART PORT



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ALINX http://www.alinx.com		
Title: 18 PS UART		
Size: A3	Document Number: AXU15EG扩展板原理图	Rev: 1.0
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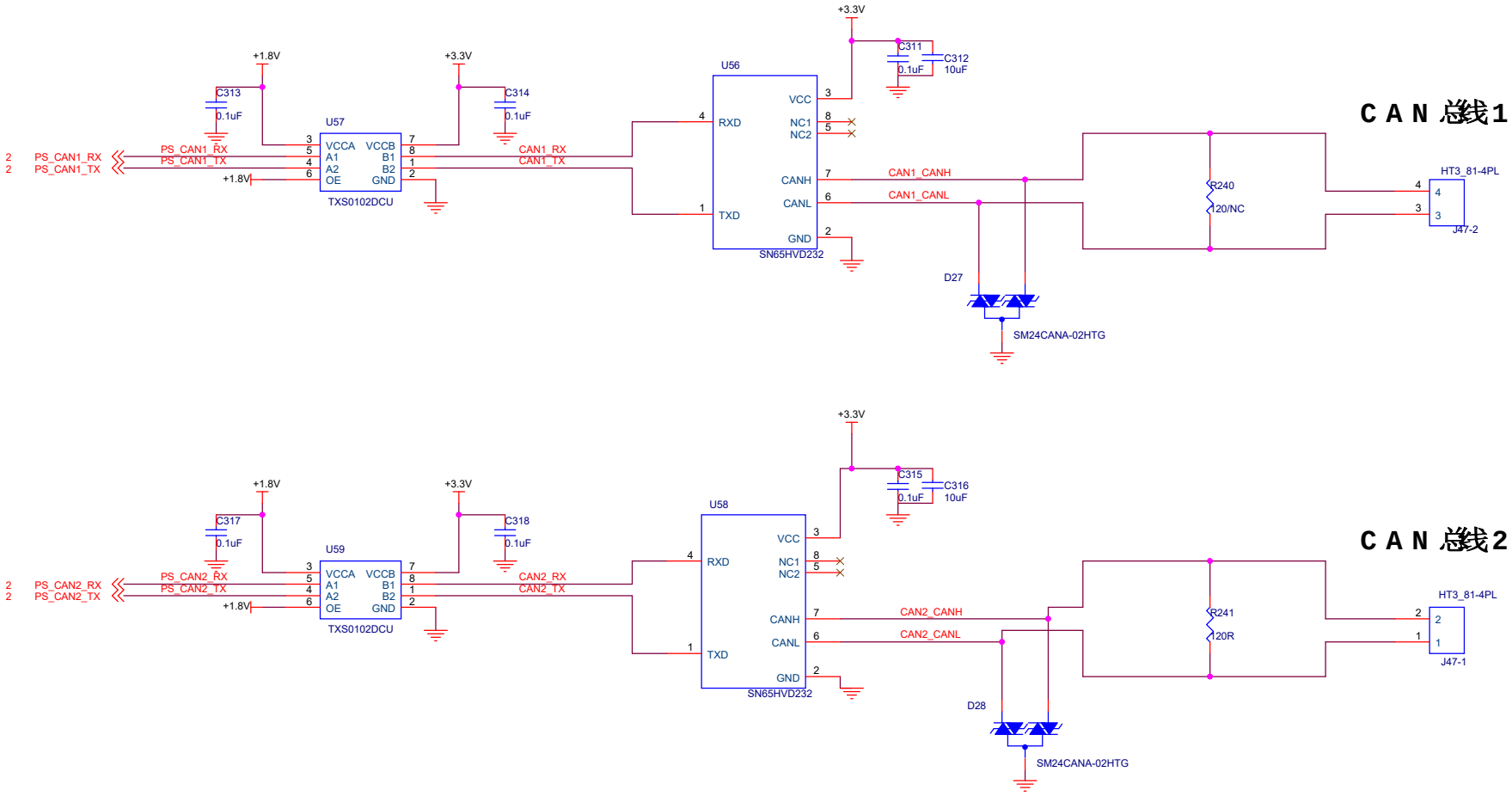

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ALINX http://www.alinx.com		
Title: 19 CAN_1 & CAN_2		
Size: A3	Document Number: AXU15EG扩展板原理图	Rev: 1.0
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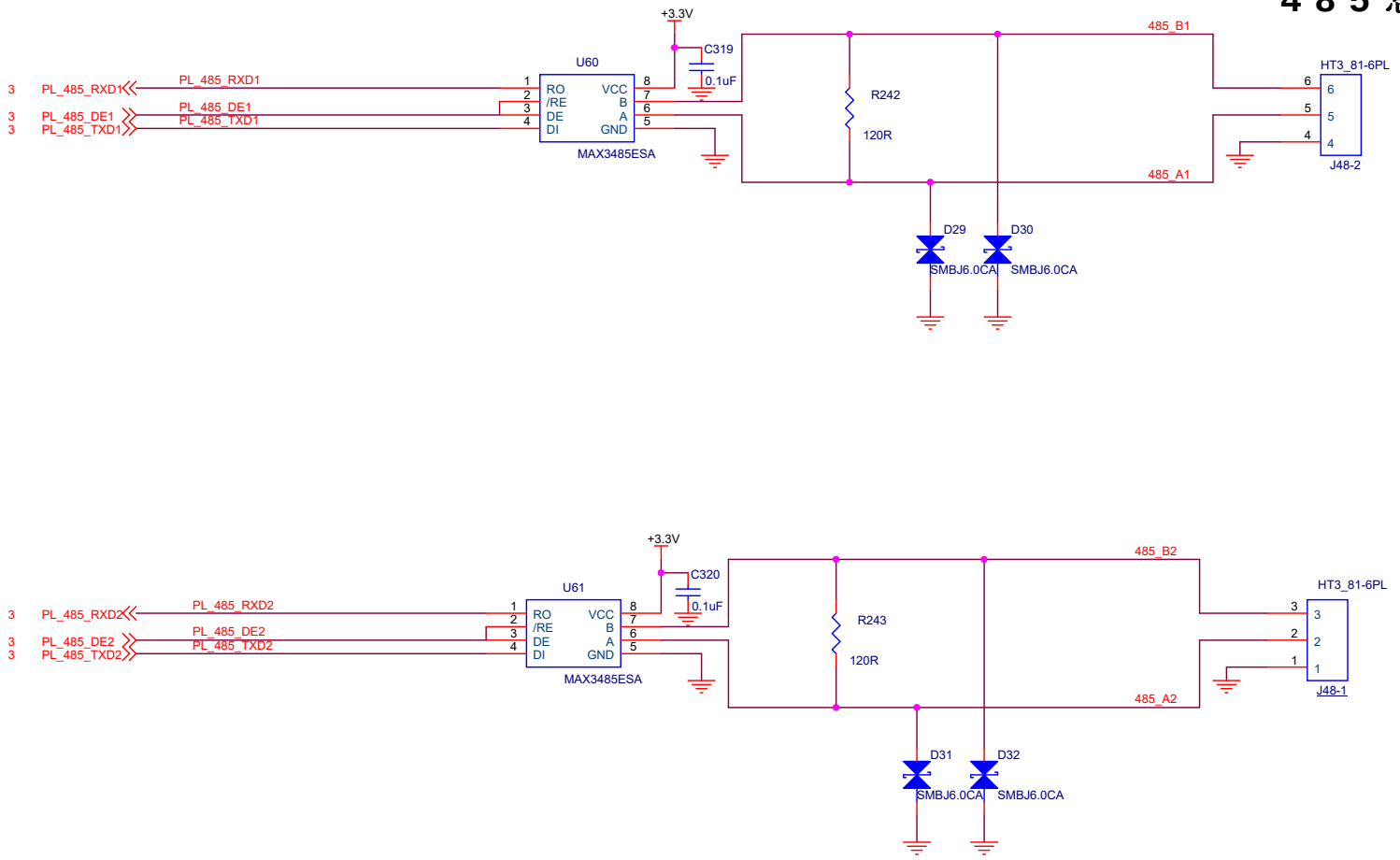

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ALINX	http://www.alinx.com
Title	20 RS485_1 & RS485_2
Size	Document Number: AXU15EG扩展板原理图
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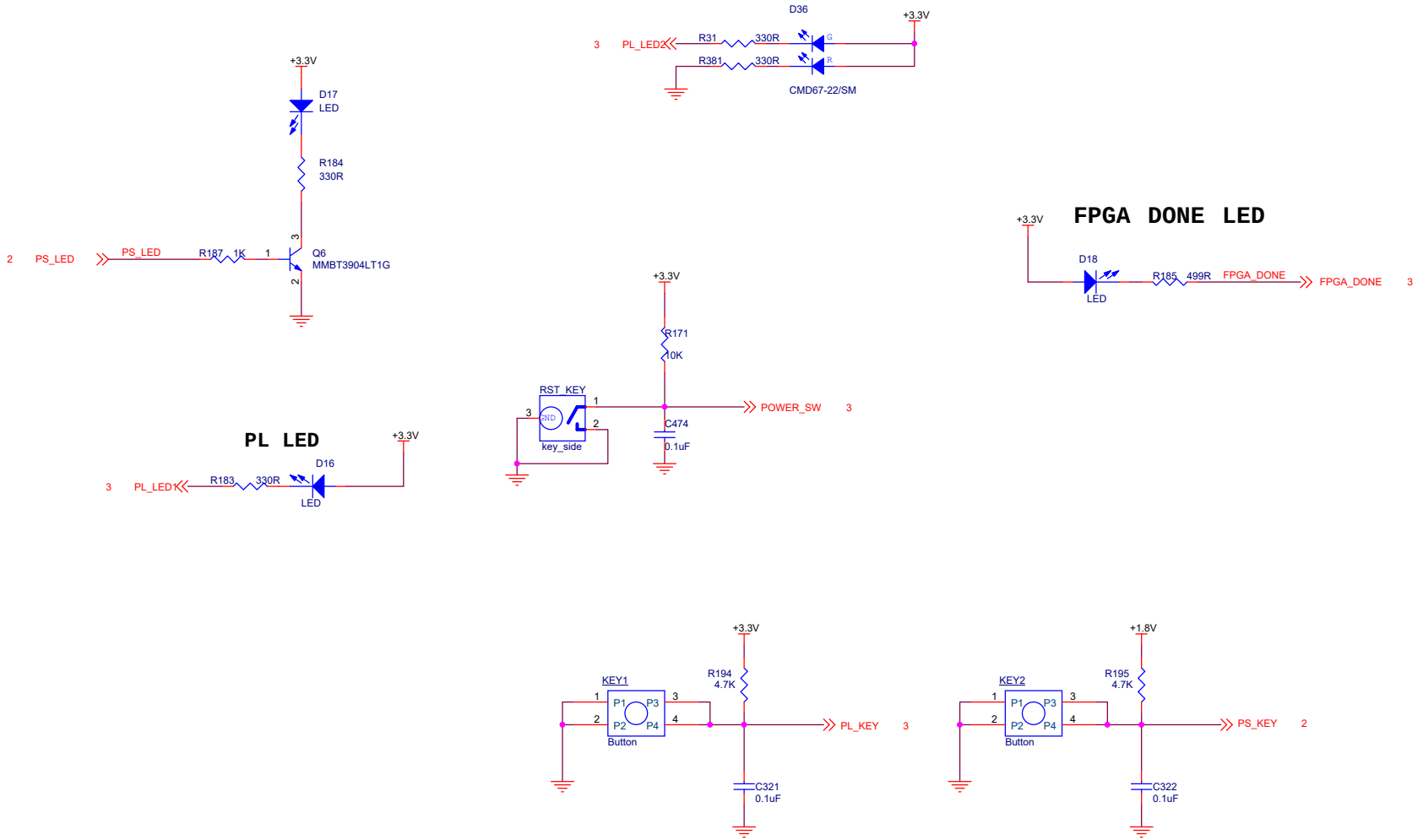

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ALINX http://www.alinx.com		
Title: 21 LED & KEY		
Size: A3	Document Number: AXU15EG扩展板原理图	Rev: 1.0
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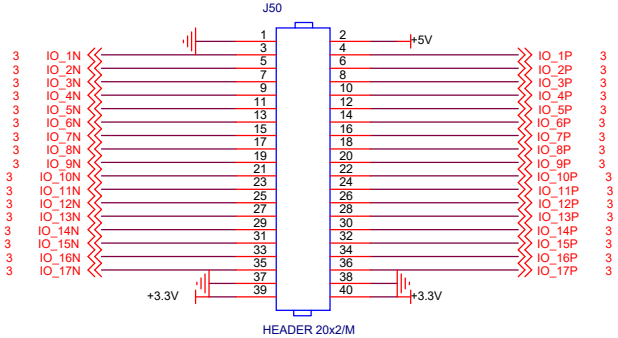

ALINX http://www.alinx.com

Title: 22 IO Expansion header		
Size: A3	Document Number: AXU15EG扩展板原理图	Rev: 1.0
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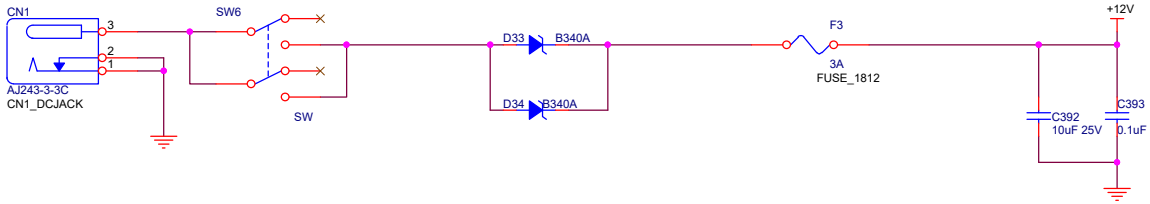
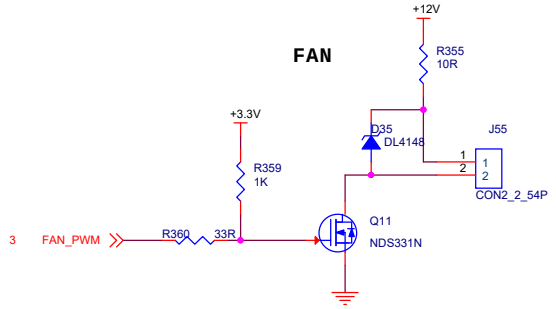
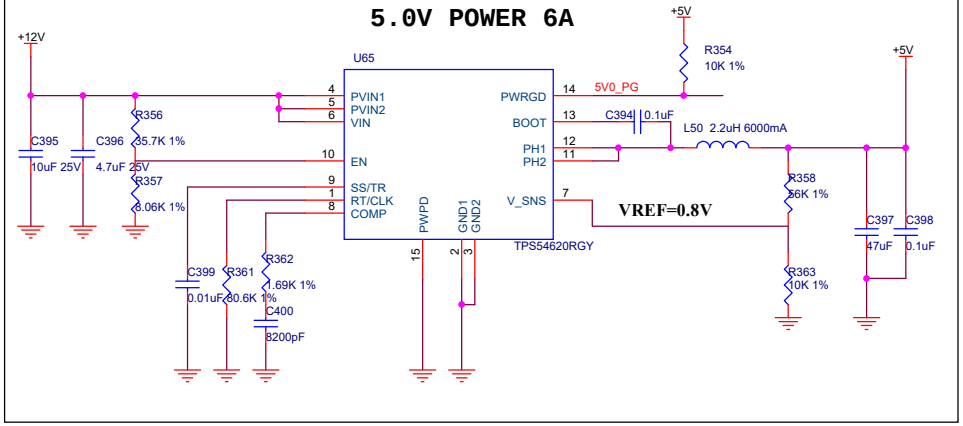
12V/3A AC Adapter



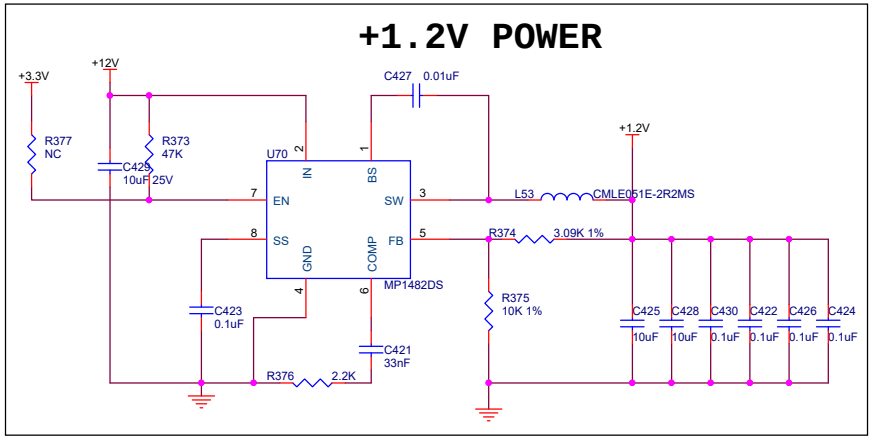
FAN



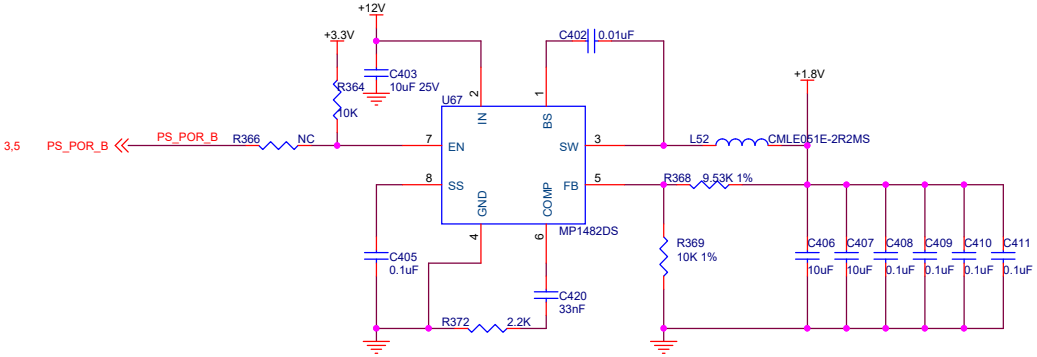
5.0V POWER 6A



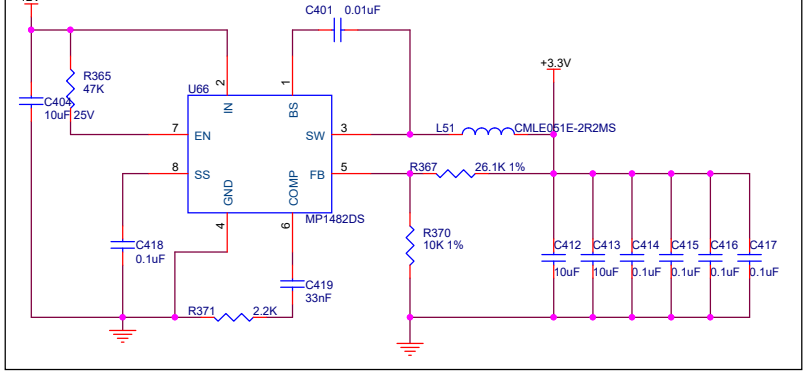
+1.2V POWER



+1.8V POWER



+3.3V POWER



M1 MARK M2 MARK M3 MARK M4 MARK M5 MARK M6 MARK M7 MARK M8 MARK

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