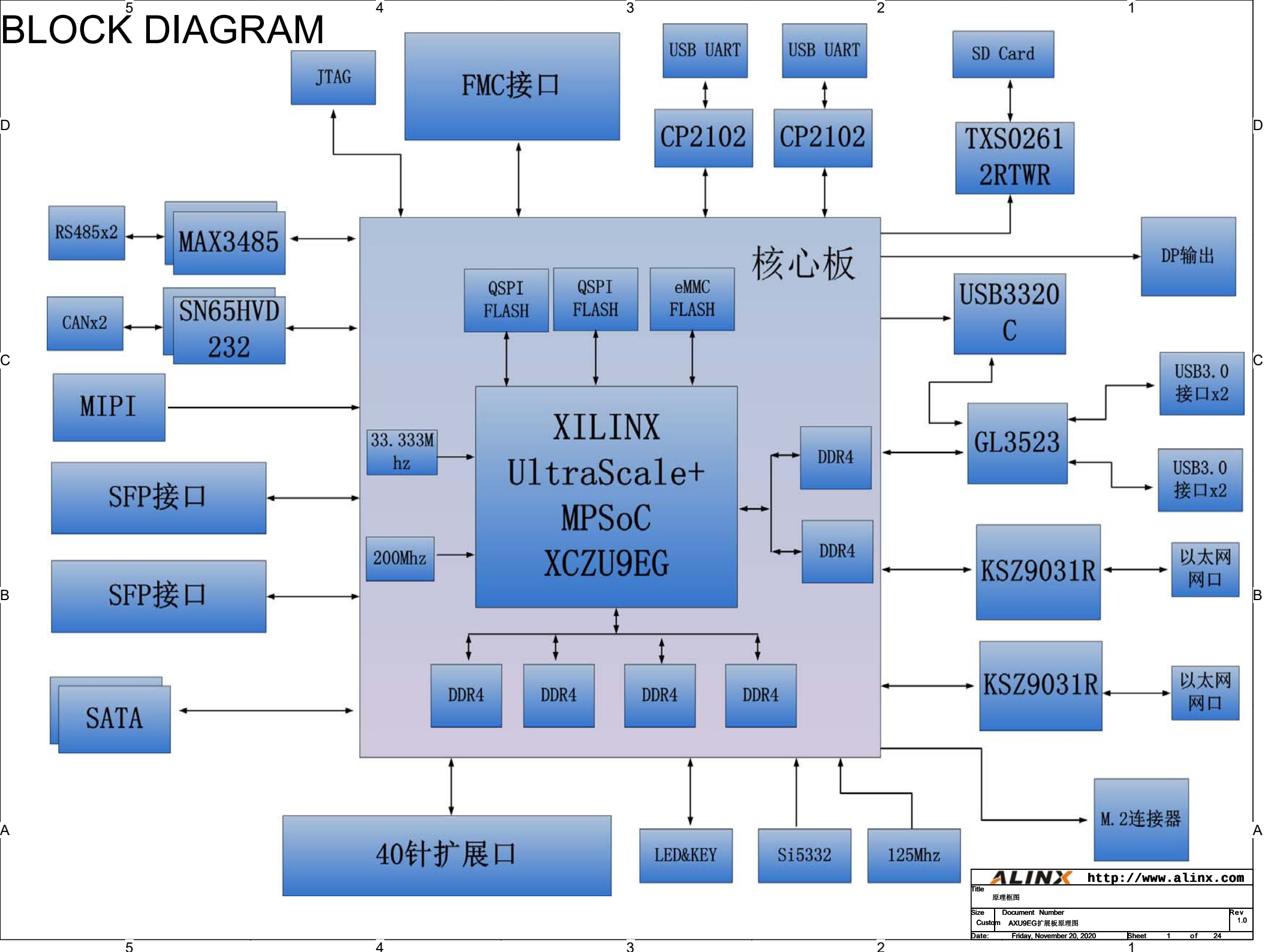
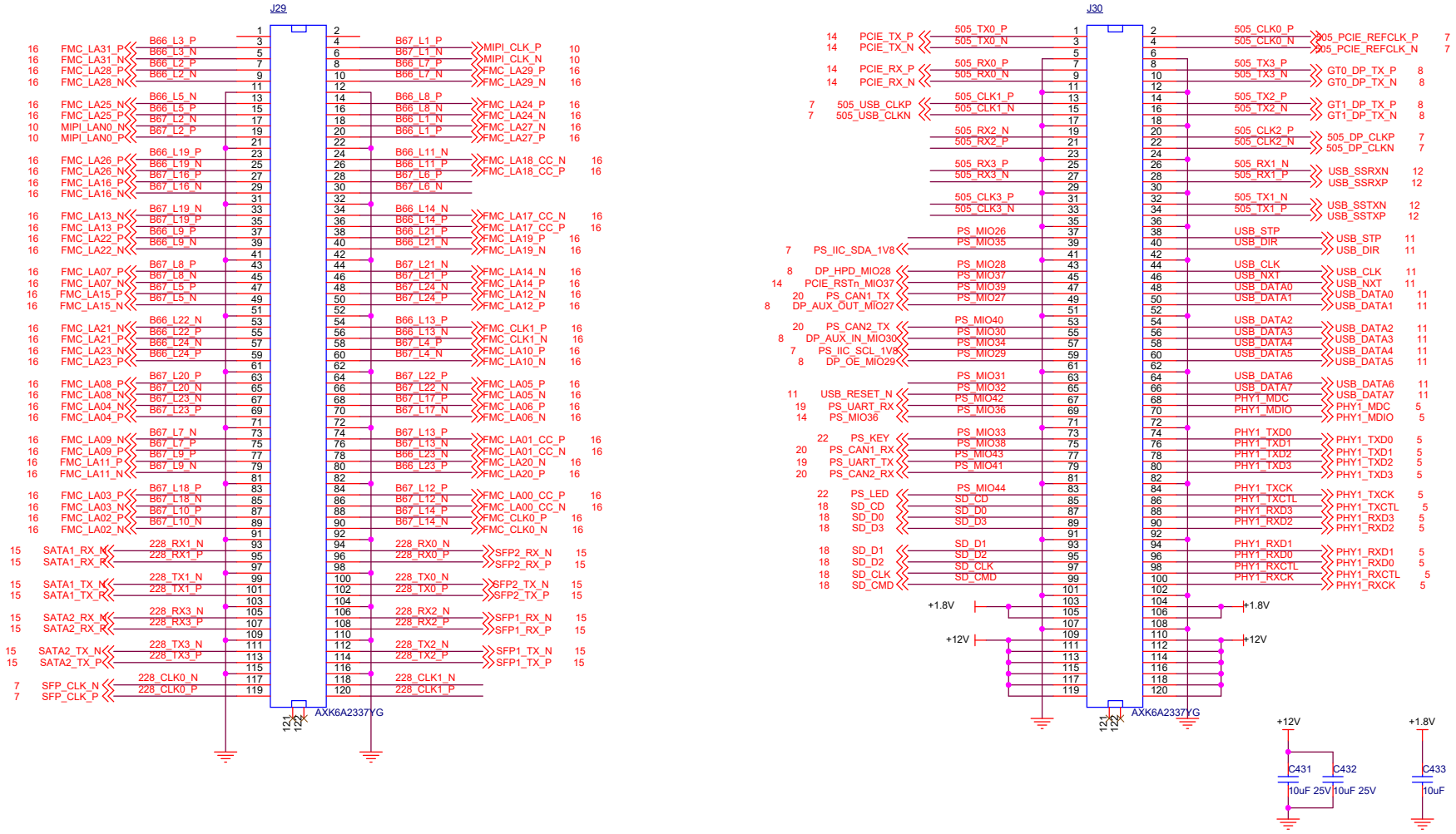
BLOCK DIAGRAM



ALINX http://www.alinx.com

Title	原理框图	
Size	Document Number	Rev
Custom	AXU9EG扩展板原理图	1.0
Date:	Friday, November 20, 2020	Sheet 1 of 24



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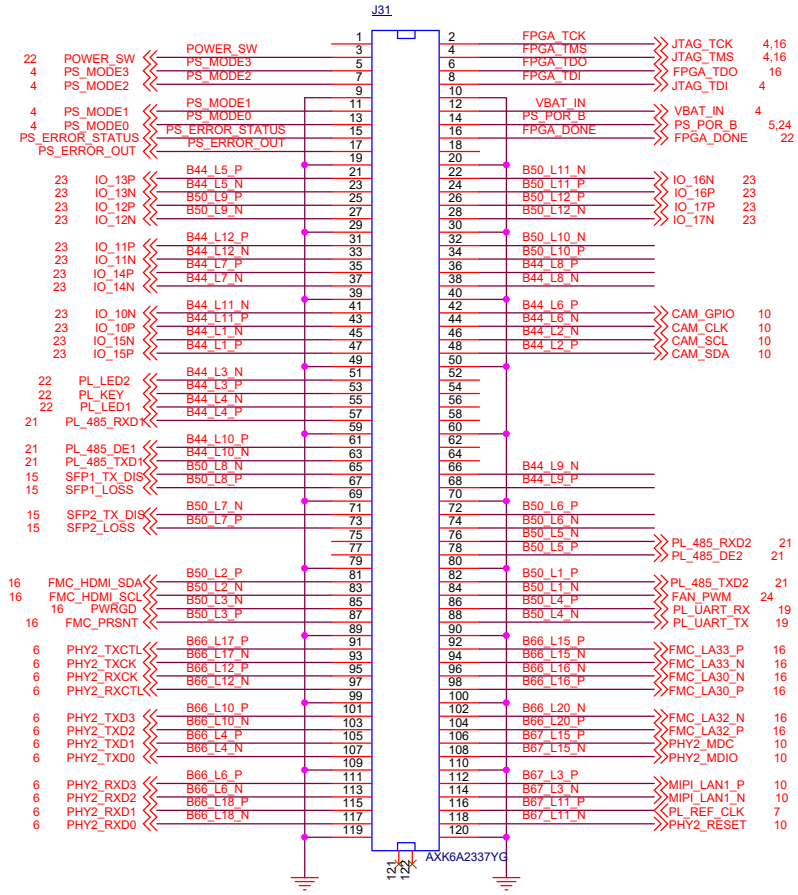
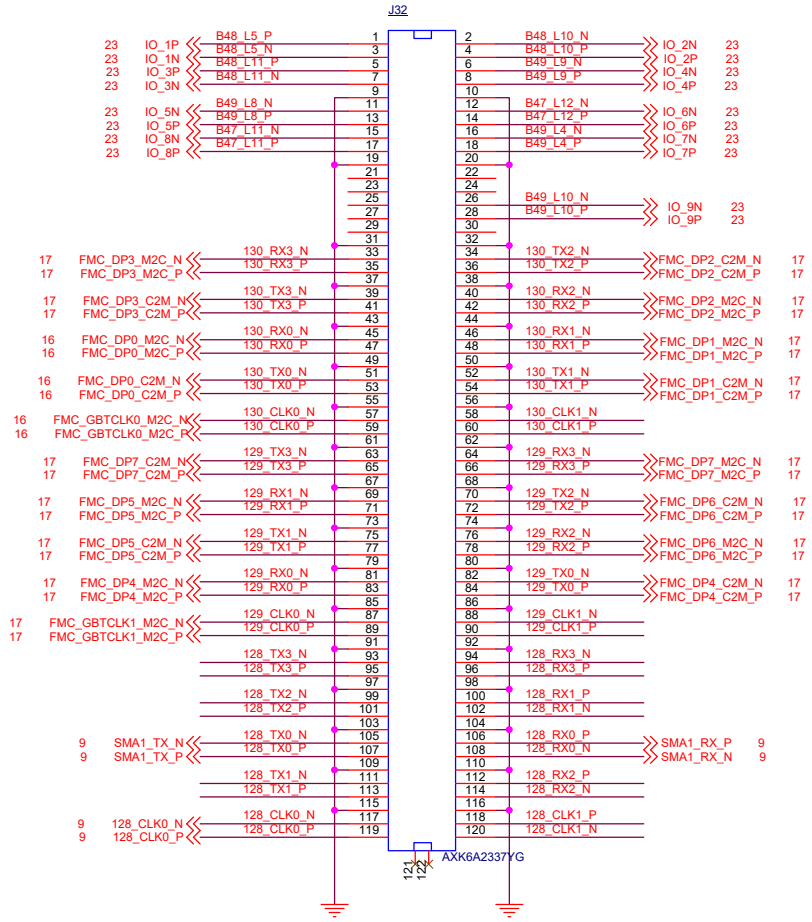
ALINX http://www.alinx.com		
Title: 02 Connector_1		
Size: A3	Document Number: AXU9EG扩展板原理图	Rev: 1.0
Date: Wednesday, September 02, 2020	Sheet 2 of 24	





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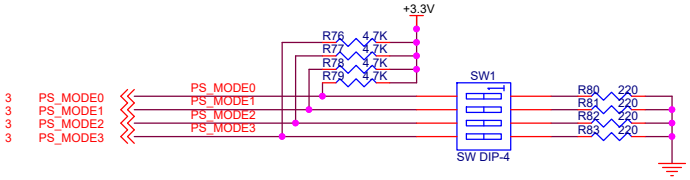
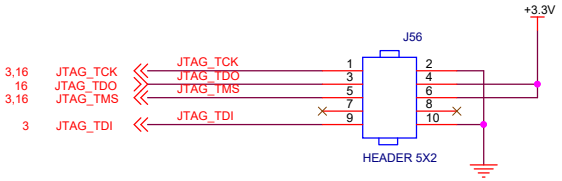
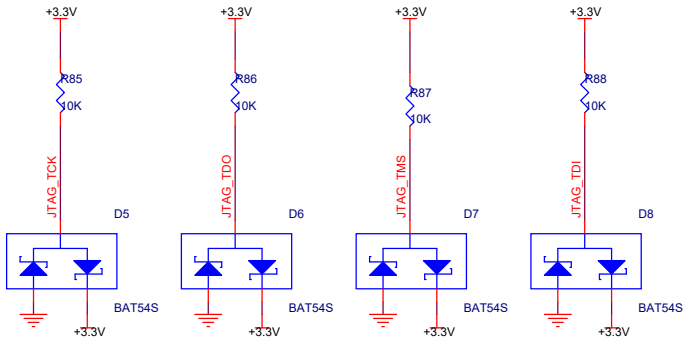
Title	03 Connector_2	
Size A3	Document Number AXU9EG扩展板原理图	Rev 1.0
Date: Friday, September 04, 2020	Sheet 3 of 24	

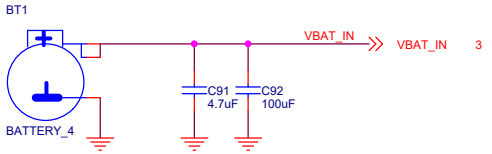


MODE[3:0]	BOOT MODE	Descritpion
0000	PS JTAG	PS JTAG Interface
0001	Quad_SPI(24b)	24-Bit addresssing(QSPI24)
0010	Quad_SPI(32b)	32-Bit addresssing(QSPI32)
0011	SD0(2.0)	SD2.0
0100	NAND	Requires 8-bit data bus width
0101	SD1(2.0)	SD2.0
0110	eMMC(1.8V)	eMMC version 4.5 at 1.8V
0111	USB0(2.0)	USB 2.0 only
1000	PJTAG(MIO #0)	PJTAG connection 0 option
1001	PJTAG(MIO #1)	PJTAG connection 1 option
1110	SD1 LS(3.0)	SD 3.0

JTAG Connector







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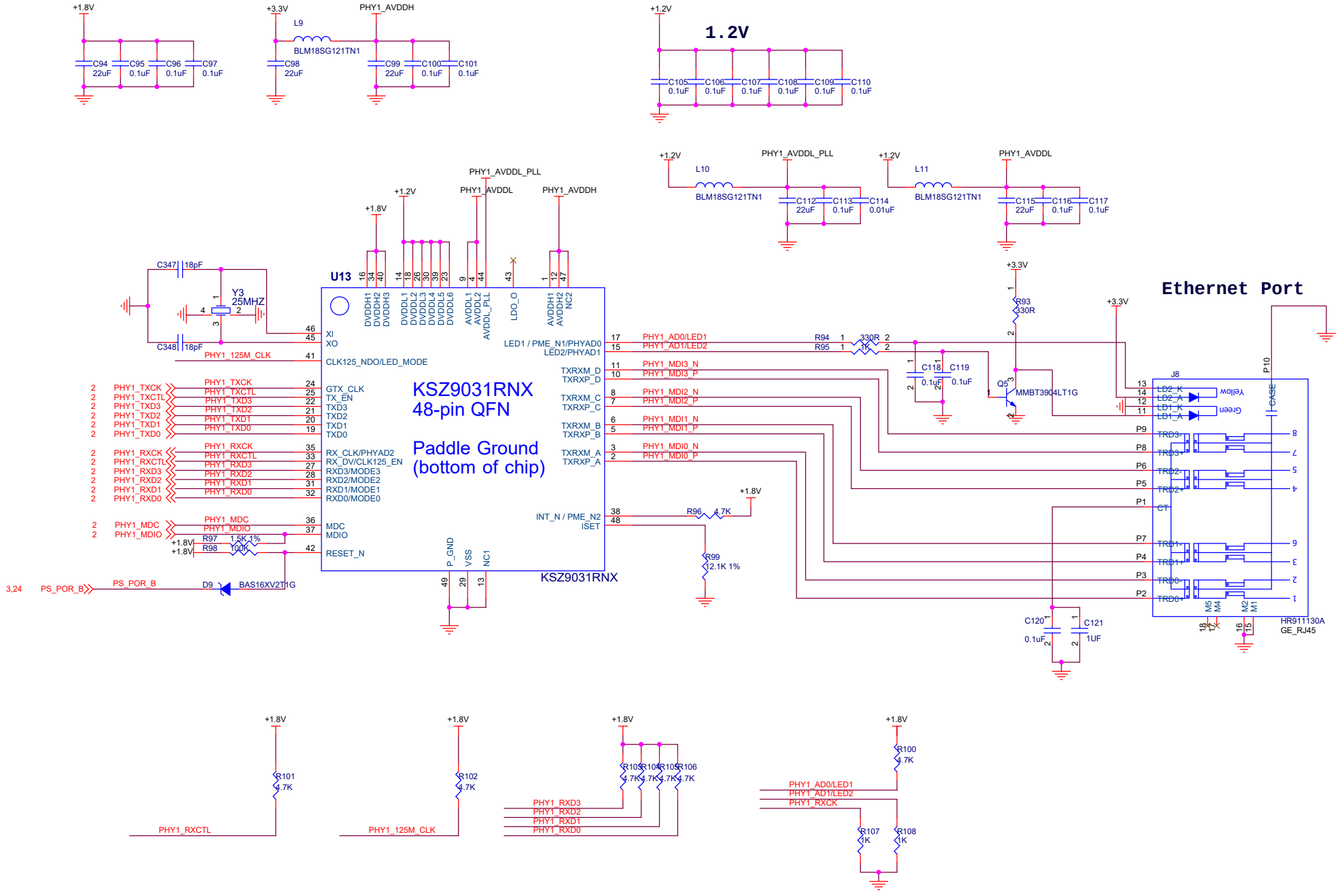
Title: 04 JTAG		
Size: A3	Document Number: AXU9EG扩展板原理图	Rev: 1.0
Date: Wednesday, September 02, 2020	Sheet 4 of 24	



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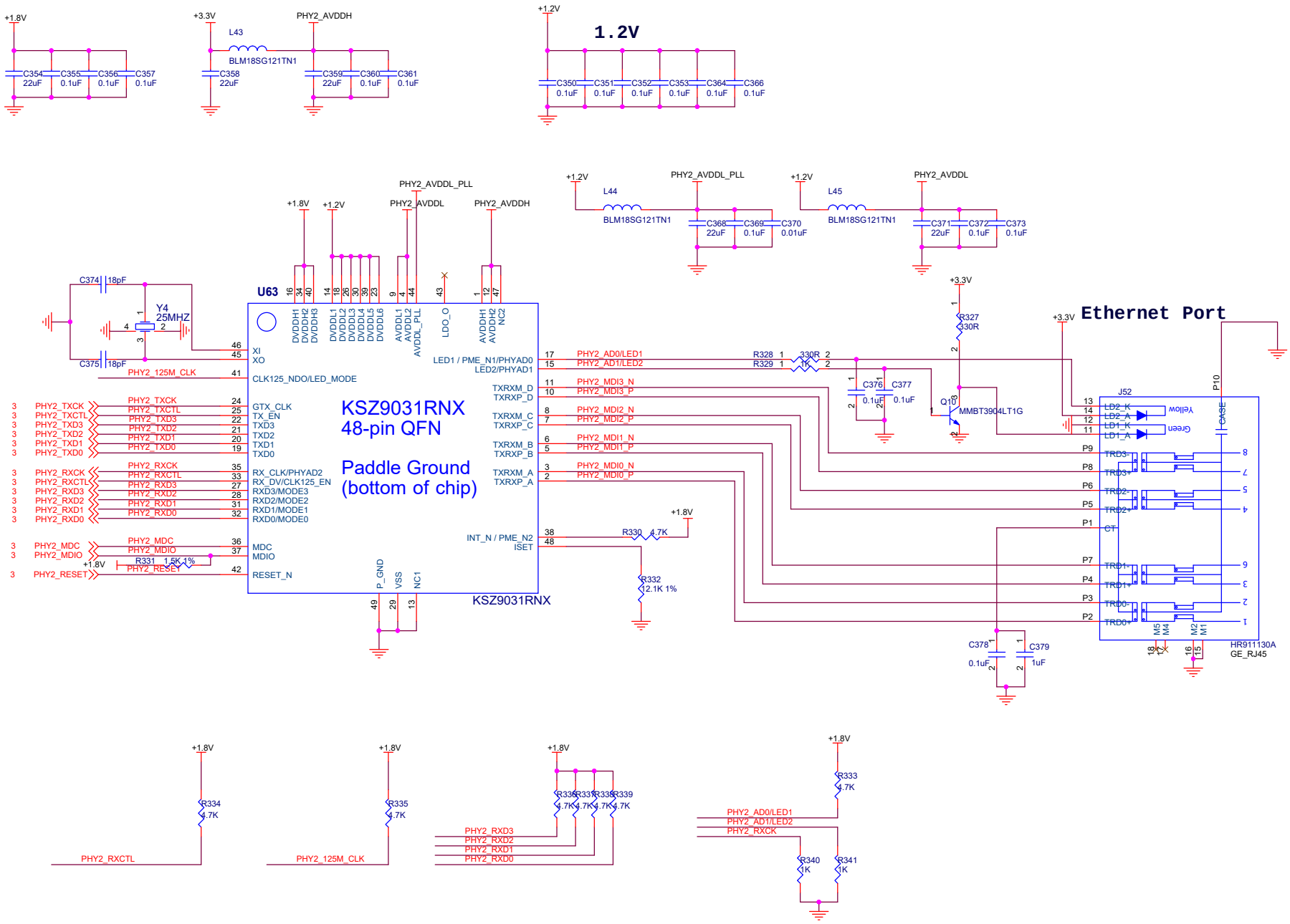
ALINX http://www.alinx.com		
Title: 05 PS Ethernet		
Size: A3	Document Number: AXU9EG扩展板原理图	Rev: 1.0
Date: Wednesday, September 02, 2020	Sheet 5 of 24	



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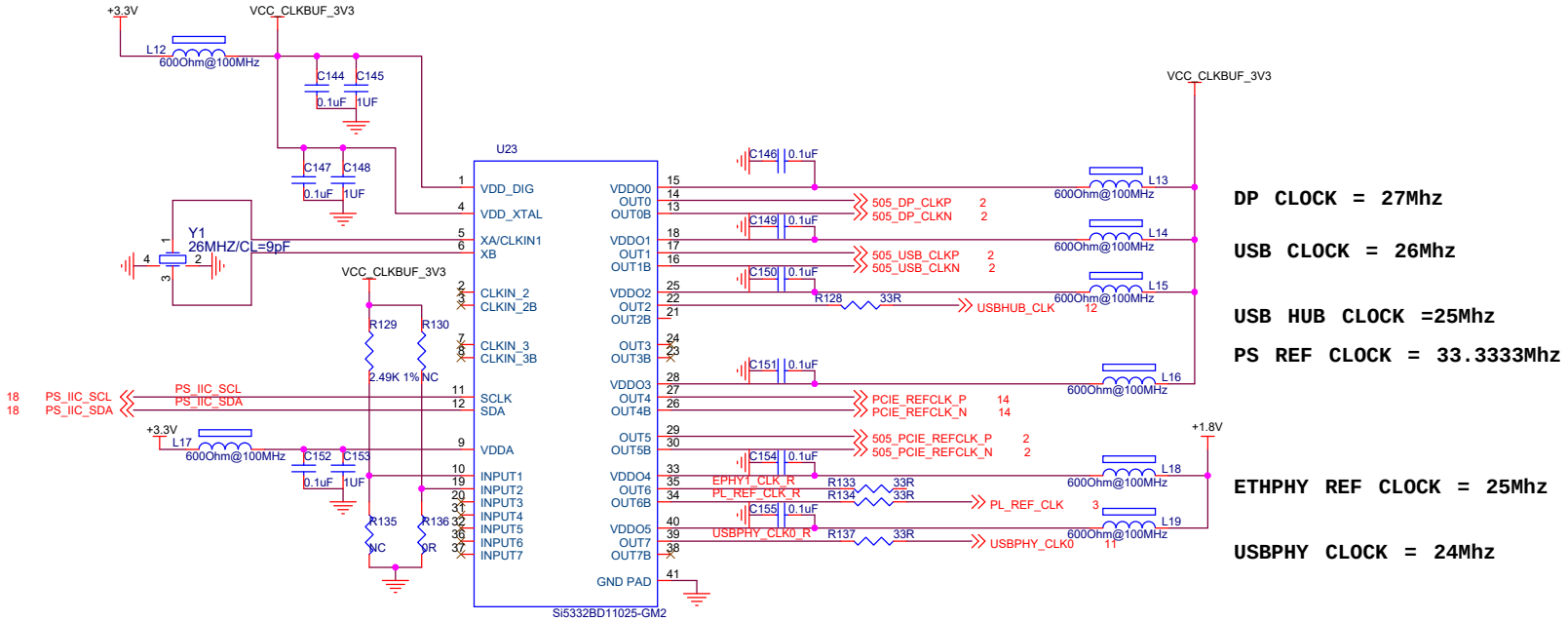
ALINX	www.alinx.com
Title	06 PL Ethernet2
Size	Document Number: AXU9EG扩展板原理图
Rev	1.0
Date:	Wednesday, September 02, 2020
Sheet	6 of 24



DP CLOCK = 27Mhz

USB CLOCK = 26Mhz

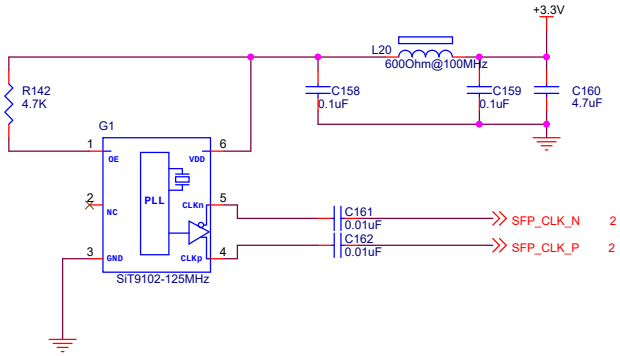
USB HUB CLOCK =25Mhz

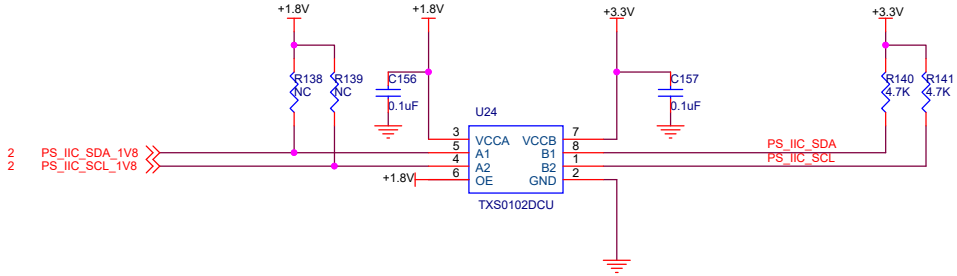
PS REF CLOCK = 33.3333Mhz

ETHPHY REF CLOCK = 25Mhz

USBPHY CLOCK = 24Mhz

PS_IIC1: IIC ADDRESS IS 0x76





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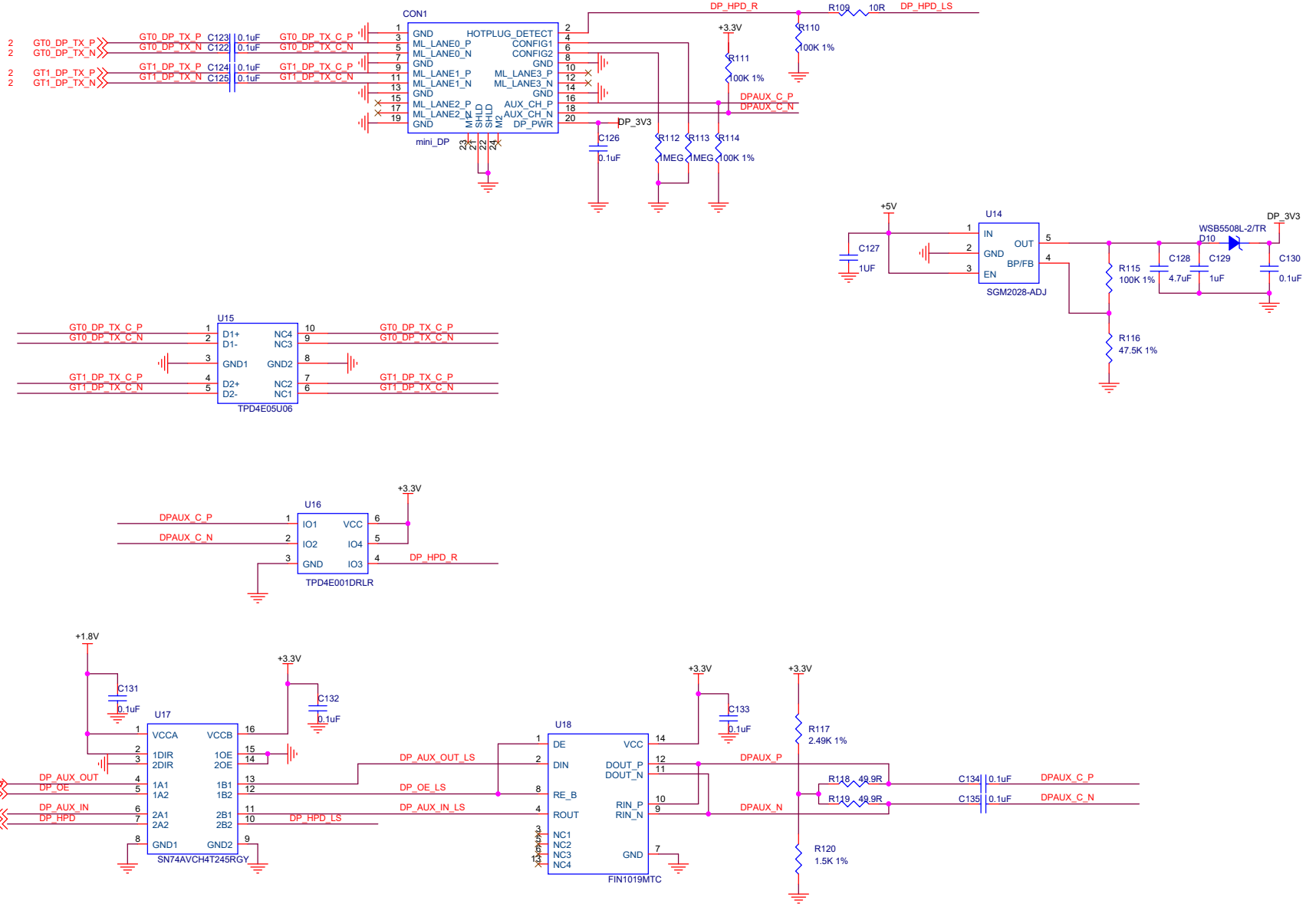
Title: 07 CLOCK		
Size: A3	Document Number: AXU9EG扩展板原理图	Rev: 1.0
Date: Friday, September 04, 2020	Sheet 7 of 24	



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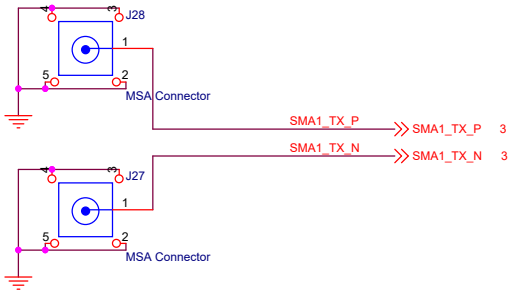
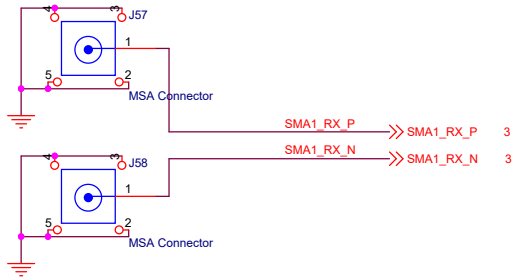
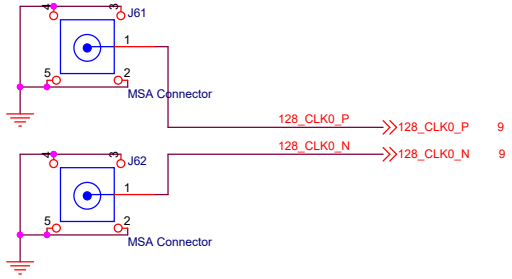
Title	08 Display Port	
Size	Document Number: AXU9EG扩展板原理图	Rev: 1.0
Date: Friday, November 20, 2020	Sheet 8 of 24	







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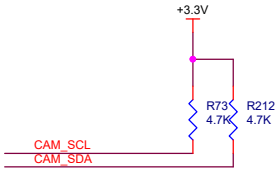
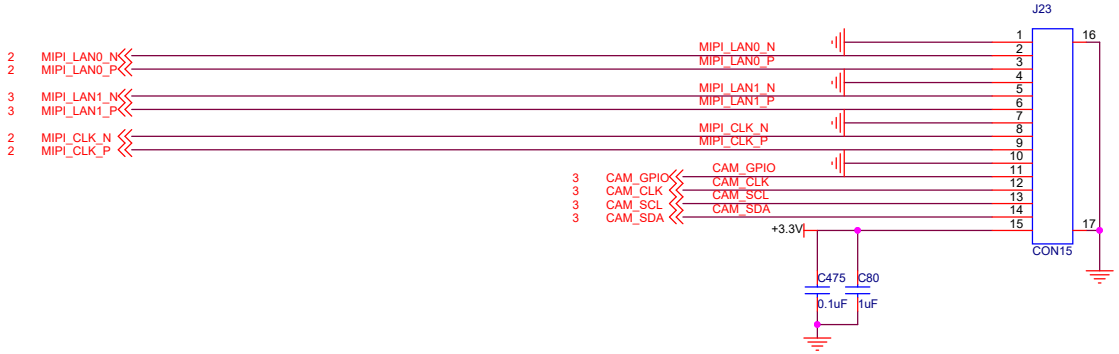
Title: 09 SMA GTH		
Size: A3	Document Number: AXU9EG扩展板原理图	Rev: 1.0
Date: Friday, November 20, 2020	Sheet 9 of 24	



MIPI Interface



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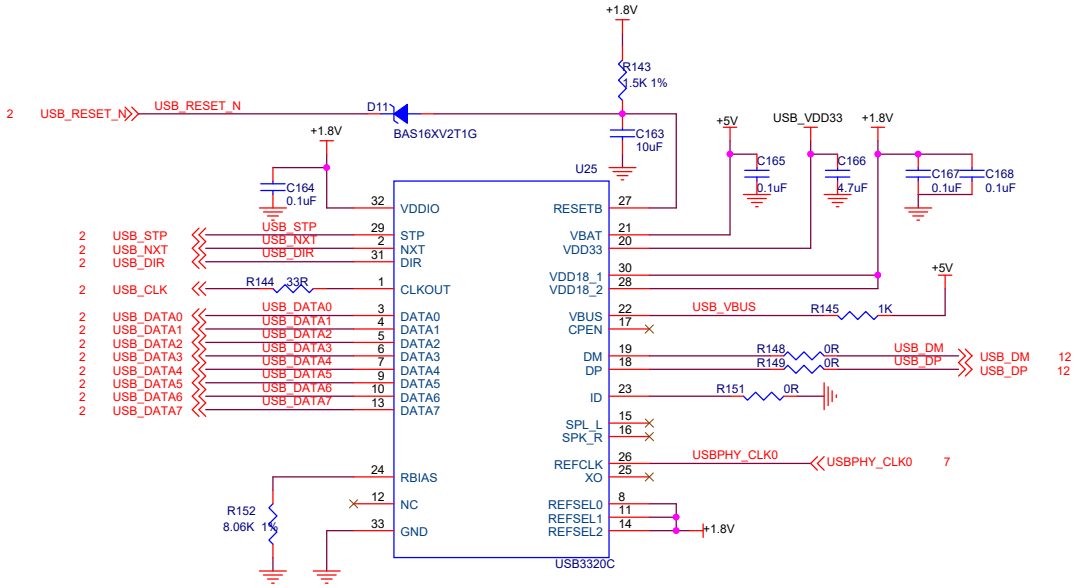
Title: 10 MIPI		
Size: A3	Document Number: AXU9EG扩展板原理图	Rev: 1.0
Date: Friday, November 20, 2020	Sheet 10 of 24	



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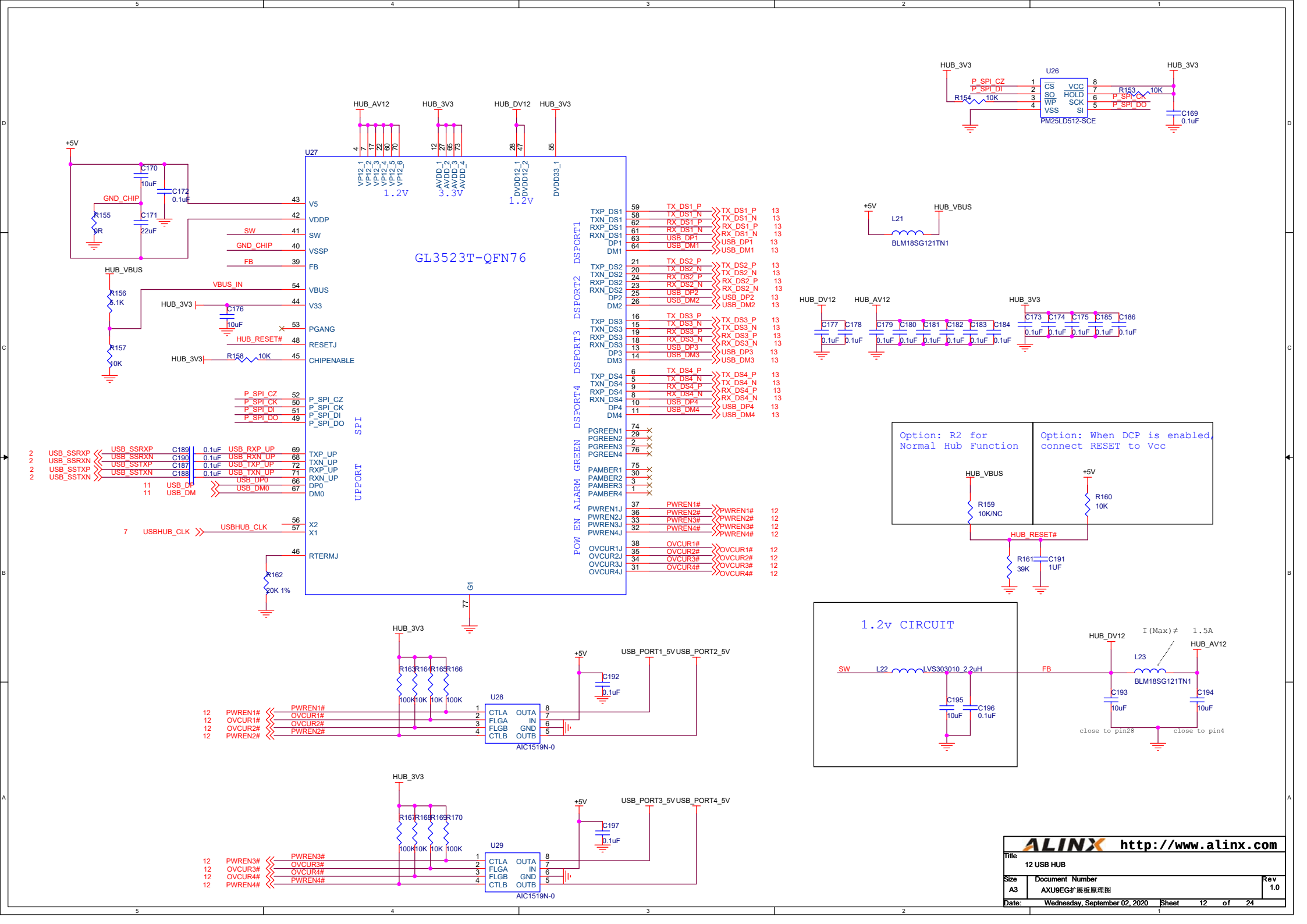
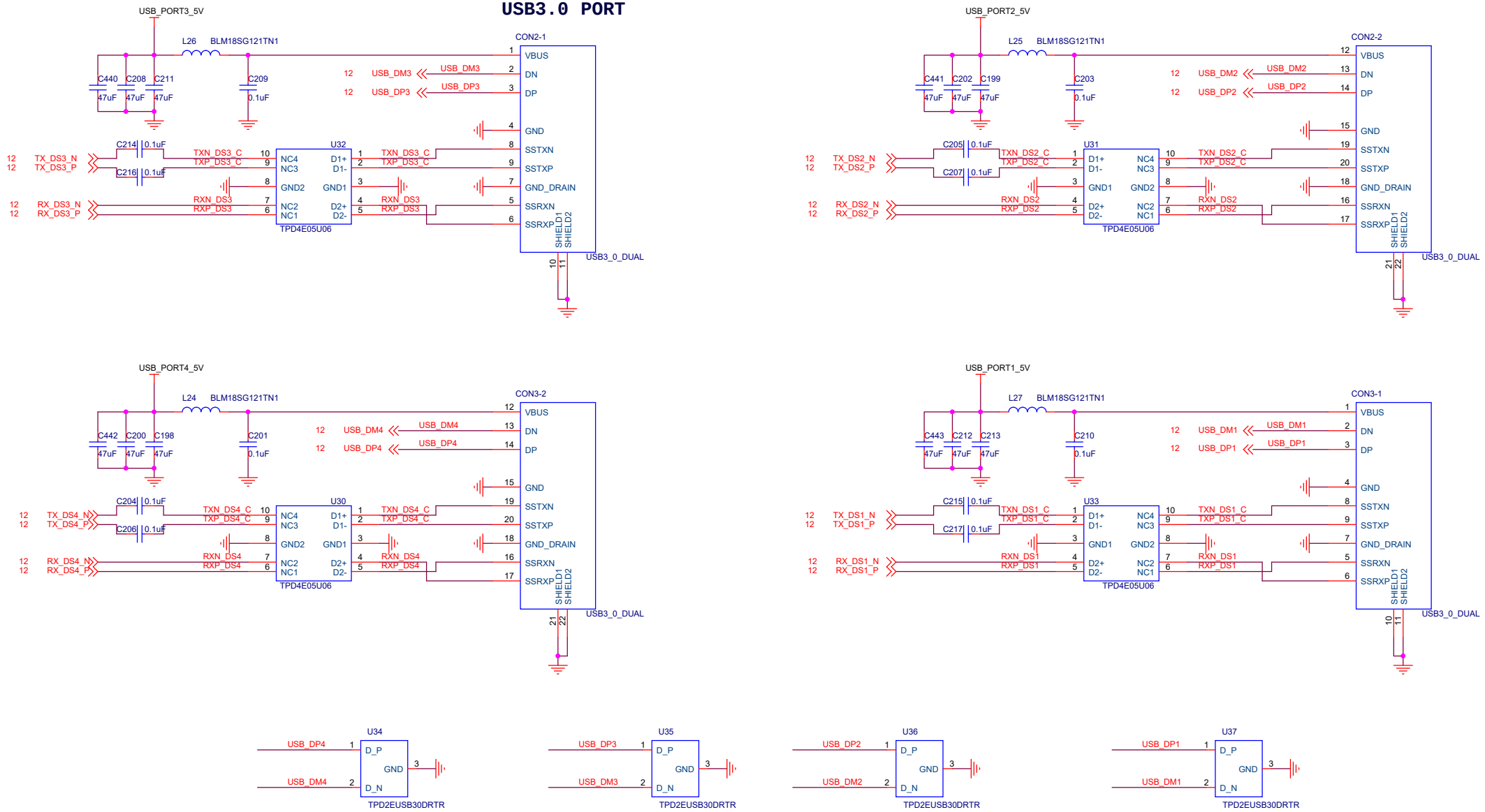
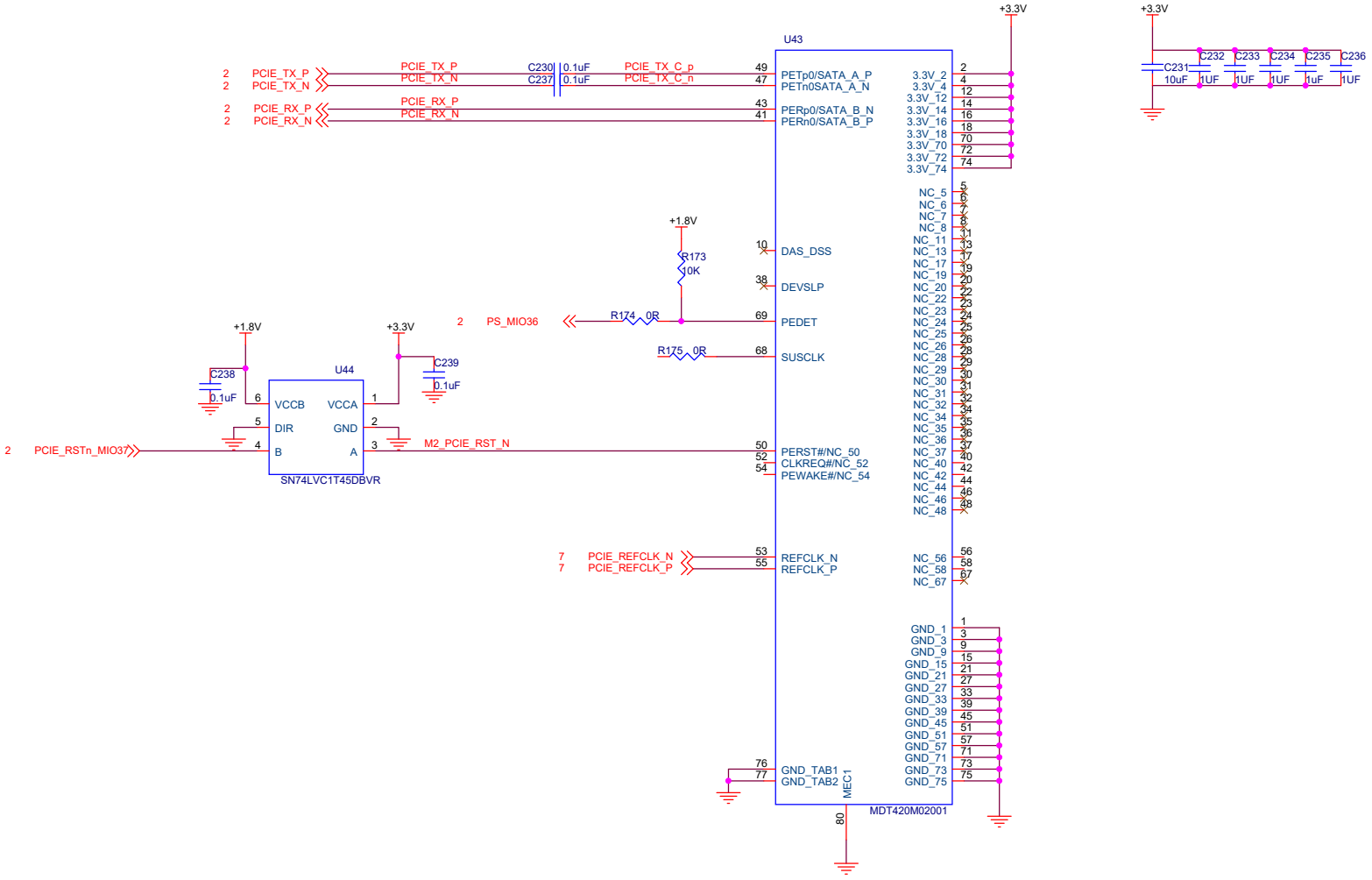
Title: 11 USB2.0 PHY		
Size: A3	Document Number: AXU9EG扩展板原理图	Rev: 1.0
Date: Thursday, September 03, 2020	Sheet 11 of 24	


GL3523T-QFN76
U27
U26
PM25LD512-SCE
U28
U29
AIC1519N-0
HUB_AV12
HUB_3V3
HUB_DV12
HUB_VBUS
+5V
1.2V
3.3V
SPI
UPPORT
DSPORT1
DSPORT2
DSPORT3
DSPORT4
GREEN
ALARM
POW EN
L21
BLM18SG121TN1
Option: R2 for Normal Hub Function
Option: When DCP is enabled, connect RESET to Vcc
1.2v CIRCUIT
LVS303010 2.2uH
I(Max)≠ 1.5A
close to pin28
close to pin4
ALINX http://www.alinx.com
12 USB HUB
AXU9EG扩展板原理图
Wednesday, September 02, 2020

USB3.0 PORT



ALINX http://www.alinx.com		
Title: 13 USB CONN		
Size: A3	Document Number: AXU9EG扩展板原理图	Rev: 1.0
Date: Wednesday, September 02, 2020	Sheet 13 of 24	



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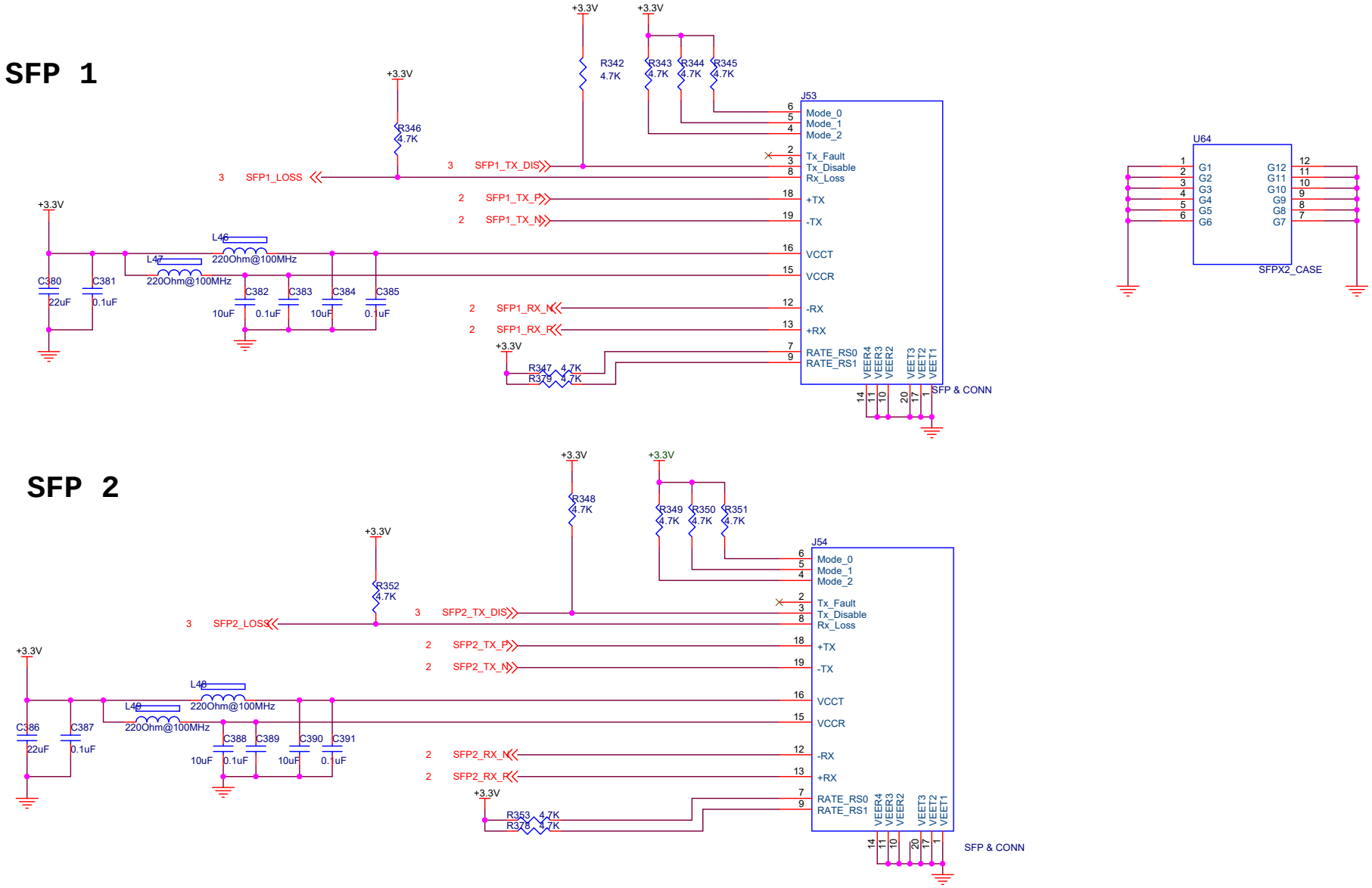
ALINX	http://www.alinx.com
Title	14 M2 Connector
Size	A3
Document Number	AXU9EG扩展板原理图
Rev	1.0
Date:	Wednesday, September 02, 2020
Sheet	14 of 24



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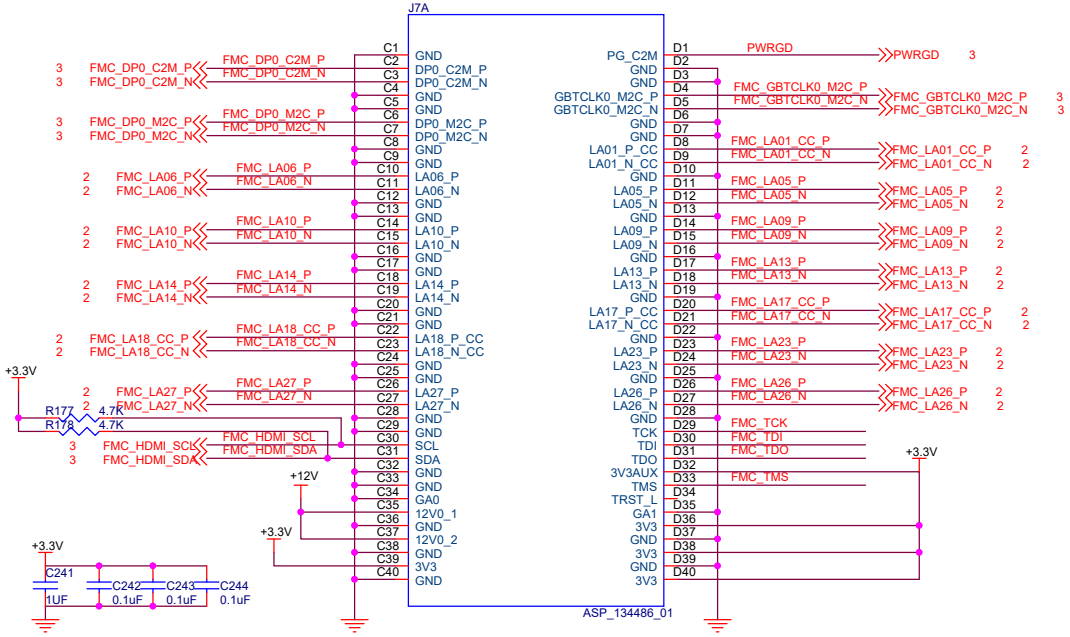
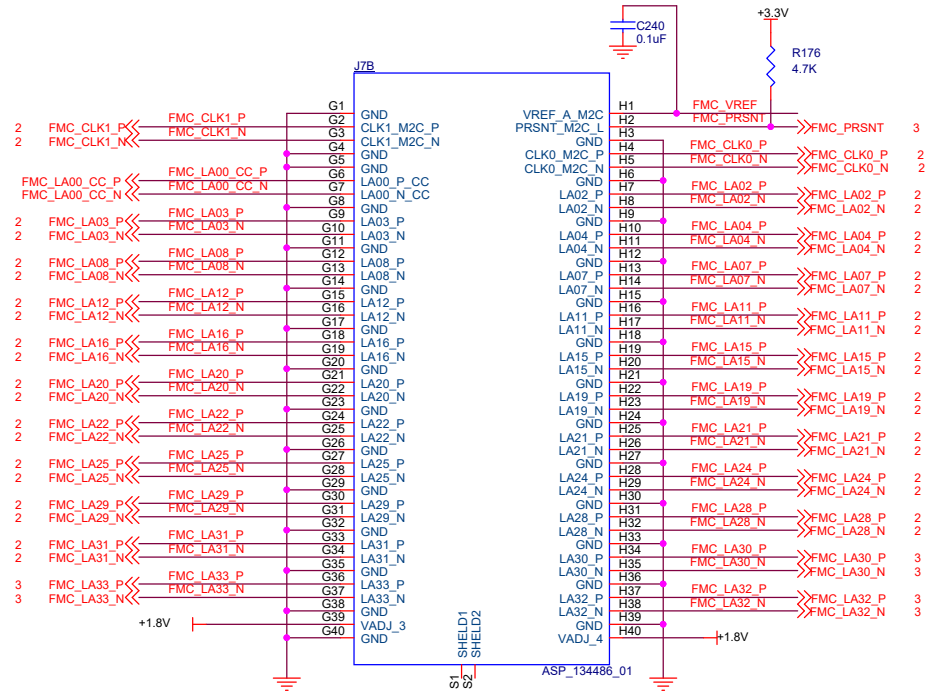
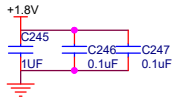
Title: 15 SFP1 & SPF2

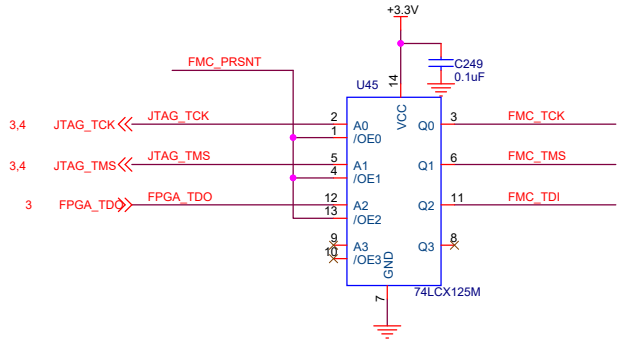
Size | Document Number: AXU9EG扩展板原理图 | Rev: 1.0

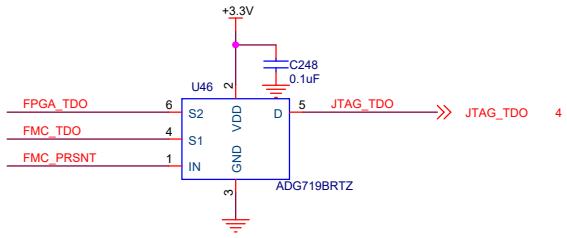
Date: Friday, November 20, 2020 | Sheet 15 of 24











FMC_PRSNT = 1: JTAG_TDO = FPGA_TDO

FMC_PRSNT = 0: JTAG_TDO = FMC_TDO

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Title: 16 FMC_LPC		
Size: A3	Document Number: AXU9EG扩展板原理图	Rev: 1.0
Date: Wednesday, September 02, 2020	Sheet 16 of 24	

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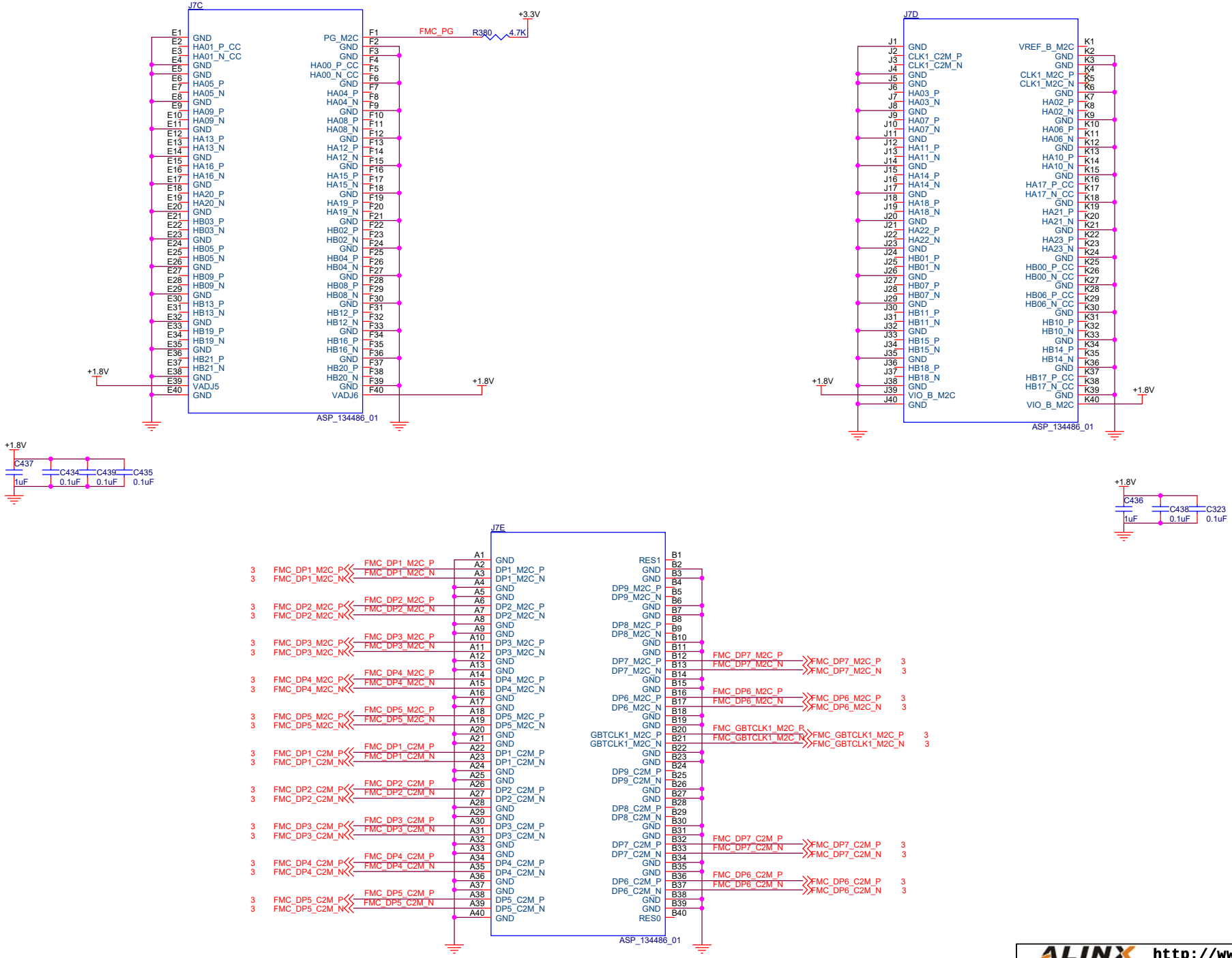

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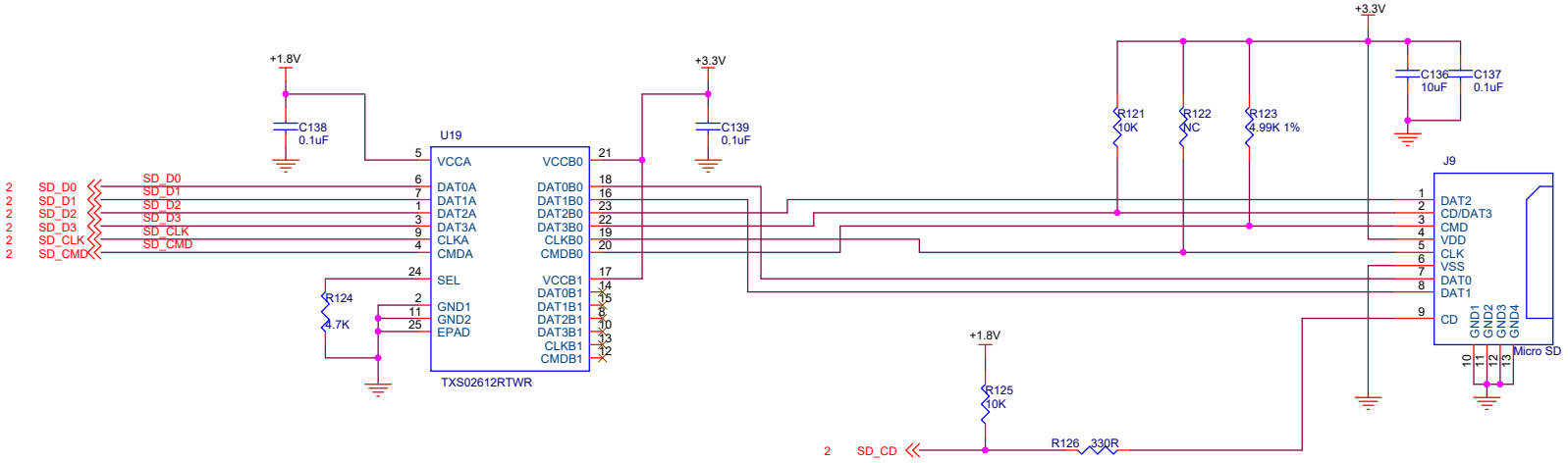
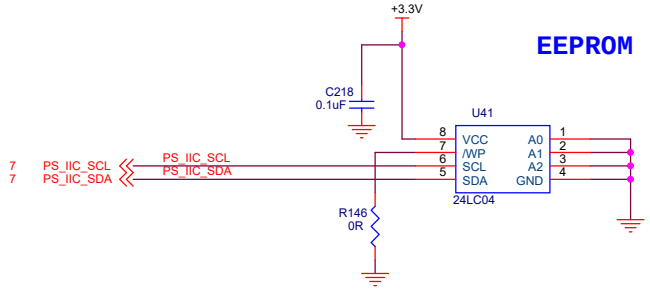
ALINX http://www.alinx.com		
Title: 16 FMC_LPC		
Size: A3	Document Number: AXU9EG扩展板原理图	Rev: 1.0
Date: Wednesday, September 02, 2020	Sheet 17 of 24	

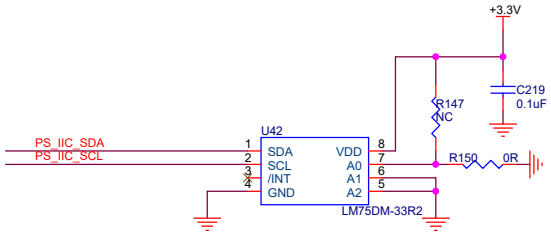
SD Card Connector



EEPROM



SENSOR



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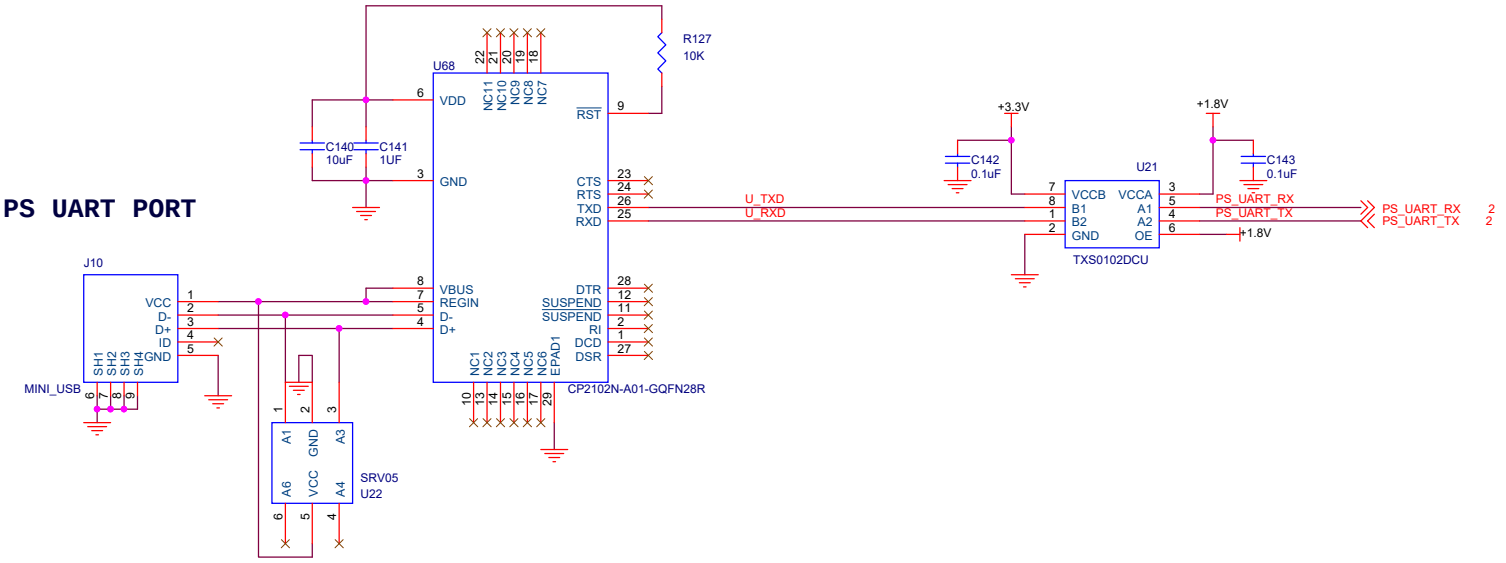
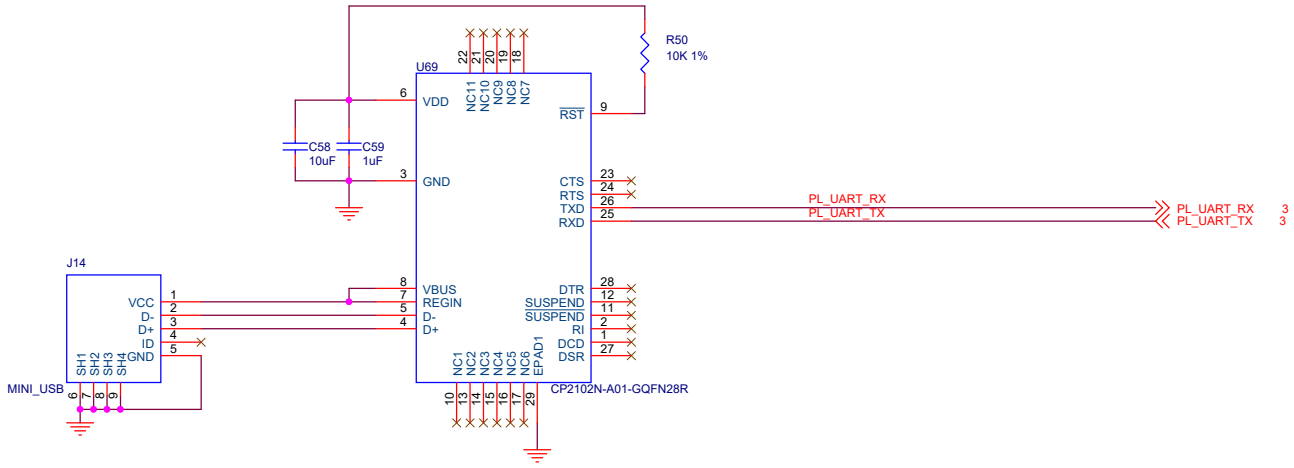
ALINX http://www.alinx.com		
Title: 17 SD CARD		
Size: A3	Document Number: AXU9EG扩展板原理图	Rev: 1.0
Date: Wednesday, September 02, 2020	Sheet 18 of 24	

PS UART PORT



PL UART PORT



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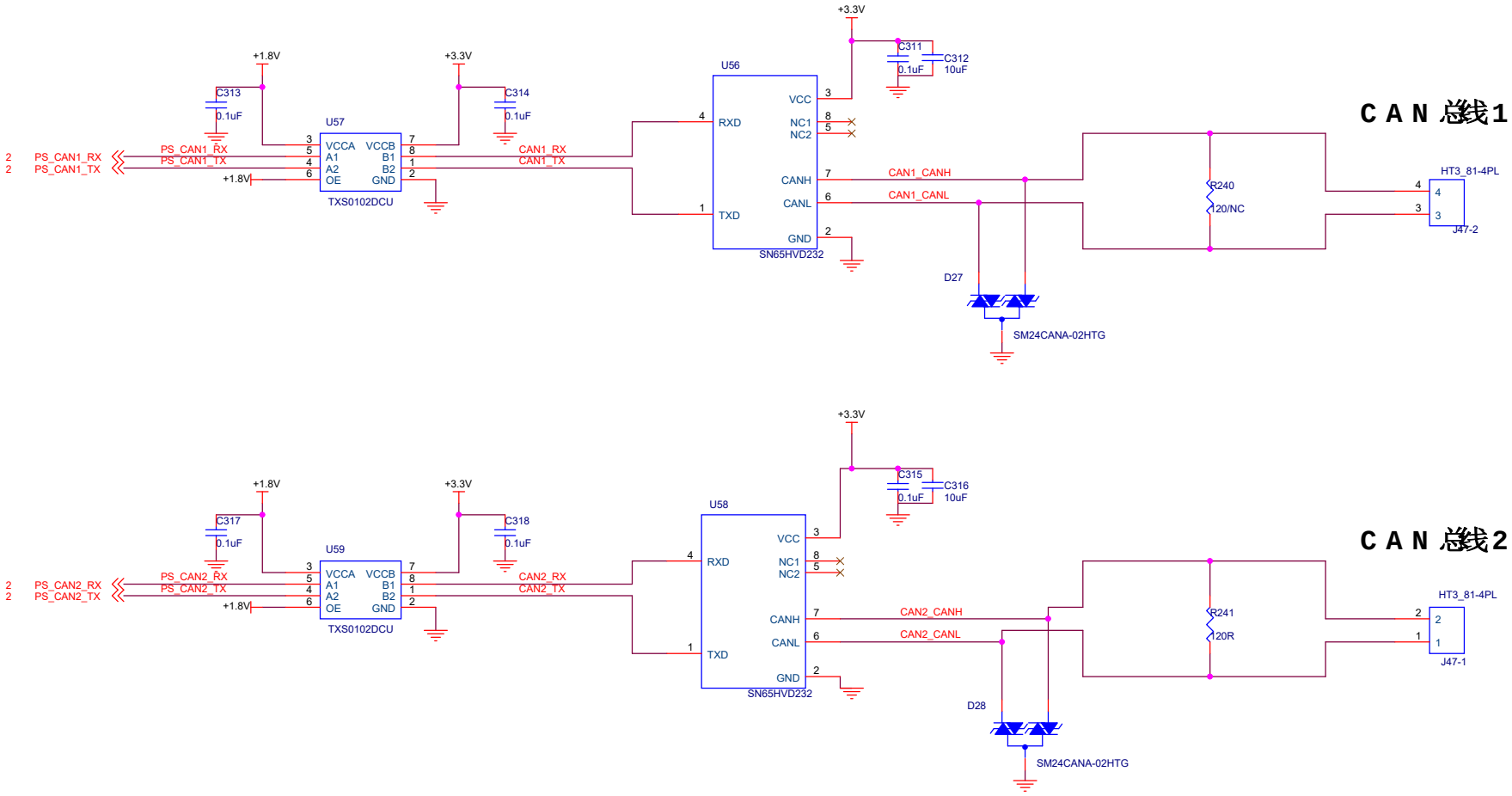
ALINX http://www.alinx.com		
Title: 18 PS UART		
Size: A3	Document Number: AXU9EG扩展板原理图	Rev: 1.0
Date: Wednesday, September 02, 2020	Sheet 19 of 24	



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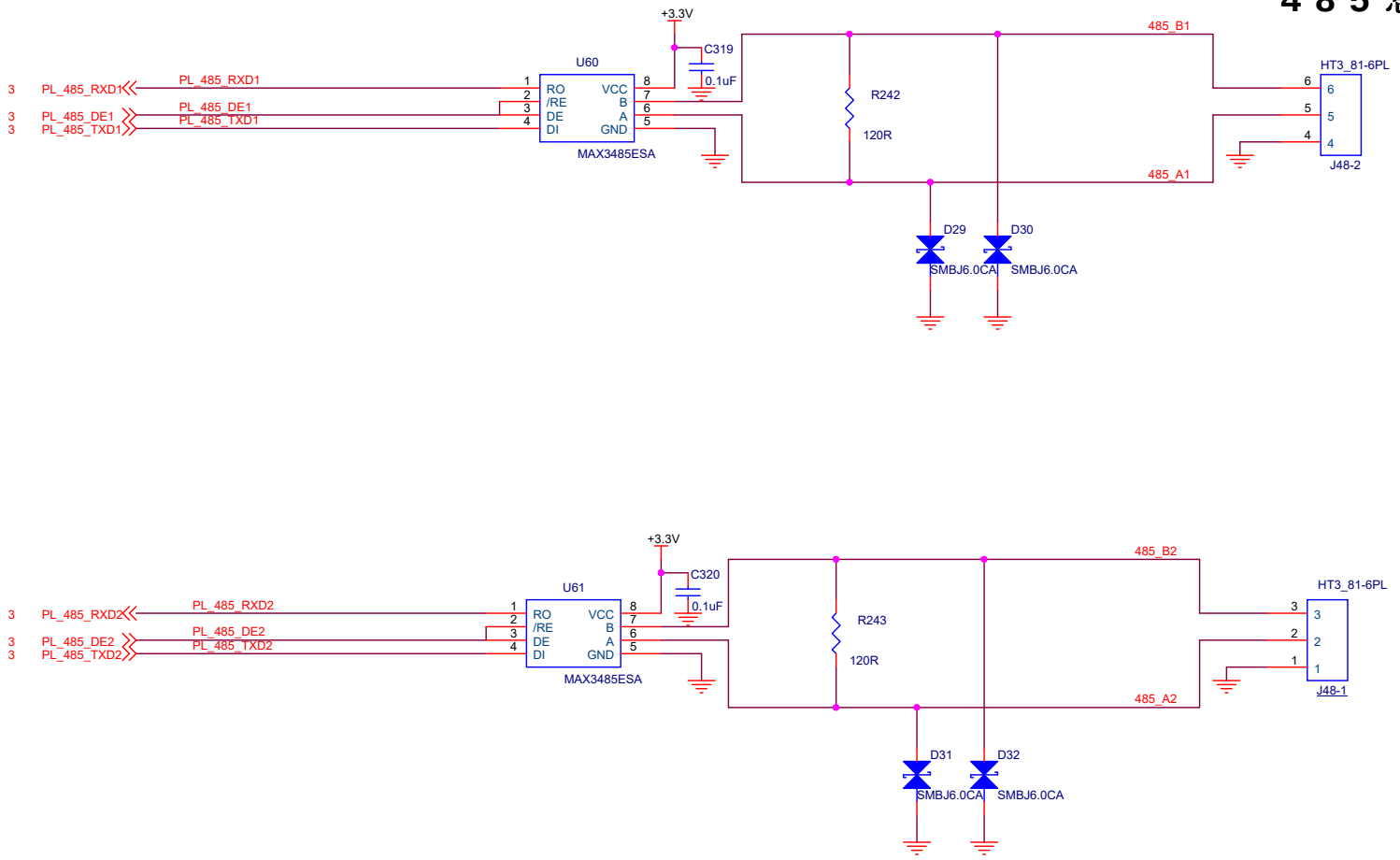
ALINX http://www.alinx.com		
Title: 19 CAN_1 & CAN_2		
Size: A3	Document Number: AXU9EG扩展板原理图	Rev: 1.0
Date: Wednesday, September 02, 2020	Sheet 20 of 24	



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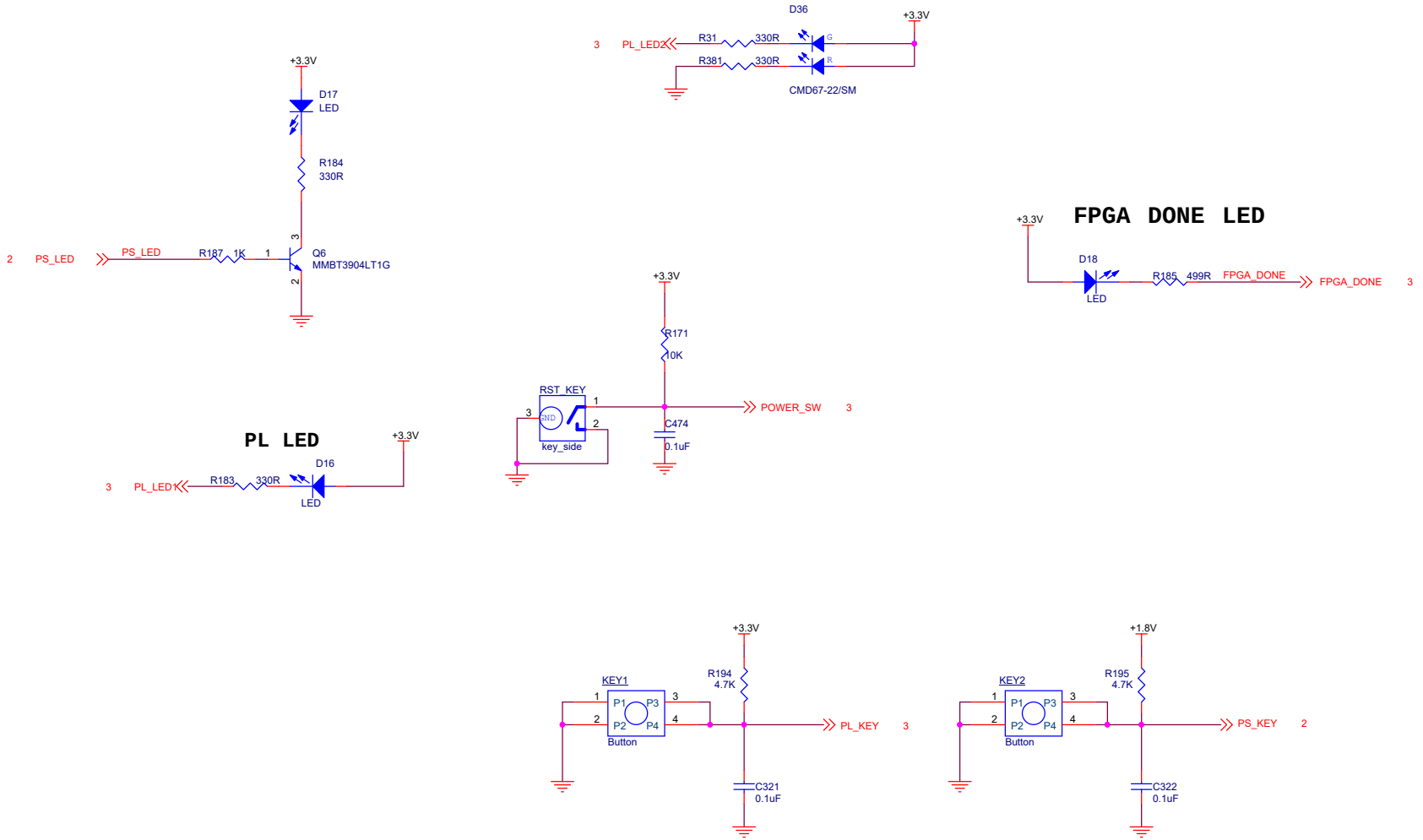
ALINX	http://www.alinx.com
Title	20 RS485_1 & RS485_2
Size	Document Number: AXU9EG扩展板原理图
Rev	1.0
Date:	Wednesday, September 02, 2020
Sheet	21 of 24



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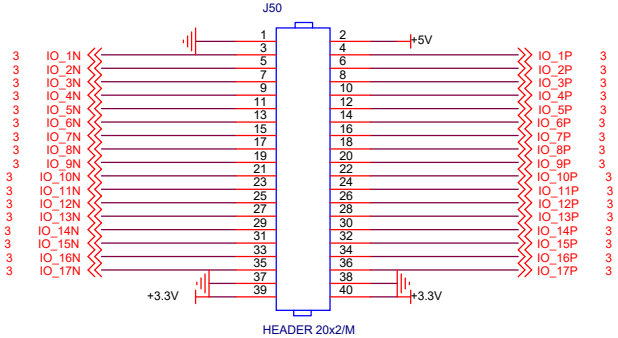
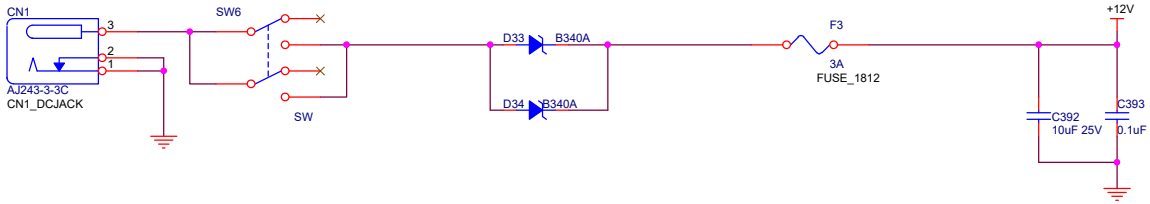
ALINX http://www.alinx.com		
Title: 21 LED & KEY		
Size: A3	Document Number: AXU9EG扩展板原理图	Rev: 1.0
Date: Thursday, September 03, 2020	Sheet 22 of 24	



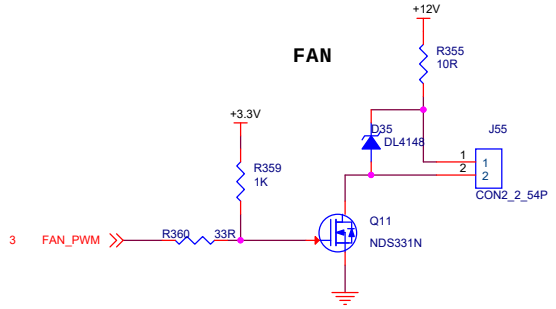
ALINX http://www.alinx.com

Title: 22 IO Expansion header		
Size: A3	Document Number: AXU9EG扩展板原理图	Rev: 1.0
Date: Wednesday, September 02, 2020	Sheet 23 of 24	

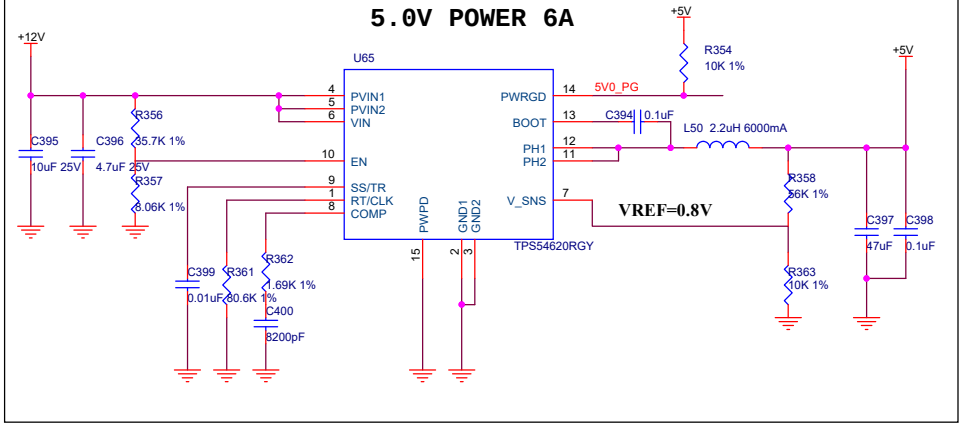
12V/3A AC Adapter



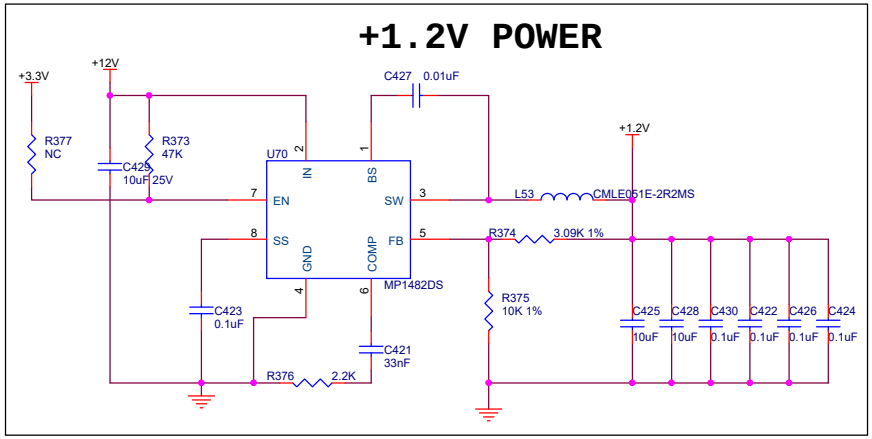
FAN



5.0V POWER 6A



+1.2V POWER



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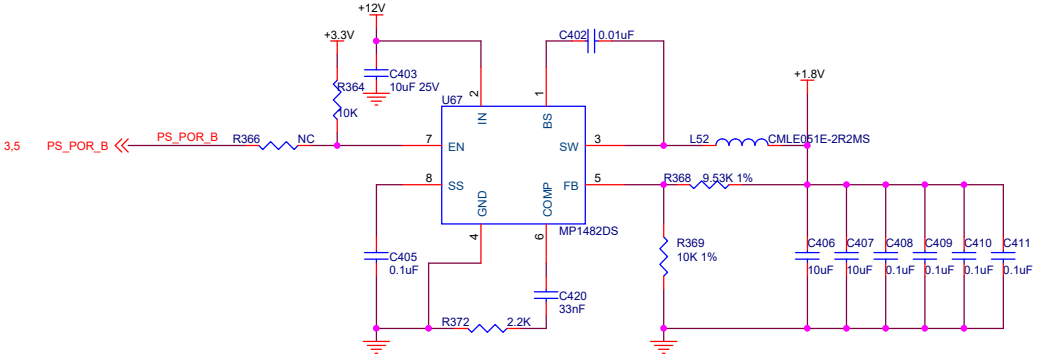
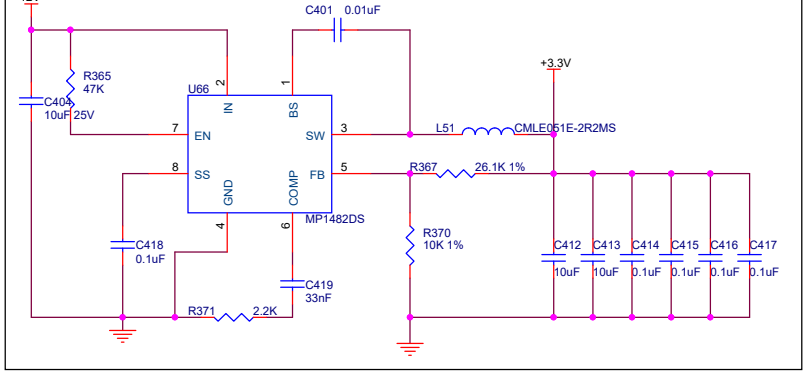


+3.3V POWER



M1 MARK M2 MARK M3 MARK M4 MARK M5 MARK M6 MARK M7 MARK M8 MARK

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Size	Document Number: AXU9EG扩展板原理图	Rev: 1.0
Date: Thursday, September 03, 2020	Sheet 24 of 24	