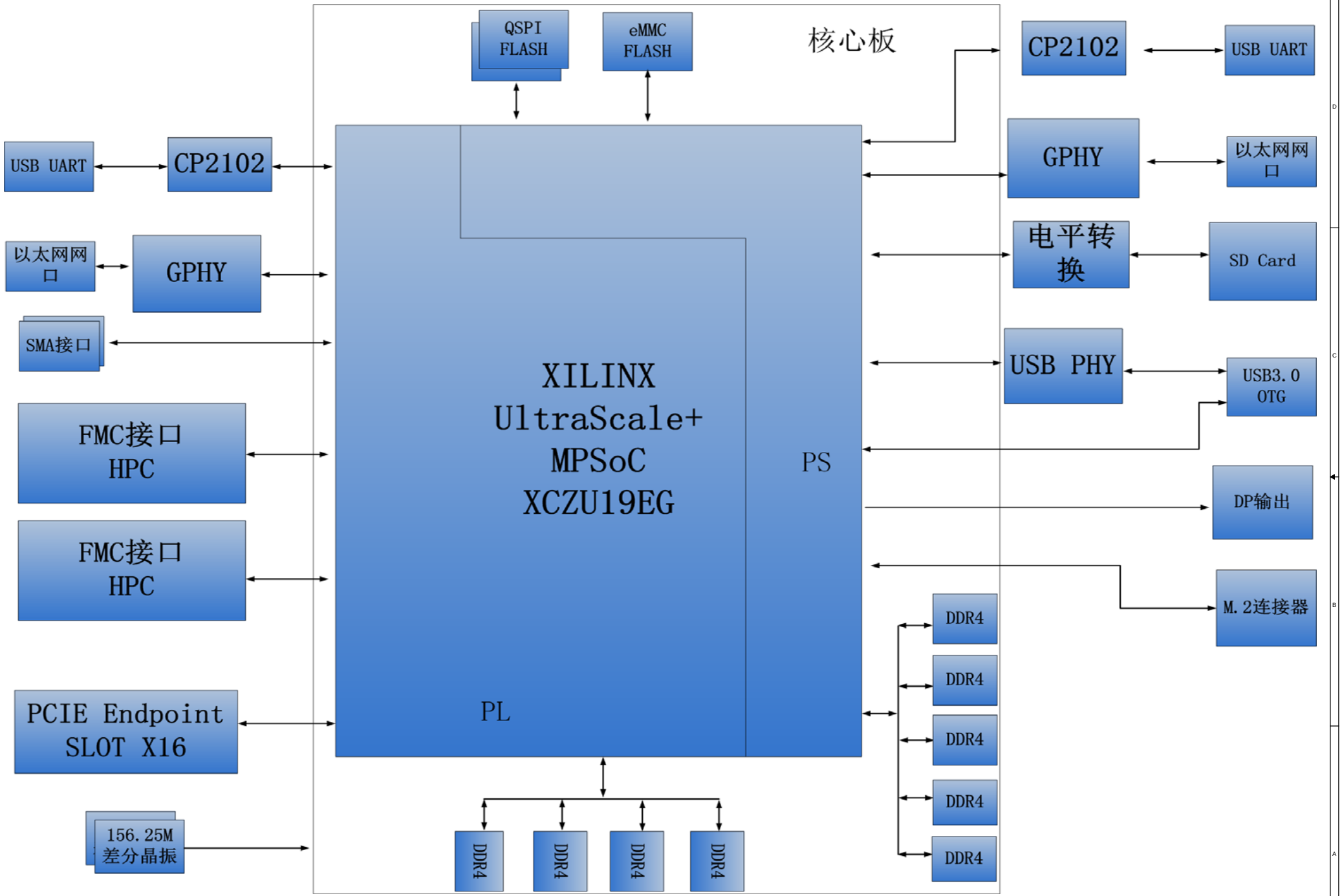

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Title	PAGE01 Block diagram
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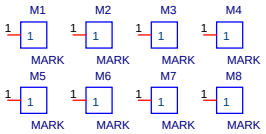
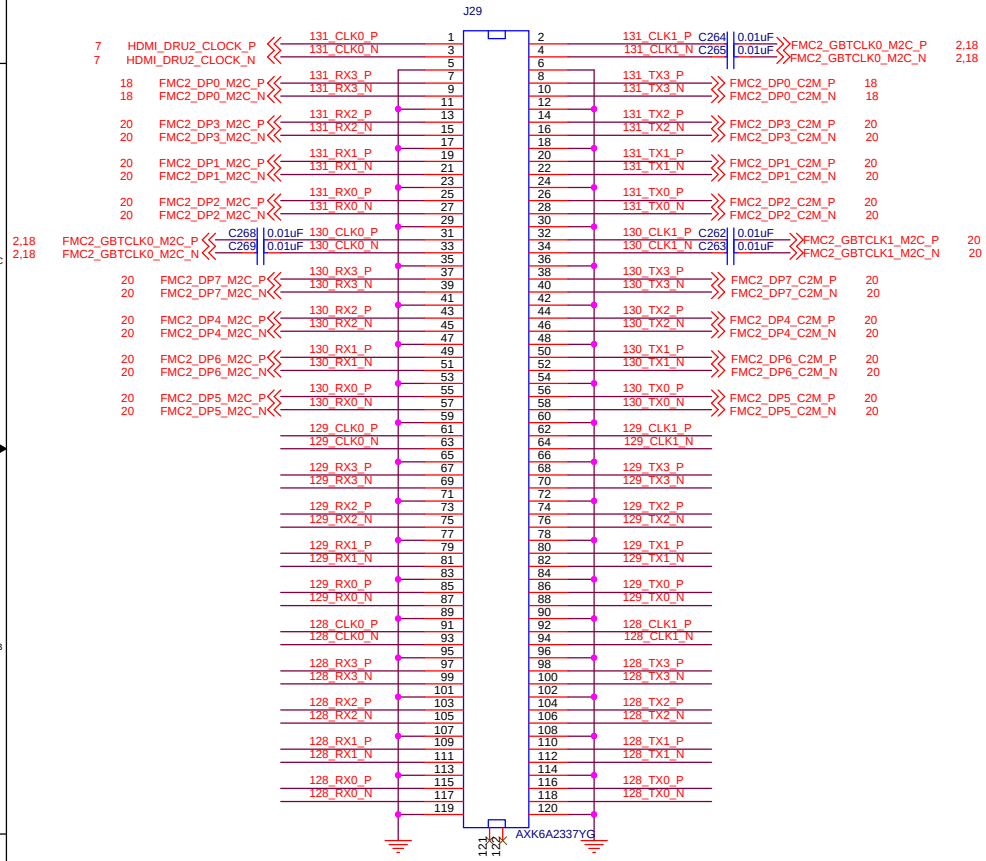
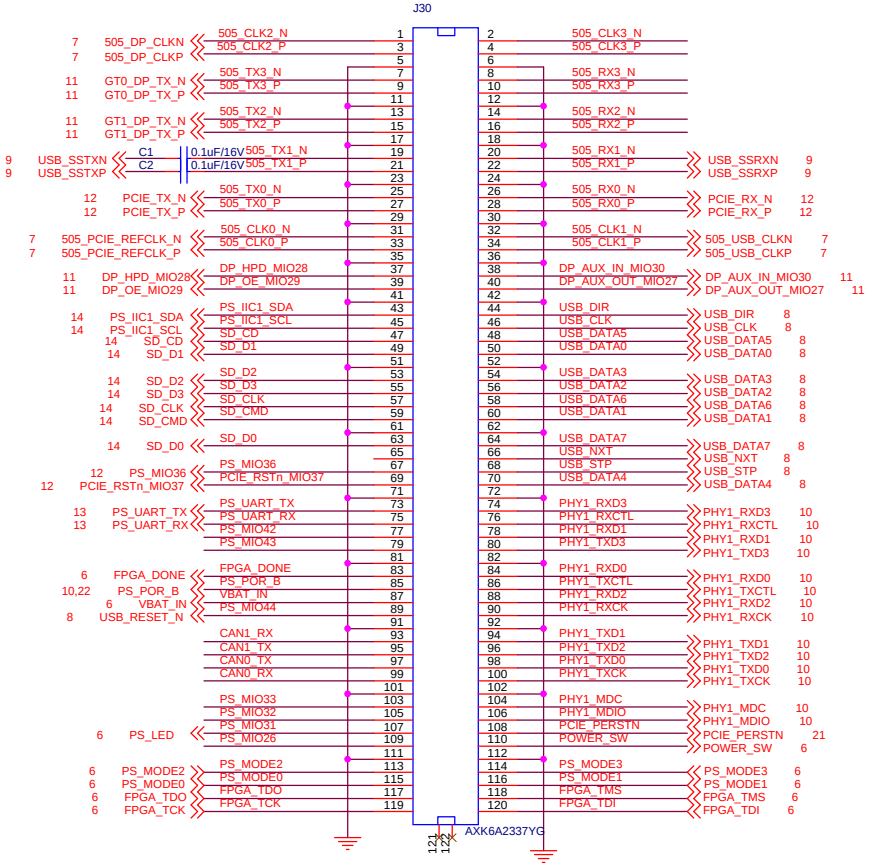


MIO/SD/USB/ETH IO Voltage is 1.8V Stardard





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Title	PAGE02 Connector1
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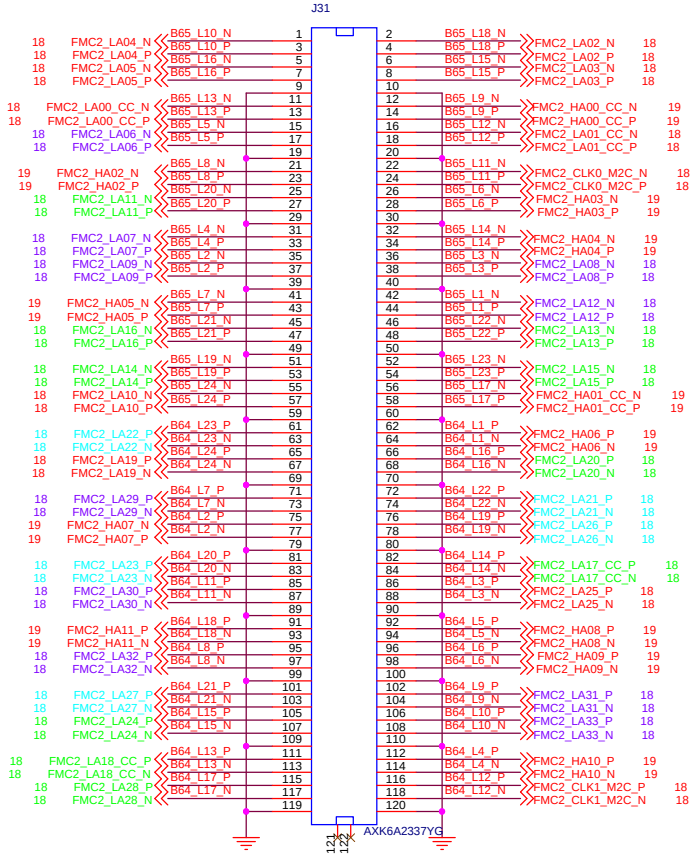
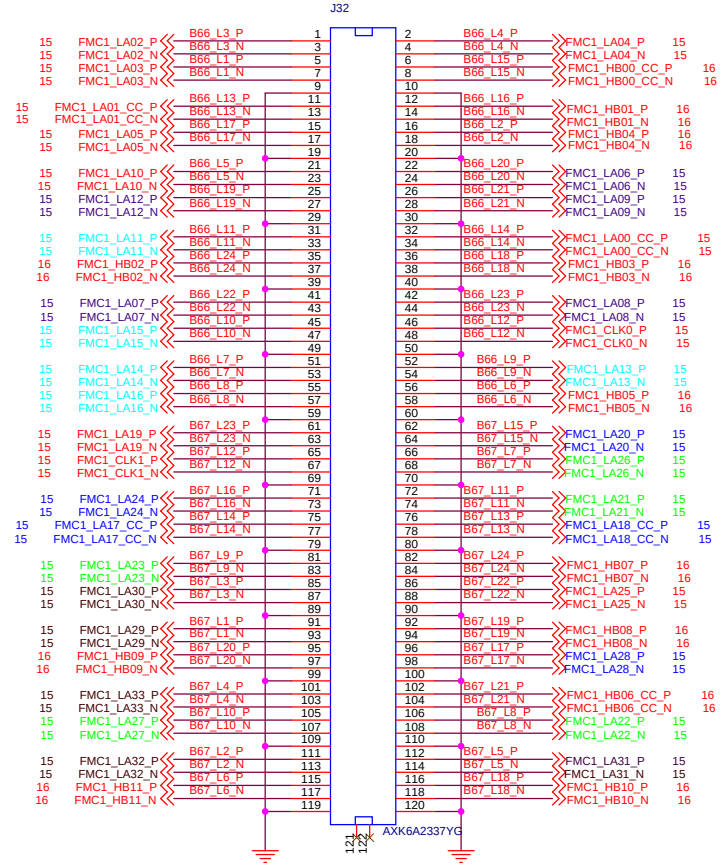
BANK64,65 is HP BANK, IO Voltage is under 1.8V



BANK66 67 is HP BANK, IO Voltage is under 1.8V



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Title	PAGE03 Connector2	
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Title: PAGE04 Connector3

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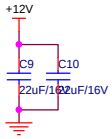
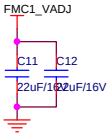
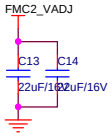
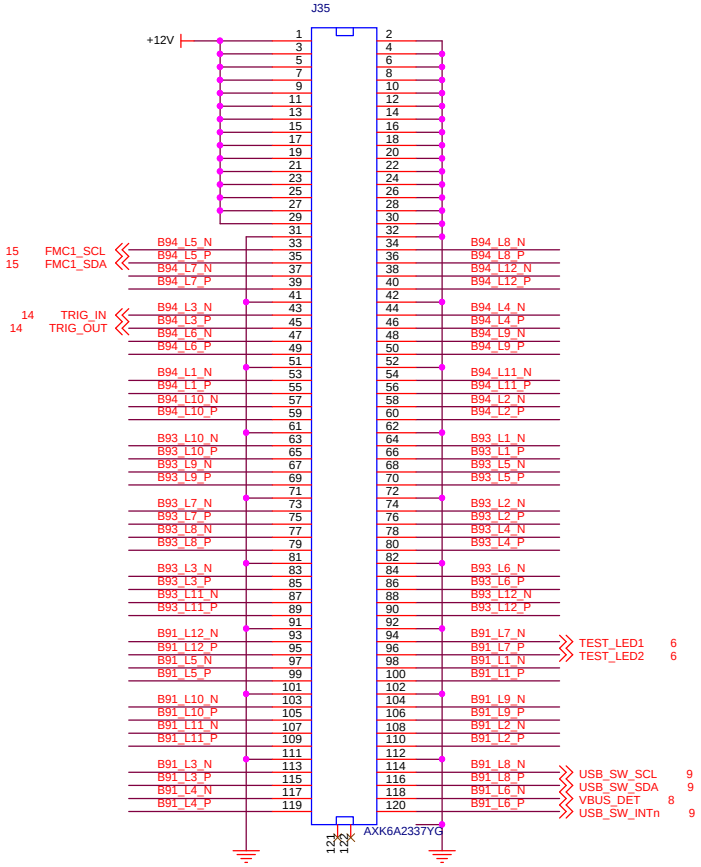


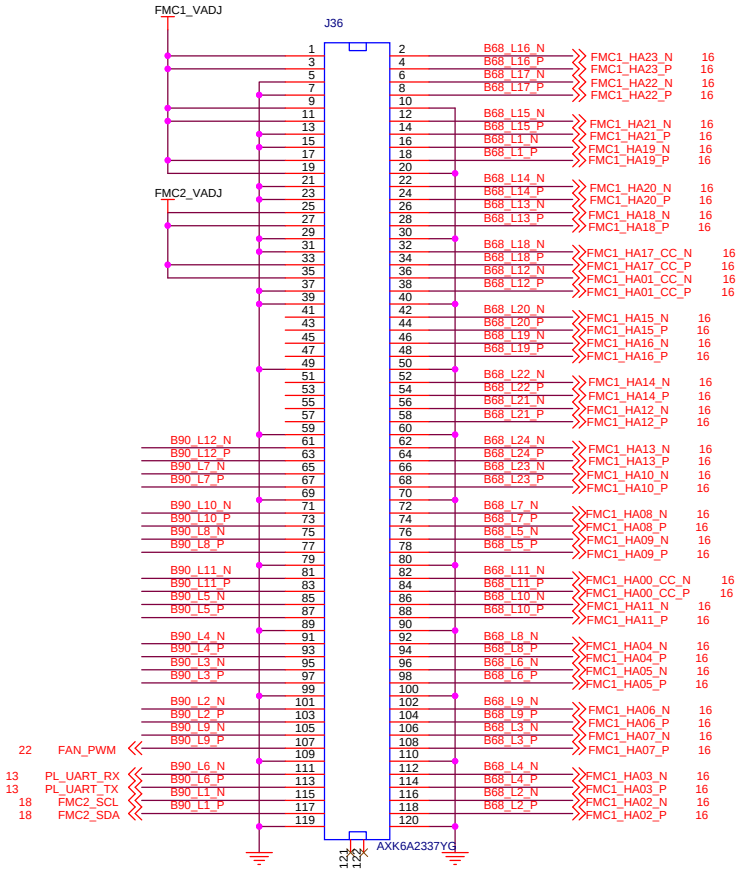




BANK90,91,93,94 is HD BANK, IO Voltage is under 3.3V

BANK68 is HP BANK, IO Voltage is under 1.8V





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Title	PAGE05 Connector4	
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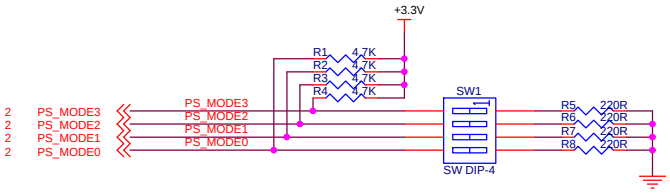
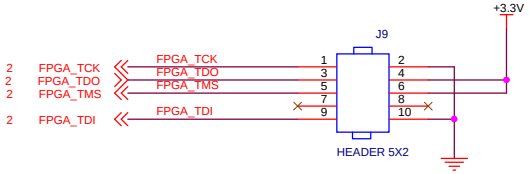
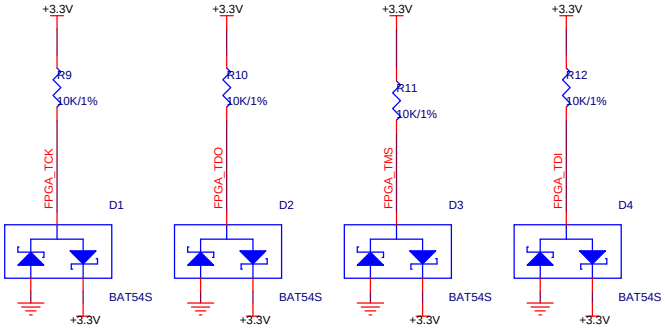
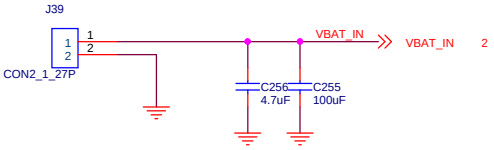
MODE[3:0]	BOOT MODE	Descritpion
0000	PS JTAG	PS JTAG Interface
0001	Quad_SPI(24b)	24-Bit addresssing(QSPI24)
0010	Quad_SPI(32b)	32-Bit addresssing(QSPI32)
0011	SD0(2.0)	SD2.0
0100	NAND	Requires 8-bit data bus width
0101	SD1(2.0)	SD2.0
0110	eMMC(1.8V)	eMMC version 4.5 at 1.8V
0111	USB0(2.0)	USB 2.0 only
1000	PJTAG(MIO #0)	PJTAG connection 0 option
1001	PJTAG(MIO #1)	PJTAG connection 1 option
1110	SD1 LS(3.0)	SD 3.0



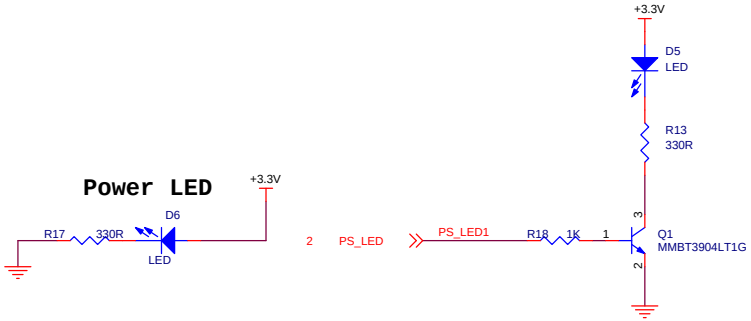
JTAG Connector

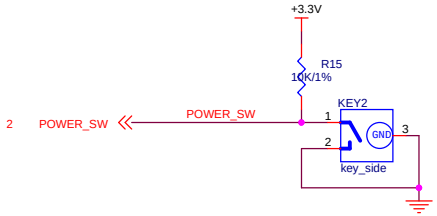




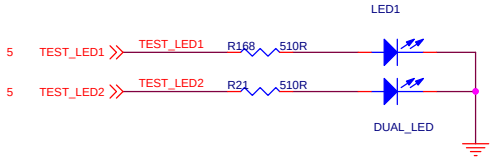


Power LED





FPGA DONE LED



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Title	PAGE06 JTAG & LED
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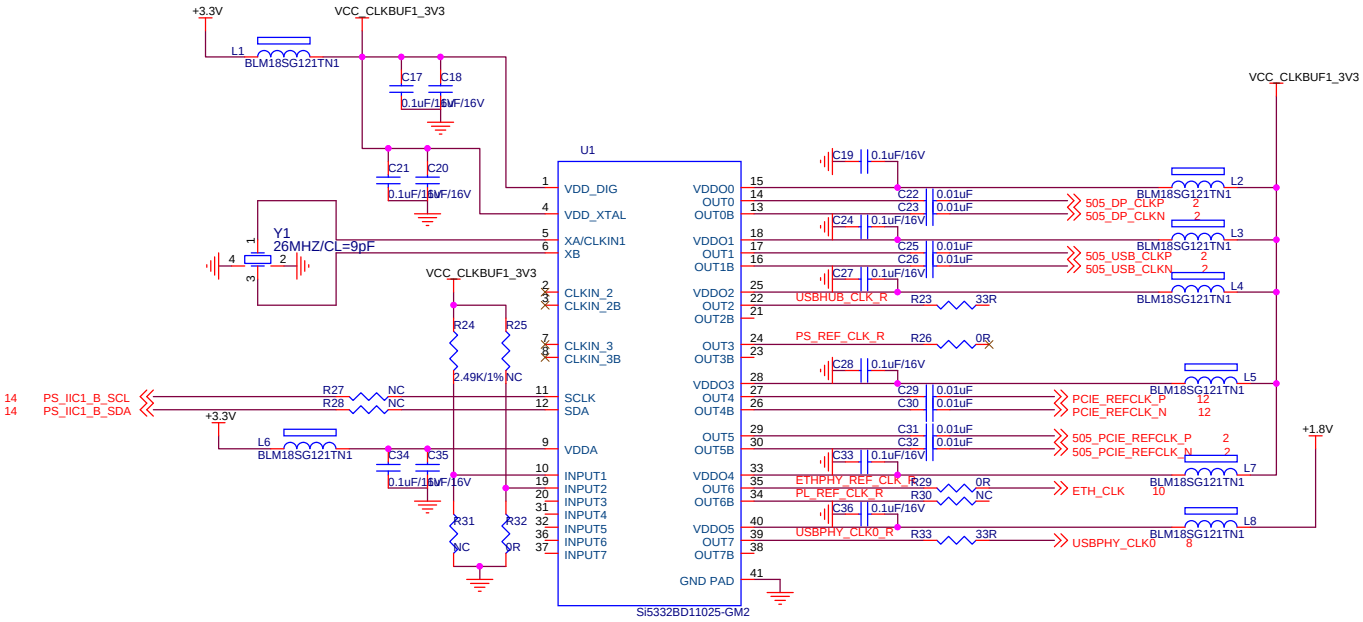


DP CLOCK = 27Mhz

USB CLOCK = 26Mhz

USB HUB CLOCK = 25Mhz

PS REF CLOCK = 33.3333Mhz

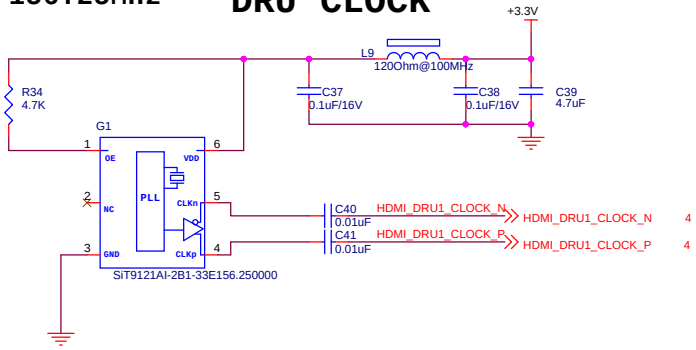
PCIE REF CLOCK = 100Mhz

PCIE REF CLOCK = 100Mhz

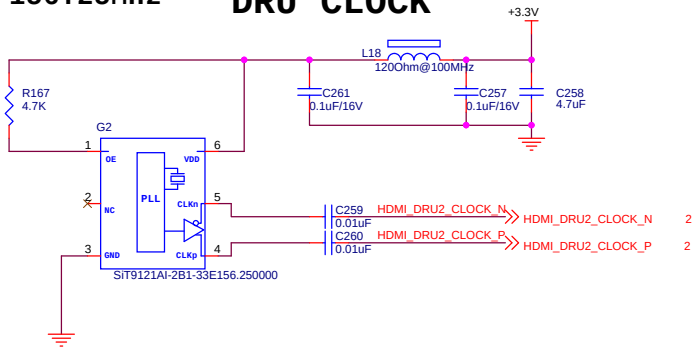
ETHPHY REF CLOCK = 25Mhz

USBPHY CLOCK = 24Mhz

156.25MHz DRU CLOCK



156.25MHz DRU CLOCK



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Title: PAGE07 CLOCK1

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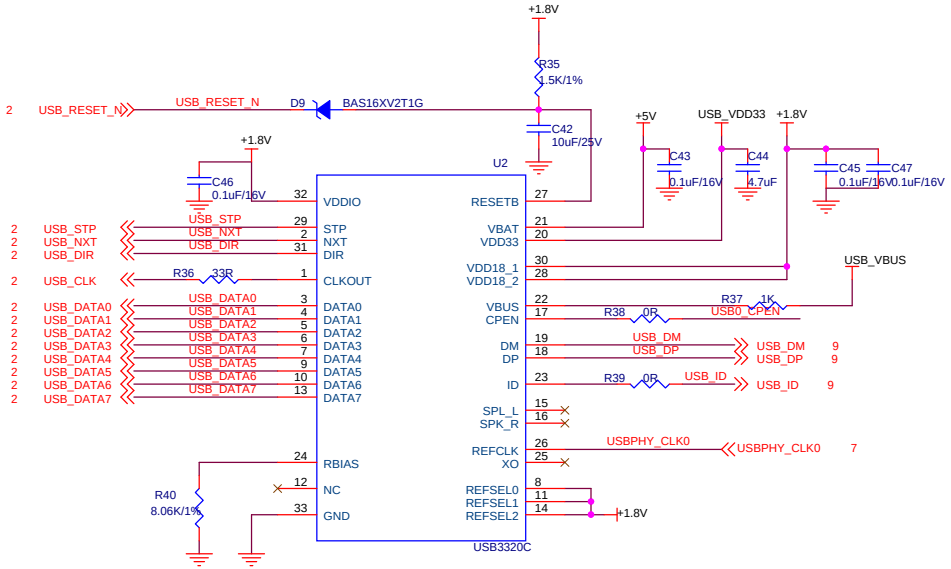

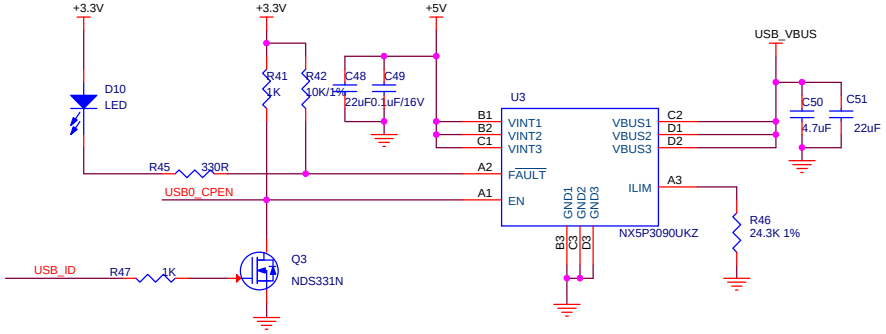

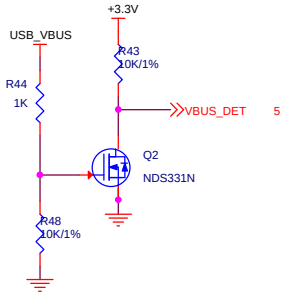

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Title	PAGE08 USB3.0	
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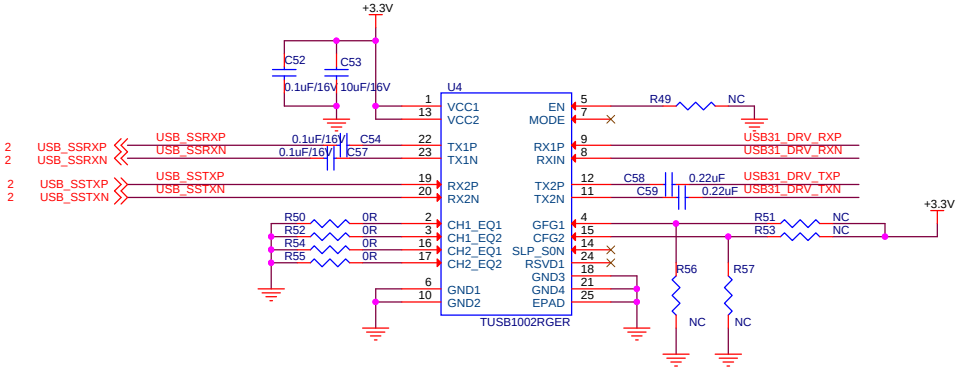

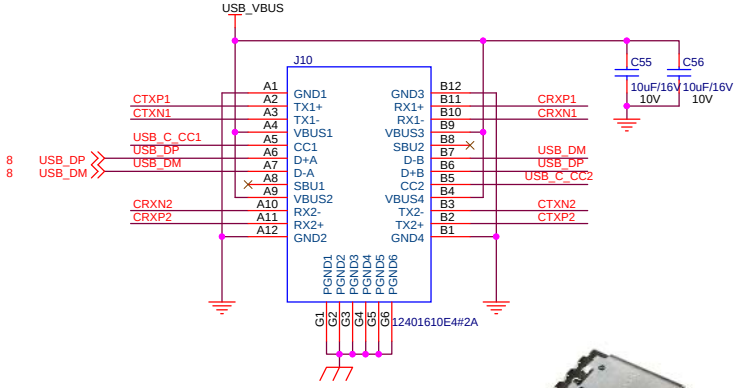

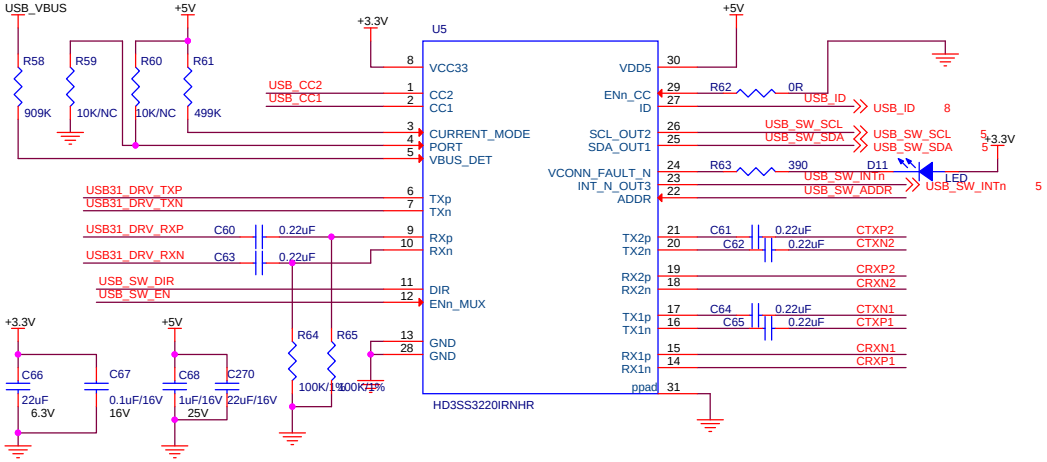

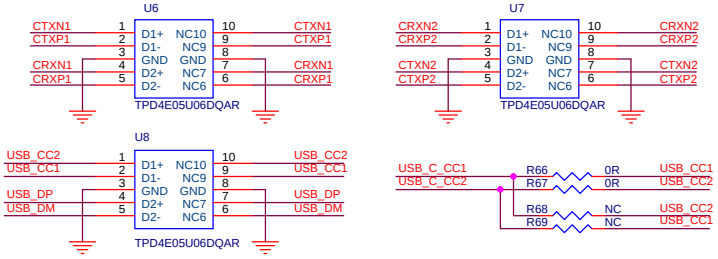

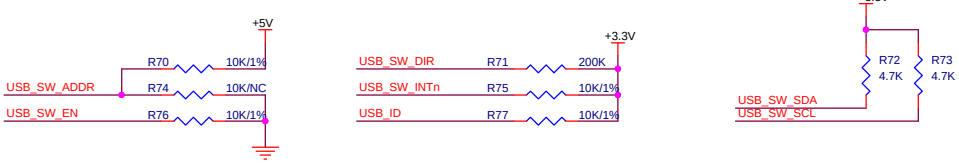

I2C 7-bit address is 0x67

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Title	PAGE09 USB Connector
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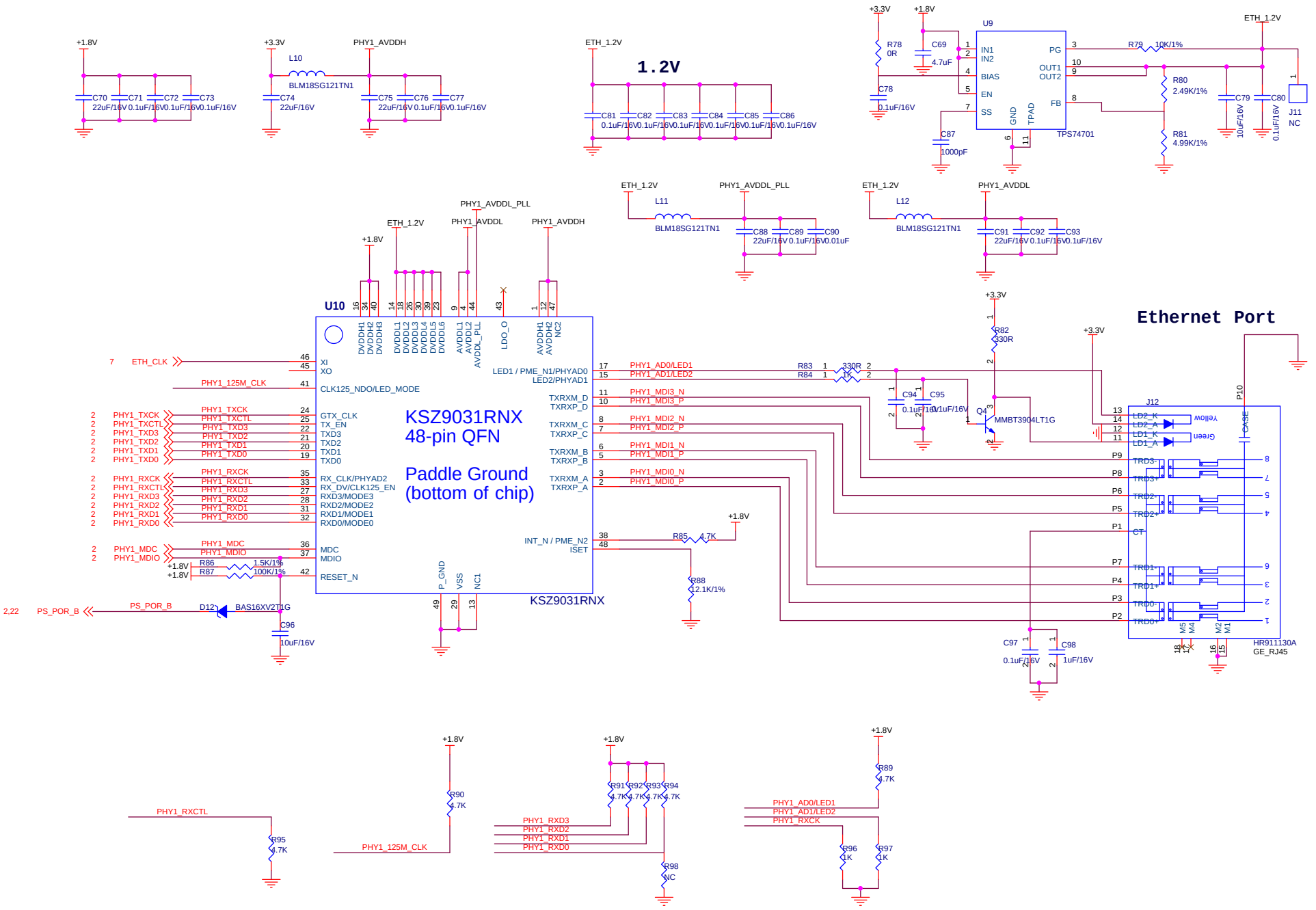

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Title	PAGE24 PS Ethernet
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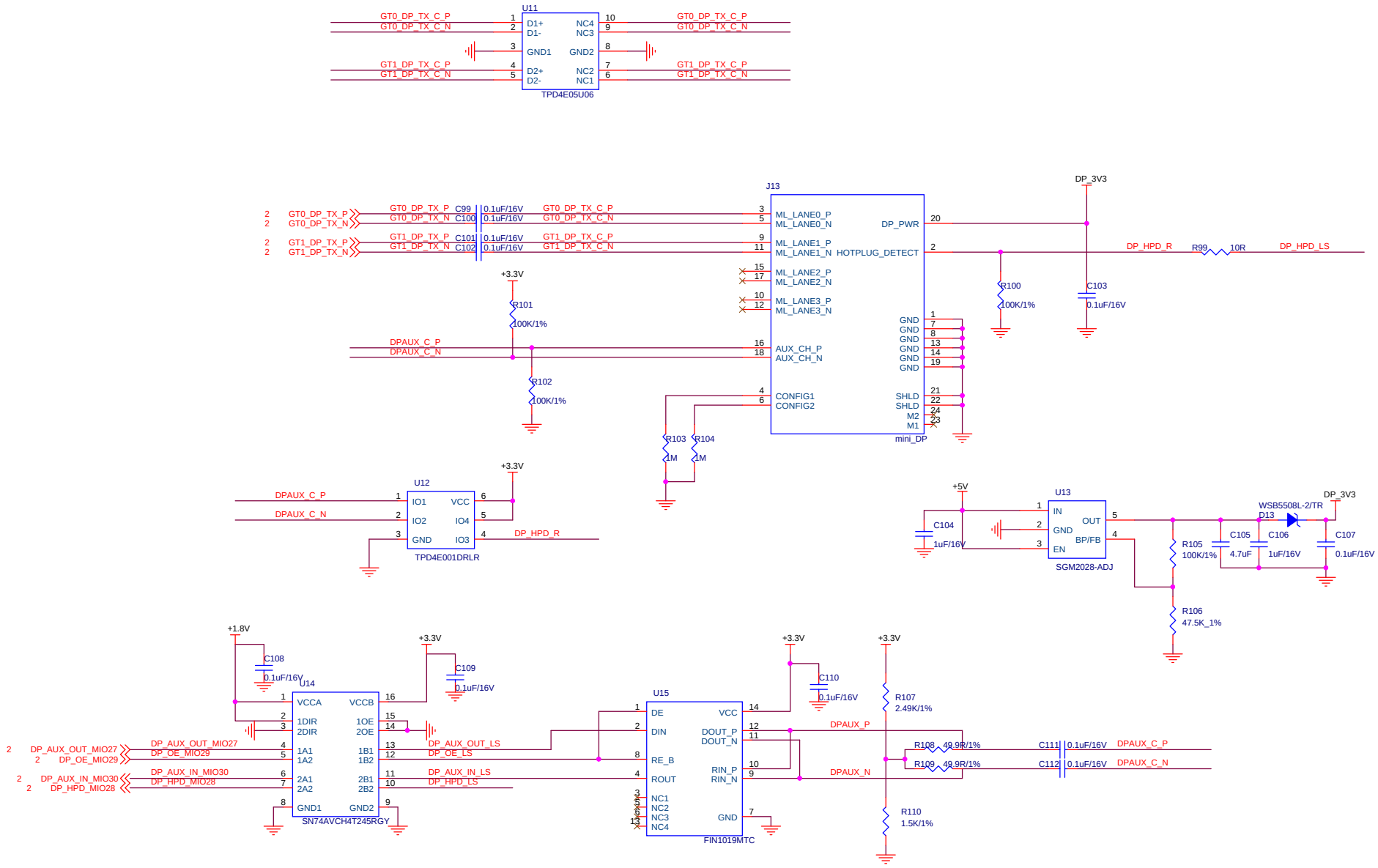

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ALINX	Http://www.alinx.com
Title	PAGE10 PS Ethernet
Size	Document Number: Z19-P开发板 Schematics
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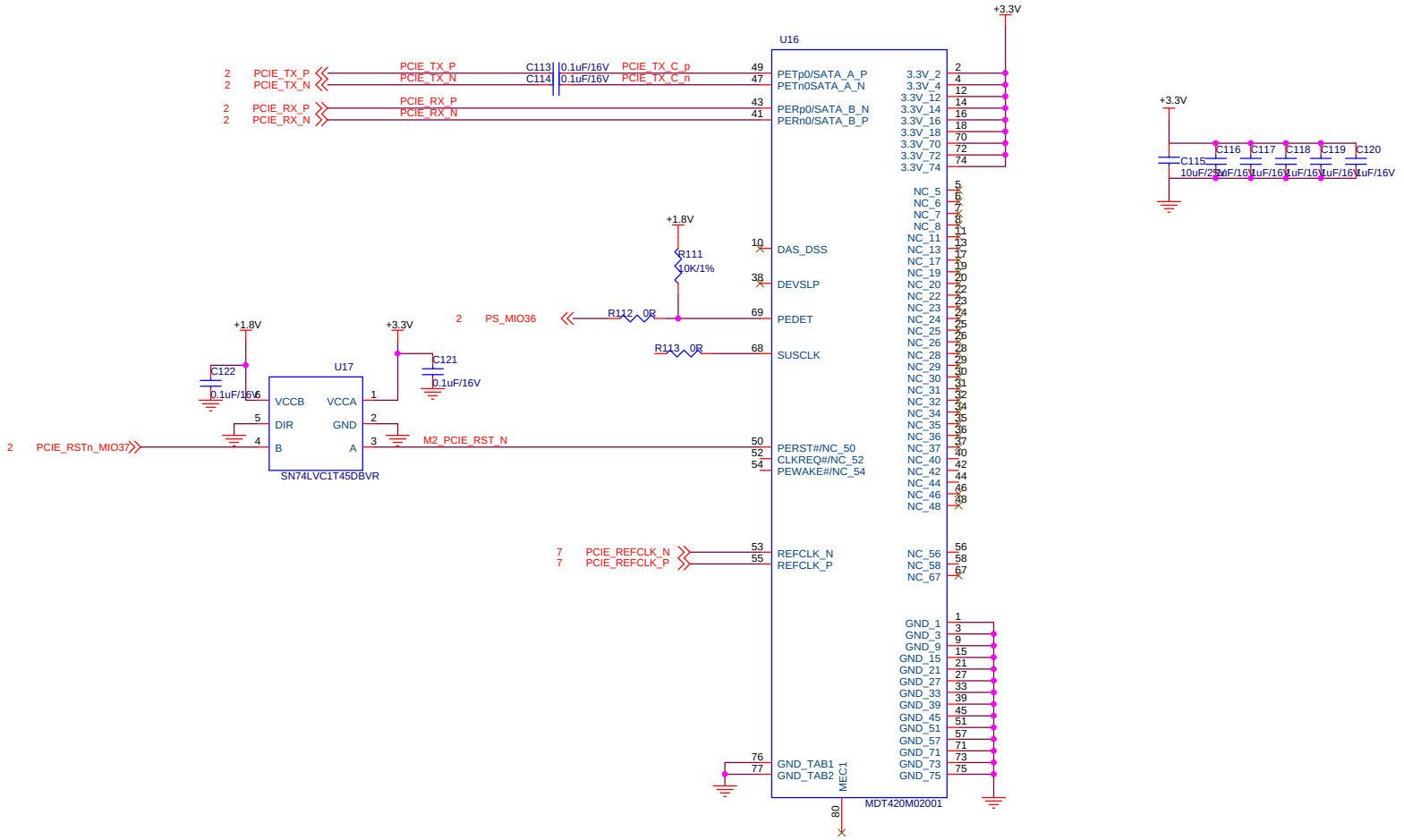

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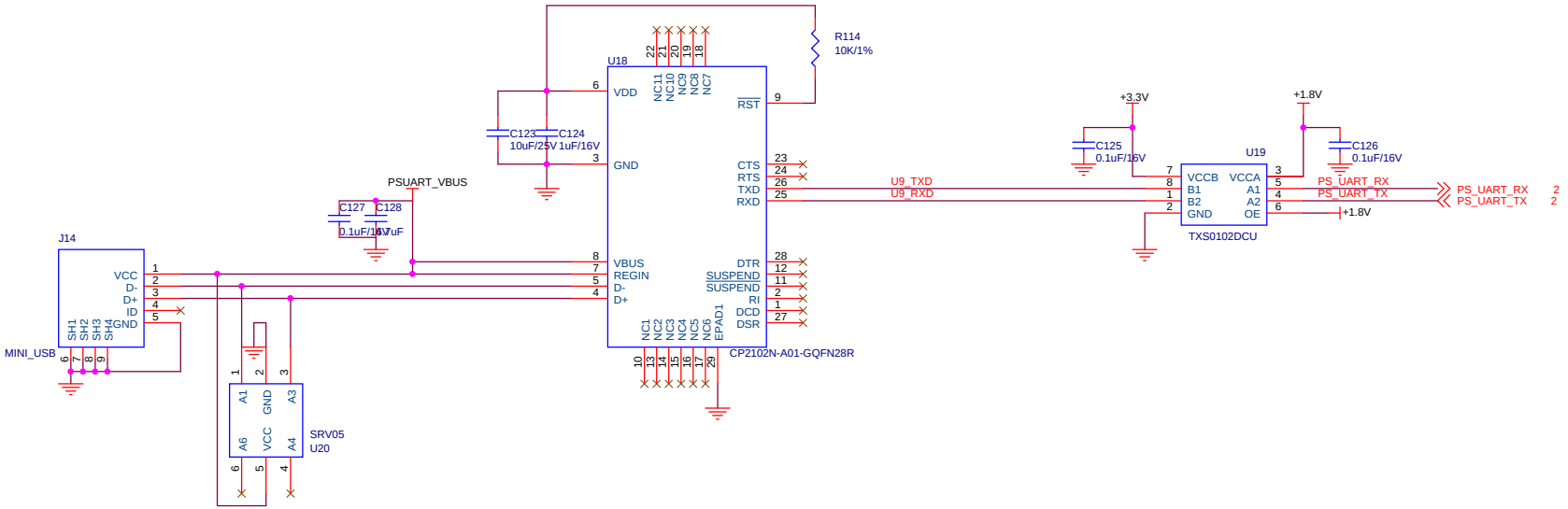
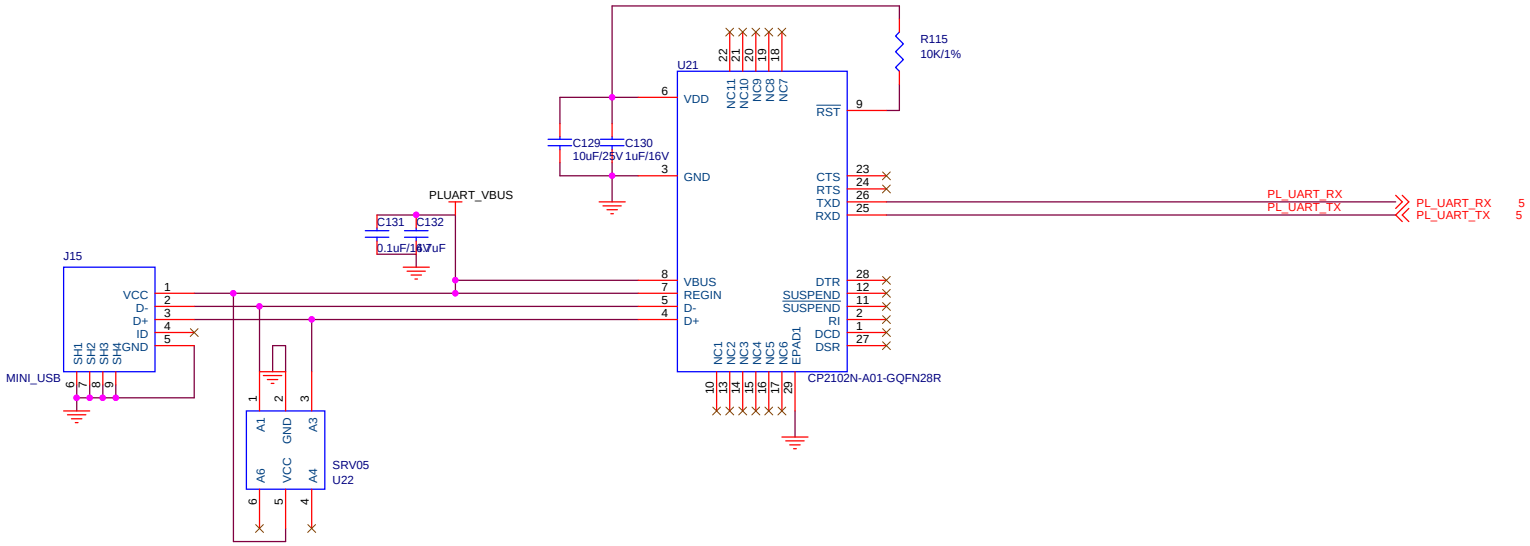
PS UART PORT



PL UART PORT



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ALINX	Http://www.alinx.com
Title	PAGE13 UART
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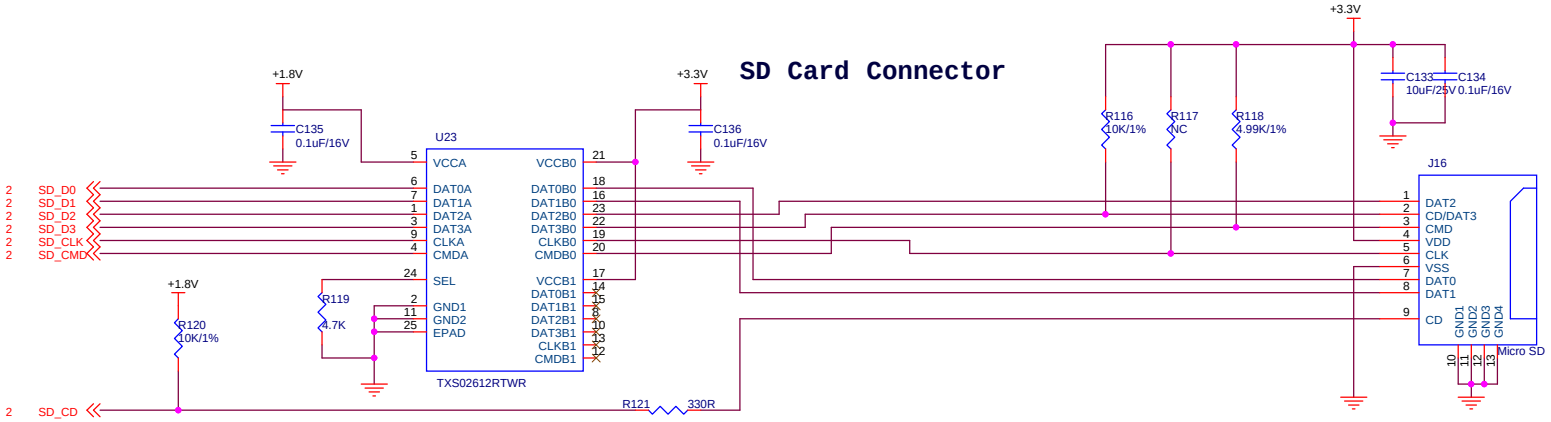
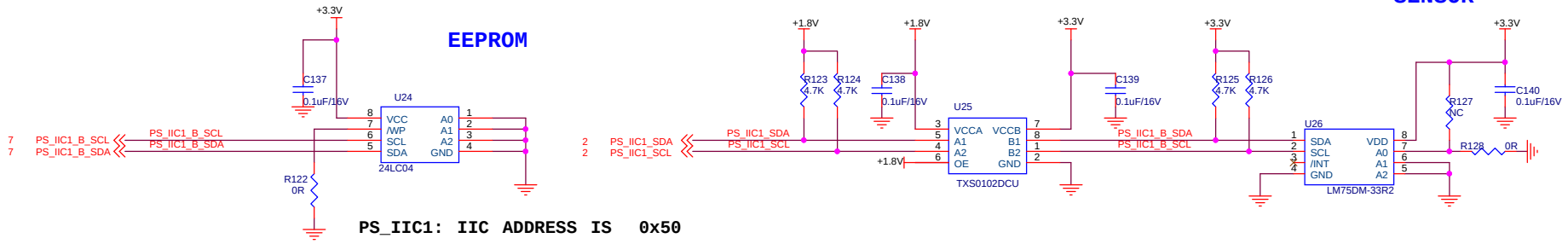
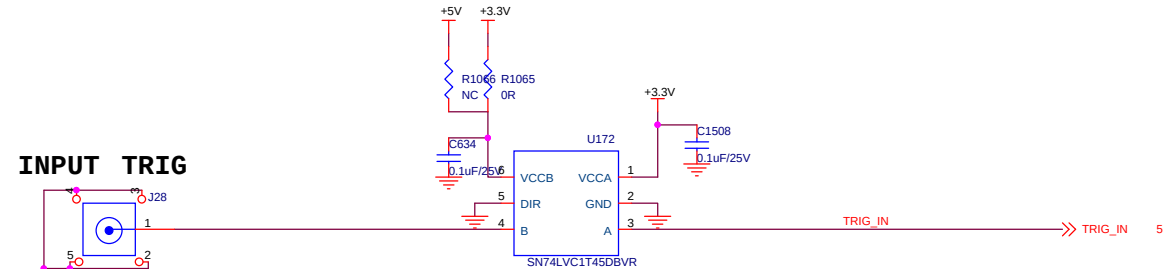
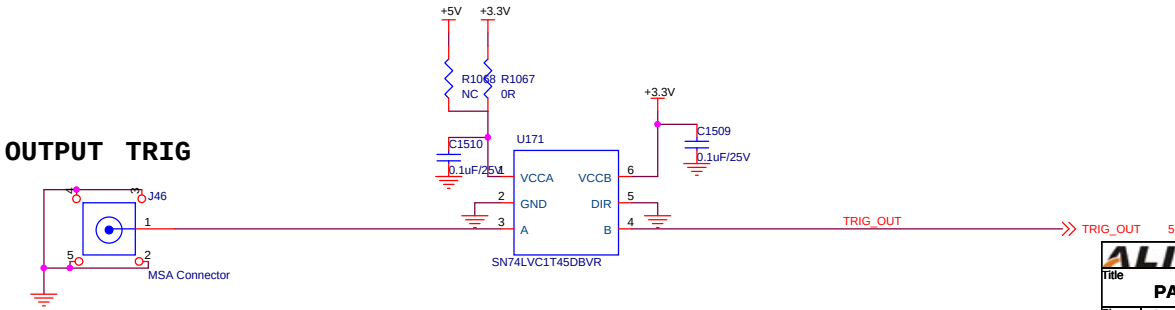




PS_IIC1: IIC ADDRESS IS 0x50





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Title	PAGE14 SD & EEPROM	
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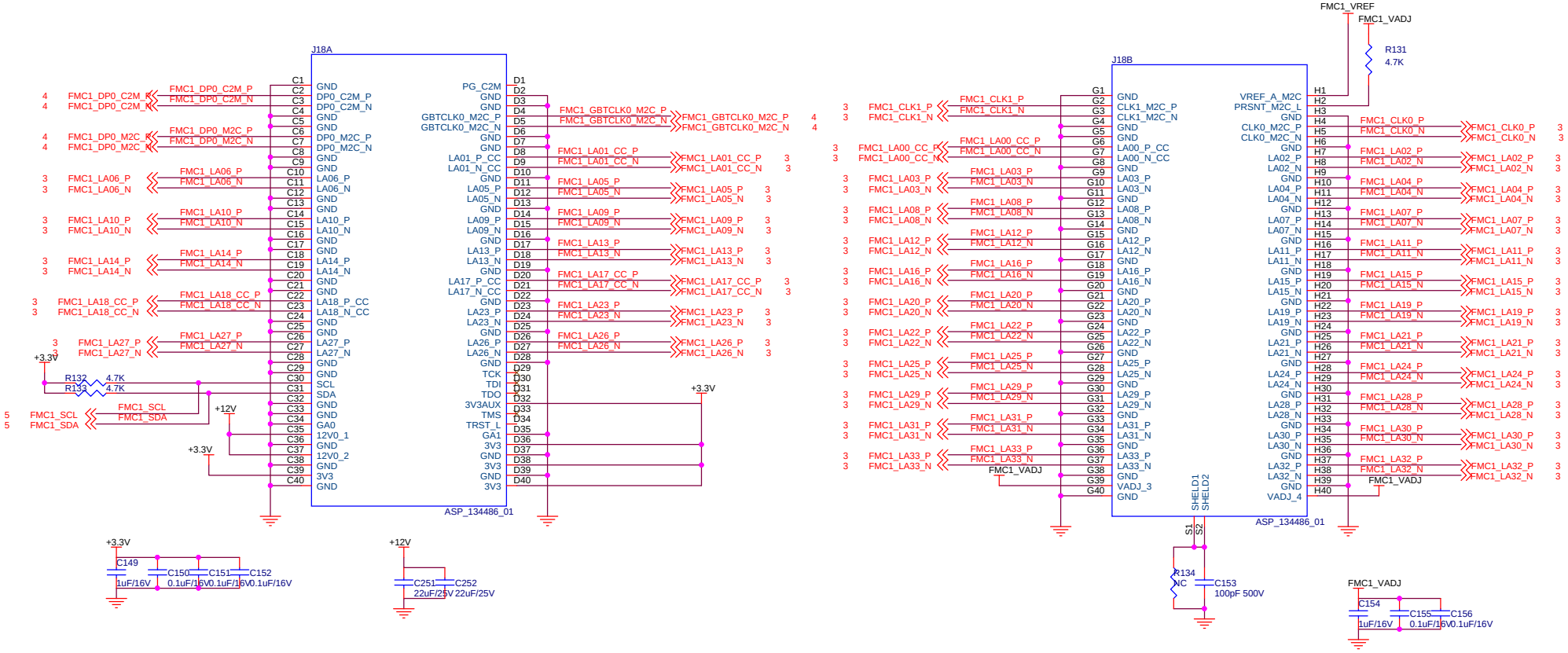

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Title	PAGE15 FMC1_HPC1
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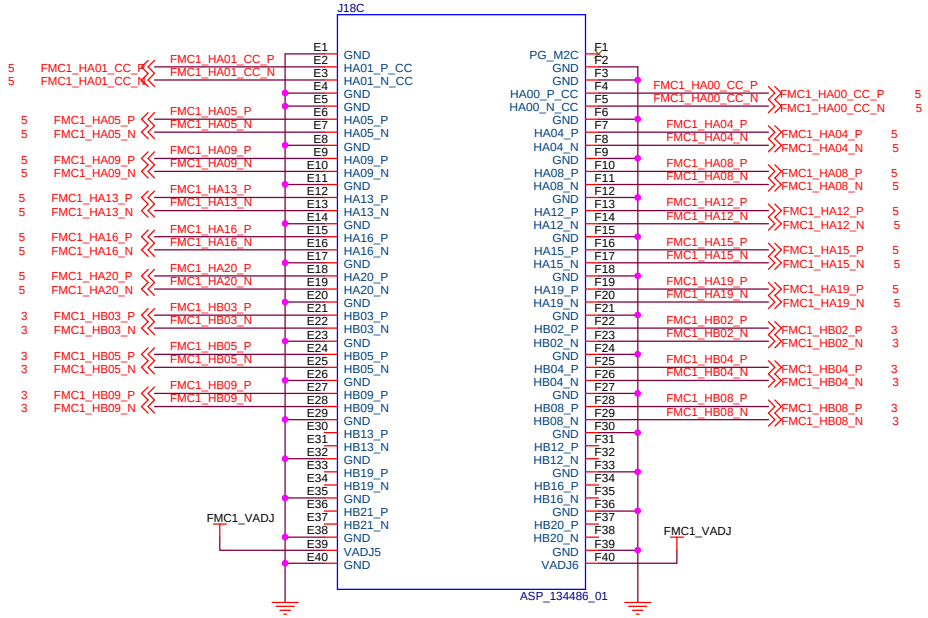

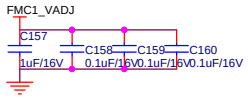

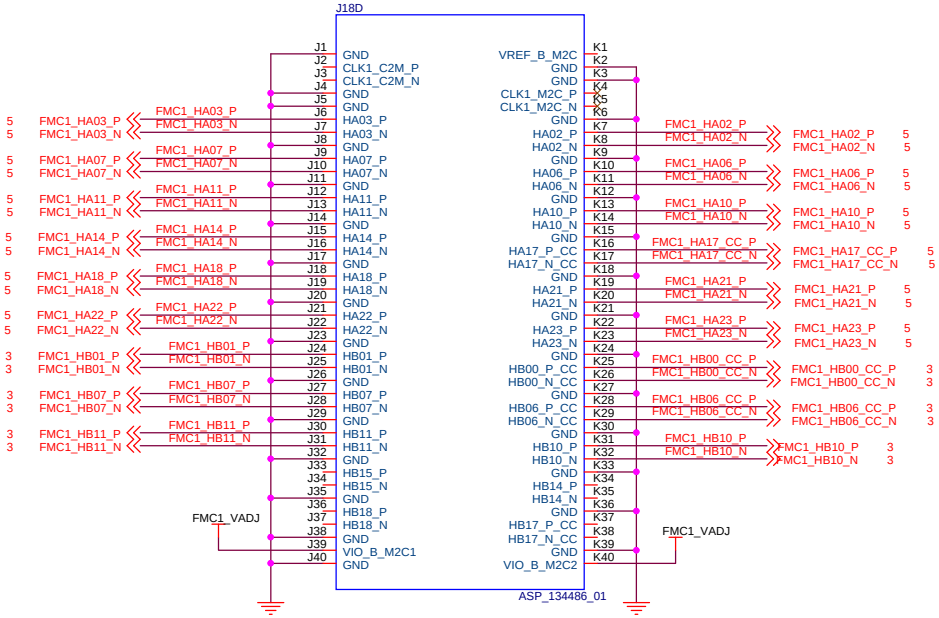

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Title	PAGE16 FMC1_HPC2	
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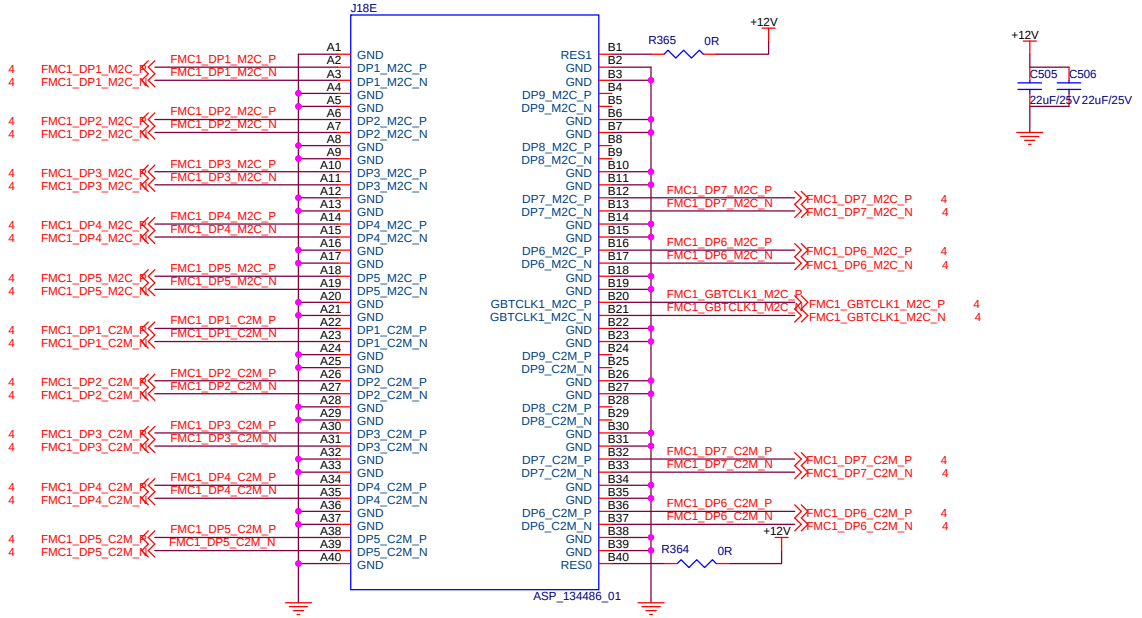

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Title	PAGE17 FMC1_HPC3
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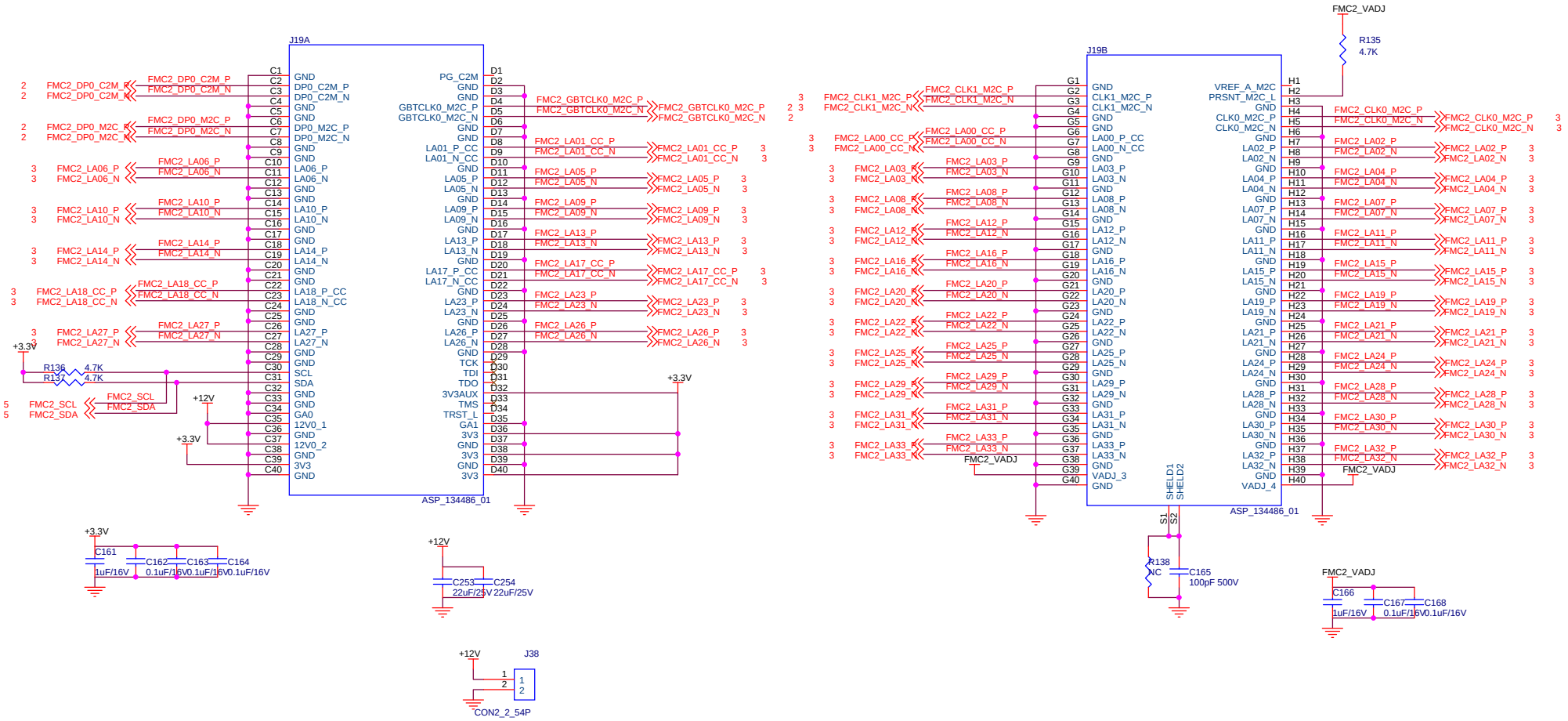

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Title	PAGE18 FMC2_HPC1
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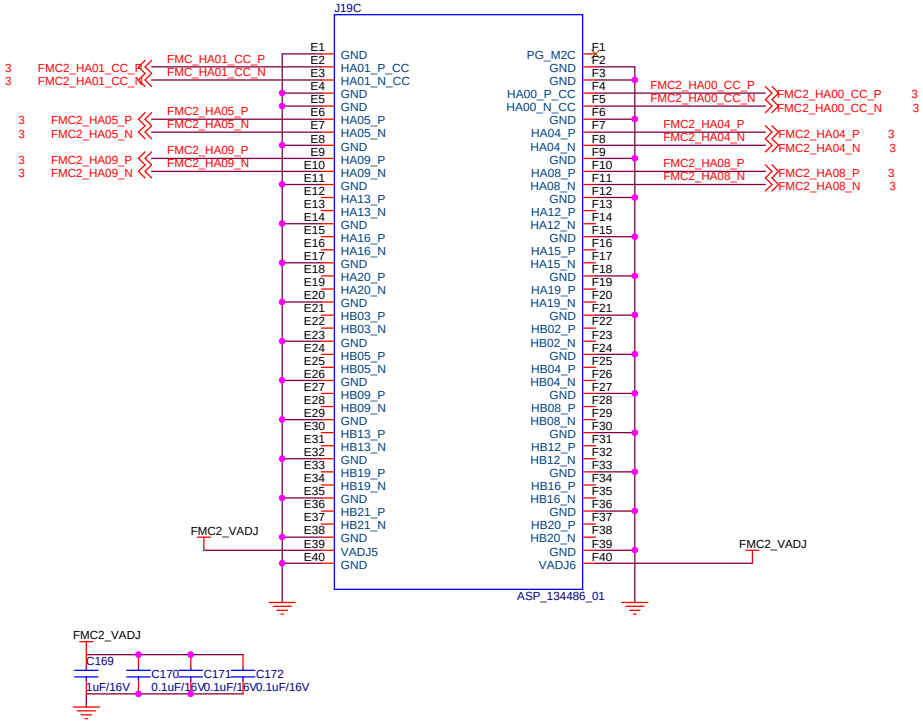

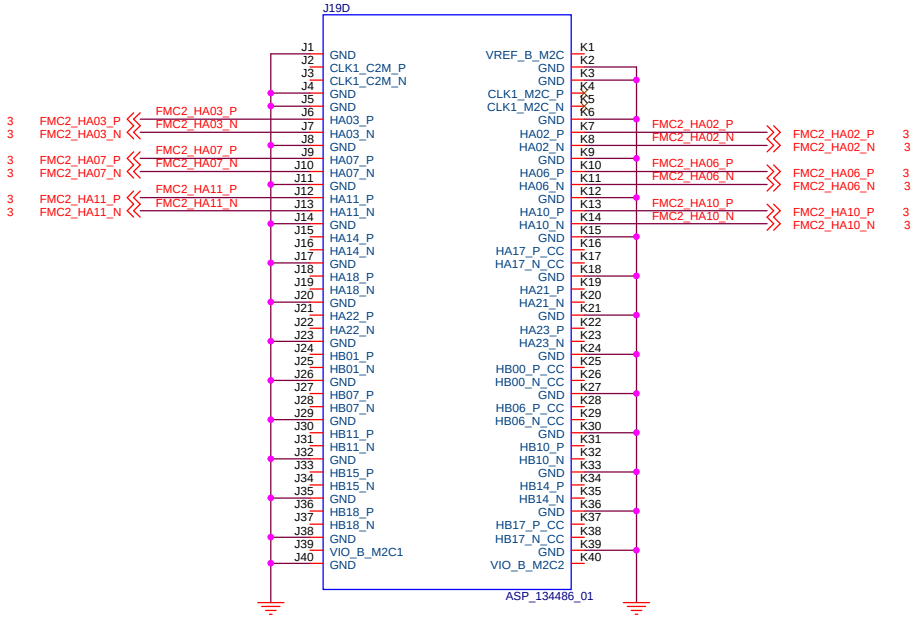

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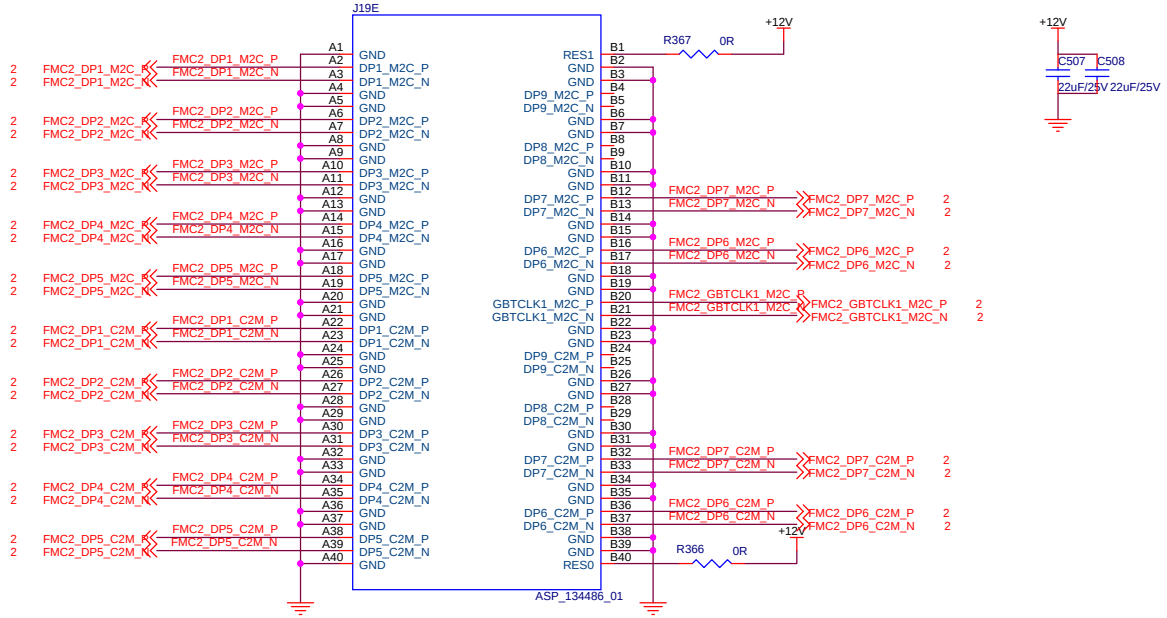

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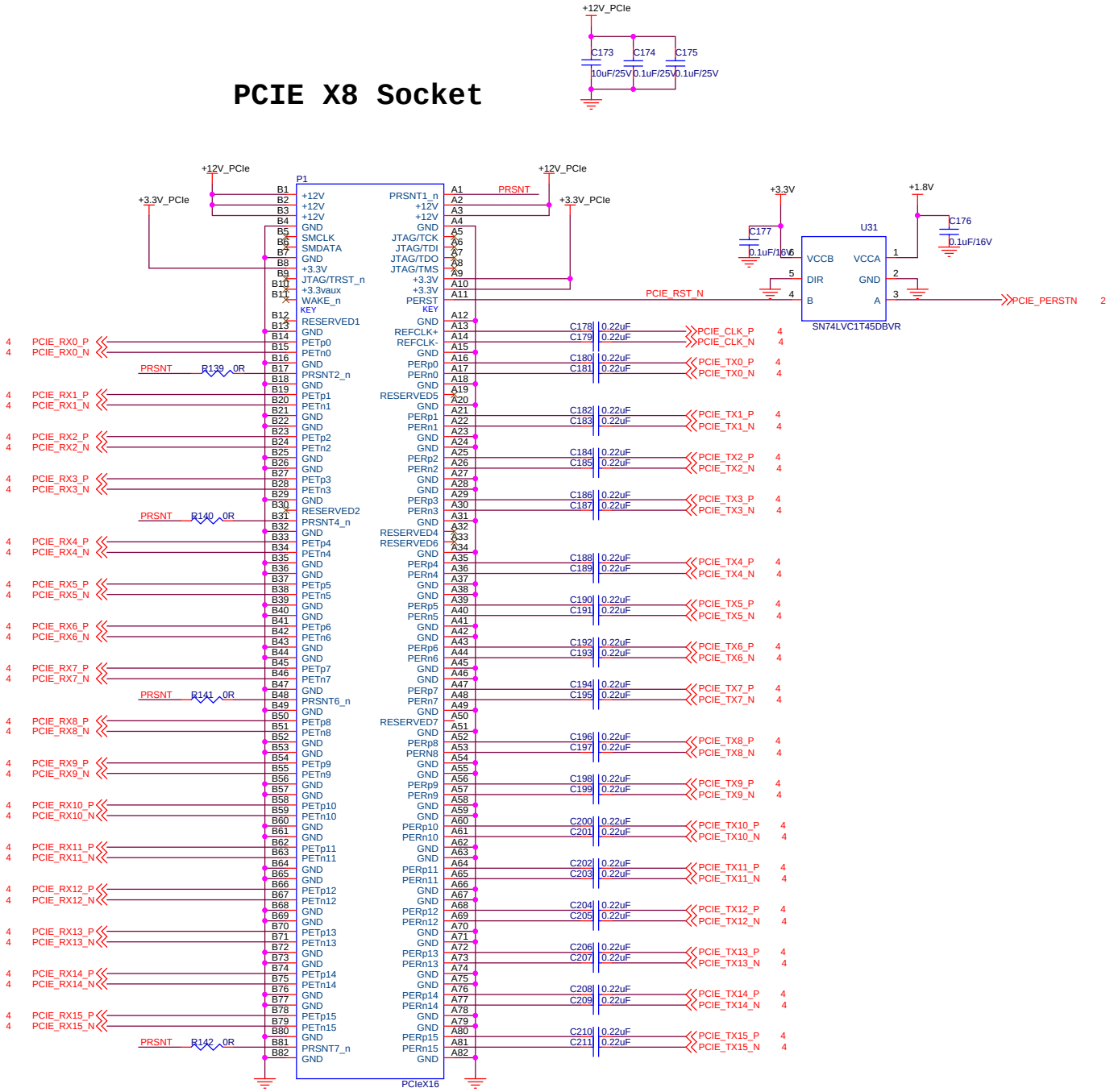
PCIE X8 Socket



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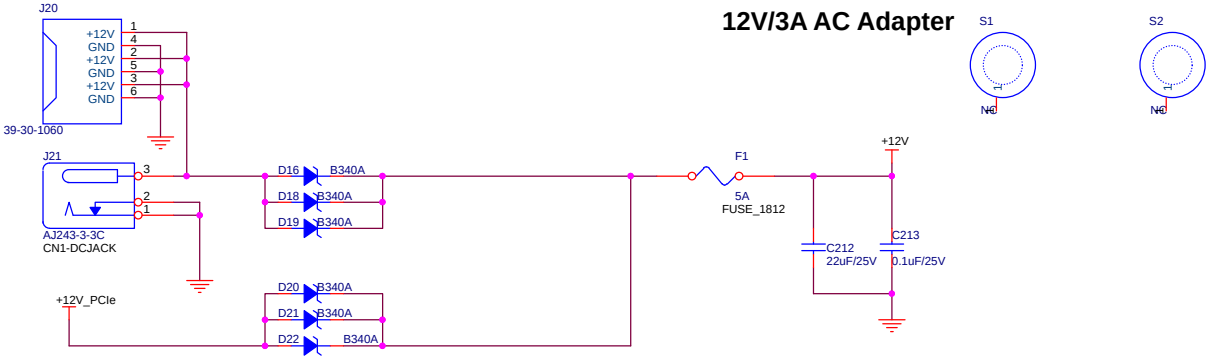
12V/3A AC Adapter



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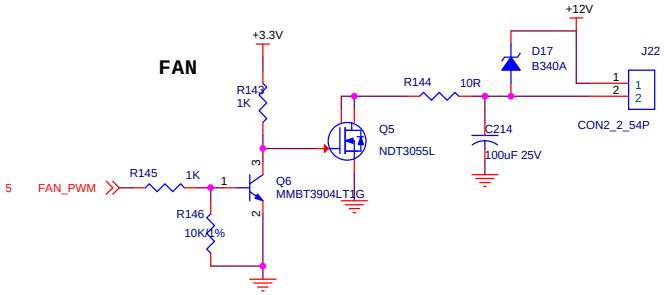
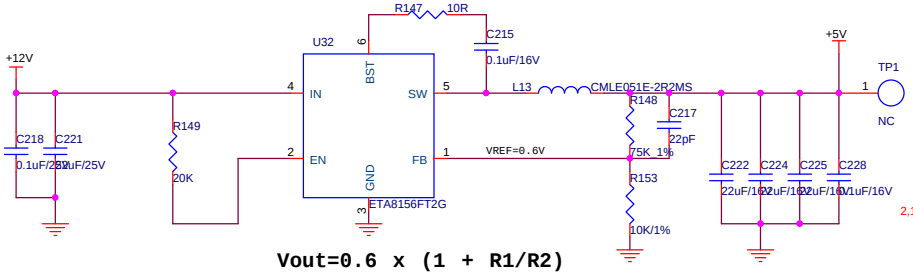
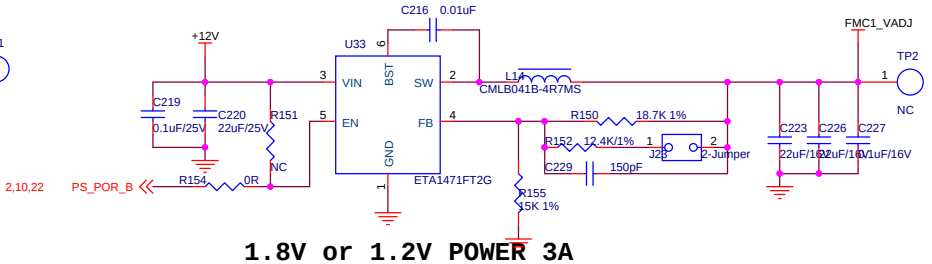


+5V POWER



Vout=0.6 x (1 + R1/R2)

1.8V or 1.2V POWER 3A



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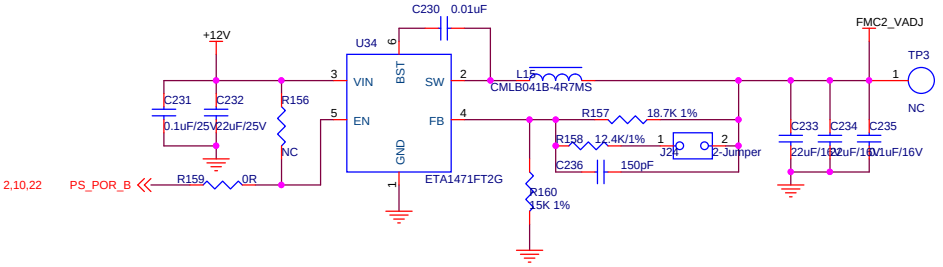
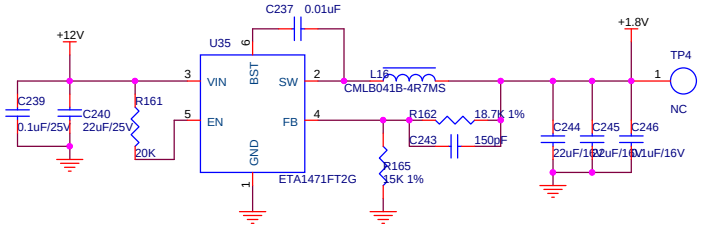
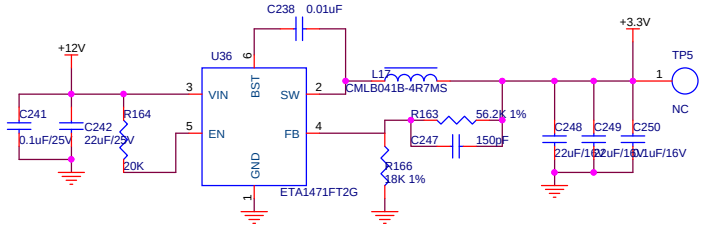


+1.8V POWER



Vout=0.8 x (1 + R1/R2)

+3.3V POWER



Vout=0.8 x (1 + R1/R2)

TP7
NC

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