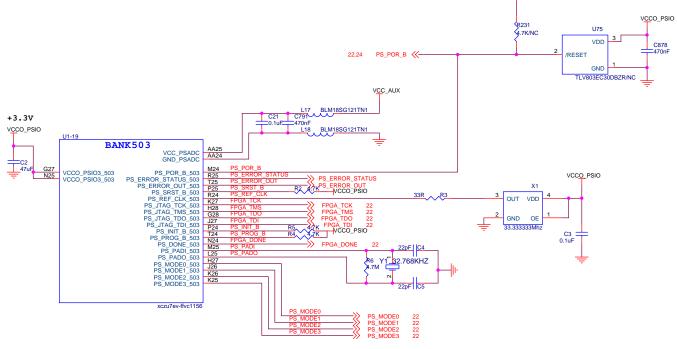
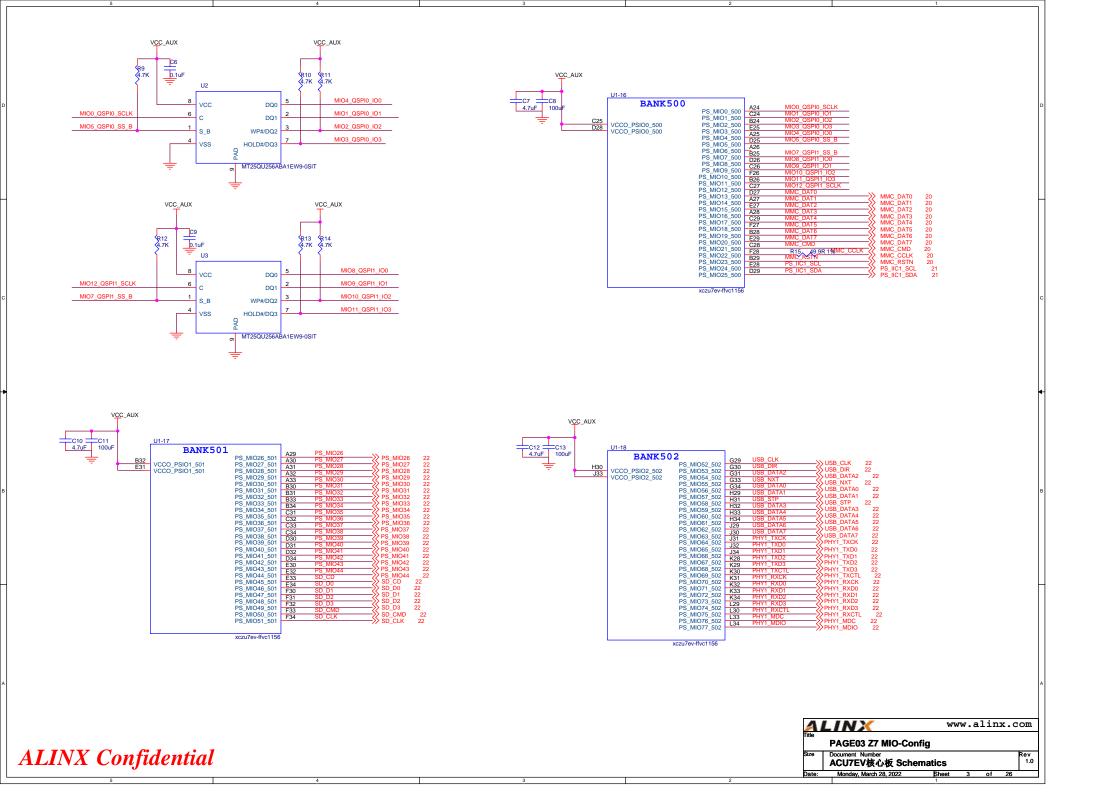
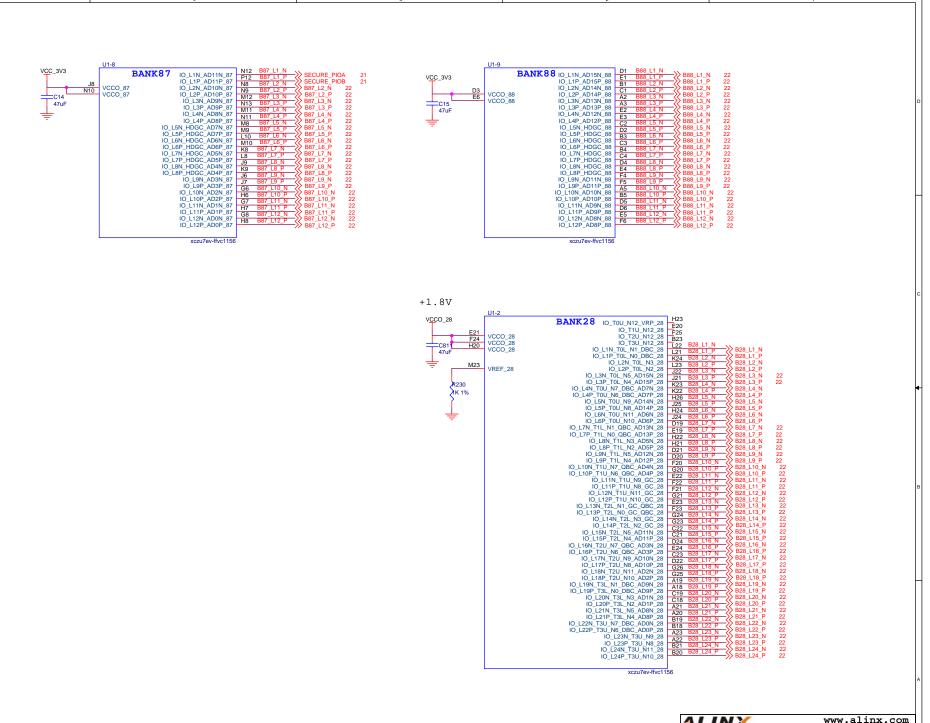


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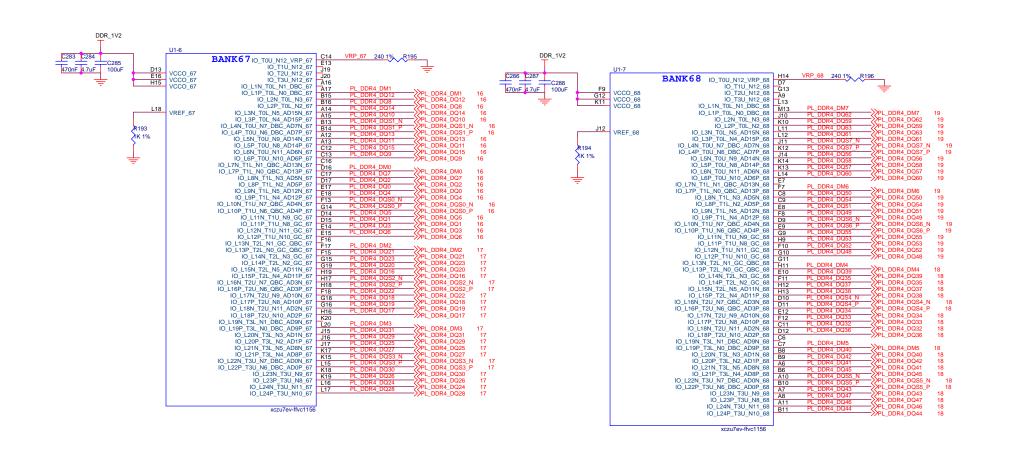






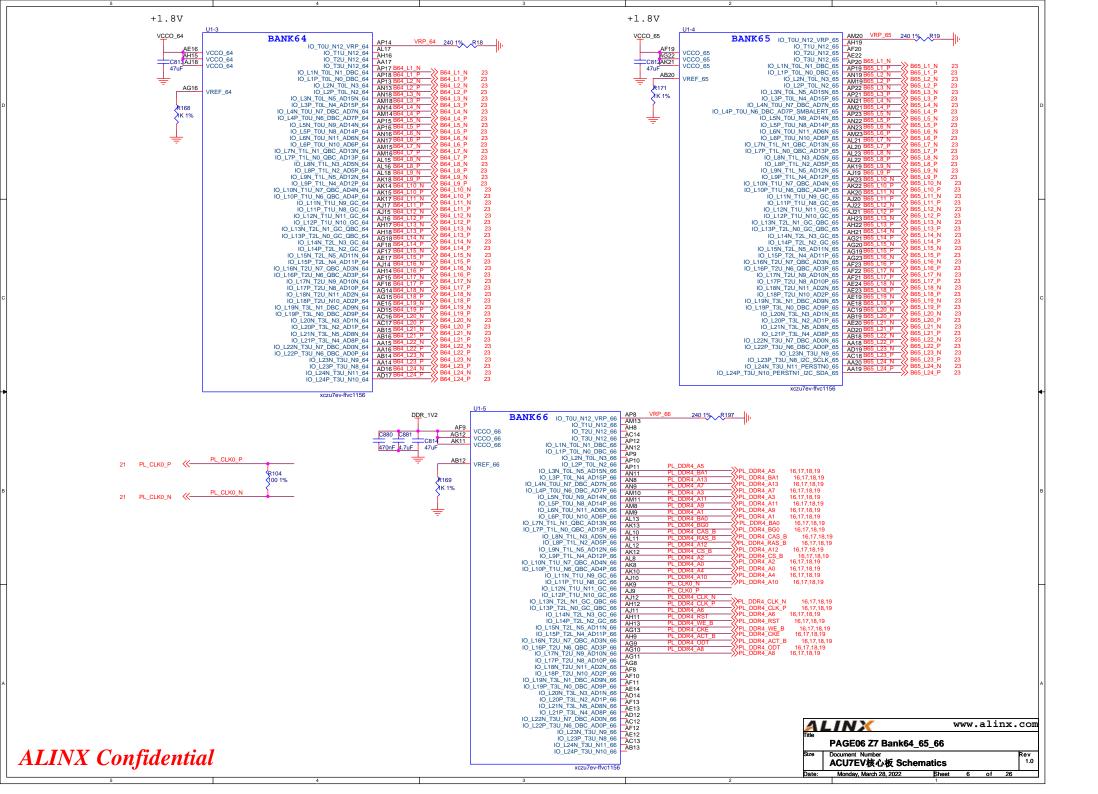


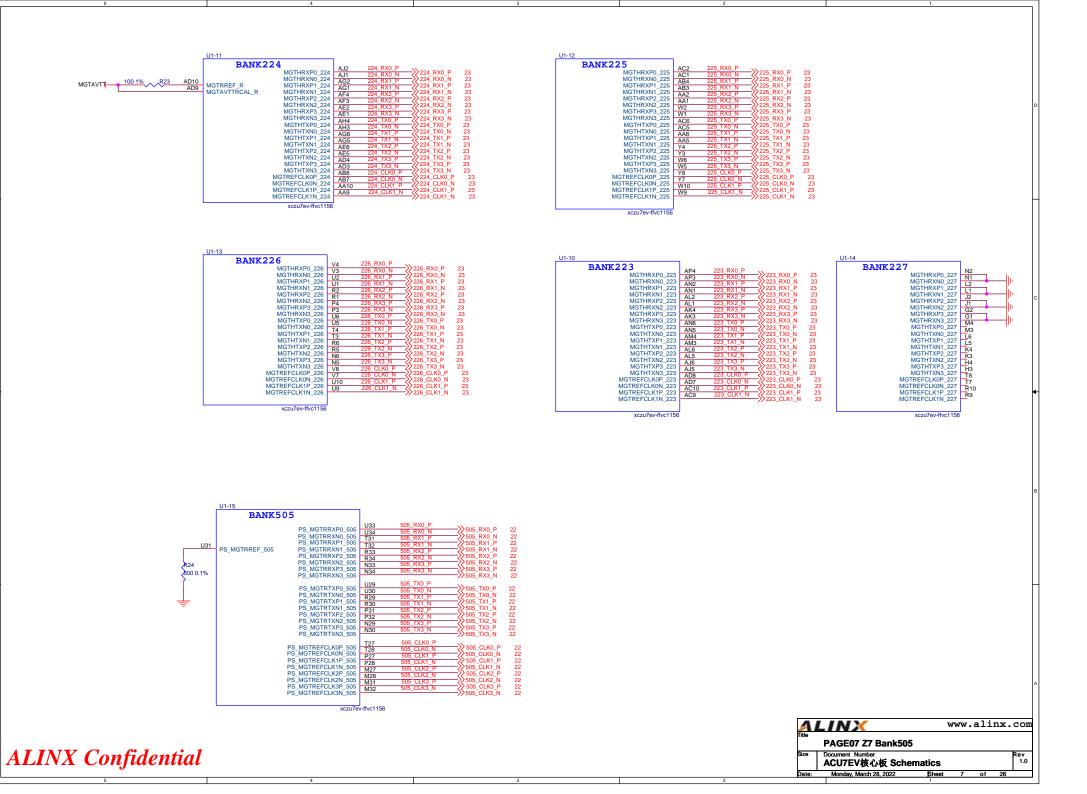
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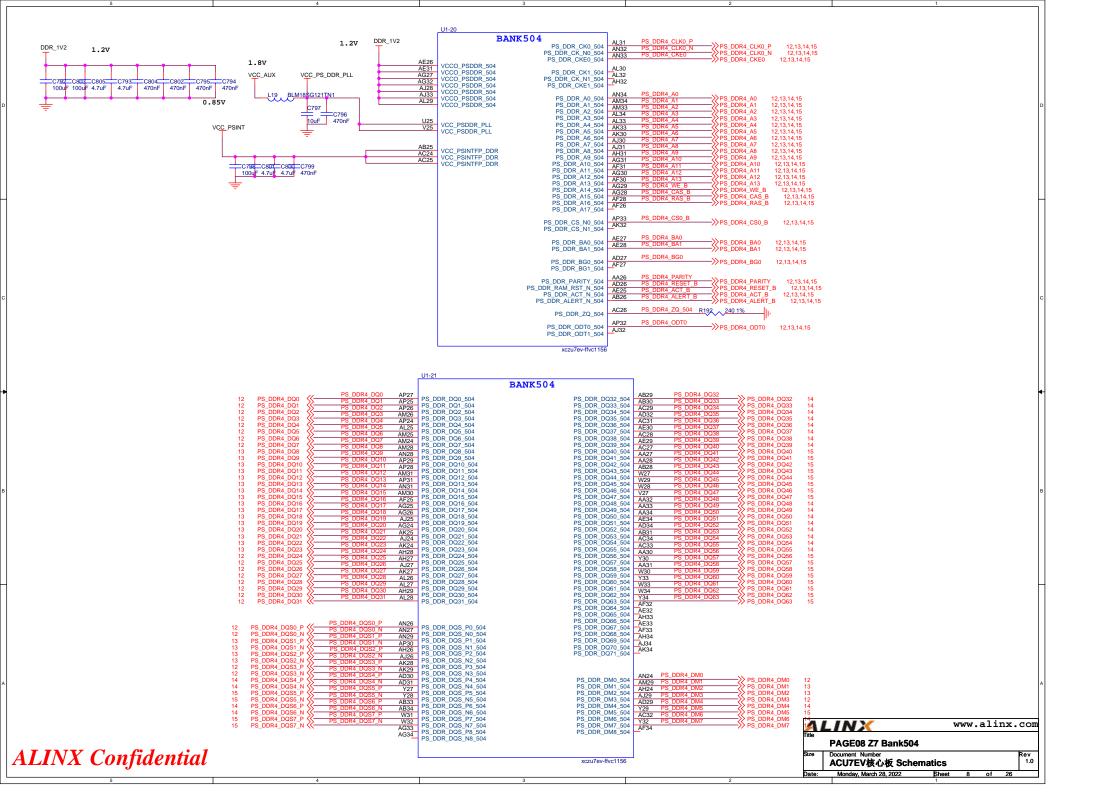


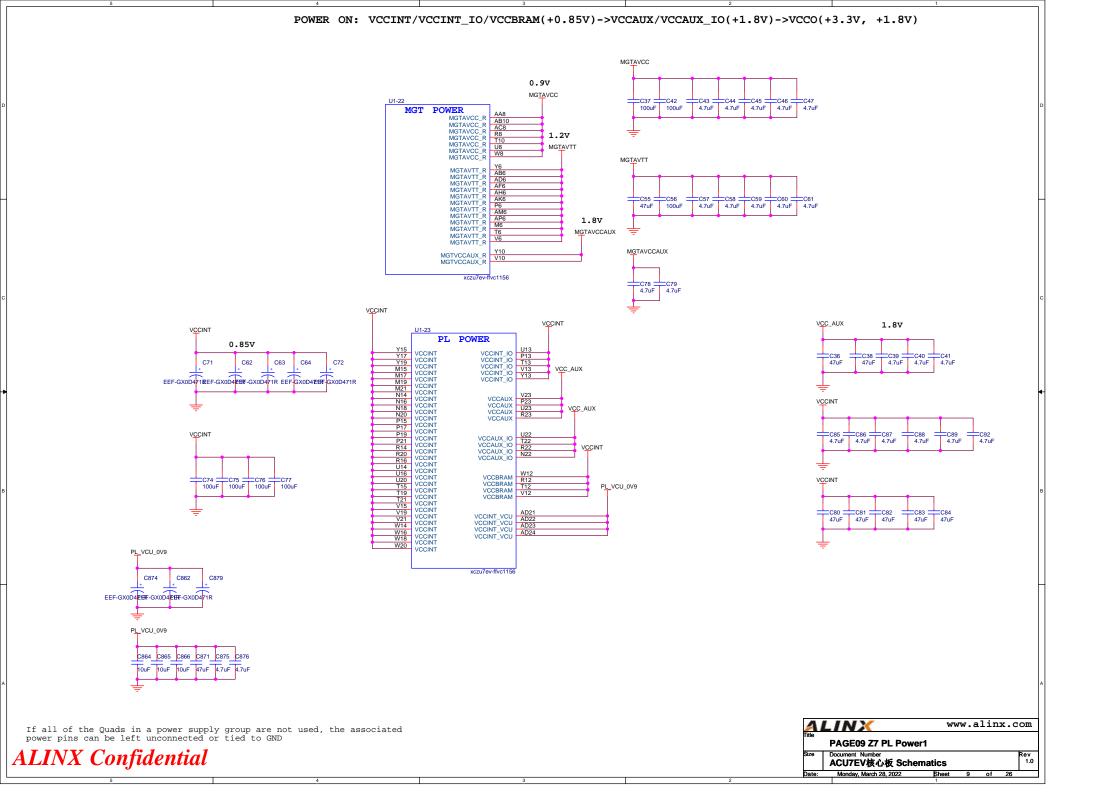
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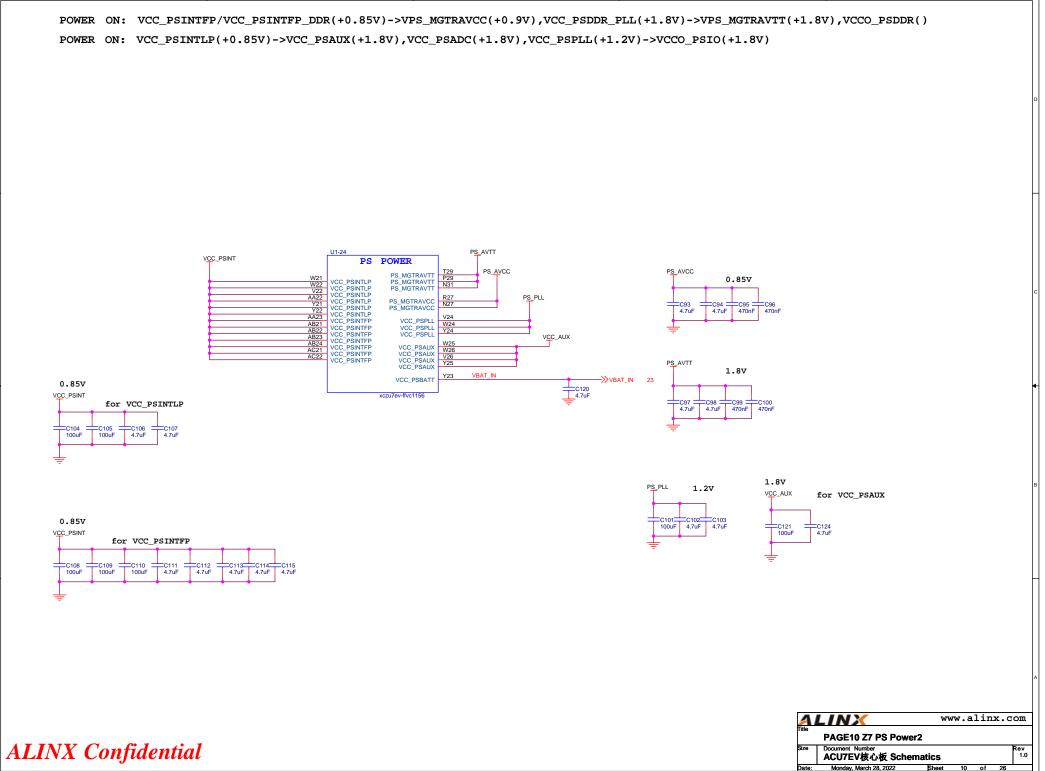
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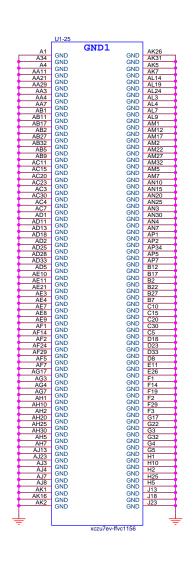


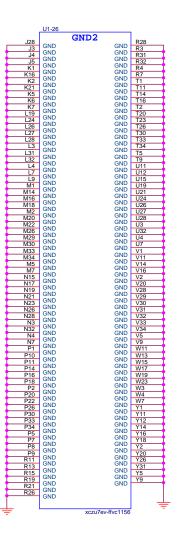


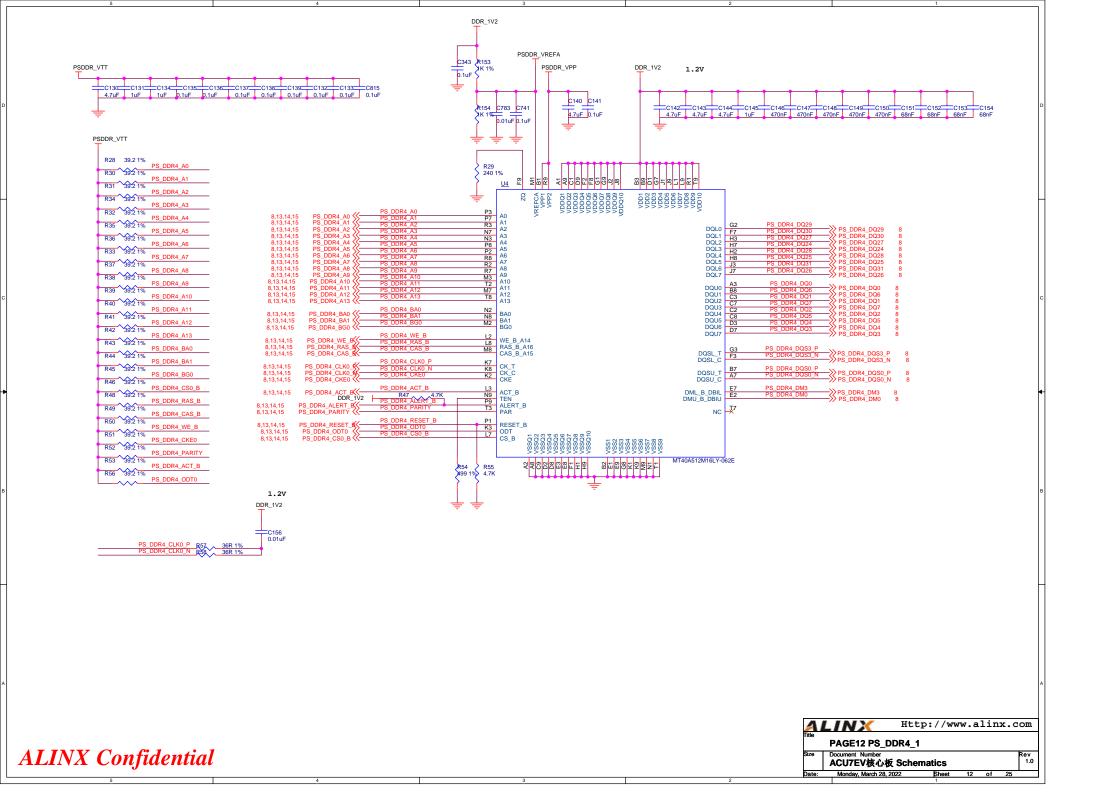


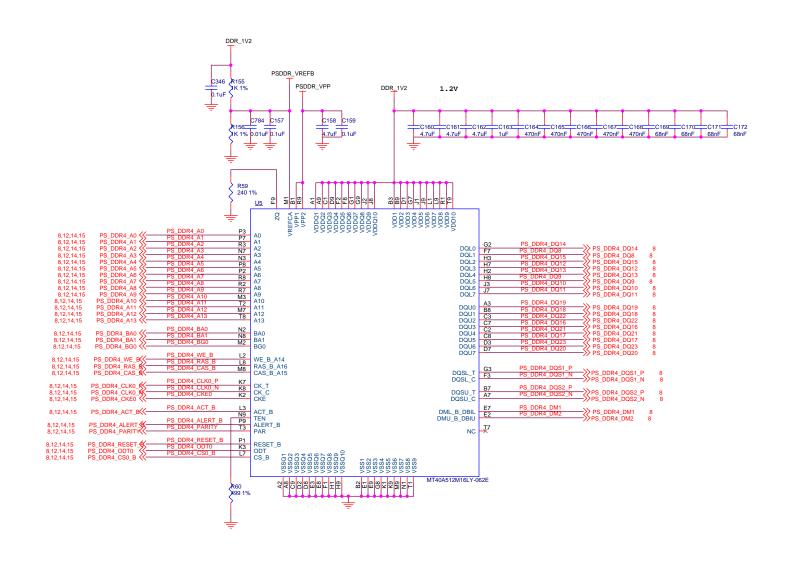








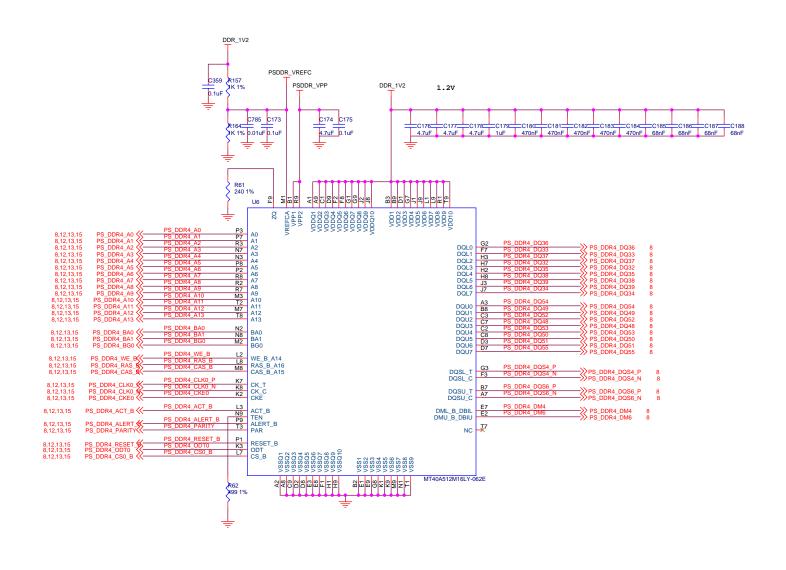




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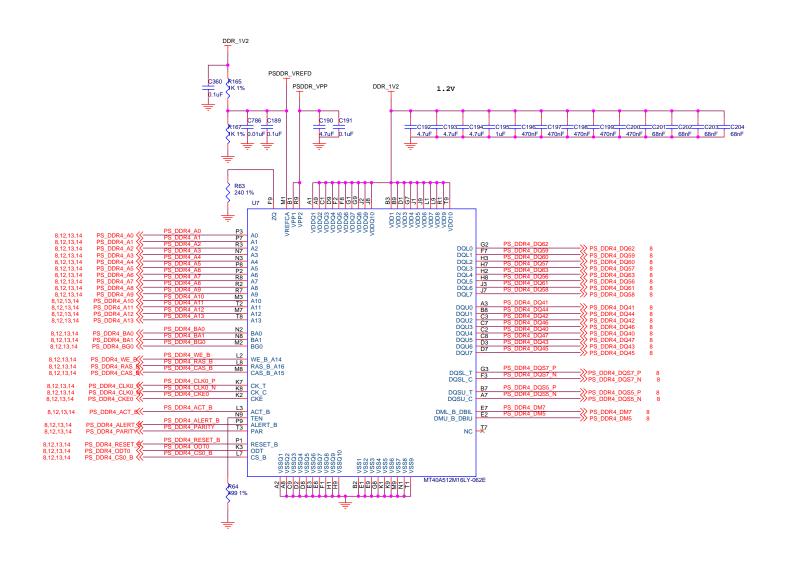
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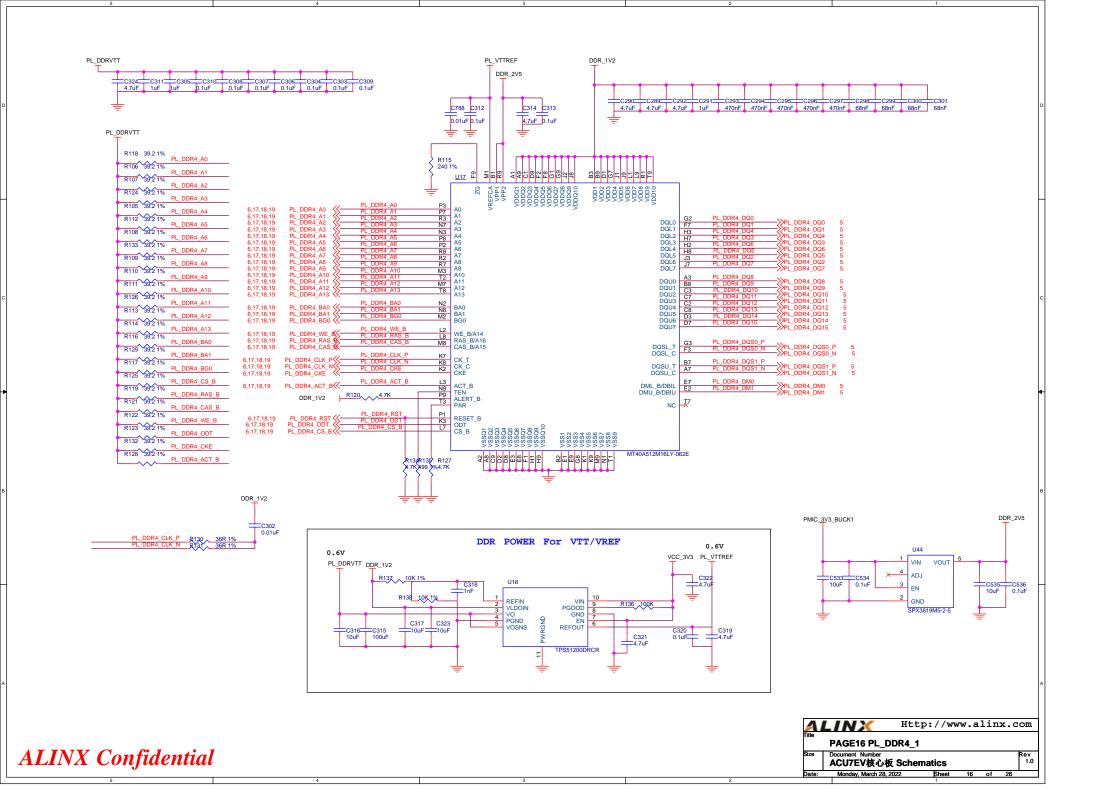


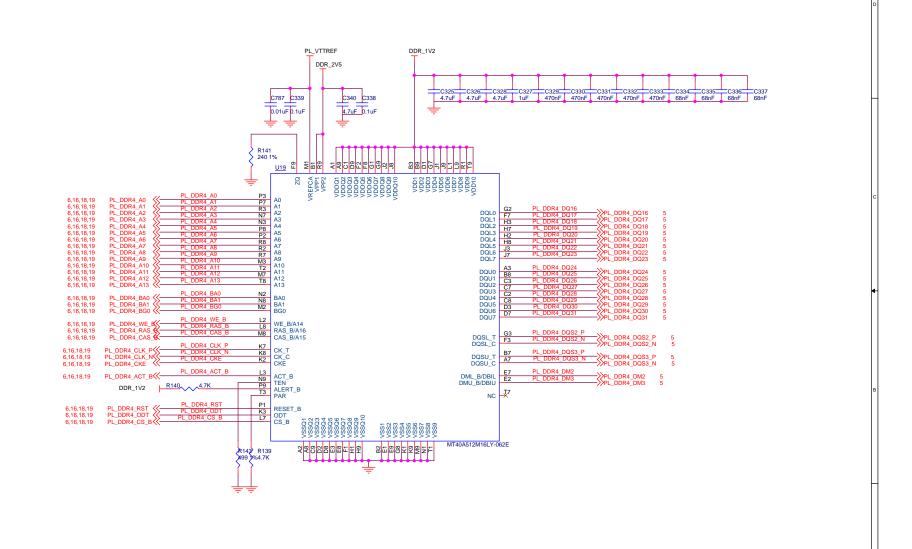
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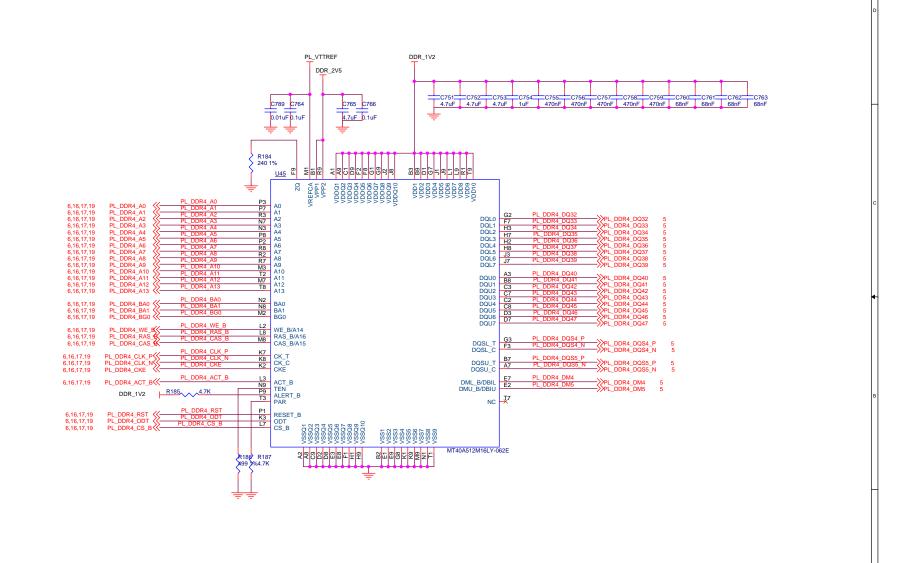


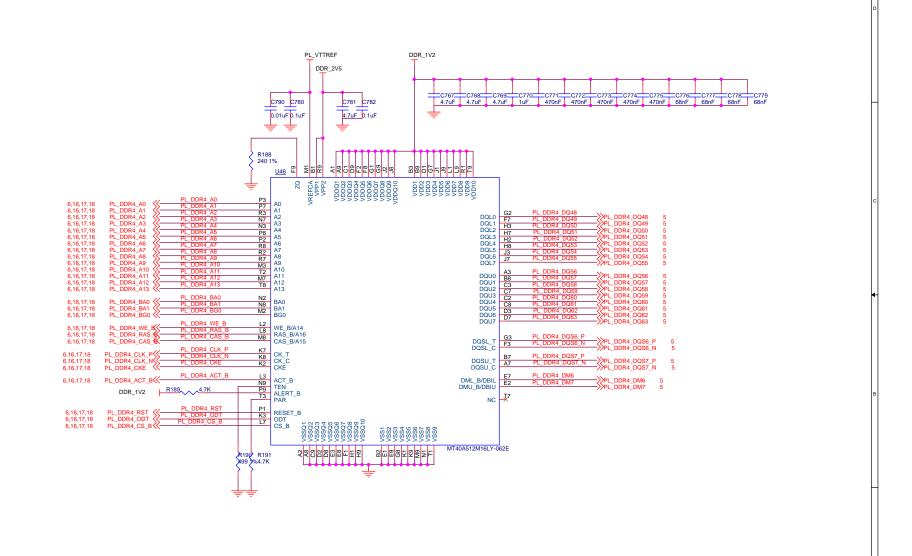
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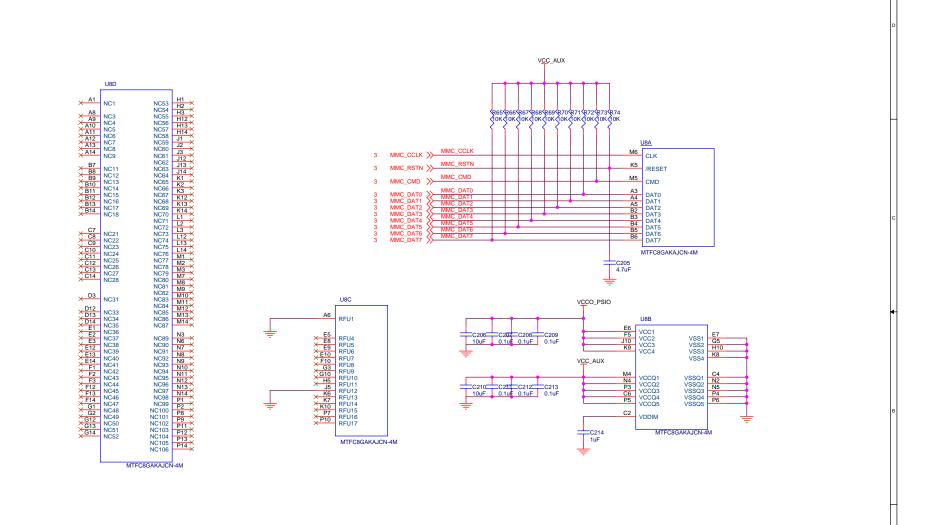
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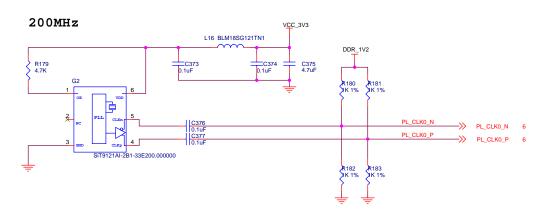
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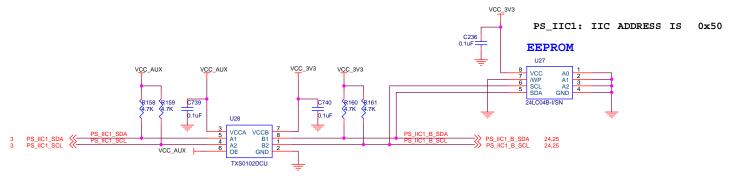
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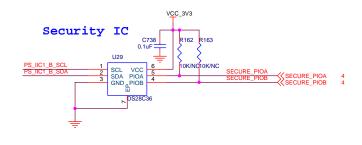
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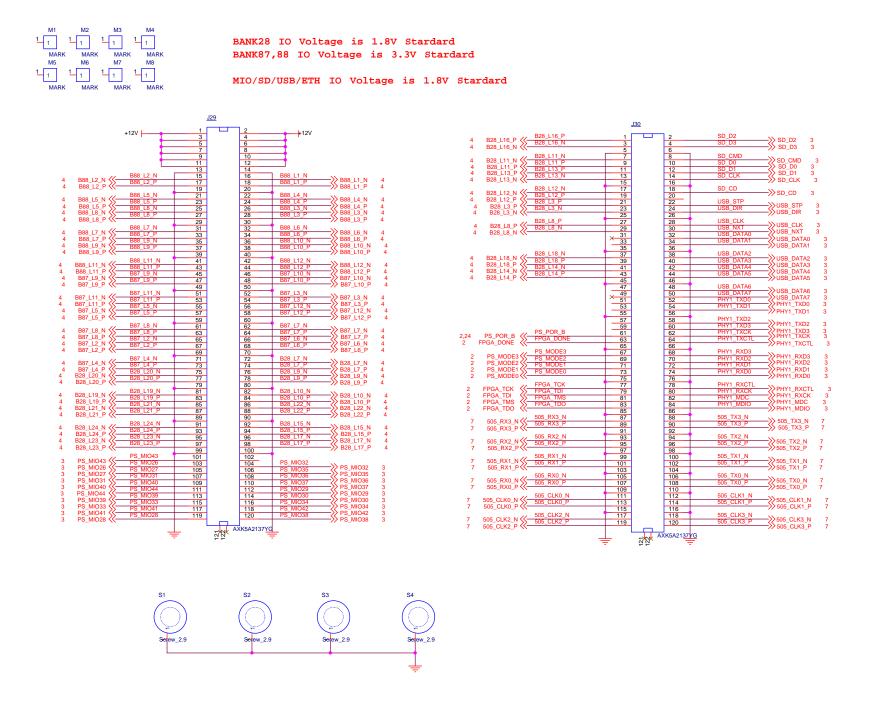
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BANK64,65 IO Voltage is 1.8V Stardard

				J31				
	,,,	POWER_SW	1		2	VBAT_IN	**	
24	POWER_SW S	B65_L24_N	3		4	B65_L2_N	>> VBAT_IN	10
6 6	B65_L24_N	B65_L24_P	5		6	B65_L2_P	B65_L2_N B65_L2_P	6 6
6	B65 L13 N	B65_L13_N	7		8	B65_L18_N		6
6	B65_L13_P <<	B65_L13_P	9		10 12	B65_L18_P	B65_L18_P	6
		B65 L8 N	11		14	B65 L16 N		
6	B65_L8_N <<	B65 L8 P	15		16	B65_L16_P	→>> B65_L16_N	6
_6	B65_L8_P <	B65_L12_N	17		18	B65 L3 N	−>> B65_L16_P	6
6 6	B65_L12_N	B65_L12_P	19		20	B65_L3_P	→>> B65_L3_N	6
0	D05_L12_P ((-		21		22		−>>> B65_L3_P	6
6	B65_L5_N <<-	B65_L5_N B65_L5_P	23		24	B65_L7_N B65_L7_P	→>> B65_L7_N	6
6	B65_L5_P <<	B65_L5_P B65_L10_N	25		26	B65_L7_P B65_L21_N	B65_L7_P	6
6	B65_E10_N <<-	B65_L10_P	27 29		28 30	B65_L21_N	−>> B65_L21_N	6
6	B65_L10_P <<-	D00_E10_1	31		32	BOO_EE1_1	−>>> B65_L21_P	6
		B65_L14_N	33		34	B65 L6 N	**	
6	B65_L14_N \$	B65_L14_P	35		36	B65_L6_P	→>> B65_L6_N	6
6 6	B65_L14_P	B65_L19_N	37		38	B65_L17_N	→>> B65_L6_P →>> B65_L17_N	6 6
6	B65 L19 P	B65_L19_P	39		40	B65_L17_P	B65_L17_P	6
Ŭ	500_2.0_1 ((DOE LAS N	41		42	DOE LA N	// 500_511_1	
6	B65_L15_N <<-	B65_L15_N B65_L15_P	43 45		44 46	B65_L4_N B65_L4_P	→>> B65_L4_N	6
6	B65_L15_P <<-	B65_L15_F	45		48	B65_L11_N	B65_L4_P B65_L11_N	6
6	B65_L20_N <<-	B65_L20_P	49		50	B65_L11_P	→>> B65_L11_N	6
6	B65_L20_P <<-		51		52		−>> B65_L11_P	6
6	B65 L23 N <<-	B65_L23_N	53		54	B65_L1_N	N DOE LA N	
6	B65 L23 P	B65_L23_P	55		56	B65_L1_P	B65_L1_N B65_L1_P	6
6	B65_L22_N X	B65_L22_N	57		58	B65_L9_N	B65_L9_N	6 6
6	B65 L22 P ⟨\-	B65_L22_P	59		60	B65_L9_P	→ B65_L9_P	6
	**	B64_L1_P	61 63		62 64	B64 L9 P		
6	B64_L1_P <	B64 L1 N	65		66	B64 L9 N	→>> B64_L9_P	6
6	B64_L1_N \$	B64_L6_P	67		68	B64_L14_P	→>> B64_L9_N	6_
6	B64_L6_P \$	B64_L6_N	69		70	B64_L14_N	B64_L14_P	6 6
6	B64_L6_N <<-		71		72		→>> B64_L14_N	0
6	B64 L5 P <<-	B64_L5_P B64_L5_N	73		74	B64_L11_P B64_L11_N	→>> B64_L11_P	6
6	B64_L5_N <	B64_L5_N B64_L3_P	75		76	B64_L11_N B64_L4_P	->> B64_L11_N	6
6	B64_L3_P <<-	B64_L3_N	77 79		78 80	B64_L4_N	→>>> B64_L4_P	6
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6	B64_L21_N X	B64_L21_N	89		90	B64_L8_N	→ B64_L8_N	6
	**	B64_L7_P	91		92	B64 L12 P	// B01_E0_11	•
6	B64_L7_P <<	B64 L7 N	93 95		94 96	B64 L12 N	→>>> B64_L12_P	6
_6	B64_L7_N <<-	B64_L10_P	97		98	B64_L16_P	→>> B64_L12_N	6
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6	B64_L18_P <<-	B64_L18_P B64_L18_N	107 109		108 110	B64_L17_P B64_L17_N	>>> B64_L17_P	6
6	B64_L18_N <<-	D04_L10_N	111		112	B04_L17_IN	→>> B64_L17_N	6
		B64 L22 P	113		114	B64 L19 P	XX == = =	_
6	B64_L22_P	B64_L22_N	115		116	B64_L19_N	B64_L19_P	6
6 6	B64_L22_N \$	B64_L13_P	117		118	B64_L23_P	B64_L19_N	6 6
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