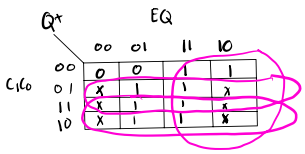
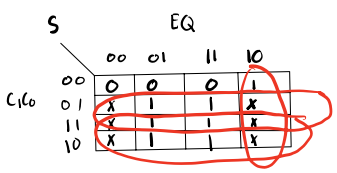
CONTROL UNIT

$S_1(B) = LDB$ $\quad S_0(B) = LDB + S$

$S_1(A) = LDA$ $\quad S_0(A) = LDA + S$



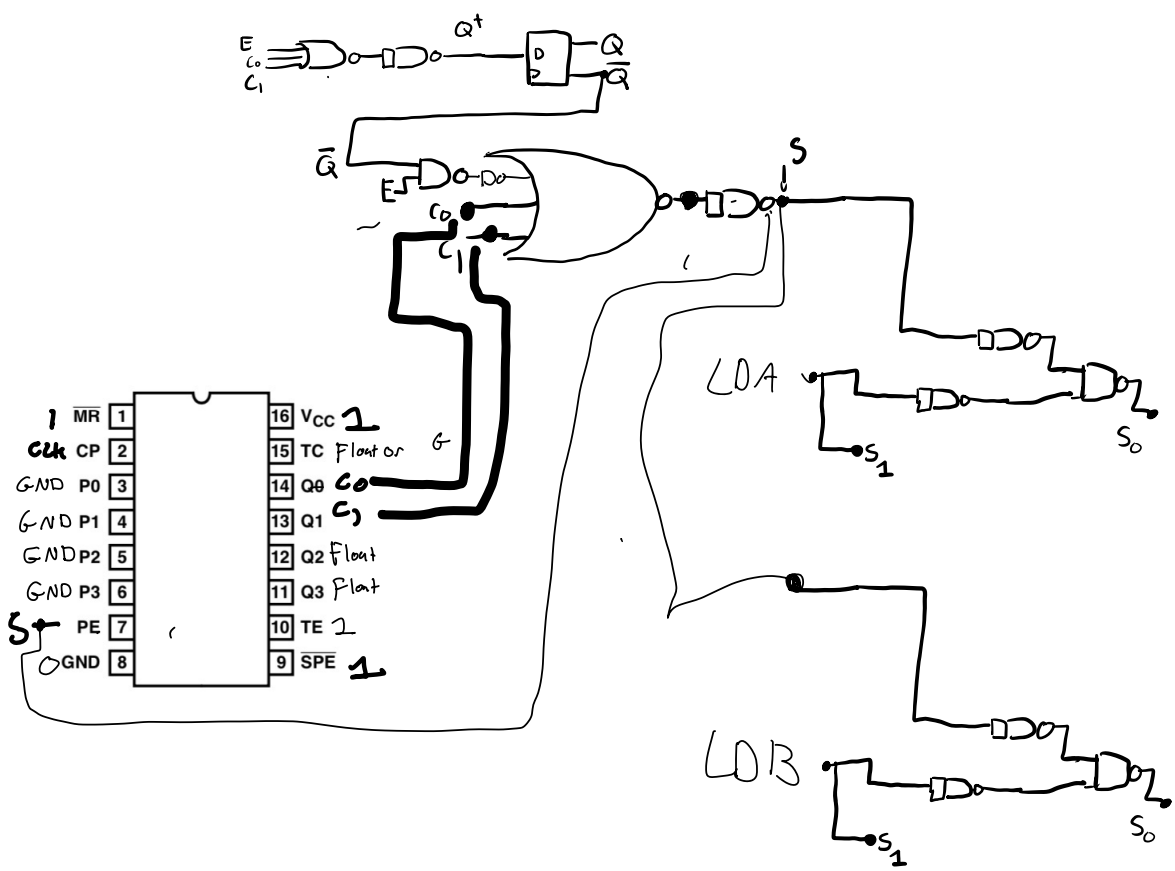
$Q^+ = E + C_0 + C_1$



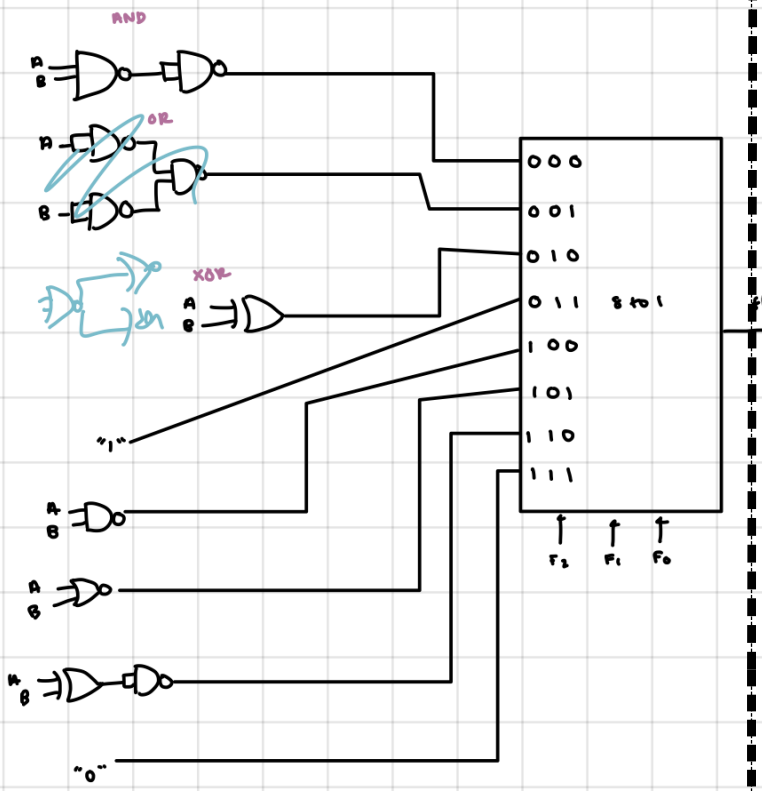
$S = C_1 + C_0 + EQ$

$S_0 = LD + S$

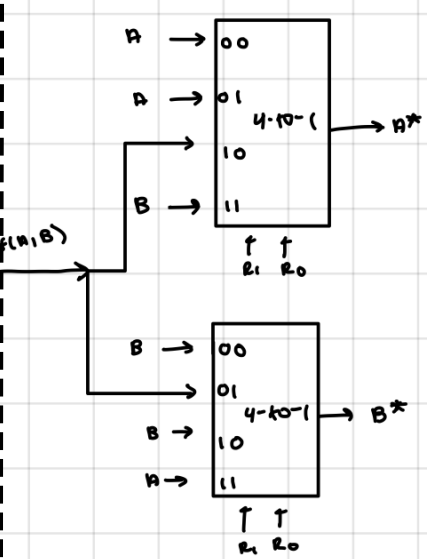
$S_1 = LD$



ALU

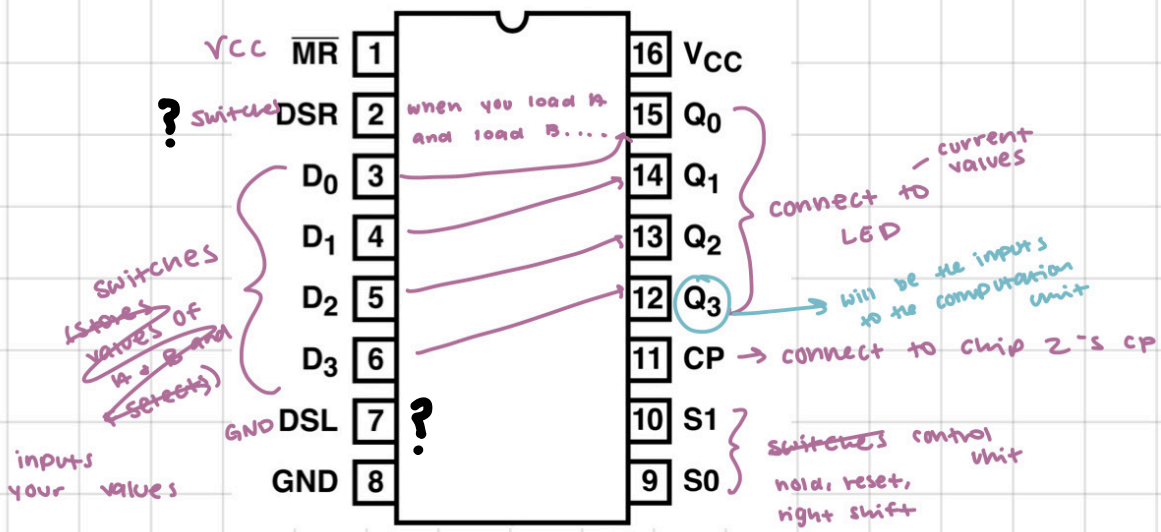


Routing Unit

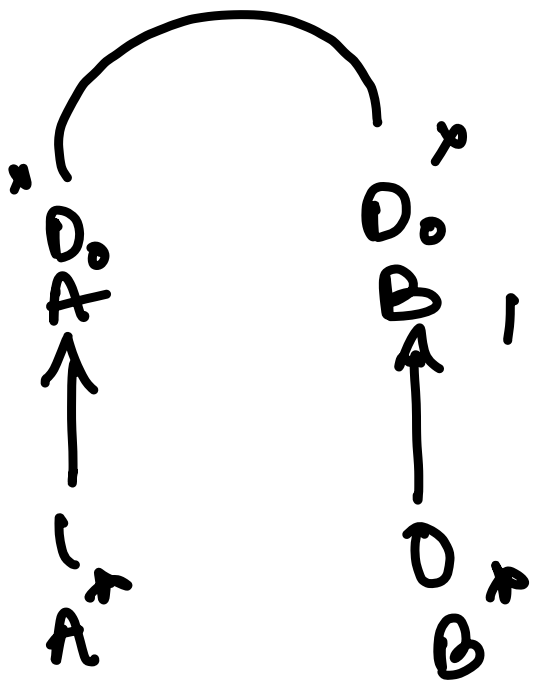


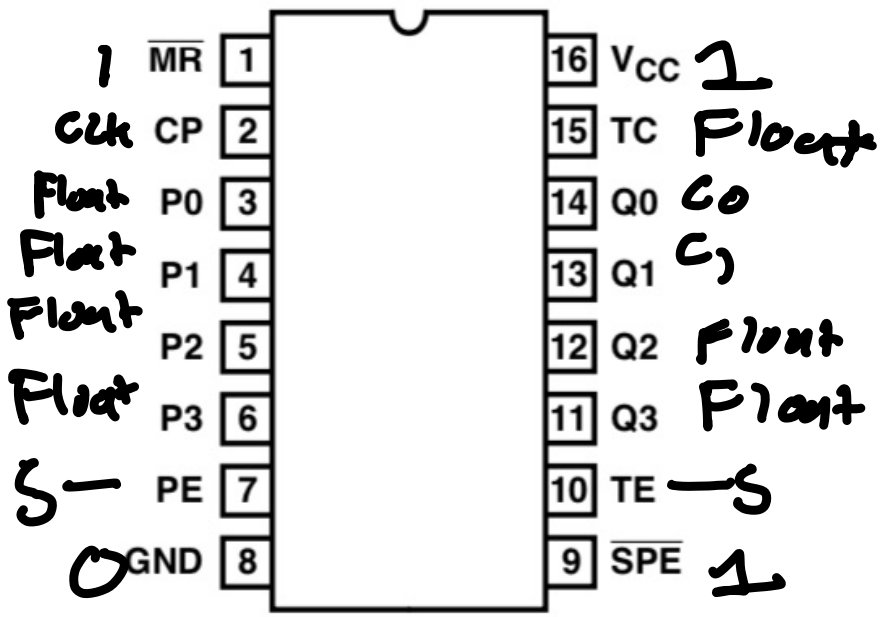
Register Unit

Still have
- CLK
- chip connections



- D0 - D3 have the input that you do manually
- S1 S0 are switches that tell the register to parallel load bits into current state (Q3 - Q0)
- load A + load B load the current
- move D0 - D3 to somewhere on board, do some logic, and then connect the output of logic to S1 and S0




1
MR
1
16
VCC
1
Clk
CP
2
15
TC
Float
Float
P0
3
14
Q0
C0
Float
P1
4
13
Q1
C1
Float
P2
5
12
Q2
Float
Float
P3
6
11
Q3
Float
S
PE
7
10
TE
S
0
GND
8
9
SPE
1