

- Data\_out
- Clock
- Reset
- Data\_in
- Clock
- Reset
- Data\_in
- calc[3:0]
- Data\_out
- state[1:0]
- A[1:0]
- B[1:0]
- C[3:0]
- opcode[1:0]
- i[31:0]
- n[31:0]
- sum\_op[1:0]
- sub\_op[1:0]
- mul\_op[1:0]
- div\_op[1:0]
- status1[1:0]
- status2[1:0]
- data1[1:0]
- data2[1:0]

