EMBEDDED SYSTEM DEVELOPER ENGINEER · HARDWARE DESIGNER ON FPGAS

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Job Application for Middle Manager

Dear Mr./Ms./Dr. LastName, [R]

About Me

I'm a qualified embedded system developer engineer with 4+ years of experience particularly in HDL circuit design and real-time processor-based systems development. I have practical experience in the customization and configuration of Linux kernel, U-Boot, and RTOS on Xilinx and TI cutting-edge SoCs (Zynq, ZynqMP, Keystone I, and II). Also, I'm an expert in developing hardware on high-tech Xilinx FPGAs (7 Series and UltraScale). In addition, I'm familiar with many low-rate and high-rate protocols (I2C, SPI, UART, etc) and microcontrollers (Atmel and STM32) programming.

I've graduated with a master's degree in electronics from Iran University of Science and Technology (IUST) which is one of the top-ranked universities of Iran.

Eager to collaborate and work in a team, interested in devising a better problem-solving method for challenging tasks, and learning new technologies and tools if the need arises.

Why Initech? _____

Prepared by experienced English teachers, the texts, articles and conversations are brief and appropriate to your level of proficiency. Take the multiple-choice quiz following each text, and you'll get the results immediately. You will feel both challenged and accomplished! You can even download (as PDF) and print the texts and exercises. It's enjoyable, fun and free. Good luck!

Why Me? _

English texts for beginners to practice reading and comprehension online and for free. Practicing your comprehension of written English will both improve your vocabulary and understanding of grammar and word order. The texts below are designed to help you develop while giving you an instant evaluation of your progress.

Sincerely,

Alireza Zakeri

Attached: Curriculum Vitae



Sony Depthsensing Solutions Rue Jules Cockx 8 1160 Brussels

Brussels, 10 July 2025

Recommendation for Alireza

It is my pleasure to endorse Alireza for any future career opportunities that come his way. Throughout 7 months of collaboration at Sony Depthsensing Solutions (IC Design Center) on a SERDES project, Alireza impressed me with his strong foundational knowledge in digital design and his ability to quickly learn and adapt to new challenges.

Alireza's initiative and autonomy were evident as he tackled the complexities of RTL design in SystemVerilog using UVM methodology. He embraced the learning curve associated with the MIPI standard and effectively applied his newfound knowledge to our project. His positive attitude and willingness to listen to feedback ensured that the final results consistently met our high standards.

Moreover, Alireza is an excellent team player who balances collaboration with independent work. His skills extend beyond design; he is also adept at writing clear and comprehensive documentation, which greatly benefits the entire team.

I am confident that Alireza will excel in any future role he pursues. If you have any questions or would like further insights into his capabilities, please do not hesitate to contact me at arnaud.withoeck@sony.com.

Best regards,

Arnaud Withoeck
Digital Design & Verification Engineer
Sony Depthsensing Solutions



Bart Peters

Principal Engineer Electronics / Teamlead FPGA T +31 (0)45 850 6134 Email bart.peters@carux.com Address: Stationstraat 39G - 6411 NK - Heerlen - The Netherlands

November 13th 2024

Letter of Recommendation about Alireza Zakeri

Dear Mr. Mrs.

I am delighted to recommend Alireza for any professional opportunity. In my role as team lead at Carux, in the driving electronics department. I have had the privilege of working closely with Alireza for a period about 16 months. I am confident in his exceptional abilities and dedication, fulfilling the required capabilities as our FPGA specialist hired from the Dekimo Company.

Alireza consistently showcased a remarkable aptitude for FPGA and IP implementation skills, contributing significantly to our Ghidorah project. Notably, his proficiency in HDL, IP's and general coding has been instrumental in achieving key milestones, exemplified by turning our IP implementations into a workable solution with our Ghidorah video platform project with an ARRIA 10 GX target device.

What sets Alireza apart is his exceptional ability to see complex structures, including effective communication, teamwork, and problem-solving. Alireza brings a proactive approach and resilience to challenging situations, making him an invaluable asset to our organization.

Beyond technical competencies, Alireza is known for his good overview in handling complex IP's and HDL structures including Nios cores. Alireza maintains a positive and group working attitude, but also able to work independently and find his way in the engineering environment or communicate across engineering teams outside the company.

I am confident that Alireza will bring the same dedication, knowledge and problem solving skills to any endeavor employer. Alireza has my recommendation, and if I was able to extend his hire period in our company I wouldn't doubt about extending his contract. But due to organizational headcount restrictions I must let him go.

Should you have any inquiries, please contact me at bart.peters@carux.com. I am more than willing to provide additional insights into Alireza's qualifications and capabilities.

Regards, Bart

