ml605\_gbt\_example\_design CPU\_RESET FMC\_HPC\_LA00\_CC\_P FMC\_HPC\_LA01\_CC\_P FMC\_HPC\_LA02\_P SFP\_RX\_N FMC\_HPC\_LA03\_P FMC\_HPC\_LA04\_P FMC\_HPC\_LA05\_P SFP\_RX\_P GPIO\_LED\_0 GPIO\_LED\_1 GPIO\_LED\_2 SGMIICLK\_QO\_N GPIO\_LED\_3 GPIO\_LED\_4 SGMIICLK\_QO\_P GPIO\_LED\_5 GPIO\_LED\_6 GPIO\_LED\_7 SMA\_REFCLK\_N SFP\_TX\_N SFP\_TX\_P USER\_SMA\_GPIO\_N SMA\_REFCLK\_P USER\_SMA\_GPIO\_P

ml605\_gbt\_example\_design