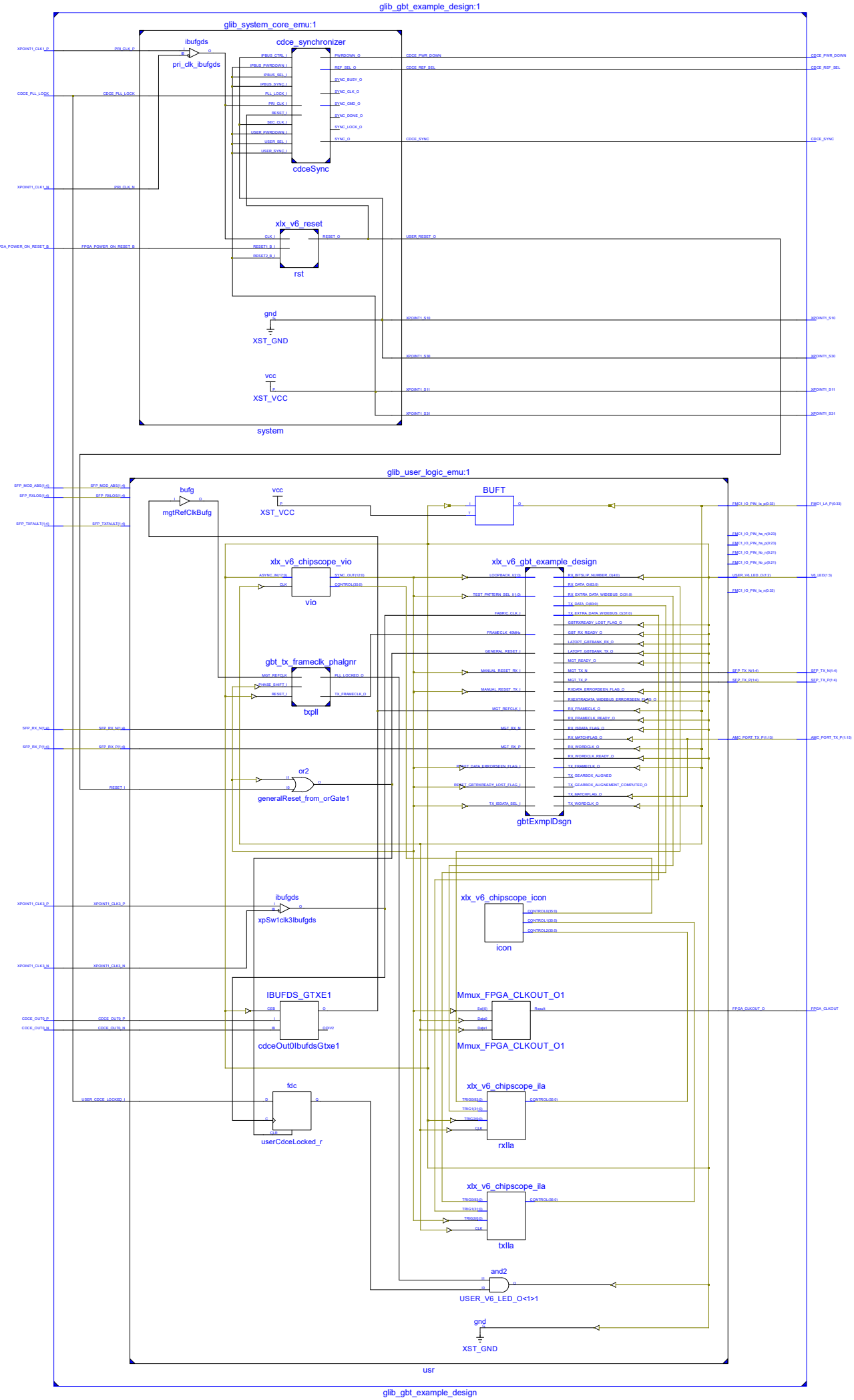
glib_gbt_example_design:1
glib_system_core_emu:1
ibufgds
pri_clk_ibufgds
cdce_synchronizer
cdceSync
xlx_v6_reset
rst
gnd
XST_GND
vcc
XST_VCC
system
glib_user_logic_emu:1
bufg
mgtRefClkBufg
BUFT
xlx_v6_chipscope_vio
vio
xlx_v6_gbt_example_design
gbtExmplDsgn
gbt_tx_frameclk_phalgnr
txpll
or2
generalReset_from_orGate1
ibufgds
xpSw1clk3Ibufgds
xlx_v6_chipscope_icon
icon
IBUFDS_GTXE1
cdceOut0IbufdsGtxe1
Mmux_FPGA_CLKOUT_O1
fdc
userCdceLocked_r
xlx_v6_chipscope_ila
rxIla
txIla
and2
USER_V6_LED_O<1>1
usr
glib_gbt_example_design