Instruction set

R-Type

Name	Type	Op code 6 bits	Rs 5 bits	Rt 5 bits	Rd 5 bits	Shamt 5 bits	Function 6 bits
add	R	000000	00001	00002	00000	00000	100000
Sub	R	000000	00001	00002	00000	00000	100001
Or	R	000000	00001	00002	00000	00000	100101
And	R	000000	00001	00002	00000	00000	100100
Mul	R	000000	00001	00002	00000	00000	011000

I-type

Name	Type	Op code 6 bits	Rs 5 bits	Rt 5 bits	Immediate 16 bits
Addi	I	001000	00000	00001	21
Subi	I	001001	00000	00001	34
Andi	I	001100	00000	00001	25
Ori	I	001101	00000	00001	24

J-type

Name	Type	Opcode 6 bits	Address 26 bits
J	J	000002	1000000