Student Names: Muhammet Ali Topcu
Section: 2
Group ID: 8

CMPE 240 Experiment 4 Preliminary Work

Truth Table:

#	CLK	X	Y	Z
0	0	0	0	x
1	1	0	0	0000
2	0	0	1	0000
3	1	0	1	0011
4	0	1	0	0011
5	1	1	0	0110
6	0	1	1	0110
7	1	1	1	1001

List of components used in the design:

1. 2 D Flip-flop

2. 3 AND Gate

3. 2 NOT Gate

4. 1 OR Gate

5. 1 Clock

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Circuit: