Project 2 Test Cases

Experiment 1:

Direct Mapped, 4KB (Cache Size), 4B (Block Size)

4 1 4 1 0 0 4 0 80

Direct Mapped, 4KB (Cache Size), 16B (Block Size)

4 1 4 1 0 0 16 0 80

Direct Mapped, 16KB (Cache Size), 4B (Block Size)

16 1 16 1 0 0 4 0 80

Direct Mapped, 16KB (Cache Size), 16B (Block Size)

16 1 16 1 0 0 16 0 80

Direct Mapped, 64KB (Cache Size), 4B (Block Size)

64 1 64 1 0 0 4 0 80

Direct Mapped, 64KB (Cache Size), 16B (Block Size)

64 1 64 1 0 0 16 0 80

4-Way Associative, 4KB (Cache Size), 4B (Block Size)

4 4 4 4 0 0 4 0 80

4-Way Associative, 4KB (Cache Size), 16B (Block Size)

4 4 4 4 0 0 16 0 80

4-Way Associative, 16KB (Cache Size), 4B (Block Size)

16 4 16 4 0 0 4 0 80

4-Way Associative, 16KB (Cache Size), 16B (Block Size)

16 4 16 4 0 0 16 0 80

4-Way Associative, 64KB (Cache Size), 4B (Block Size)

64 4 64 4 0 0 4 0 80

4-Way Associative, 64KB (Cache Size), 16B (Block Size)

64 4 64 4 0 0 16 0 80

Experiment 2:

4-Way Associative, 16KB (Cache Size), 16B (Block Size)

A 512KB, 4-way associative L2 cache with access time of 5 cycles

16 4 16 4 512 4 16 5 80

A 512KB, 8-way associative L2 cache with access time of 6 cycles

16 4 16 4 512 8 16 6 80

A 1MB, 8-way associative L2 cache with access time of 8 cycles

16 4 16 4 1024 8 16 8 80