

Figure 9-4 Format of a standard asynchronous transmission.

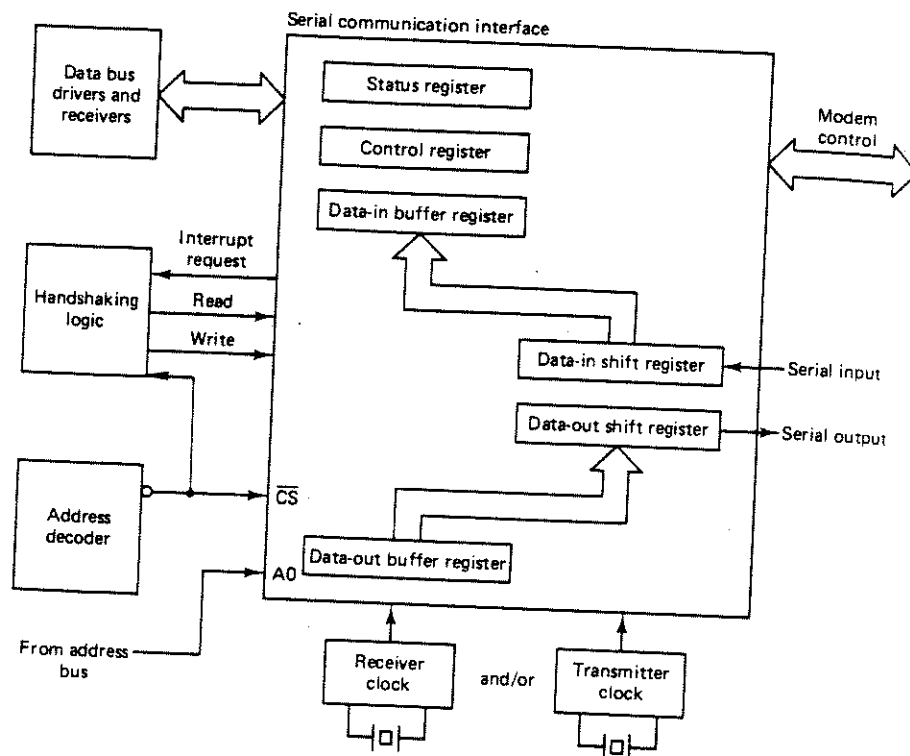
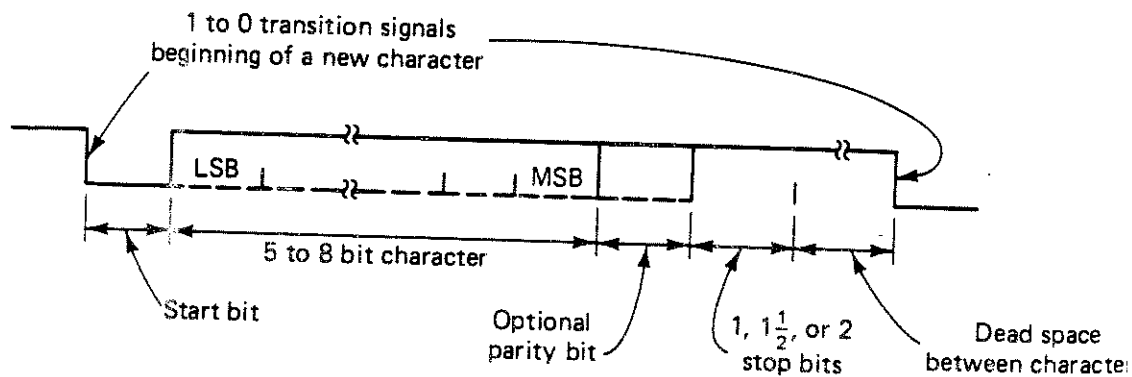
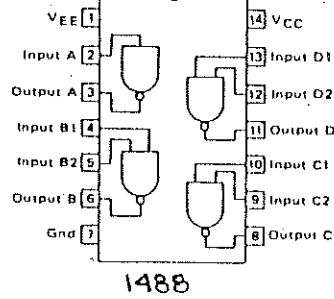
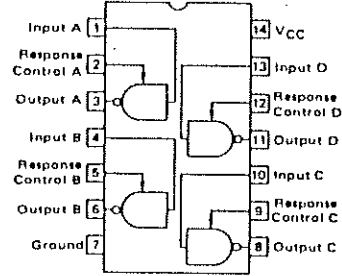


Figure 9-2 Serial interface.

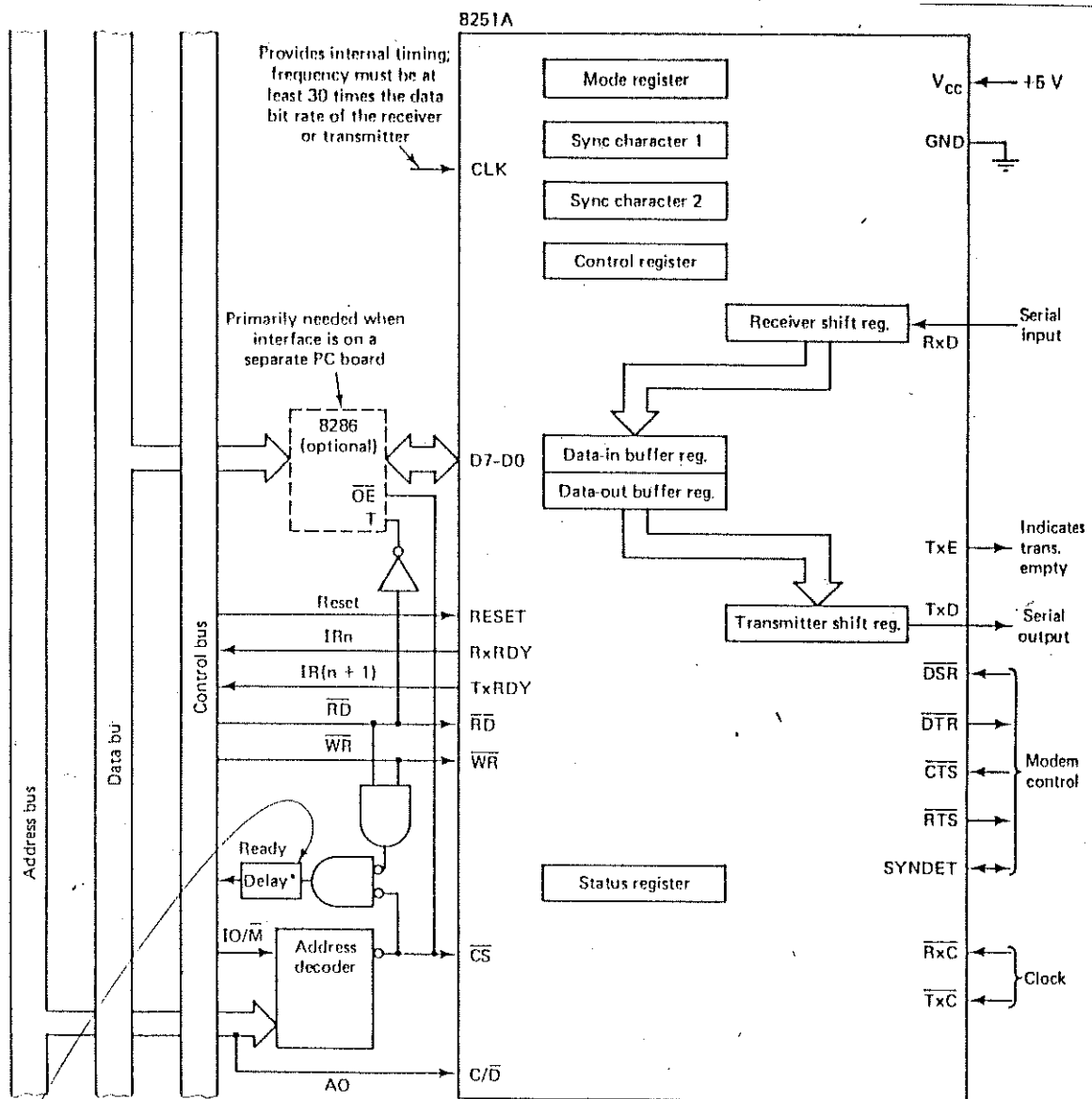
PIN CONNECTIONS



1488

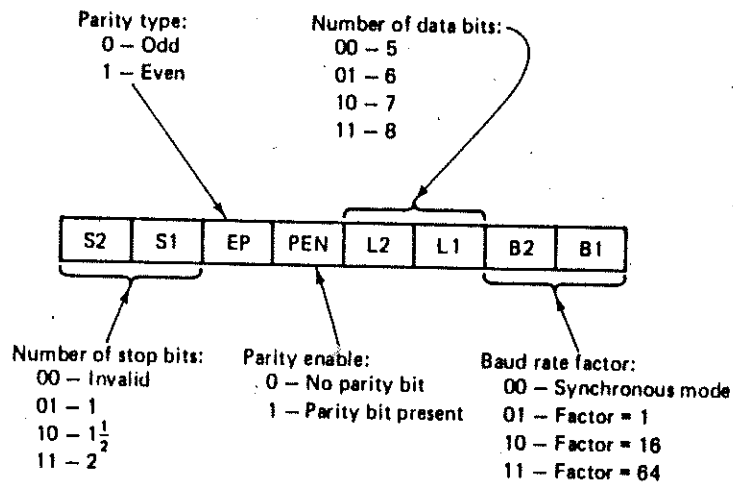


1489



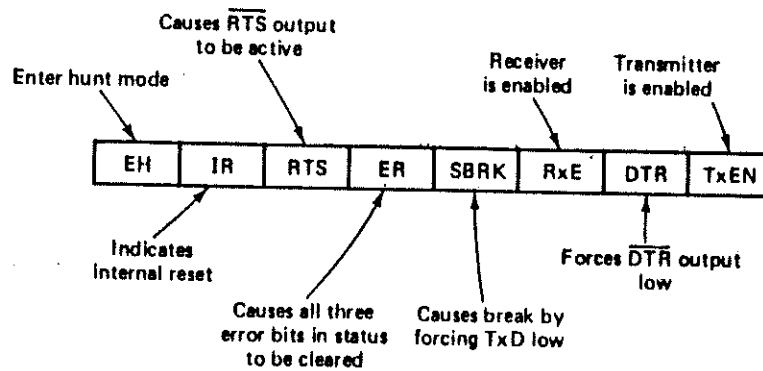
*A 1-wait state delay may be required if external transceivers are used in a minimum mode system.

Figure 9-13 8251A serial communication interface.



(a) Asynchronous mode

Figure 9-16 Format of the control register.



Note: In all cases action is taken when the bit is set to 1.

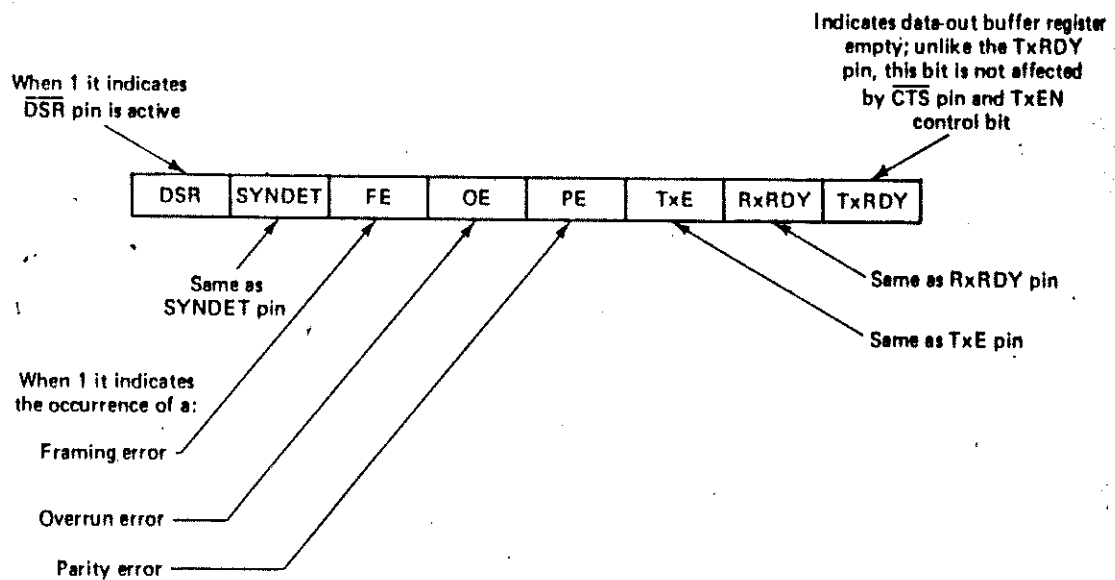
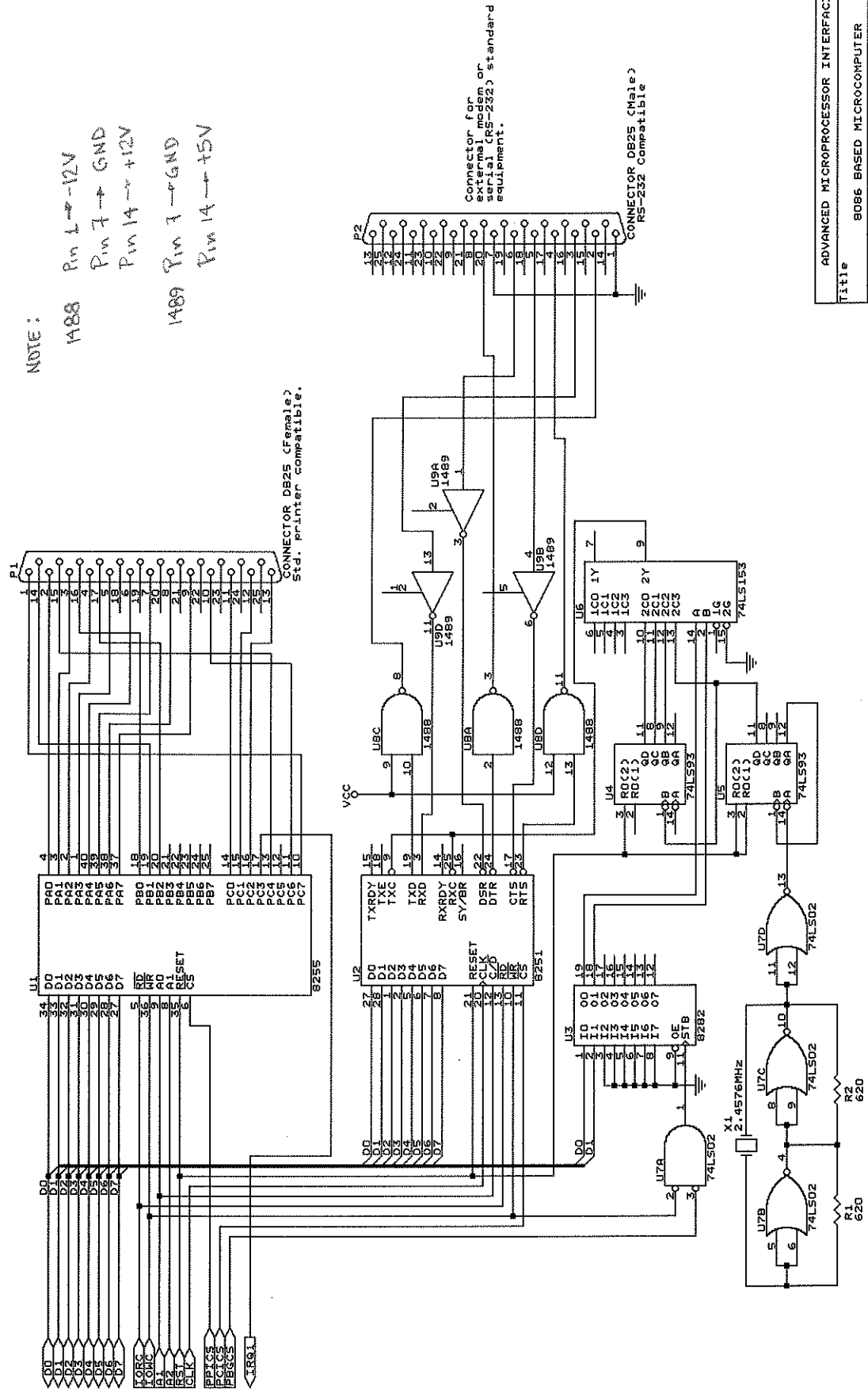


Figure 9-18 Format of the status register.

NOTE:

1488 Pin 1 → -12V
 Pin 7 → GND
 Pin 14 → +12V
 1489 Pin 7 → GND
 Pin 14 → +5V



```
;*****  
; Initializes 8251A (address 0C0H) for:  
;*****
```

```
; - Asynchronous mode  
; - Baud rate 1200 (factor 16)  
; - Parity even  
; - 8 data bits  
; - 1 stop bit  
; - Receiver or Transmitter must be enabled  
;   to establish specific communication.
```

```
xor ax,ax      ; Three zeros before reset  
out 0c1h,al  
out 0c1h,al  
out 0c1h,al  
mov al,01000000b ; Reset word  
out 0c1h,al  
mov al,01111110b ; Mode word for above format  
out 0c1h,al
```