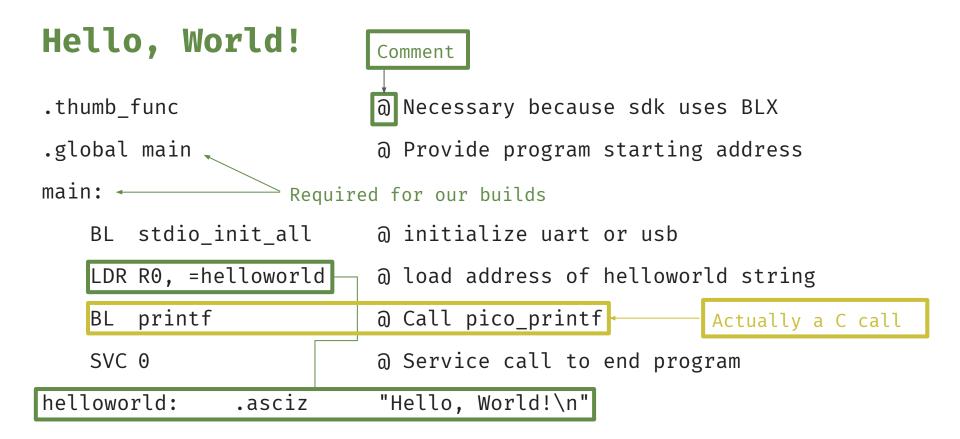
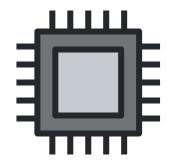


# **ARM** Cross-Compiler Toolchain

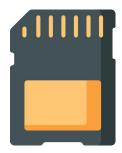


### Registers vs. Memory



#### **REGISTERS**

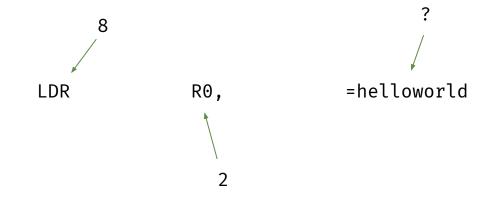
- On the processor
  - But are not the processor
- Called by r[0-12]
- Operate on data
- Limited to 32 bytes of instruction and/or storage



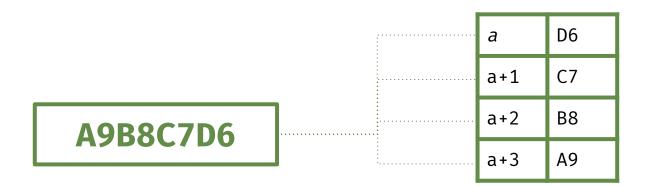
#### **MEMORY**

- Outside of processor
- Called by mnemonics like 0x123f
- Cannot be operated *on* 
  - Can only store

## **ARMv6 Assembly opcodes**



### **Endian-ness**



Raspberry Pi Pico Memory Scheme "Little Endian"

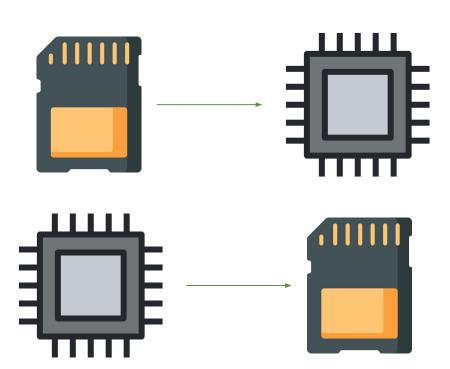
#### STRB vs LDR

#### **LDR**

Load contents of memory into register

#### **STRB**

Store contents of register in memory



Base									
Decimal	0	1	2	3	4	8	10	16	31
Binary	0	01	10	11	100	1000	1010	10000	11111
Octal	0	1	2	3	4	10	12	20	37
Hexadecimal	0	1	2	3	4	8	А	10	1F

Base			
Decimal	64	159	318
Binary			
Octal			
Hexadecimal			

Base				
Decimal	64	159	318	
Binary	1000000			
Octal	100			
Hexadecimal	40			

Base					
Decimal	64	159	318		
Binary	1000000	10011111			
Octal	100	237			
Hexadecimal	40	9F			

Base					
Decimal	64	159	318		
Binary	1000000	10011111	100111110		
Octal	100	237	400		
Hexadecimal	40	9F	13E		



(a)

		10	Blue	Blue		11 Black		
	10	10	10	10	10	10	10	1
	10	10	01	01	10	01	01	1
	10	01	11	00	01	01	10	1

	10	10	10	10	10	10	10	10
	10	10	01	01	10	01	01	10
	10	01	11	00	01	01	10	10
	10	01	00	00	01	01	10	10
	10	10	01	01	10	01	01	10
Г	10	10	10	10	10	10	10	10

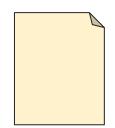
(b)

01 Orange

#### Actual bits (binary)

		_			
10101010	10101010				
10100101	10010110				
10011100	01011010	/			
10010000	01011010	)			
10100101	10010110				
10101010	10101010				
(c)					

0xa24d9100	0xa24d91a1
0xa24d9111	0xa24d91bb
0xa24d913f	0xa24d91c9
0xa24d914b	0xa24d91aa
0xa24d917a	0xa24d91e0
0xa24d9199	0xa24d91ef

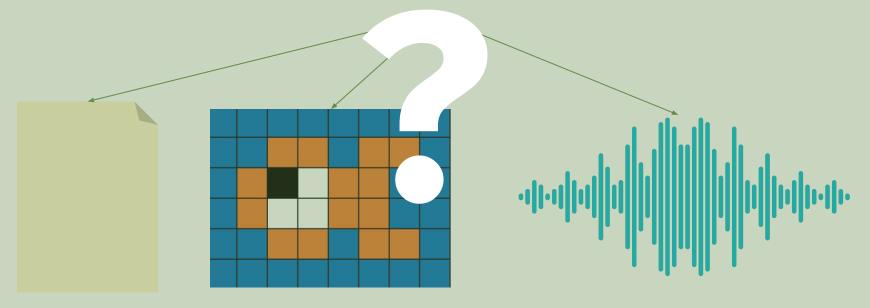


File permissions (octal)

gone\_fishin.png

Memory locations (hexadecimal)

### Data representation



200 Even 0

200<sub>10</sub> \_\_\_\_ ?<sub>2</sub>

200	Even	0
200 ÷ 2	Even	0

**200**<sub>10</sub> —

?;

200	Even	0
200 ÷ 2	Even	0
100 ÷ 2	Even	0

200 <sub>10</sub>		7
	-	• •
10		4

200	Even	0
200 ÷ 2	Even	0
100 ÷ 2	Even	0
50 ÷ 2	Odd	1

**200**<sub>10</sub> —

**200**<sub>10</sub> —

?2

200			Even	0
200	÷	2	Even	0
100	÷	2	Even	0
50	÷	2	Odd	1
25	÷	2	Even	0

**200**<sub>10</sub> —

2

200			Even	0
200	÷	2	Even	0
100	÷	2	Even	0
50	÷	2	Odd	1
25	÷	2	Even	0
12	÷	2	Even	0

**200**<sub>10</sub> —

200			Even	0
200	÷	2	Even	0
100	÷	2	Even	0
50	÷	2	Odd	1
25	÷	2	Even	0
12	÷	2	Even	0
6	÷	2	Odd	1

**200**<sub>10</sub> —

• 2

200			Even	0
200	÷	2	Even	0
100	÷	2	Even	0
50	÷	2	Odd	1
25	÷	2	Even	0
12	÷	2	Even	0
6	÷	2	Odd	1
3	÷	2	Odd	1

200<sub>10</sub> ---- ?

200		Even	0
200	÷ 2	Even	0
100	÷ 2	Even	0
50	÷ 2	Odd	1
25	÷ 2	Even	0
12	÷ 2	Even	0
6	÷ 2	Odd	1
3	÷ 2	Odd	1
1	÷ 2	No carry	0

20	200		Even	0
21	200	÷ 2	Even	0
2 <sup>2</sup>	100	÷ 2	Even	0
2 <sup>3</sup>	50	÷ 2	Odd	1
2 <sup>4</sup>	25	÷ 2	Even	0
2 <sup>5</sup>	12	÷ 2	Even	0
2 <sup>6</sup>	6	÷ 2	Odd	1
2 <sup>7</sup>	3	÷ 2	Odd	1
28	1	÷ 2	No carry	0

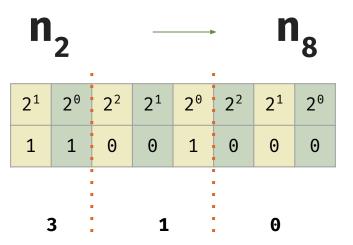
**200**<sub>10</sub> \_\_\_\_

?2

2 <sup>7</sup>	2 <sup>6</sup>	2 <sup>5</sup>	2 <sup>4</sup>	<b>2</b> <sup>3</sup>	2 <sup>2</sup>	2 <sup>1</sup>	2 <sup>0</sup>
1	1	0	0	1	0	0	0

**11001000** is an **8 bit** number.

### **BIN2OCT**



66	
123	
253	
491	

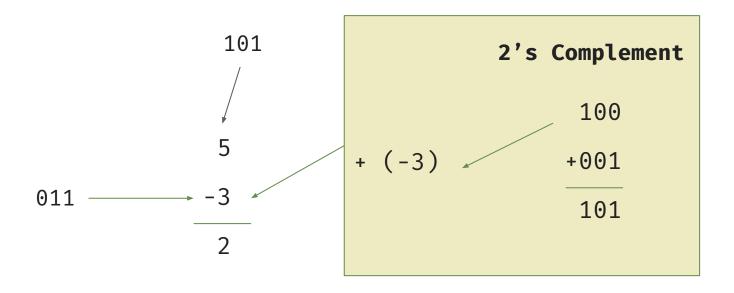
66	01000010	
123		
253		
491		

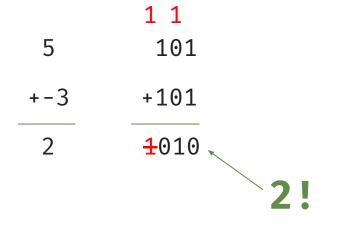
66	01000010
123	01111011
253	
491	

66	01000010
123	01111011
253	11111101
491	

66	01000010
123	01111011
253	11111101
491	NOPE, NOT IN 8 BITS

$$\begin{array}{ccc}
 & 1 & 1 \\
 & 5 & & 101 \\
 & +5 & & +101 \\
\hline
 & 10 & & 1010
\end{array}$$





### **ARMv6 Assembly opcodes**

