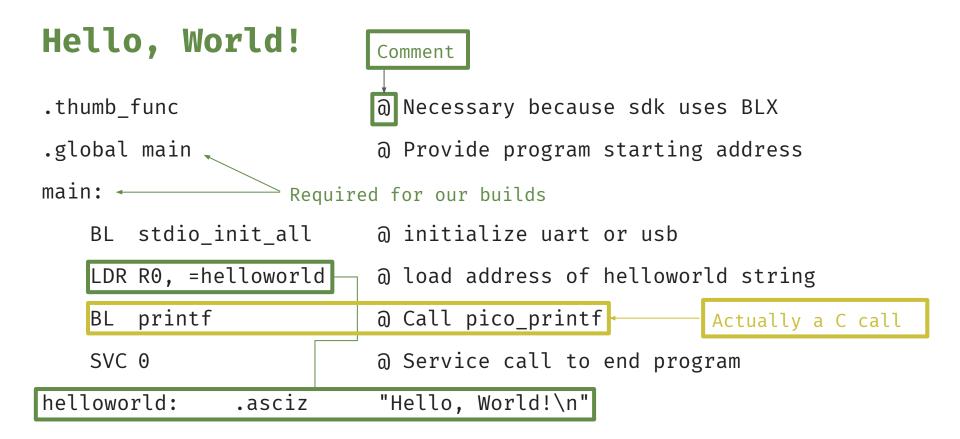
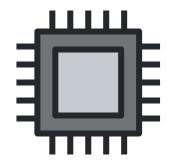


ARM Cross-Compiler Toolchain

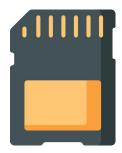


Registers vs. Memory



REGISTERS

- On the processor
 - But are not the processor
- Called by r[0-12]
- Operate on data
- Limited to 32 bytes of instruction and/or storage



MEMORY

- Outside of processor
- Called by mnemonics like 0x123f
- Cannot be operated *on*
 - Can only store

ARMv6 Assembly opcodes

```
r0, #2 @ moves the value 2 into register 0
mov
mov
       r1, r0
                     @ copies the value 2 from register 0 to register 1
                     @ register 0 is still equal to 2
add r0, r1, r2 @ Add r1 and r2 and store in r0
add r1, r2 @ Add r1 and r2 and store in r1
          r0, #'A' @ load the value of A into r0
 mov
 ldr
            r1, =outstr @ load address of outstr into r1
 strb
            r0, [r1]
                         @ store the first byte in r0 into the address
                         @ starting at r1
            r0, r1
 strb
                         @ Does not work
```

ARMv6 Assembly opcodes

OPCODE
$$R_{M}$$
 , R_{N} , R_{D} ADD $R5$, $R3$, $R2$ $R5 = R3 + R2$ OPCODE R_{M} , I_{MM} MOV $R5$, $\#200$ $R5 \leftarrow 200_{10}$

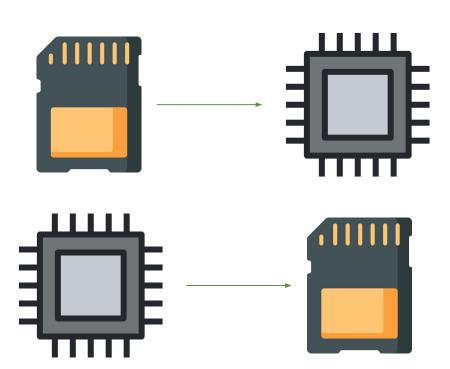
STRB vs LDR

LDR

Load contents of memory into register

STRB

Store contents of register in memory





(a)

		10	10 Blue		11	Black		
	10	10	10	10	10	10	10	1
	10	10	01	01	10	01	01	1
	10	01	11	00	01	01	10	1

	10	10	10	10	10	10	10	10
	10	10	01	01	10	01	01	10
	10	01	11	00	01	01	10	10
	10	01	00	00	01	01	10	10
	10	10	01	01	10	01	01	10
Г	10	10	10	10	10	10	10	10

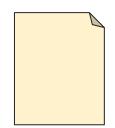
(b)

01 Orange

Actual bits (binary)

		_		
10101010	10101010			
10100101	10010110			
10011100	01011010	/		
10010000	01011010)		
10100101	10010110			
10101010	10101010			
(c)				

0xa24d9100	0xa24d91a1
0xa24d9111	0xa24d91bb
0xa24d913f	0xa24d91c9
0xa24d914b	0xa24d91aa
0xa24d917a	0xa24d91e0
0xa24d9199	0xa24d91ef

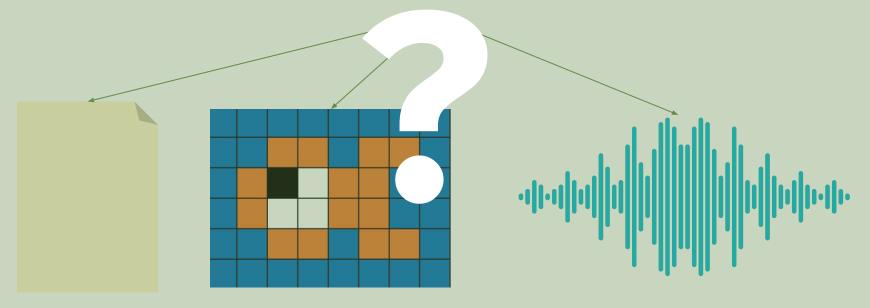


File permissions (octal)

gone_fishin.png

Memory locations (hexadecimal)

Data representation



Base									
Decimal	0	1	2	3	4	8	10	16	31
Binary	0	01	10	11	100	1000	1010	10000	11111
Octal	0	1	2	3	4	10	12	20	37
Hexadecimal	0	1	2	3	4	8	А	10	1F

Base			
Decimal	64	159	318
Binary			
Octal			
Hexadecimal			

Base					
Decimal	64	159	318		
Binary	1000000				
Octal	100				
Hexadecimal	40				

Base					
Decimal	64	159	318		
Binary	1000000	10011111			
Octal	100	237			
Hexadecimal	40	9F			

Base						
Decimal	64	159	318			
Binary	1000000	10011111	100111110			
Octal	100	237	400			
Hexadecimal	40	9F	13E			

200 Even 0

200₁₀ ____ ?₂

200	Even	0
200 ÷ 2	Even	0

200₁₀ —

?;

200	Even	0
200 ÷ 2	Even	0
100 ÷ 2	Even	0

200 ₁₀	 ?
10	

200	Even	0
200 ÷ 2	Even	0
100 ÷ 2	Even	0
50 ÷ 2	Odd	1

200₁₀ —

200₁₀ —

?2

200			Even	0
200	÷	2	Even	0
100	÷	2	Even	0
50	÷	2	Odd	1
25	÷	2	Even	0

200₁₀ —

2

200			Even	0
200	÷	2	Even	Θ
100	÷	2	Even	0
50	÷	2	Odd	1
25	÷	2	Even	0
12	÷	2	Even	0

200₁₀ — 3

200			Even	0
200	÷	2	Even	Θ
100	÷	2	Even	0
50	÷	2	Odd	1
25	÷	2	Even	0
12	÷	2	Even	0
6	÷	2	Odd	1

200₁₀ —

• 2

200		Even	0
200	÷ 2	Even	0
100	÷ 2	Even	0
50	÷ 2	Odd	1
25	÷ 2	Even	0
12	÷ 2	Even	0
6	÷ 2	Odd	1
3	÷ 2	Odd	1

200₁₀ ---- ?

200		Even	0
200	÷ 2	Even	0
100	÷ 2	Even	0
50	÷ 2	Odd	1
25	÷ 2	Even	0
12	÷ 2	Even	0
6	÷ 2	Odd	1
3	÷ 2	Odd	1
1	÷ 2	No carry	0

20	200		Even	0
21	200 ÷	2	Even	0
2 ²	100 ÷	2	Even	0
2 ³	50 ÷	2	Odd	1
2 ⁴	25 ÷	2	Even	0
2 ⁵	12 ÷	2	Even	0
2 ⁶	6 ÷	2	Odd	1
2 ⁷	3 ÷	2	Odd	1
28	1 -	÷ 2	No carry	0

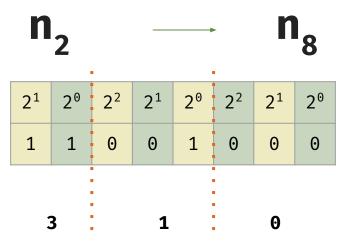
200₁₀ ____

?2

2 ⁷	2 ⁶	2 ⁵	2 ⁴	2 ³	2 ²	2 ¹	2 ⁰
1	1	0	0	1	0	0	0

11001000 is an **8 bit** number.

BIN2OCT



66	
123	
253	
491	

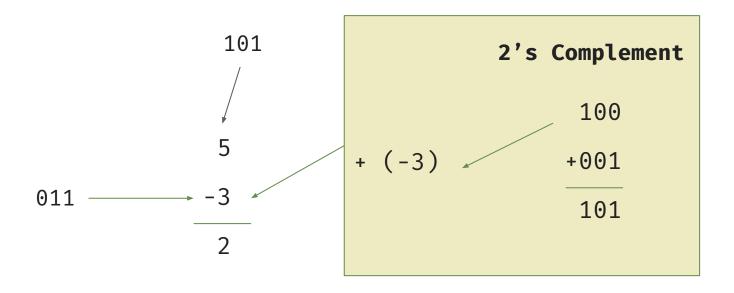
66	01000010	
123		
253		
491		

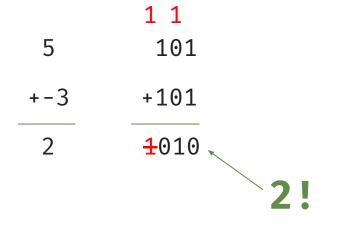
66	01000010
123	01111011
253	
491	

66	01000010
123	01111011
253	11111101
491	

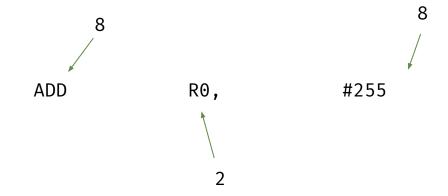
66	01000010
123	01111011
253	11111101
491	NOPE, NOT IN 8 BITS

$$\begin{array}{ccc}
 & 1 & 1 \\
 & 5 & & 101 \\
 & +5 & & +101 \\
\hline
 & 10 & & 1010
\end{array}$$

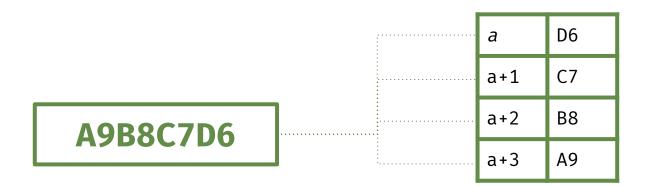




ARMv6 Assembly opcodes



Endian-ness



Raspberry Pi Pico Memory Scheme "Little Endian"