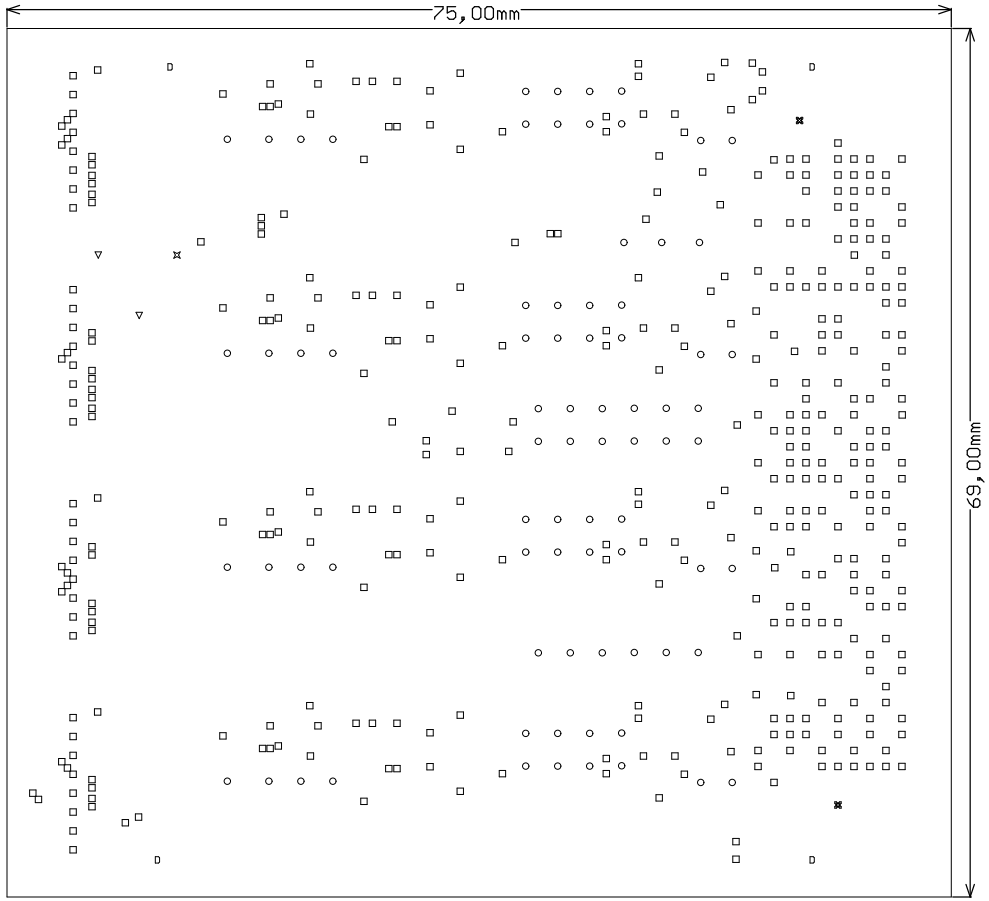
Layer	Name	Material	Thickness	Constant
	Top Overlay			
	Top Solder	Solder Resist	0,025mm	4
1	Layer1 Top		0,035mm	
	Dielectric1	PP-006	0,160mm	4.7
2	Layer2		0,035mm	
	Dielectric2	Core-039	1,130mm	4.74
3	Layer3		0,035mm	
	Dielectric3	PP-006	0,160mm	4.7
4	Layer4 Bottom		0,035mm	
	Bottom Solder	Solder Resist	0,025mm	4
	Bottom Overlay			
Total board thickness:			1,640mm	

Zdiff = 100 Ohm on Layer 1/4 (Ref. layer 2/3)
L 125um / S 125um / L 125um



Symbol	Hit Count	Finished Hole Size	Plated	Hole Type	Drill Layer Pair
□	406	0,200mm (7,87mil)	PTH	Round	Layer1 Top – Layer4 Bottom
▽	2	0,800mm (31,50mil)	PTH	Slot	Layer1 Top – Layer4 Bottom
○	77	1,000mm (39,37mil)	PTH	Round	Layer1 Top – Layer4 Bottom
⌑	1	1,000mm (39,37mil)	PTH	Slot	Layer1 Top – Layer4 Bottom
⊠	2	1,300mm (51,18mil)	NPTH	Round	Layer1 Top – Layer4 Bottom
◫	4	2,600mm (102,36mil)	PTH	Round	Layer1 Top – Layer4 Bottom
	492 Total				

Slot definitions : **Routed Path Length** = Calculated from tool start centre position to tool end centre position.
Hole Length = Routed Path Length + Tool Size = Slot length as defined in the PCB layout

All drills (PTH and NPTH) should run in one work process

Title: CSI2-ZCU106-EVK_Rev01	Allied Vision Technologies Taschenweg 2a Stadtroda Germany		Bare Board Revision: 01
Project:	PCB Designer: OFU	Layer Name: Drill Drawing	Bare Board Number:
Date: 12.05.2023	File Name: CSI2-ZCU106-EVK_Rev01.PcbDoc	SCALE: 1.00	