



Layer	Name	Material	Thickness	Constant
	Top Overlay			
	Top Solder	Solder Resist	0,025mm	4
1	Layer1 Top		0,035mm	
	Dielectric1	PP-006	0,160mm	4,7
2	Layer2		0,035mm	
	Dielectric2	Core-039	1,130mm	4,74
3	Layer3		0,035mm	
	Dielectric3	PP-006	0,160mm	4,7
4	Layer4 Bottom		0,035mm	
	Bottom Solder	Solder Resist	0,025mm	4
	Bottom Overlay			

Total board thickness: 1,640mm

L 125um / S 125um / L 125um : Zd=100 Ohm

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Symbol	Hit Count	Finished Hole Size	Plated	Hole Type	Drill Layer Pair
✕	190	0,200mm (7,87mil)	PTH	Round	Layer1 Top - Layer4 Bottom
⊗	8	0,400mm (15,75mil)	NPTH	Round	Layer1 Top - Layer4 Bottom
⊕	4	0,550mm (21,65mil)	PTH	Slot	Layer1 Top - Layer4 Bottom
✕	4	0,800mm (31,50mil)	NPTH	Round	Layer1 Top - Layer4 Bottom
◇	12	0,800mm (31,50mil)	PTH	Round	Layer1 Top - Layer4 Bottom
☆	4	0,850mm (33,47mil)	PTH	Slot	Layer1 Top - Layer4 Bottom
▽	4	0,900mm (35,43mil)	PTH	Round	Layer1 Top - Layer4 Bottom
○	34	1,000mm (39,37mil)	PTH	Round	Layer1 Top - Layer4 Bottom
⊞	4	1,550mm (61,02mil)	PTH	Round	Layer1 Top - Layer4 Bottom
★	1	2,000mm (78,74mil)	NPTH	Slot	Layer1 Top - Layer4 Bottom
□	8	2,600mm (102,36mil)	PTH	Round	Layer1 Top - Layer4 Bottom
273 Total					

Slot definitions : Routed Path Length = Calculated from tool start centre position to tool end centre position.
Hole Length = Routed Path Length + Tool Size = Slot length as defined in the PCB layout

Title: CS12-IMX8M-EVK		Allied Vision Technologies Taschenweg 2a Stadtroda		Germany	Bare Board Revision: 02
Project:		PCB Designer: OFU/IHA		Layer Name: Drill Drawing	
Date: 21.04.2023		File Name: CS12-IMX8M-EVK_Rev02.PcbDoc			Bare Board Number:
				SCALE: 1:00	

All drills (PTH and NPTH) should run in one work process