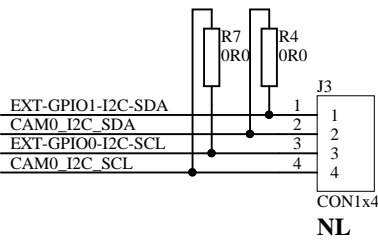
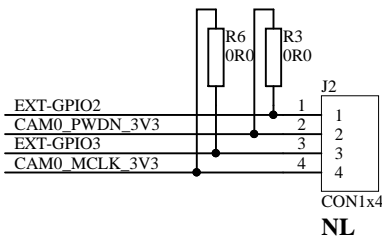


I2C

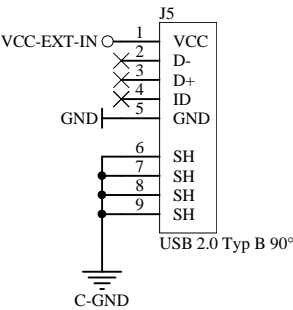


GPIOs

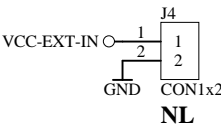


VCC-EXT-IN : DCIN 4V5..5V5, max. 1.5 A

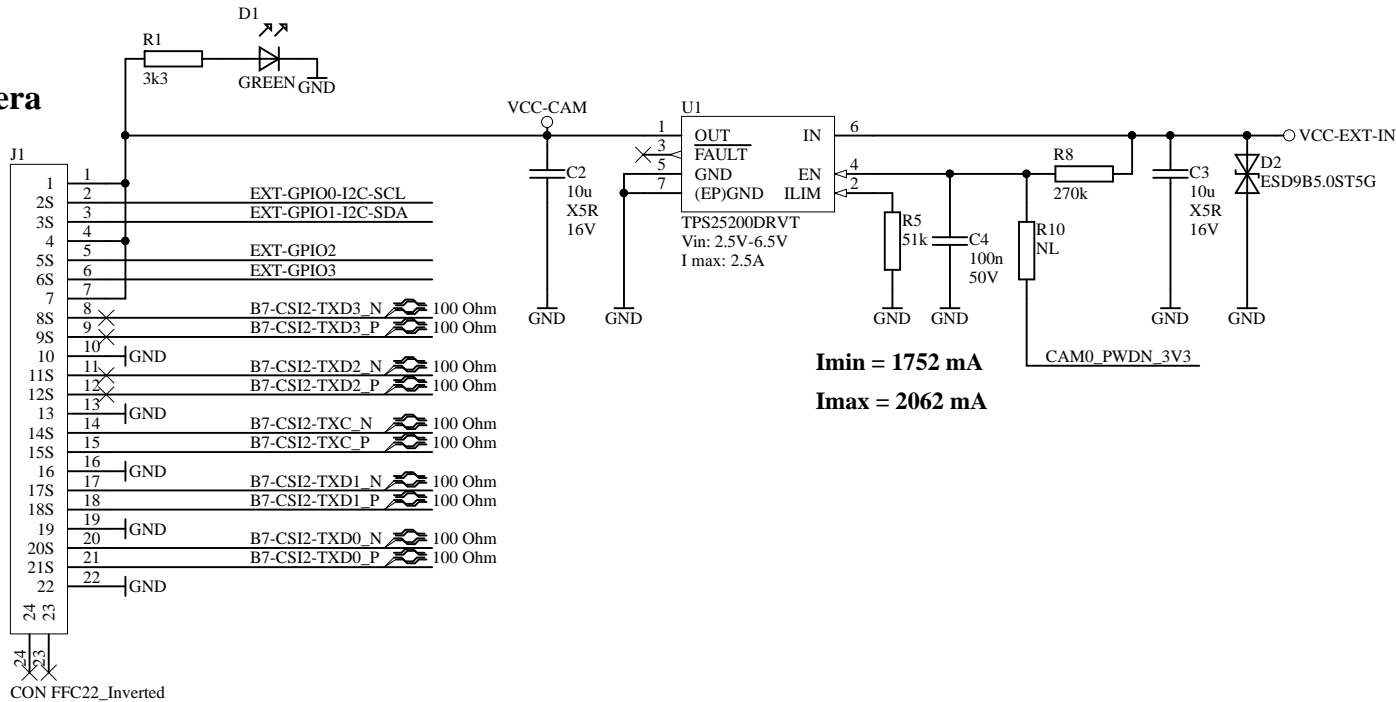
external USB Power



external Power

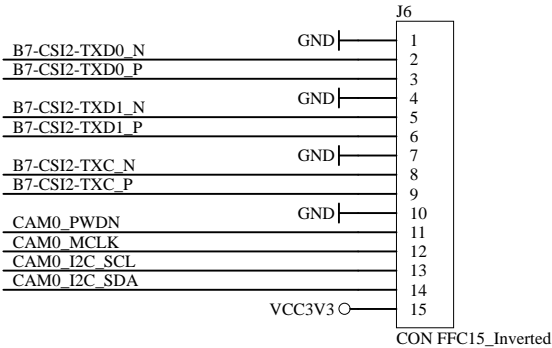


to Camera

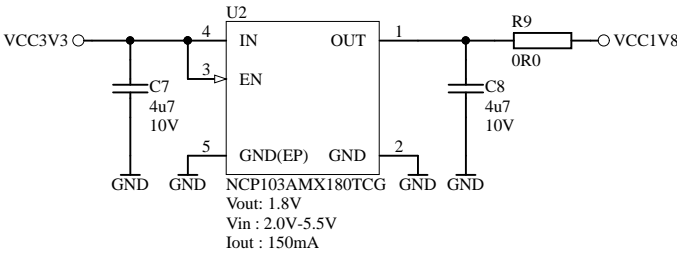


Imin = 1752 mA
Imax = 2062 mA

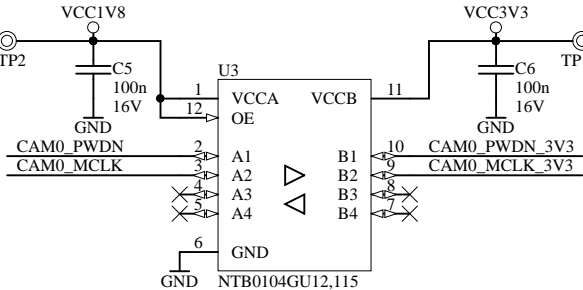
to Nano



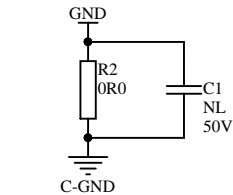
3.3 V -> 1.8V



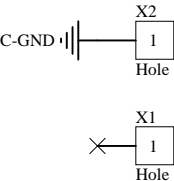
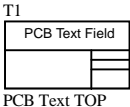
GPIO Levelshifter 1V8 <-> 3V3




GND <-> Chassis-GND



Holes on Nano Board are connected to GND



Based on Nano Carrier Board Schematics 08-May-2019

Allied Vision Technologies GmbH			Taschenweg 2a	07646 Stadtroda			
Title: CSI2-RPI-NANO_Rev01			Confidential				
PCB:	PCBA:	3					
Author: CWA / MTH	Checked by: OFU	2					
Date: 27.11.2019	Page: 1 of 1	1					
File: CSI2-RPI-NANO_Rev01.SchDoc		No.	Changes	Rev.:	Date:	Name:	