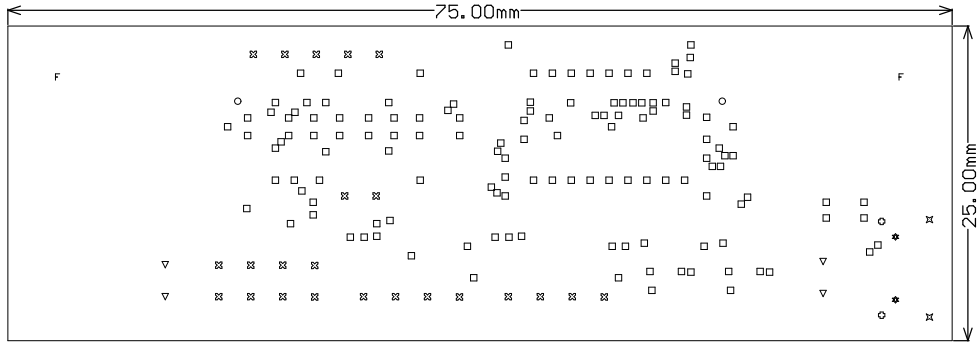
Layer Stackup 4 FR4 TG150			
Top Soldermask	25 um	standard	
Layer1 Top	35 um	Signal / Zdiff 100R	L 125um / S 125um / L 125um
Prepreg 2x1080	140 um		
Layer2	35 um	Plane / Ref.	
Core / Prepreg	1200 um		
Layer3	35 um	Plane / Ref.	
Prepreg 2x1080	140 um		
Layer4 Bottom	35 um	Signal / Zdiff 100R	L 125um / S 125um / L 125um
Bottom Soldermask	25 um	standard	
Thickness approx.	1600 um		

Symbol	Hit Count	Finished Hole Size	Plated	Hole Type	Drill Layer Pair
□	134	0.200mm (7.87mil)	PTH	Round	Layer1 Top - Layer4 Bottom
⊠	2	0.550mm (21.65mil)	PTH	Slot	Layer1 Top - Layer4 Bottom
✱	2	0.800mm (31.50mil)	NPTH	Round	Layer1 Top - Layer4 Bottom
✪	2	0.850mm (33.47mil)	PTH	Slot	Layer1 Top - Layer4 Bottom
▽	4	0.900mm (35.43mil)	PTH	Round	Layer1 Top - Layer4 Bottom
○	2	1.000mm (39.37mil)	NPTH	Round	Layer1 Top - Layer4 Bottom
⊠	23	1.000mm (39.37mil)	PTH	Round	Layer1 Top - Layer4 Bottom
F	2	3.200mm (125.98mil)	PTH	Round	Layer1 Top - Layer4 Bottom
	171 Total				

Slot definitions : Routed Path Length = Calculated from tool start centre position to tool end centre position.
Hole Length = Routed Path Length + Tool Size = Slot length as defined in the PCB layout



All drills (PTH and NPTH) should run in one work process

Title: CSI2-TX2-XAVIER_Rev02	Allied Vision Technologies Taschenweg 2a Stadtroda Germany		Bare Board Revision: 02
Project: CSI2-TX2-XAVIER	PCB Designer: OFU	Layer Name: Drill Drawing	Bare Board Number:
Date: 07.07.2023	File Name: CSI2-TX2-XAVIER_Rev02.PcbDoc	SCALE: 1.00	