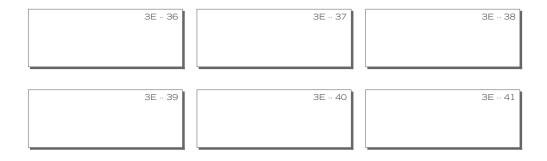


LABORATORY 6

Please print this sheet prior to coming to laboratory. Complete the pre-laboratory tasks in your lab notebook. Complete the lab tasks in **both** your lab notebook and this submission sheet.

1 Pre-laboratory Verification



2 Laboratory Verification



3 Deliverables

1.	Att	Attach multiplex.vhd VHDL code (decoder is not necessary).																											
2.	Att	Attach block diagram.																											
3.	Att	Attach three required photos of board, printed on a single sheet of paper.																											
4.	When implementing blanking for the seven-segment display that was created using hardward (previous laboratory), a combinational circuit had to be developed. What would need to be changed in order to implement blanking on the FPGA?																												
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6.	. What is the function of a multiplexer (summarize, as if explaining to a child)? Give two real-world applications of multiplexers.															O													
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