

LABORATORY 8

Please print this sheet prior to coming to laboratory. Complete the pre-laboratory tasks in your lab notebook. Complete the lab tasks in **both** your lab notebook and this submission sheet.

1 Pre-laboratory Verification

| | | |
|----------|----------|----------|
| 3E .. 51 | 3E .. 52 | 3E .. 53 |
| 3E .. 54 | 3E .. 55 | |

2 Laboratory Verification

| |
|----------|
| 3E .. 56 |
|----------|

3 Deliverables

1. Attach **axl_interface.vhd** VHDL code.
2. Attach a printout of the block diagram.