

Kit No. Date: / /

USE TEMPLATE
01234
56789
MISFILL -1 PT



LABORATORY 8

Please print this sheet prior to coming to laboratory. Complete the pre-laboratory tasks in your lab notebook. Complete the lab tasks in **both** your lab notebook and this submission sheet.

1 Pre-laboratory Verification

<div>3E .. 51</div>	<div>3E .. 52</div>	<div>3E .. 53</div>
<div>3E .. 54</div>	<div>3E .. 55</div>	

2 Laboratory Verification

<div>3E .. 56</div>

3 Deliverables

1. Attach **axl_interface.vhd** VHDL code.
2. Attach a printout of the block diagram.