



LABORATORY 8

Please print this sheet prior to coming to laboratory. Complete the pre-laboratory tasks in your lab notebook. Complete the lab tasks in **both** your lab notebook and this submission sheet.

1 Pre-laboratory Verification

Diagram showing five vertical rectangles representing pages 51 to 55. Each rectangle is labeled at the top with "4E .." followed by the page number. The rectangles are arranged horizontally, with a small gap between each one.

2 Laboratory Verification

4E .. 56

3 Deliverables

1. Attach **axl_interface.vhd** VHDL code.
2. Attach a printout of the block diagram.