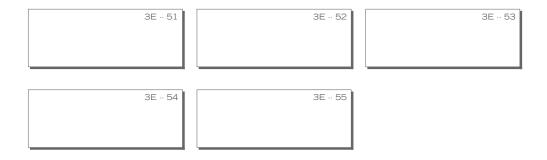
							USE TEMPLATE			PARTNER
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LABORATORY 8

Please print this sheet prior to coming to laboratory. Complete the pre-laboratory tasks in your lab notebook. Complete the lab tasks in **both** your lab notebook and this submission sheet.

1 Pre-laboratory Verification



2 Laboratory Verification



3 Deliverables

- 1. Attach axl_interface.vhd VHDL code.
- 2. Attach a printout of the block diagram.