

Kit No.   Date:   /   /

USE TEMPLATE  
01234  
56789  
MISFILL -1 PT



# LABORATORY 8

Please print this sheet prior to coming to laboratory. Complete the pre-laboratory tasks in your lab notebook. Complete the lab tasks in **both** your lab notebook and this submission sheet.

## 1 Pre-laboratory Verification

4E .. 51	4E .. 52	4E .. 53
4E .. 54	4E .. 55	

## 2 Laboratory Verification

4E .. 56

## 3 Deliverables

1. Attach **axl\_interface.vhd** VHDL code.
2. Attach a printout of the block diagram.